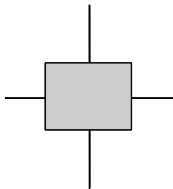
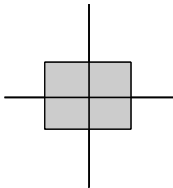


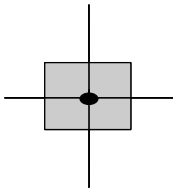
a Memristor as a switch



b Unknown State



c Open State



d Close State