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EFFECTS OF GROWTH CONDITIONS ON DEEP-LEVEL DEFECTS IN VPE AND LPE GaAs*

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Abstract. - Studies of the deep-level defects in the VPE and LPE GaAs layers under various growth conditions have been made in this work, using DLTS and C-V techniques. In the VPE grown GaAs, characterization of grown-in defects vs. Ga/As ratio (i.e., 2/1, 3/1, 4/1, and 6/1) were made on GaAs layers grown on <100>, <211A>, and <211B> substrates. The two common electron traps observed in these samples were due to the EB-4 (i.e., E_-0.71 eV) level and the EL-2 (i.e., E_-0.83eV) level. The density of these two electron traps was found to depend on the stoichiometry as well as substrate orientation. For examples, in the <211> orientation samples, the density of EL2 was found to decrease with increasing Ga/As ratio(e.g., $N_T = 1.2x10^{14} cm^{-3}$ for Ga/As = 2/1, and reducing to 5.4x10¹¹ cm⁻³ for Ga/As = 6/1). A similar result was also observed in the EB-4 electron trap. Low temperature (230°C) thermal annealing and recombination enhanced annealing studies on these samples showed significant reduction in the density of EB-4 trap and slight reduction in EL-2 trap density. For the LPE grown GaAs layers, two growth temperatures (700 and 800°C) and two temperature dropping rates (1°C/min. and 0.4°C/min.) were used. The DLTS and C-V measurements were made on these samples, and the results showed that only EB-4 electron trap (i.e., E_-0.71 eV) was observed in the LPE GaAs samples with 1°C/min. dropping rate. Details of our DLTS analysis of the grown-in defects vs. growth conditions in both LPE and VPE grown GaAs layers will be discussed in this paper.

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Fig. 1 DLTS scans of electron traps for VPE GaAs with $<\!\!211\!\!>$ orientation



Fig. 2 DLTS scans of electron traps for VPE GaAs with ${<}100{>}$ orientation

Growth	Temp. (°C)	725	725	725	725	750	750	725	715	715	725	725	725	720	725
	Ga/As ratio	3/1	4/1	4/1	4/1	4/1	4/1	5/1	2/1	2/1	3/1	6/1	6/1	6/1	5/1
	N_{TT}/N_D	2.1x10 ⁻²	7.3x10 ⁻³	9.6x10 ⁻²	2.6x10 ⁻³	1.7×10 ⁻³	2.8x10 ⁻³	1.4×10 ⁻²	1.1x10 ⁻²	7x10 ⁻²	1x10 ⁻³	3.8×10 ⁻³	I	1.8×10 ⁻³	1
	$N_{\rm TT}$ (cm ⁻³)	1.5x10 ¹³	2.2x10 ¹³	9x10 ¹³	2.6x10 ¹²	1.2x10 ¹³	1.6x10 ¹³	1.43x10 ¹⁴	7.6x10 ¹²	1.2x10 ¹⁴	4.5x10 ¹²	4.9x10 ¹²	~101 ⁰	1.24×10 ¹²	
Traps	E _T (eV)	EL2 = Ec-0.83	Ec-0.83	.=	=	=	=	=	=	=	Ξ	=	=	2	2
Electron	UTT/ND	1	1	7.3x10 ⁻³	8x10 ⁻⁴	1.4x10 ⁻³	1.8x10 ⁻³	2.9x10 ⁻³	1	1	6x10 ⁻⁴	1.9x10 ⁻³	1.9x10 ⁻⁴	1	1.1×10 ⁻²
	$N_{\rm TT}$ (cm ⁻³)	1	1	4x10 ¹³	8x10 ¹¹	1x10 ¹³	1x10 ¹³	2.9x10 ¹³	1	1	2.4x10 ¹²	2.5x10 ¹²	6x10 ¹¹	I	4.3×1013
	E _T (eV)	EB4 = Ec-0.71	1	EB4	=	=	=	=	1	ı	EB4	1	I	1	1
	N _D (cm ⁻³)	7.2x10 ¹⁴	3x10 ^{1.5}	5.5x10 ¹⁵	• 10 ^{1 5}	7.2x10 ¹⁵	5.6x10 ¹⁵	1x10 ¹⁶	6.7x10 ¹⁴	1.7×10 ¹⁵	4x10 ¹⁵	1.3x10 ¹⁵	3.1x10 ¹⁵	7x10 ¹⁴	3.8×10 ¹⁵
	Orientation	100	100	211A	100	211A	2118	2118	100	211A	100	100	211A	211A	211A
	Sample No.	75A	76A	768	77A	77B	77BB	78BB	35CA	35CB	36HA	37HA	37HB	39HB	788

Table 1. Electron Traps in VPE GaAs as functions of Ga/As ratio and substrate orientation

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