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Parametric LCA based Ecodesign Process for Power Electronics Product Development

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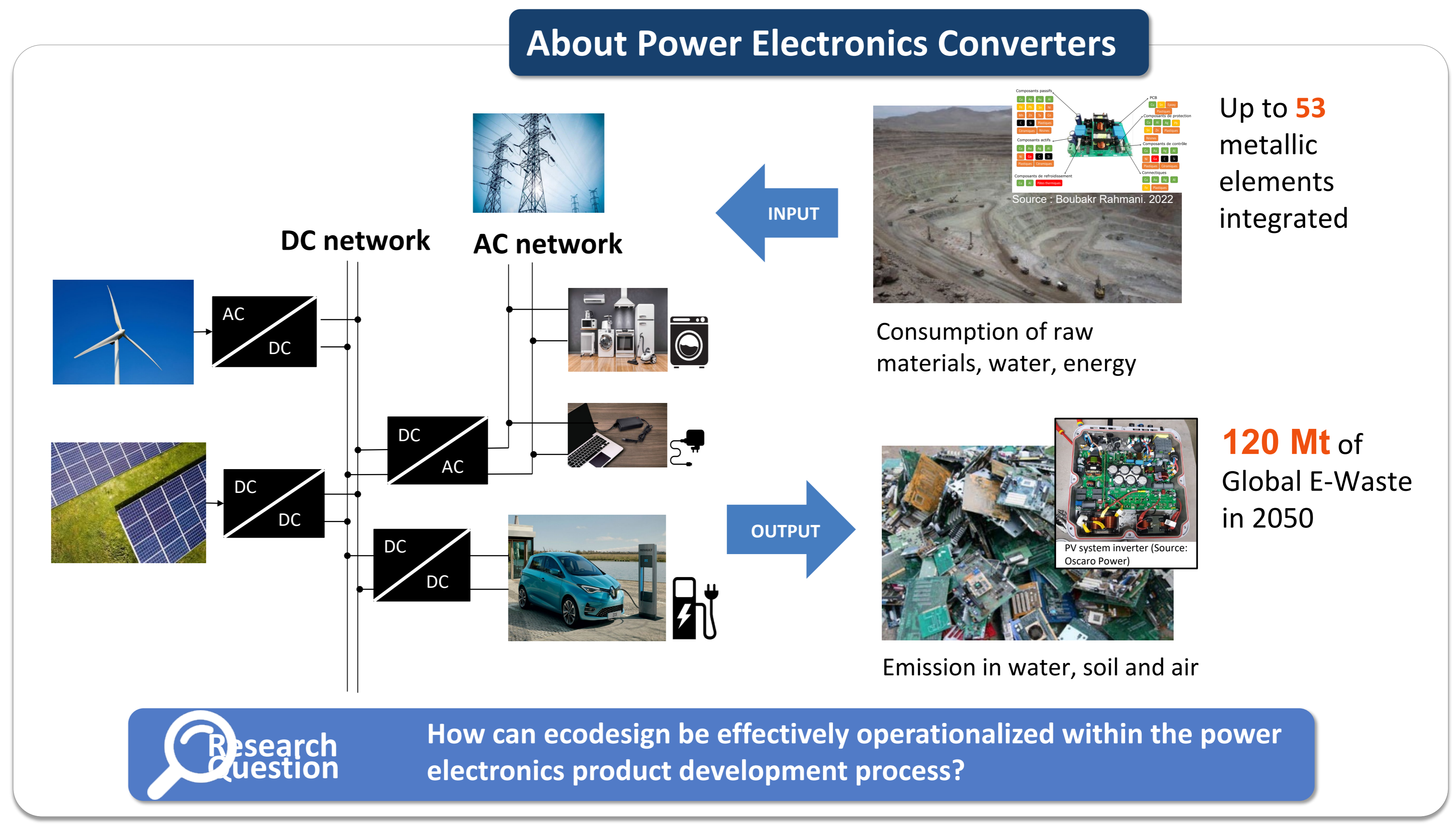
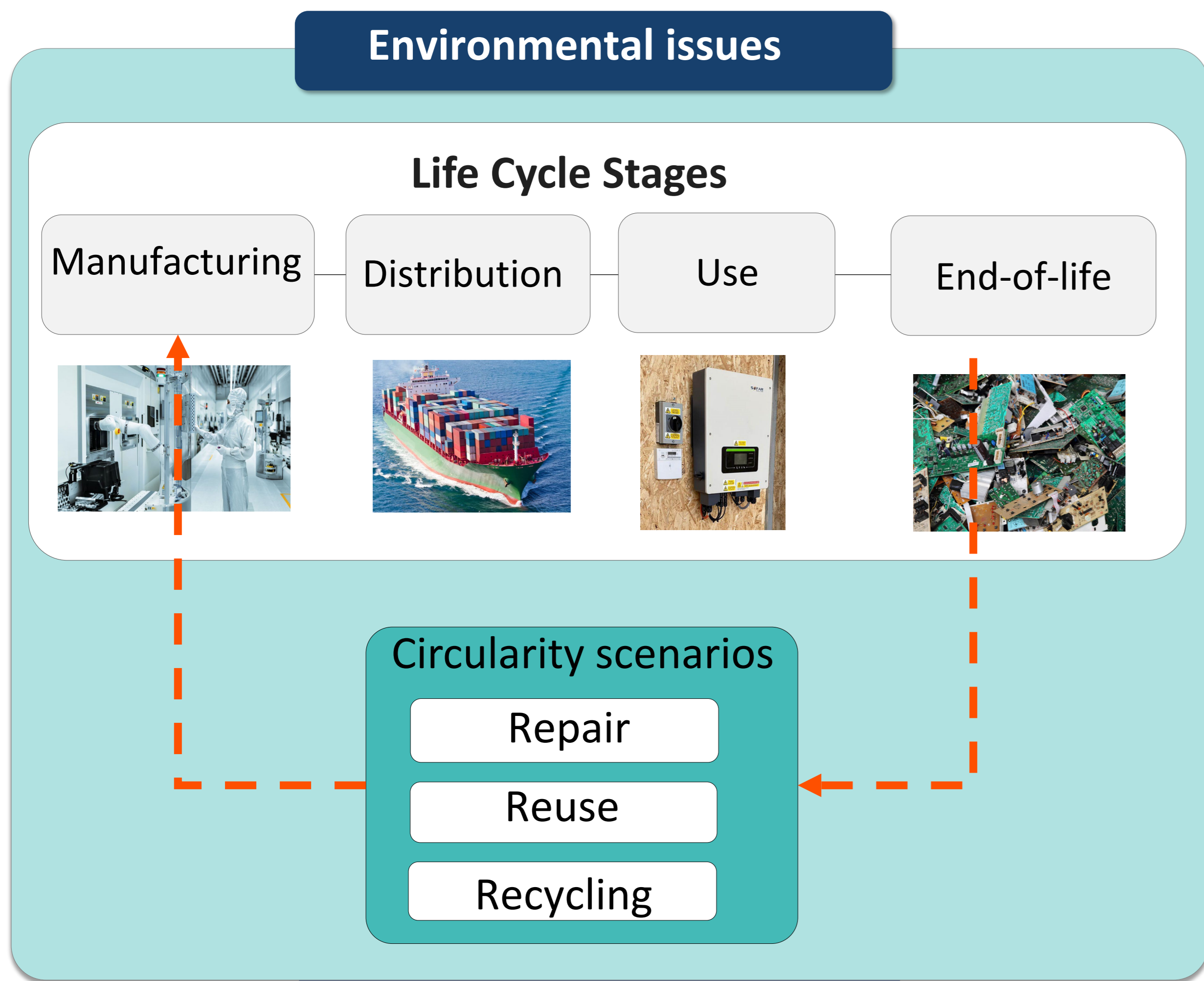
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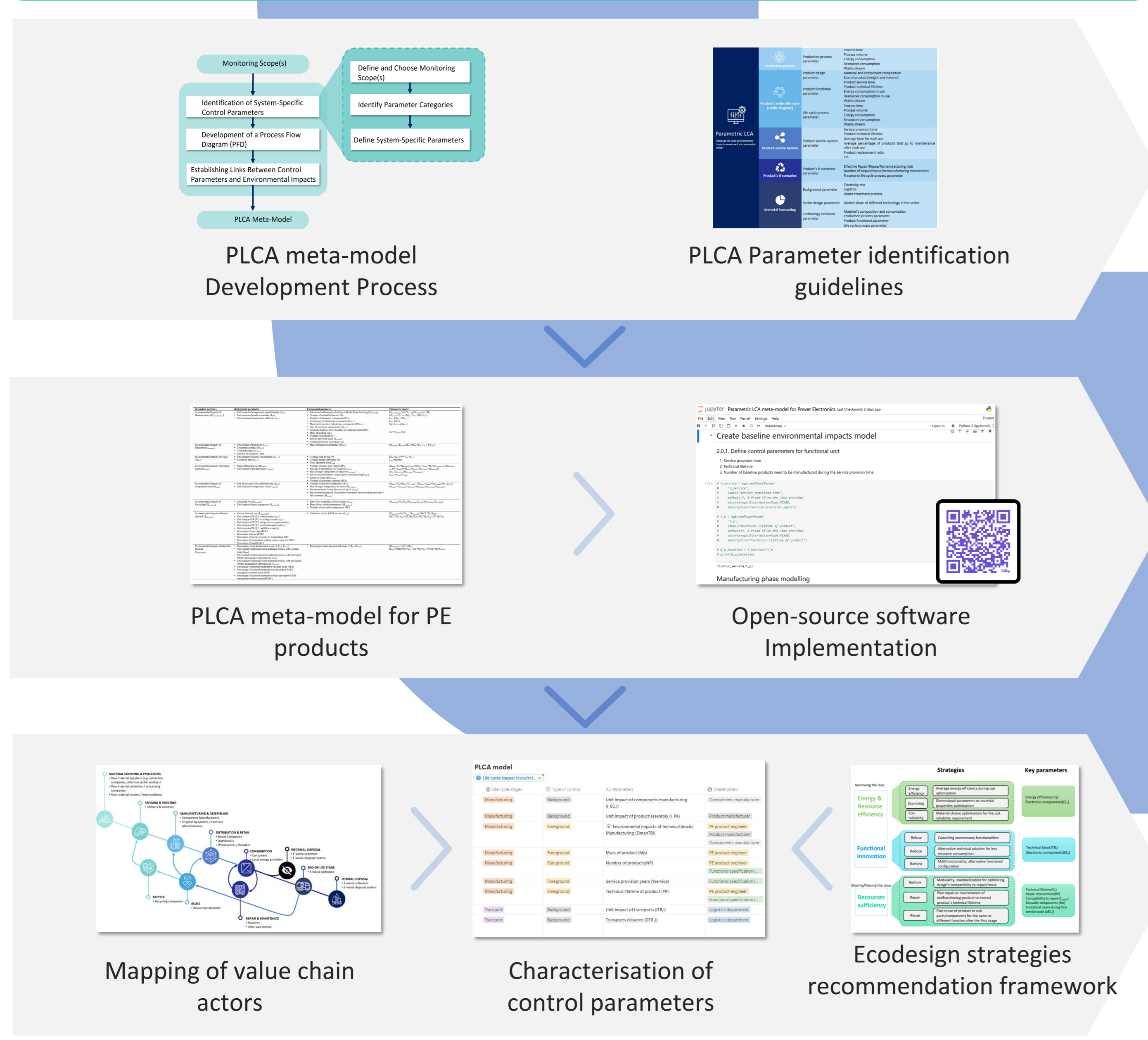
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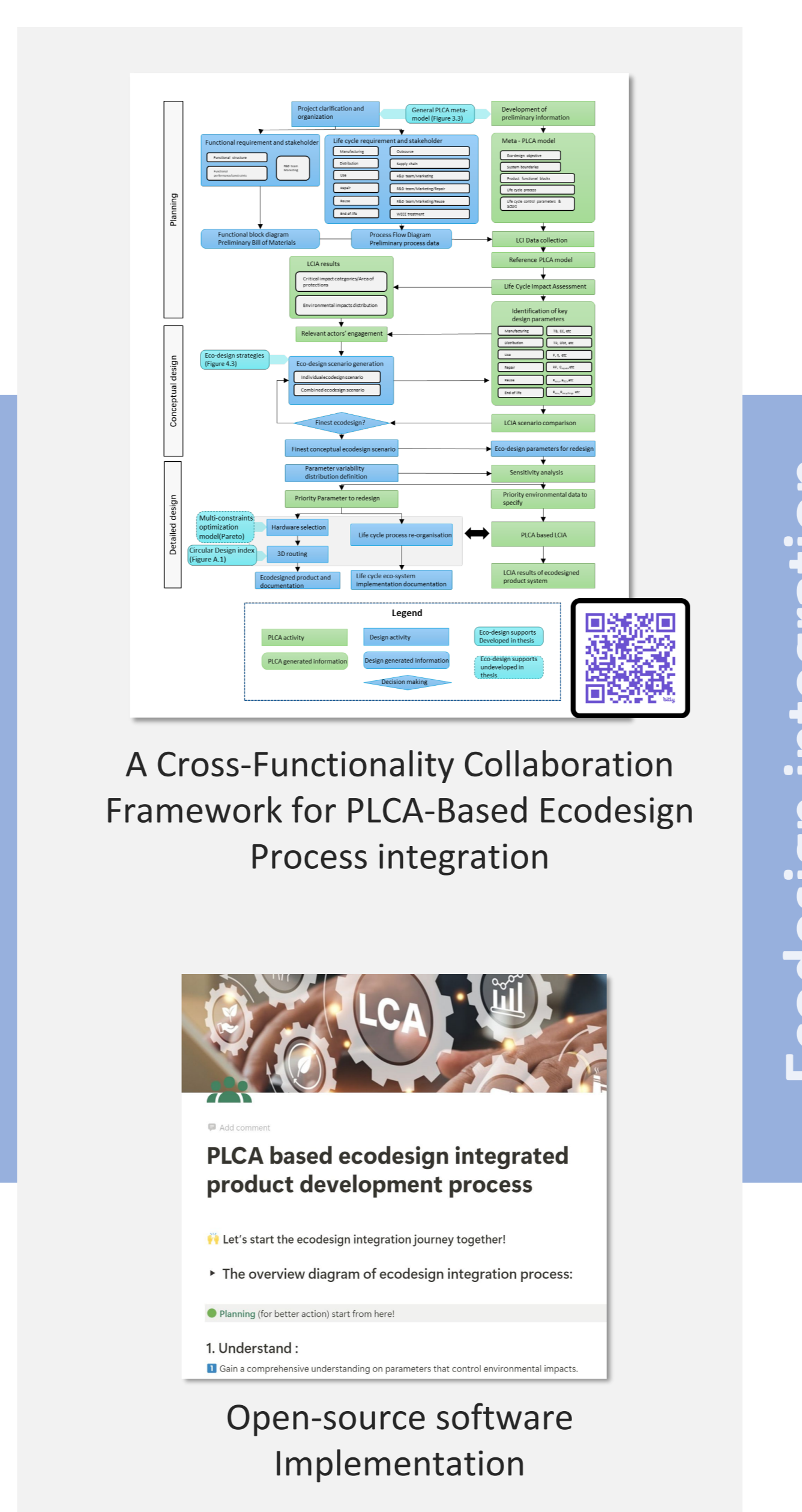
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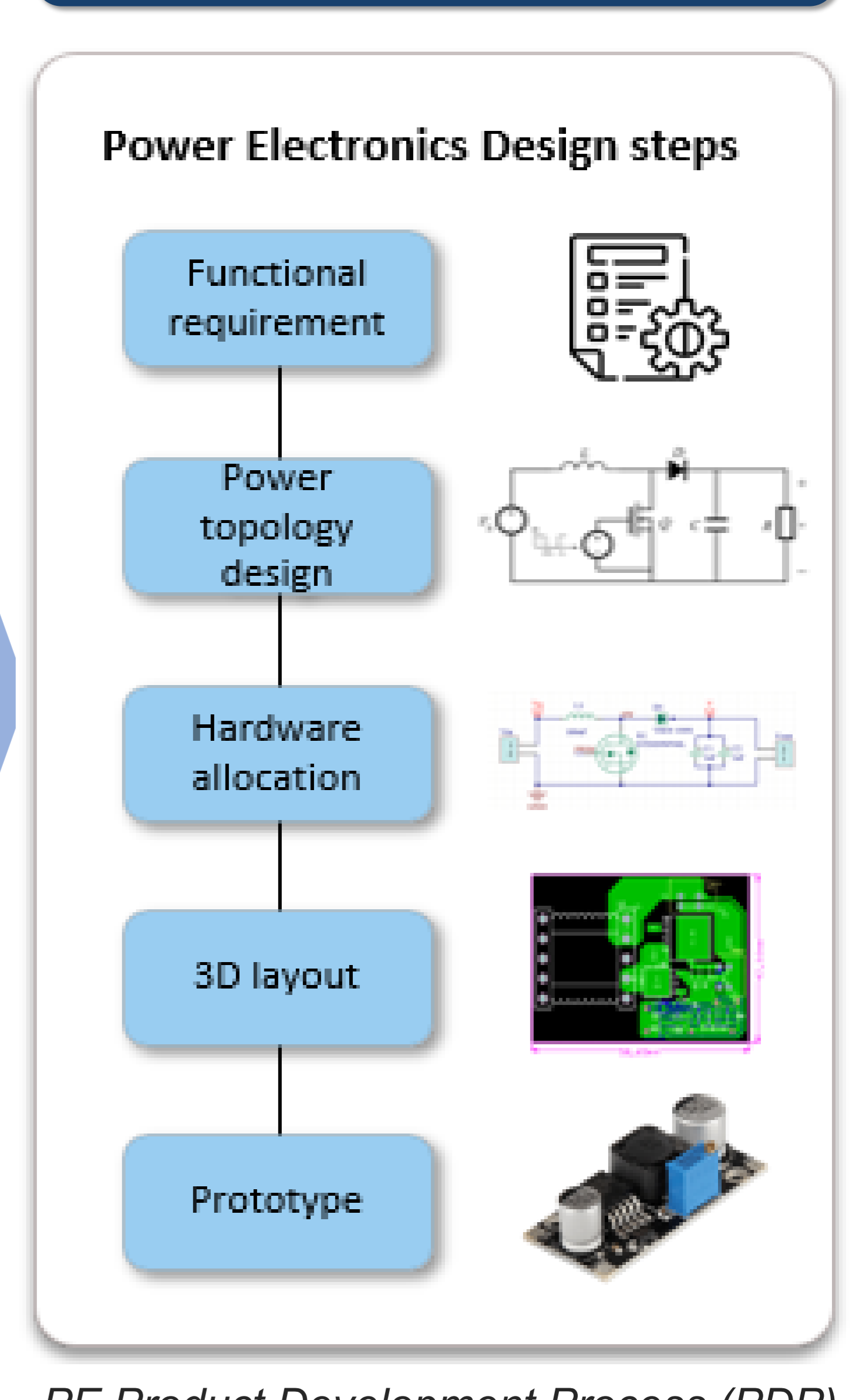
Phase 1 : Ecodesign Supports



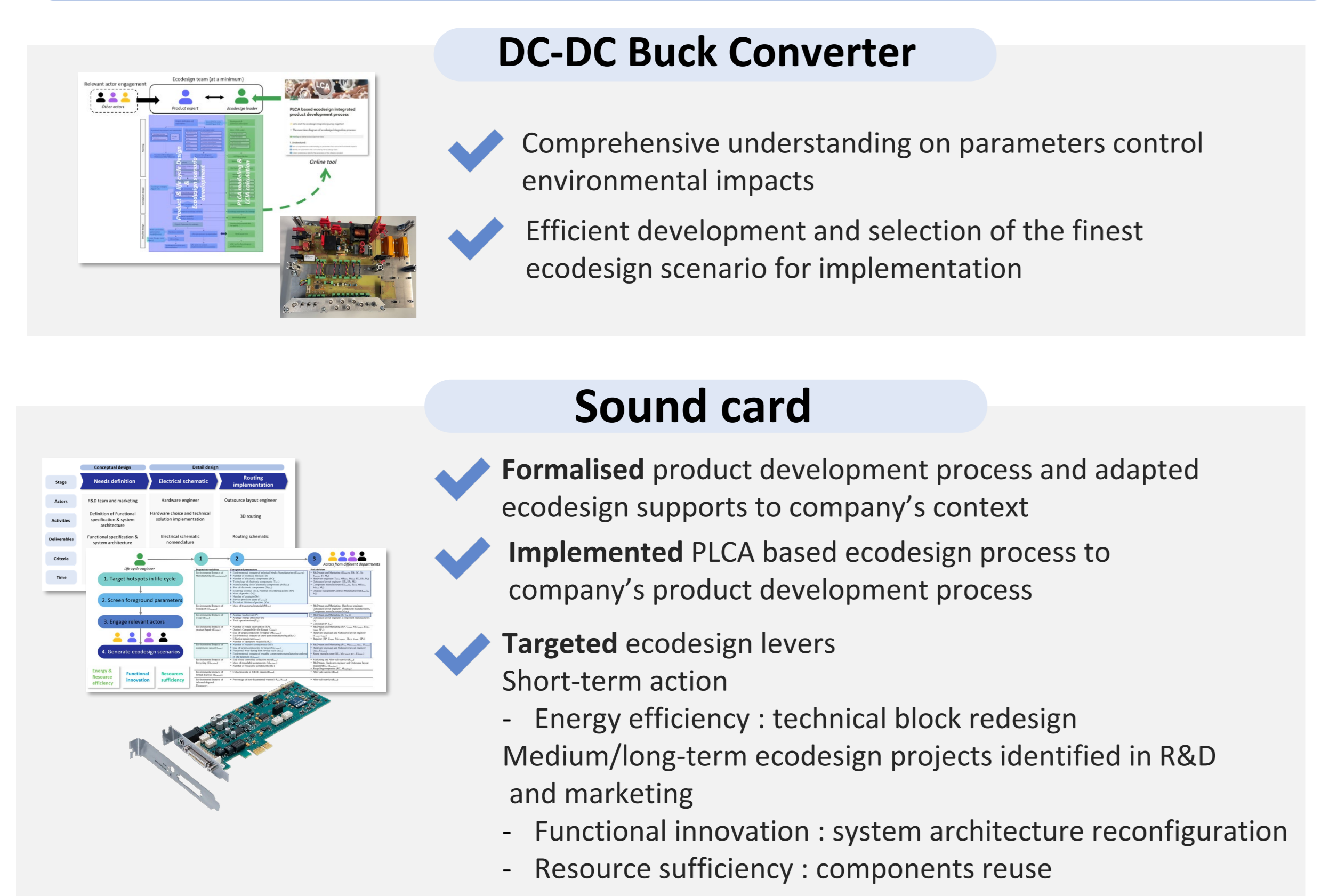
Phase 2 : Operation Supports



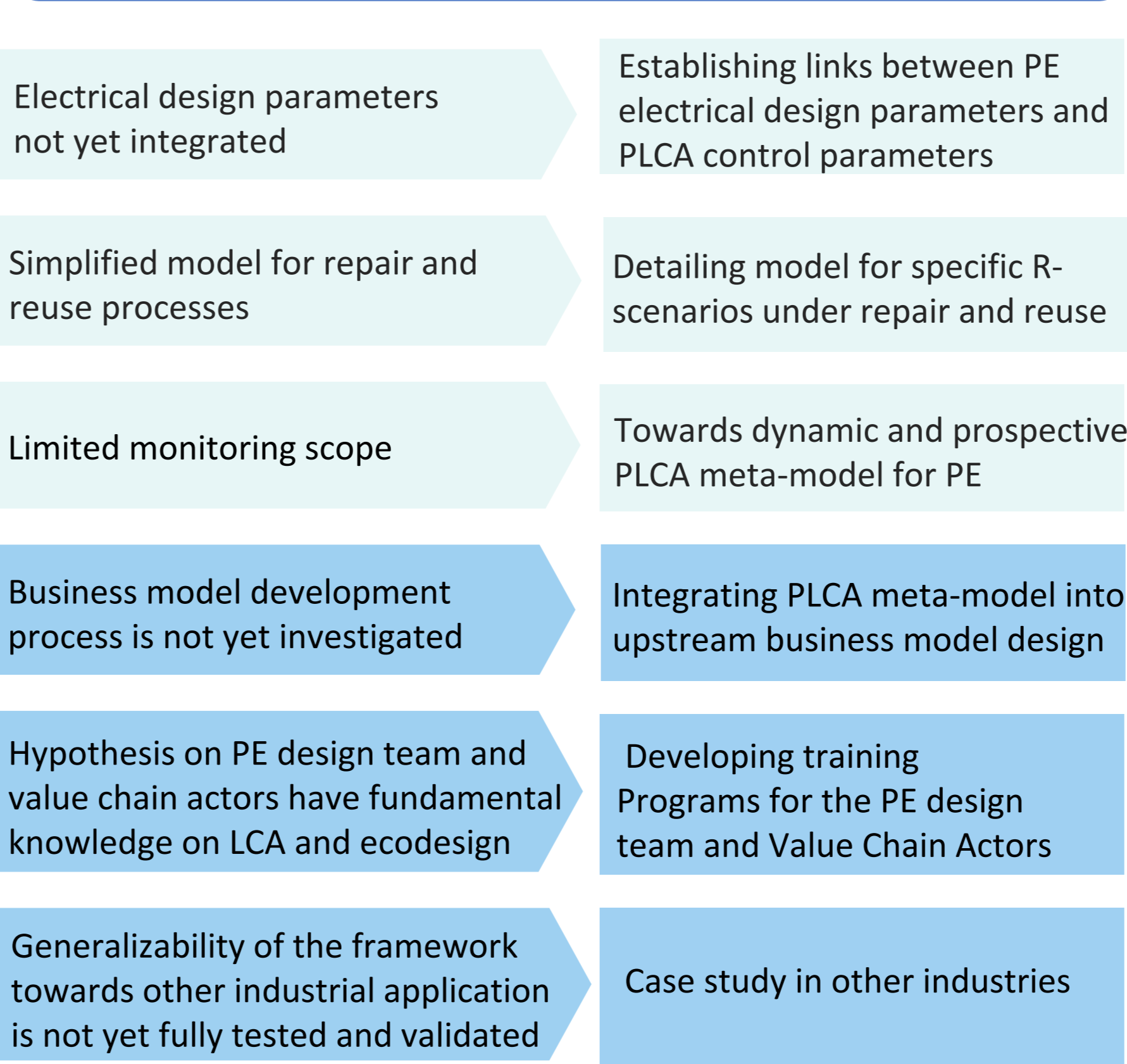
Target PDP for Ecodesign integration



Phase 3 : Case Studies



Limitations and Future works



References

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