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## Formally Verifying Optimizations with Block Simulations

Gourdin Léo, Bonneau Benjamin, Boulmé Sylvain, Monniaux David, Bérard Alexandre

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# FORMALLY VERIFYING OPTIMIZATIONS WITH BLOCK SIMULATIONS



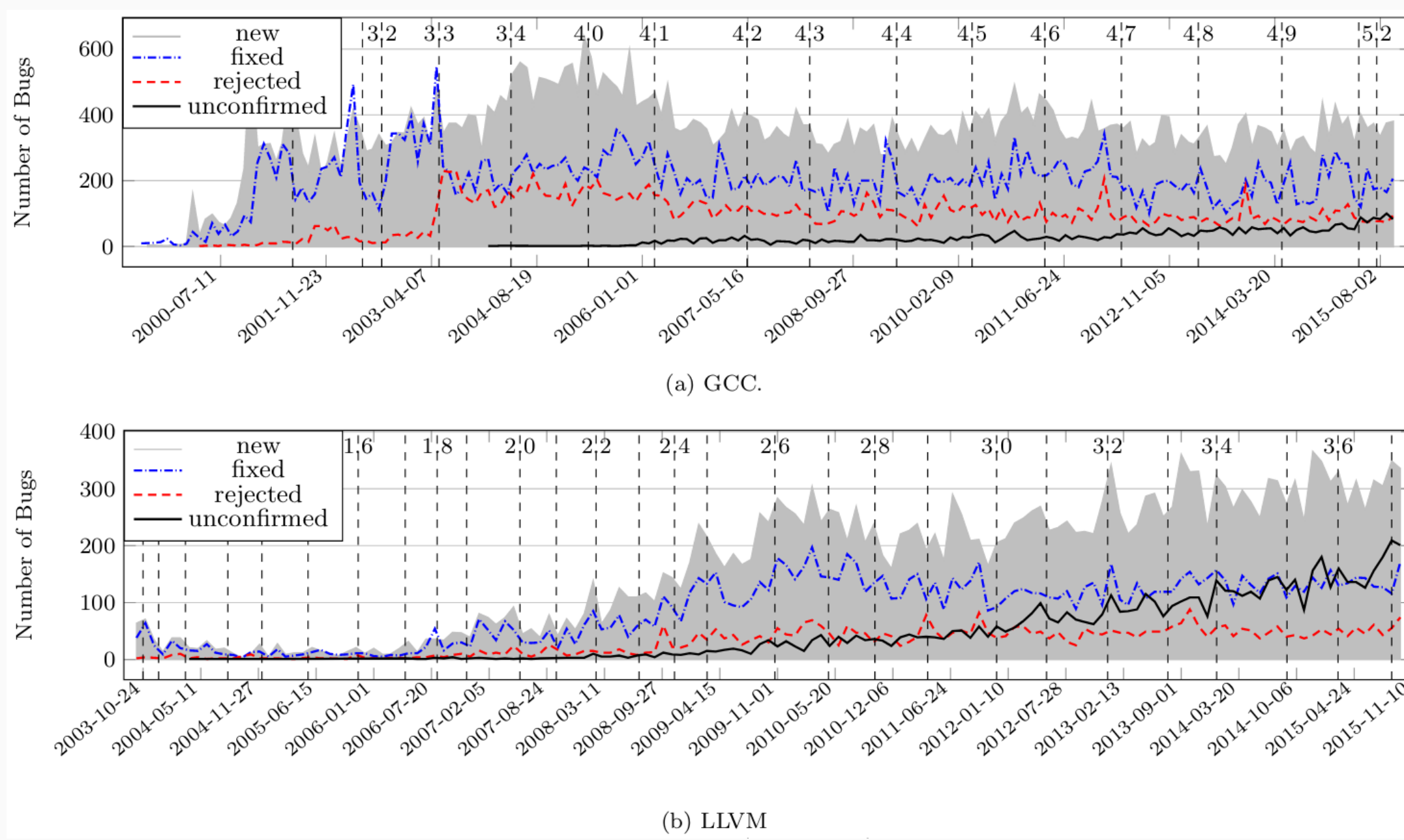
L. Gourdin, B. Bonneau, S. Boulmé, D. Monniaux & A. Bérard\*

Université Grenoble Alpes, CNRS, Grenoble INP, Verimag

<https://gricad-gitlab.univ-grenoble-alpes.fr/certicompil/Chamois-CompCert>



## Avoiding Bugs in GCC & LLVM (cf. [1])?



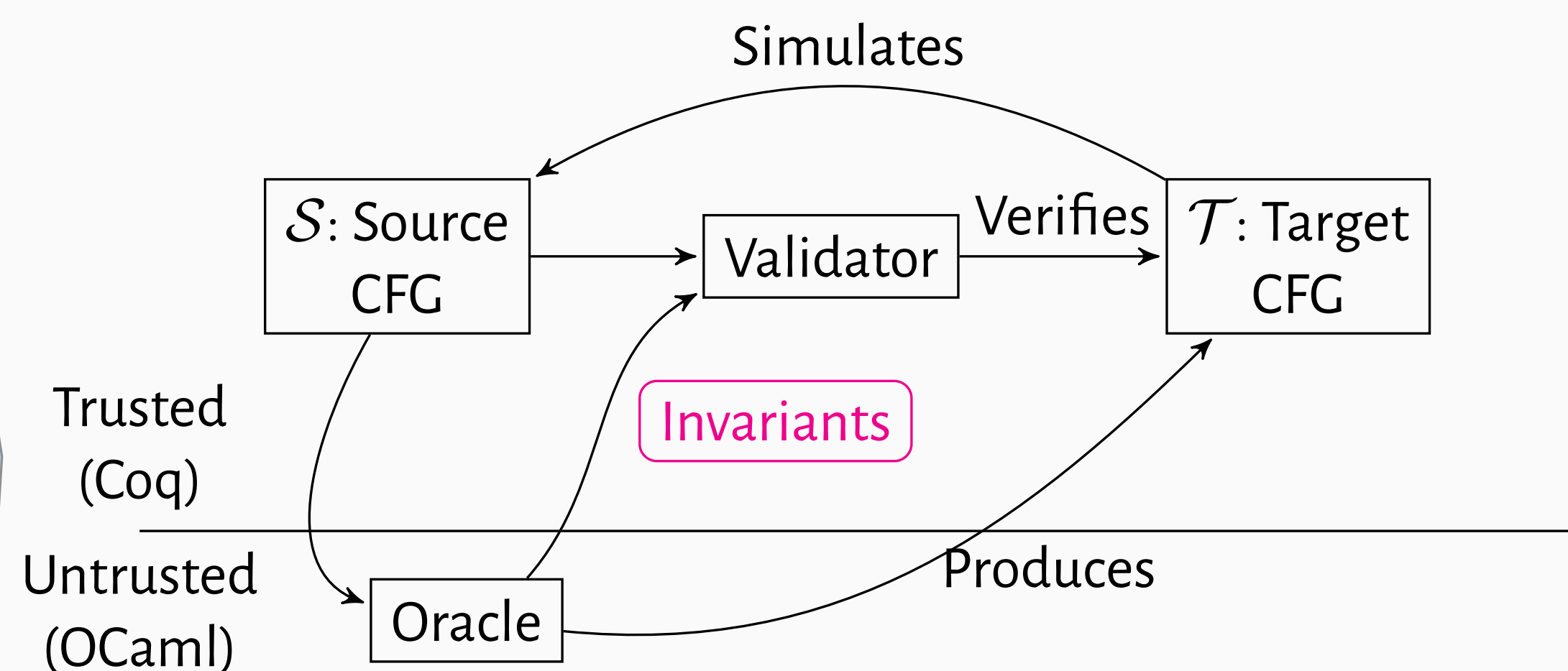
**CompCert<sup>1</sup> solution** (ACM Software System Award 2021): the 1<sup>st</sup> formally verified (= machine-checked mathematical proof of correctness) compiler optimizing **safety-critical** software [2, 3].

<sup>1</sup><https://www.absint.com/compcert/>

## Block Transfer Language and its formally verified Translation Validator

The oracle takes source CFG  $S$  and yields its optimized version  $T$  along with an **invariant at the entry of each (loop-free) block**. Our verified symbolic simulation validator either ensures semantic preservation or aborts compilation.

**Invariants** express relations between source and target states.



## Purposes of BTL TV

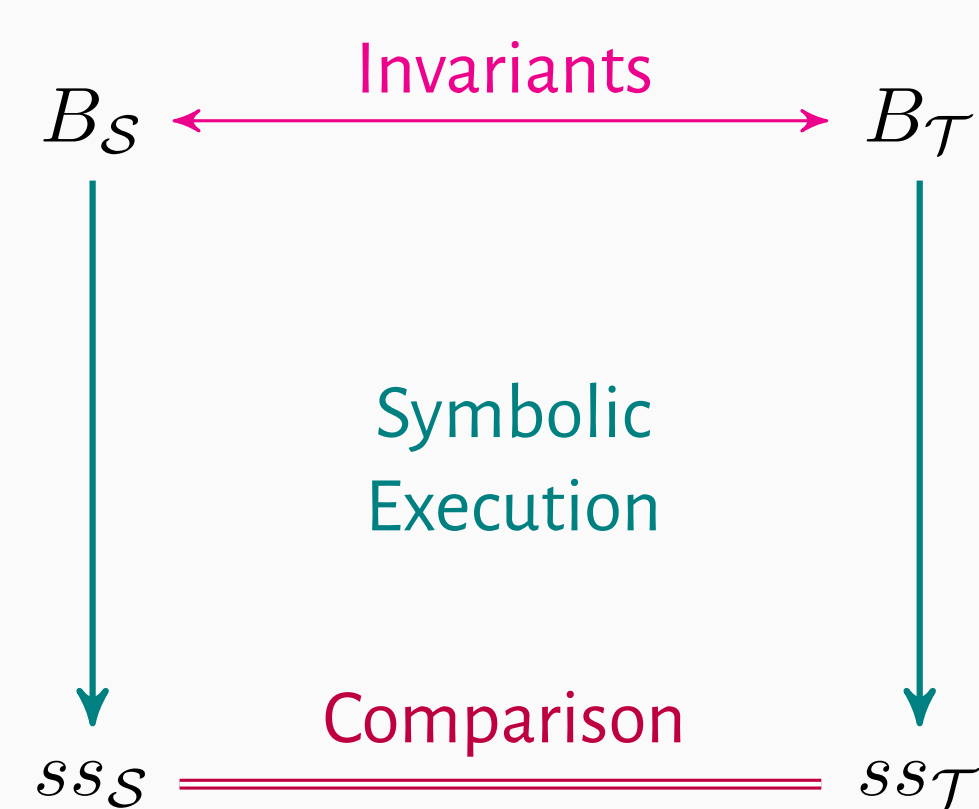
Block-by-block comparison of a source CFG and an oracle-provided target CFG modulo **oracle-provided invariants** and **oracle-dependent rewriting rules**

### Features

- Formally verified integration in Chamois-CompCert middle-end
- Parametrized by the architecture
- Predictable (against false alarms)
- Fast and Versatile
- 1<sup>st</sup> verified strength-reduction** of induction variables
- Validates various loop optimizations & *superblock scheduling* with code compensation and register renaming.

## Block-by-block Symbolic Simulation

For each pair of blocks ( $B_S, B_T$ ), it compares the symbolic states ( $ss_S, ss_T$ ) resulting from their symbolic execution, themselves initialized from **entry invariants**: it tests equalities of **exit invariants** after **normalized rewriting**.



## Validating a Lazy Code Transformations Oracle

**Combining and improving Lazy Code Motion [4] & Lazy Strength Reduction [5].**

- Search for loop-invariant or “reducible” candidates;
- Supports decomposed patterns like a *left shift* + an *addition*;
- Based on data-flow analyses performed by an OCaml oracle;

**Optimizing in two steps:**

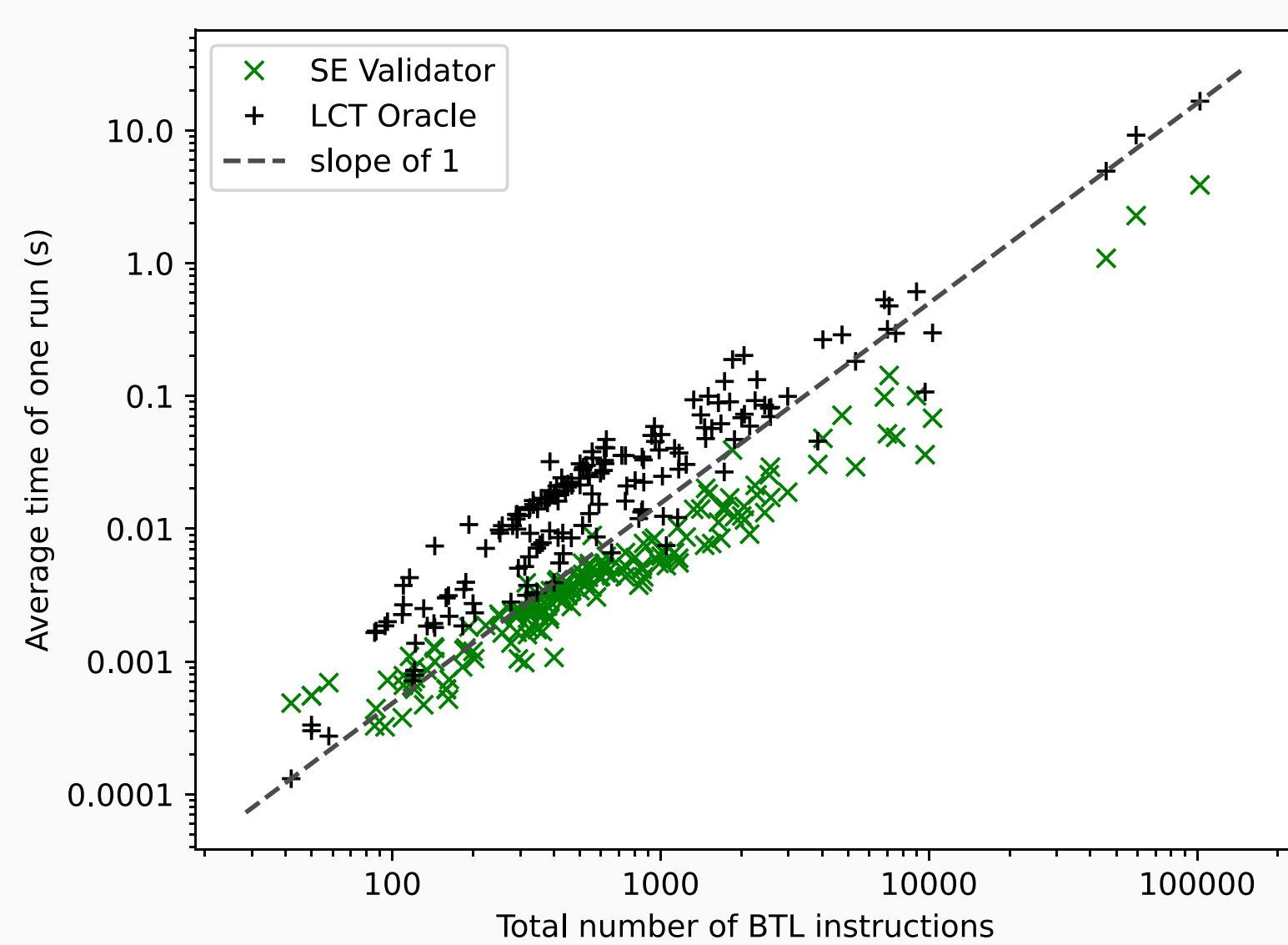
- Lifting* candidates out of the loop;
- Inserting *compensation code* in the loop body if necessary.

**Generating invariants** from lifted computations and variables that remain live.

```
void init7(long *t,
           unsigned long i,
           unsigned long l)
{ for (; i<l; i++) t[i]=7; }
```

C source code

## Compile Times That Scale (thanks to formally verified hash-consing)



```
.L100: //NB: x0=0; x10=t; x11=i; x12=1
bgeu x11,x12,.L101//exit if x11>=x12
slli x7,x11,3 // x7 = x11*2^3
add x13,x10,x7 // x13 = x10+x7
addi x5,x0,7 // x5 = 7
sd x5,0(x13) // *x13 = x5
addi x11,x11,1 // x11 += 1
j .L100
```

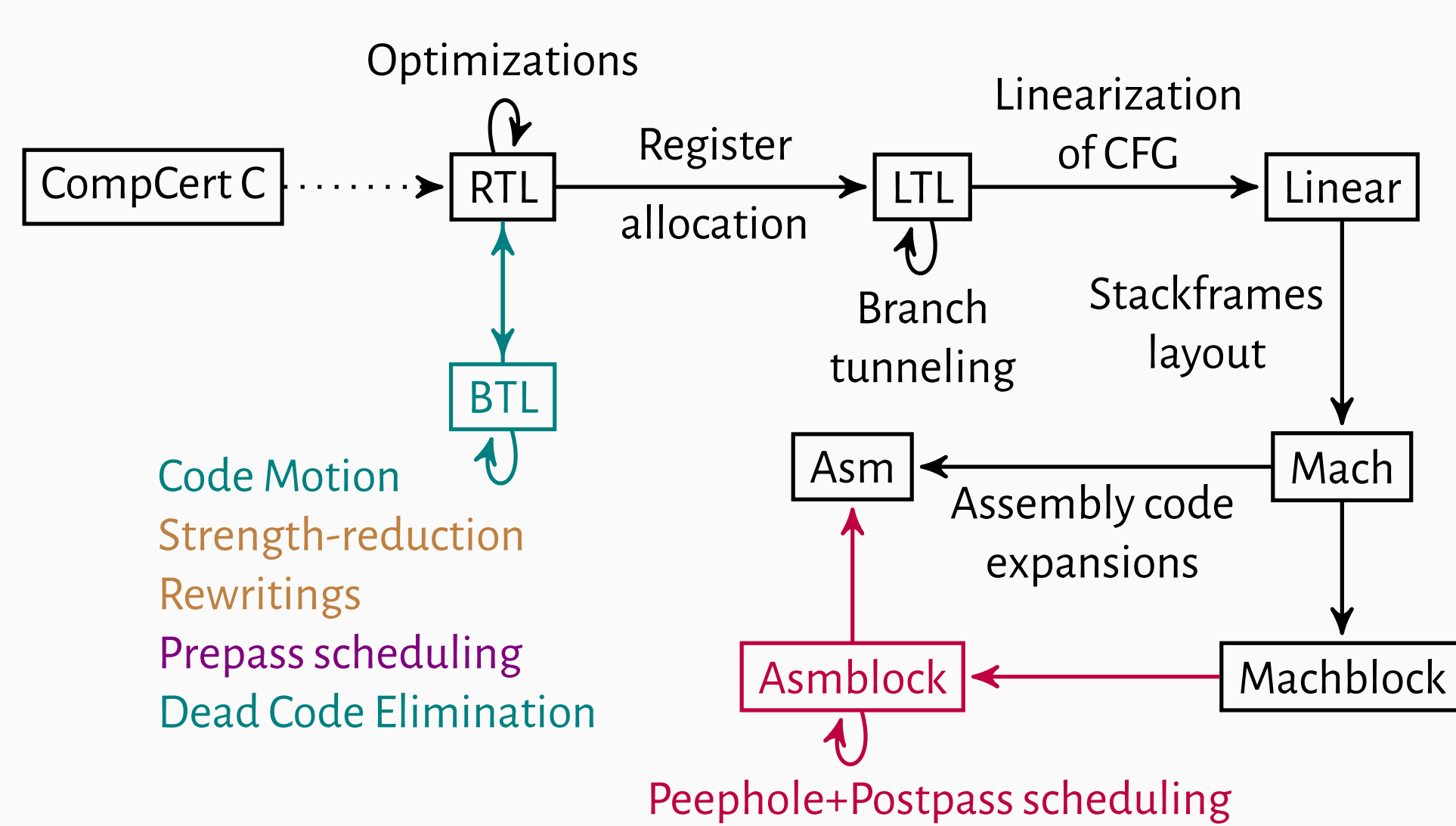
RISC-V ASM Before

→

```
slli x7,x11,3 // x7 = x11*2^3
add x13,x10,x7 // x13 = x10+x7
addi x5,x0,7 // x5 = 7
.L100:
//invariant x5_T = 7 ^ x13_T = x10_S + x11_S * 8
// ^ x11_T = x11_S ^ x12_T = x12_S
bgeu x11,x12,.L101//exit if ...
sd x5,0(x13) // *x13 = x5
addi x13,x13,8 // x13 += 8
addi x11,x11,1 // x11 += 1
j .L100
```

RISC-V ASM After

## Chamois-CompCert Optimizations

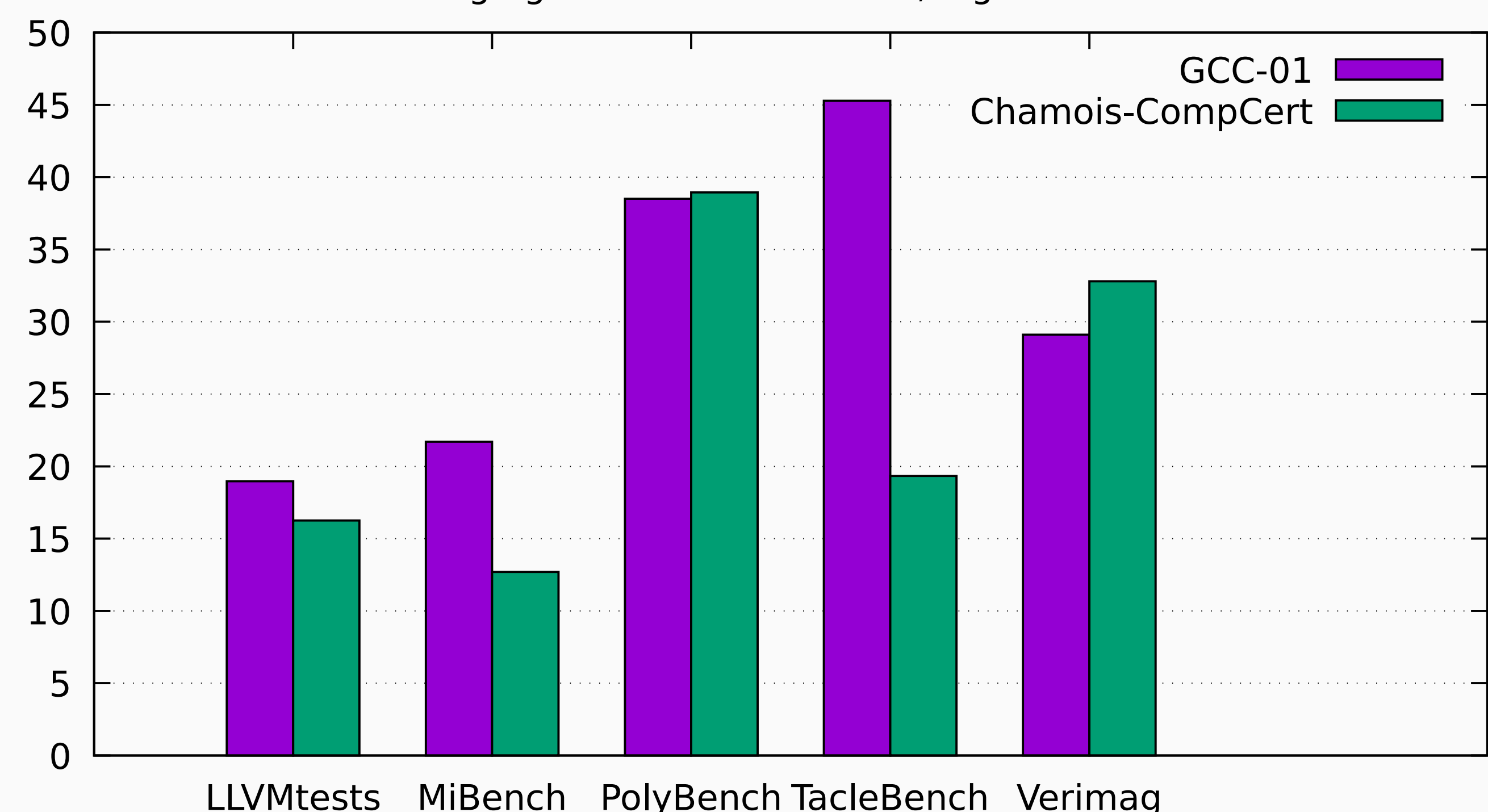


Legend:

- Brown: RISC-V only
- Violet: AArch64+ARMv7+RISC-V+K VX
- Red: AArch64+K VX
- Teal: All (AArch64+ARMv7+RISC-V+K VX+PPC+x86)

## Optimized Generated Code That You Can Trust

Comparing w.r.t. Official CompCert on RISC-V target over five test suites  
Percentage gain in execution time, higher is better



## References

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## Git Repo

