Direct growth of GaSb on Ge, Ge-on-Si and SiGe-on-Si platforms for integrated mid infra-red photonics

M. Silvestre, ^{1,2} J-B. Rodriguez,¹ S. Calcaterra,³ G. Isella,³ E. Tournié,^{1,4*}
¹IES, University of Montpellier, CNRS, F – 34000 Montpellier, France
²RIBER SA, F – 95870 Bezons, France
³L-NESS, Politecnico di Milano Dipartimento di Fisica, I - 22100 Como Italy
⁴Institut Universitaire de France (IUF), F-75005 Paris, France
*E-mail: eric.tournie@umontpellier.fr

Integrated mid infra-red (MIR) photonics is of major interest for free-space communications, bio-sensing, etc. III-V antimony based lasers are efficient light sources in the MIR range that can be grown on passive Si photonics circuits [1]. However, most Si-based materials absorb a large part of the MIR spectrum above 8 μ m, in contrast to germanium which has a wide MIR transparency window [2]. The direct growth of III-Sb materials on Ge substrates or Ge-on-Si templates would allow developing integrated photonic circuits operating at longer wavelengths (up to 14 μ m). Still, III-V heteroepitaxy on group-IV substrates is hampered by various crystal defects. In particular, anti-phase boundaries (APBs) are 2D defects particularly detrimental to devices because they act as short circuits. In this work, we investigated the growth of GaSb on Ge substrates and on Ge-on-Si templates grown by low-energy plasma-enhanced CVD (LEPECVD) [3].

The starting surface preparation is critical for growing APB-free GaSb films on group-IV substrates [4]. We found that APB-free GaSb layers can be achieved only when the Ge substrates are prepared by UHV annealing in a narrow temperature window. The Ge-on-Si and SiGe/Si templates, however, exhibit cross hatch patterns arising from strain relaxation during template growth, which prevents proper surface organization prior and during III-V growth. This, in turn, precludes APB annihilation. This effect can be alleviated either by using slightly miscut (1° - 2°) Si substrates, or by smoothing the starting surface of on-axis Ge-on-Si templates by chemical-mechanical polishing. Under these conditions, APB-free GaSb layers can be achieved after 500 nm growth. They exhibit a rms roughness of 1.0 nm. Such layers can be used as templates for the direct growth of III-V devices. These results represent a further step in the development of integrated MIR photonics.

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