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Few-Layer Bi₂O₂Se: A Promising Candidate for High-Performance Near-Room-Temperature Thermoelectric Applications

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Abstract

Advancements in high-temperature thermoelectric materials have been substantial, yet identifying promising near-room-temperature candidates for efficient power generation from low-grade waste heat or thermoelectric cooling applications has become critical but proven exceedingly challenging. Bismuth oxyselenide (Bi₂O₂Se) emerges as an ideal candidate for near-room-temperature energy harvesting due to its low thermal conductivity, high carrier mobility and remarkable air-stability. In this study, the thermoelectric properties of few-layer Bi₂O₂Se over a wide temperature range (20 – 380 K) are investigated, where a charge transport mechanism transitioning from polar optical phonon (POP) to piezoelectric scattering at 140 K is observed. Moreover, the Seebeck coefficient (S) increases with temperature up to 280 K then stabilizes at $\sim -200 \mu\text{V/K}$ through 380 K. Bi₂O₂Se demonstrates high mobility ($450 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$) within the optimum power factor (PF) window, despite its $T^{-1.25}$ dependence. The high mobility compensates the minor reduction in carrier density n_{2D} hence contributes to maintain a robust electrical conductivity $\sim 3 \times 10^4 \text{ S/m}$. This results in a remarkable PF of

860 $\mu\text{W m}^{-1}\text{K}^{-2}$ at 280 K without the necessity for gating ($V_g = 0$ V), reflecting the innate performance of the as-grown material. These results underscore the considerable promise of $\text{Bi}_2\text{O}_2\text{Se}$ for room temperature thermoelectric applications.

1. Introduction

Thermoelectric (TE) materials are capable of directly transforming the heat energy absorbed into electrical energy, alleviating the damages on environment due to the rapid consumption of fossil fuels to satisfy the high energy demand. Moreover, TE generators have garnered significant attention due to its unparalleled advantages, including generation of electrical energy in the absence of physical moving parts, eco-friendliness (silent operation) and long service life (low maintenance cost)[1]. Recent advancements in room-temperature thermoelectric materials have showcased their potential for diverse applications including power generation, sensing, and thermal management. Innovations include integrated systems that combine solar absorbers, radiative coolers, and thermoelectric generators to convert thermal energy from various sources into electricity, offering a sustainable alternative to fossil fuels[2]. Additionally, microscale thermoelectric devices (micro-TEDs) have been developed with enhanced microfabrication techniques, improving performance and durability while enabling sensitive environmental monitoring[3]. These developments highlight the versatility of room-temperature thermoelectric materials in seamlessly integrating into everyday devices and industrial systems, significantly enhancing energy efficiency across various applications.

The power generation efficiency can be evaluated through a dimensionless figure of merit ZT , defined as $ZT = S^2 \sigma T / \kappa$; S is the Seebeck coefficient, defined as $S = -\Delta V / \Delta T$ where ΔV is the thermoelectric voltage generated and ΔT is the temperature difference at the two extremities of the TE material, σ is the electrical conductivity, T is the absolute temperature and κ is the sum of electronic thermal conductivity (κ_e) and lattice thermal conductivity (κ_L)[4-7]. In addition to ZT , the power factor ($PF = S^2 \sigma$) serves as a crucial metric that underscores a material's potential for efficient energy conversion. The power factor directly impacts the power output of thermoelectric devices, making it an essential consideration in applications where maximizing immediate energy conversion from limited heat sources is a priority. This characteristic is especially critical in designing materials that are not only efficient but also capable of generating substantial power under practical operating conditions. It is obvious that by increasing S and σ while minimizing κ should ideally increase ZT , however, these parameters are interdependent which makes it challenging to obtain a high ZT [8, 9]. For example, increasing the carrier concentration (n) will lead to an increase in σ and κ_e but results in a decrease of S . Low dimensional material have attracted attention and the quantum confinement taking place at such scale can be beneficial to enhance the efficiency of thermoelectric materials[10]. Over the past few decades, numerous strategies have been employed to optimize the ZT value by decoupling its numerator and denominator. These approaches encompasses band convergence, anharmonicity, modulation doping[11] to increase the σ as well as the introduction of lattice defects[12-14] to reduce

κ . Moreover, the prevalence of interfaces and disparities in mass among the constituent atoms in certain 2D materials can result in comparatively reduced κ [15, 16]. Lee *et al.*[17] observed a negative correlation between κ and σ in tin disulfide (SnS_2) while scaling the material from bulk to 2D nanosheets substantially increasing the thermoelectric performance. In that regard, two-dimensional (2D) semiconducting materials with distinctive density of states can achieve significant Seebeck coefficients. This characteristic is crucial as it directly amplifies their PF , which correlates with the maximum power output a material can achieve. It is important to emphasize that achieving a high PF is essential because the ZT value, while indicating overall thermoelectric performance, does not reflect the maximum power output that is achievable[18].

The layered bismuth oxychalcogenides ($\text{Bi}_2\text{O}_2\text{X}$) materials such as ($\text{Bi}_2\text{O}_2\text{S}$, $\text{Bi}_2\text{O}_2\text{Se}$ and $\text{Bi}_2\text{O}_2\text{Te}$) stood out due to its unparalleled properties of remarkable air stability, tunable band structures, high carrier mobilities and low thermal conductivity which are desired for high thermoelectric performance[19]. Among the three $\text{Bi}_2\text{O}_2\text{X}$, $\text{Bi}_2\text{O}_2\text{Se}$ have been proven to be the easiest to grow due to its stable chemical potential. Particularly, 2D $\text{Bi}_2\text{O}_2\text{Se}$ exhibits high mobility and suitable bandgap of about 0.8 eV[20] which have been predicted to be a promising thermoelectric material[21]. For example, Wu *et al.* noted an ultrahigh Hall mobility exceeding $20,000 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at low temperatures[22], while Guo *et al.* [23] calculated and measured the anisotropy of thermal conductivity between through-plane and in-plane as a function of the thickness owing to the electrostatic interactions between layers in ferroelectric $\text{Bi}_2\text{O}_2\text{Se}$. Their findings revealed that the κ did not exceed $1.5 \text{ W m}^{-1}\text{K}^{-1}$ for thickness up to 20 nm, that is as low as silicon oxide (SiO_2). Moreover, Yang *et al.* reported a high-power factor as high as $400 \mu\text{W m}^{-1}\text{K}^{-2}$ by manipulating the scattering mechanisms over a temperature range of 80-200 K[24], however the performance around room temperature is still lacking. These state-of-the-art references will be our benchmark for this work.

In this study, we characterize the electrical transport and PF of $\text{Bi}_2\text{O}_2\text{Se}$ in its 2D form ($\sim 14 \text{ nm}$) over a wide temperature range (20 – 380 K). We measured a large variation of the electric mobility over this temperature range and observe a clear transition from POP to piezoelectric scattering regimes at 140 K. Despite the power dependence $T^{-1.25}$ of the mobility and that the σ ($\sim 10^4 \text{ S/m}$) decreases with temperature from decreasing carrier concentration, the mobility remains substantially high at $\sim 450 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at 280 K with a Seebeck coefficient that plateaus at approximately $-200 \mu\text{V/K}$ thereafter. As a result, we achieve a peak PF of $860 \mu\text{W m}^{-1}\text{K}^{-2}$ near room temperature operation without the necessity of gating ($V_g = 0\text{V}$). These orders of magnitude are several folds larger than recent reports[24]

and highlights the potential of as deposited Bi₂O₂Se as a candidate for a large range of TE application such as remote sensing, imaging or thermoelectric generators, especially at room temperature.

2. Materials and methods

2.1. LPCVD synthesis of Bi₂O₂Se

The setup for the low-pressure chemical vapor deposition (LPCVD) synthesis of Bi₂O₂Se on fluorine mica substrate (f-mica, KMg₃(AlSi₃O₁₀)F₂) involves a split tube furnace equipped with an interconnected control system (1-Zone, up to 1200 °C). Fused quartz tubes measuring 1200 mm in length and 50 mm in outside diameter (O.D.) were employed. Precursors, namely powders of Bi₂Se₃ and Bi₂O₃, were positioned in separate alumina crucibles. Bi₂O₃ was placed in the central zone, while Bi₂Se₃ was positioned upstream at 8 cm. The f-mica substrate was situated downstream with a separation of 12 cm. Initially, the chamber was evacuated to 6.0×10^{-2} Torr, following which 170 sccm (standard cubic centimeter per minute) of Argon (Ar) gas was introduced to achieve a pressure within the range of 350–400 Torr. The chamber was then gradually heated to 620 °C over a span of 25 minutes and maintained at this temperature for 40 minutes. Subsequently, the furnace was allowed to cool naturally to room temperature, and the chamber was replenished with Ar gas to return to atmospheric pressure.

2.2. Characterization of Bi₂O₂Se

Atomic force microscopy (AFM) was performed using the Asylum Research Cypher Scanning Probe Microscope system in tapping mode. Raman spectra were acquired using the Witec Raman system equipped with a 100x objective lens, a grating of 1200 lines mm⁻¹ and a Rayleigh peak filter, employing 532 nm laser excitation. The transmission electron microscopy (TEM) samples were prepared using a polymethyl methacrylate (PMMA)-based transfer method. Initially, the f-mica substrates with the as-grown CVD samples were spin-coated with PMMA at 3000 r.p.m. for 60 seconds, followed by baking at 150 °C for 5 minutes. Subsequently, the PMMA-coated sample was carefully peeled off by etching away the underlying f-mica using a 2% hydrofluoric (HF) solution. Next, the floating PMMA film was delicately transferred to DI water and lifted onto a TEM grid. The TEM grid was then dried at 50 °C for 30 minutes and immersed in acetone for approximately 24 hours to remove the PMMA. Finally, the TEM grid was rinsed with isopropyl alcohol (IPA) and air-dried for further analysis.

2.3. Transfer method

The transfer of as-grown Bi₂O₂Se onto a Si substrate utilized a polystyrene (PS)-assisted approach. Initially, PS was spin-coated onto the f-mica substrate containing the Bi₂O₂Se at 3500 r.p.m. for 60 seconds and then baked at 85 °C for 15 minutes. Following this, the edge of the f-mica was trimmed, and the PS-coated sample was gently separated using a drop of DI water. Subsequently, the detached PS film was meticulously placed onto the Si substrate. The Si substrate was dried at 80 °C for 1 hour and then subjected to a 150 °C heating for 30 minutes to enhance the contact between the sample and the Si substrate. Next, the Si substrate was immersed in dimethylformamide (DMF) solution for 24 hours to remove the PS, followed by a 5-hour immersion in acetone to eliminate any remaining organic residues. Finally, the Si substrate was dried using a stream of floating nitrogen.

2.3. Device fabrication and measurement method

Upon transferring Bi₂O₂Se onto the Si substrate, a layer of PMMA was spin-coated onto the sample. Following this, electrode patterns were defined using electron-beam lithography (EBL) carried out on the ZEISS EVO 10. After the pattern development in methyl isobutyl ketone (MIBK)/IPA, metal contacts (10 nm Ti/80 nm Au) were deposited using the AJA electron beam evaporator. To characterize the electrical properties of the FETs, the Agilent B1500A Semiconductor Device Parameter Analyzer within the MicroXact Probe Station.

To measure the Seebeck coefficient, we established a temperature gradient by passing a DC current bias through the micro-heater using a source-meter (Keithley 6221). This setup induced Joule heating, creating a temperature gradient along the sample. The temperature gradient, in turn, generated a thermoelectric voltage, which we measured using a voltmeter (Keithley 6430). As we applied a series of DC current biases with increasing values to the heater, we recorded the corresponding voltages. The temperature gradient was determined by measuring the 4-probe resistance of the thermometers.

3. Results and discussions

3.1. Structural analysis

Bi₂O₂Se crystallizes with a tetragonal phase (I4/mmm, $a = 3.891 \text{ \AA}$, $c = 12.21 \text{ \AA}$ and $Z = 2$)[25]. In the crystal cell as shown in Figure 1a, each oxygen atom is covalently bonded with four bismuth atoms, forming a Bi₄O tetrahedron. The tetrahedrons that share edges are arranged together which configures a positively charged $[\text{Bi}_2\text{O}_2]_n^{2n+}$ layer. Meanwhile, the negatively charged $[\text{Se}]_n^{2n-}$ layer along with the upper and lower $[\text{Bi}_2\text{O}_2]_n^{2n+}$ layer are held together by weak electrostatic force, forming an

interlayer spacing of roughly 0.61 nm. High-quality $\text{Bi}_2\text{O}_2\text{Se}$ on 300 nm SiO_2/Si substrate are prepared with LPCVD method (Figure 1b) followed by a non-corrosive transfer method[25]. Generally, Bi_2O_3 and Bi_2Se_3 powders used as precursors are placed apart in different crucibles upstream of the quartz tube and positioned in the high-temperature zone. Argon gas is used to aid vapor transport to the growth substrate (f-mica) which is positioned downstream of the quartz tube. The quartz tube is held at low-pressure throughout the growth of $\text{Bi}_2\text{O}_2\text{Se}$. The synthesized samples are usually squares in shape with lateral size over $50\ \mu\text{m}$ (Figure 1c). Such morphology indicates the tetragonal crystal group of $\text{Bi}_2\text{O}_2\text{Se}$. Raman spectroscopy is an effective method to confirm the phase of the $\text{Bi}_2\text{O}_2\text{Se}$ flakes. In Figure 1d, an obvious peak observed at 159.1cm^{-1} in the Raman spectrum matches well with the A_{1g} peak of $\text{Bi}_2\text{O}_2\text{Se}$ in past reports[26, 27]. The topography and the thickness of the as-grown sample were determined through atomic force microscopy (AFM, Figure 1e) where the surface is clean and flat with a thickness of 14.9 nm. The crystal structure of the as-grown $\text{Bi}_2\text{O}_2\text{Se}$ samples was further confirmed using scanning transmission electron microscopy (STEM). The annular dark-field (ADF) imaging conducted on an aberration-corrected STEM image (Figure 1f) reveals a d -spacing of 0.28 nm for the (110) plane of few-layer $\text{Bi}_2\text{O}_2\text{Se}$, signifying the pristine quality of the samples. Furthermore, Figure 1g shows the fast Fourier transform (FFT) of Figure 1f which showcases $(1\bar{1}0)$, (110), and (200) spots arranged in a square pattern, confirming the single-crystalline nature of the $\text{Bi}_2\text{O}_2\text{Se}$ flakes.

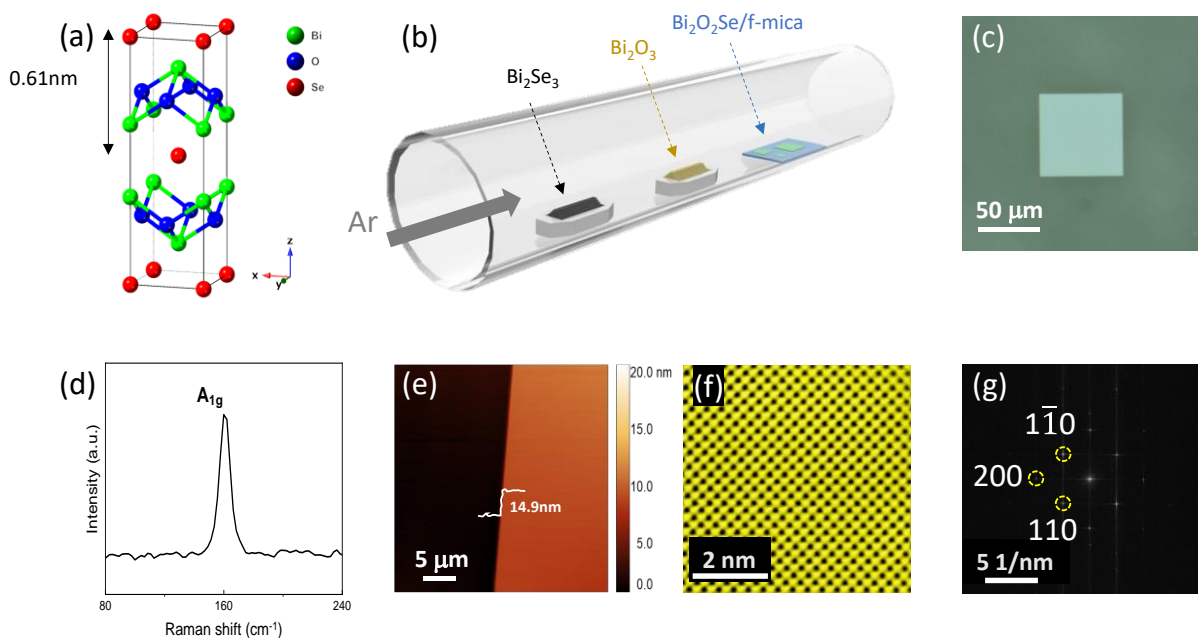


Figure 1. LPCVD synthesis and characterization of $\text{Bi}_2\text{O}_2\text{Se}$. a) Layered crystal structure of $\text{Bi}_2\text{O}_2\text{Se}$. b) Schematic of the LPCVD setup for the synthesis of $\text{Bi}_2\text{O}_2\text{Se}$. c) Optical image of square $\text{Bi}_2\text{O}_2\text{Se}$ on f-mica with domain size larger than $50\ \mu\text{m}$. d) Raman spectra of $\text{Bi}_2\text{O}_2\text{Se}$ with the characteristic A_{1g} peak at $159.1\ \text{cm}^{-1}$. e) AFM image of as-grown $\text{Bi}_2\text{O}_2\text{Se}$ on f-mica with a thickness of 14.9 nm. f) ADF-STEM image of $\text{Bi}_2\text{O}_2\text{Se}$. g) FFT of Figure 1f, indicating the crystal group of $I4/mmm$.

In this work, PS-assisted transfer method (Supplementary Figure 1) is adopted to transfer Bi₂O₂Se flakes onto a desired working substrate; here a SiO₂/Si substrate is used where the thickness of SiO₂ is 300nm. This method is preferred over the PMMA-assisted transfer method due to an absence of an etching step. In PMMA-assisted transfer method, HF acid is required to etch away the underlying growth substrate (f-mica) which can be detrimental to the Bi₂O₂Se flakes[25] and its electronic properties. Hence, a non-corrosive transfer method such as PS-assisted transfer method is highly desired as demonstrated by the transport properties exhibited in the following sections.

3.2. Electrical properties

To study the electrical and the thermoelectric performances of the samples, a fanout micro-test structure was fabricated on the Bi₂O₂Se flake (Figure 2a), which is commonly used for thermoelectric measurements on 2D and 1D materials[28-30]. The test structure includes a micro-heater that is not in direct contact with the sample which purpose is to create a lateral temperature gradient through Joule heating. The Ti/Au lines connected to the sample function as the source or drain electrodes for electrical transport measurements and as local resistive thermometers. The performance of the device hinges significantly on post-fabrication treatments, and annealing is well-known to reduce contact resistance between 2D materials and metal electrodes, which is pivotal for ensuring efficient electron flow between the electrodes and the 2D flakes. In this case, we performed an annealing step – subjecting the device to a temperature of 380 K for 4 hours within an ultrahigh vacuum environment ($p \approx 1 \times 10^{-7}$ mbar). Validating our approach, post-annealing results were quite telling: the two-probe resistance experiences a reduction from a rather high resistance of 3.49 M Ω to a much lower resistance of 1.95 k Ω (Supplementary Figure 2) when a back gate voltage of 60 V was applied. This drastic change is also attributed to the result of electrostatic doping moving the fermi-level (E_F) closer to conduction band minima (CBM) which increases the carrier concentration and consequently reducing resistance.

Next, we characterize the electrical properties of Bi₂O₂Se device through two-probe measurement to obtain the transfer characteristics ($I_d - V_g$) as shown in Figure 2b. An essential observation was the manifestation of n-type transport, a characteristic often seen in semiconductors where the primary charge carrier is an electron. Notably, the maximum-to-minimum current ratio is $I_{max}/I_{min} \approx 10^5$ at room temperature (300 K). Shedding light on the finer details, Figure 2c and 2d highlight the conductivity's variation concerning both back gate voltage and temperature which are crucial in understanding the inherent properties of semiconductors. For high-precision measurements, a four-probe configuration (Supplementary Figure 3) was employed. This approach effectively bypasses the

issues of contact resistances that can introduce errors in readings. The measurements revealed a notable trend where the conductivity decreased as the temperature increased, suggesting the sample's metallic properties or its behavior as a degenerate semiconductor. From the conductance versus temperature measurement, the carrier density n_{2D} is extracted (Supplementary Figure 4) and we note that the carrier density is higher at low temperature. Furthermore, the field-effect mobility was obtained by fitting the linear region of the four-probe transfer curve through the equation $\mu = \frac{1}{C_{ox}} \times \frac{L}{W} \times \frac{dG}{dV_g}$ where C_{ox} is the capacitance of the dielectric layer (300 nm thick SiO₂), G is the channel conductance, L and W is the length and width of the channel respectively. To understand the scattering mechanism, μ is plotted as a function of temperature and fitted with a power-law relationship of $\mu \sim T^{-\gamma}$ as shown in Figure 2e. Typically, at elevated temperature (exceeding 100 K), μ is primarily constrained by either acoustic phonon (AP) or optical phonon (OP) scattering[31-34]. AP scattering occurs when charge carriers are disrupted by low-energy lattice vibrations, like sound waves. Conversely, OP scattering is caused by high-energy, short-lived lattice vibrations akin to optical light frequencies. With decreasing temperature, the vibrational motion of phonons diminishes, making room for ionized impurity scattering to become predominant factor in the charge transport. This shift results in a reduction or plateauing of μ as the temperature continues to decrease. However, instead of transitioning into the ionized impurity scattering regime, the μ of our Bi₂O₂Se sample continues to increase with a milder slope ($\gamma = -0.19$) as the temperature decreases below 140 K[24]. This is owing to the polar nature of ferroelectric properties of Bi₂O₂Se where piezoelectric scattering becomes dominant below 140 K, following a temperature dependence of $T^{-0.19}$. In a piezoelectric material below a certain temperature threshold (such as Bi₂O₂Se below 140 K), the piezoelectric effect can counteract the negative impact of ionized impurity scattering. The piezoelectrically-induced electric field counterbalance with the fields from the ionized impurities in such a way that it reduces their effectiveness in scattering charge carriers. Essentially, the piezoelectric field can "screen" or reduce the impact of the Coulombic fields caused by ionized impurities, resulting in maintenance or enhancement in μ [35].

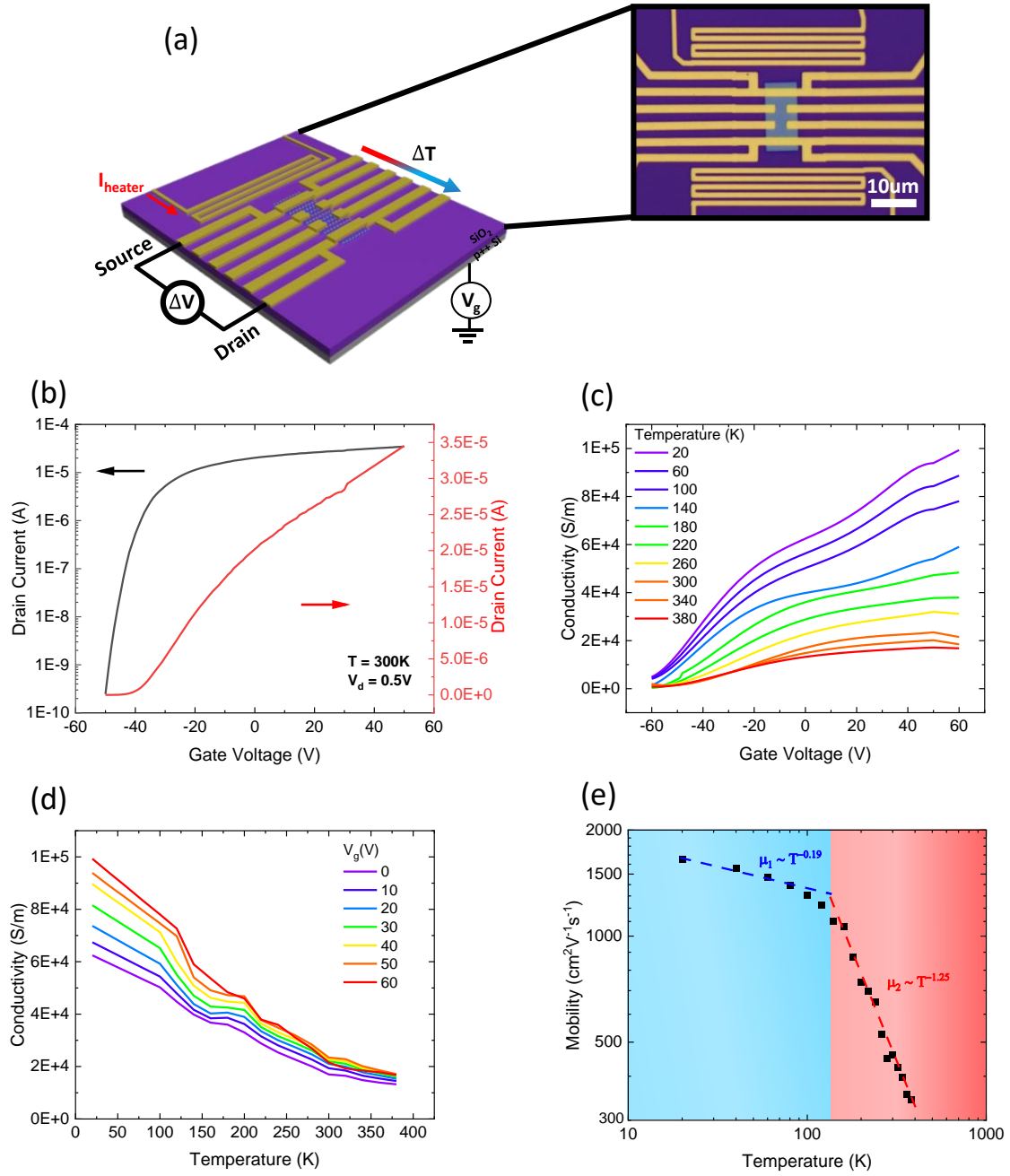


Figure 2. Electrical transport measurements of Bi₂O₂Se. a) Schematic illustration of fan-out micro-test structure on Bi₂O₂Se for thermoelectric measurements. b) Two-probe transfer characteristic (I_d vs V_g) of Bi₂O₂Se. c) Four-probe electrical conductivity plotted against V_g for various temperature. d) Four-probe electrical conductivity plotted against temperature for various V_g . e) FET mobility plotted against temperature, blue region indicates piezoelectric-dominant scattering regime below 140 K and red region indicates POP-dominant scattering regime above 140 K.

3.3. Thermoelectric properties

To capture the thermoelectric transport of Bi₂O₂Se, its electrical conductivity (σ) and Seebeck coefficient (S) were measured across a wide temperature range from 20 K to 380 K while applying different gate voltages. The Seebeck coefficient, defined as $S = -\Delta V / \Delta T$, essentially measures how much voltage is produced for a given temperature difference. This metric provides a crucial window

into the intrinsic thermoelectric behavior of a material. The process of measurement is methodical and split into two main parts: measurement for ΔV and ΔT , respectively. Before the measurement of ΔT (Supplementary Figure 5), the Ti/Au resistive thermometers at the two extremities of the sample must be calibrated by measuring their temperature coefficient of resistance (TCR). Next, the sample is subjected to a lateral temperature difference through Joule heating of the micro-heater. This temperature difference is then quantified with the help of the calibrated TCR values of the two resistive thermometers. We then proceed to measure the induced ΔV (Supplementary Figure 6) across the $\text{Bi}_2\text{O}_2\text{Se}$ to calculate S and plotted it as a function of temperature as shown in Figure 3a. Evidently, the negative polarity of S indicates carrier transport of electrons which aligns with the n-type transport observed from the transfer characteristics in Figure 2b. Moreover, the increase in S with decreasing carrier density and temperature resonates with the characteristics of a semiconducting material[36]. The rise in S continues until it plateaus at a maximum $\sim -200 \mu\text{V/K}$ from 280 K onwards which is attributed to the balance reached between carrier energy and scattering mechanisms. At lower temperatures, an increase in thermal energy leads to more active carriers, enhancing S . As the temperature continues to rise beyond 280 K, additional thermal excitations result in increased carrier scattering, such as phonon scattering, which tends to decrease mobility and can offset gains in S from increased carrier energy, leading to a saturation point. Moreover, by leveraging Nano Angle-Resolved Photoemission Spectroscopy (nano-ARPES), an advanced tool allowing us to examine electronic structures, we discern the Fermi level (E_F) nestling within an electron pocket at the Γ point, as illustrated in Supplementary Figure 7. In the case of $\text{Bi}_2\text{O}_2\text{Se}$, as indicated by the nano-ARPES findings, the Fermi level positioning within an electron pocket can lead to a high density of states at the Fermi level. A sharp peak in the density of states around the Fermi level can enhance the S [10]. This observation not only aligns with the general principles of condensed matter physics but further cements $\text{Bi}_2\text{O}_2\text{Se}$'s status as a degenerated semiconductor with an impressively elevated S .

The Seebeck coefficient, which illuminates the thermoelectric behavior of materials, can be mathematically framed using a well-accepted model based on Boltzmann statistics. In this context, the expression for Seebeck is represented as $S = \pm \frac{k_B}{q} (\ln\sigma - \ln\sigma_0)$. This equation is rooted in the statistical mechanics of thermoelectric behavior and speaks to how electron or hole concentrations shift with temperature, affecting the voltage change for a given temperature gradient. Here, the symbols "+" and "-" respectively denote n-type and p-type semiconductors, denoting whether the primary charge carriers are electrons or holes[37]. The parameters k_B and q represent Boltzmann's constant and the elementary charge of an electron, respectively. The ratio $\frac{k_B}{q}$ translates to $86.15 \mu\text{V/K}$

for phonon-dominated scattering which is also typically used as a guide for the eye as shown in the Jonker plot (S vs $\ln\sigma$) in Figure 3b. We observed that the slope of the line in the Jonker plot closely aligns with the expected slope of $86.15 \mu\text{V}/\text{K}$ for temperatures exceeding 140 K (red region), indicating a phonon-dominated scattering regime. However, for temperatures below 140 K (blue region), an anomalous trend emerges as the slope of the line starts to deviate. This is likely due to a decoupling between S and σ , where there is no substantial decrease in S despite a sharp increase in σ . Such an observation suggests that the enhanced conductivity is not primarily driven by changes in carrier concentration. Instead, it arises from a notable boost in carrier mobility owing to the piezoelectric scattering regime, which does not influence the Seebeck coefficient. Supporting this, our conductance versus temperature measurement (refer to Supplementary Figure 4) reveals a higher carrier density n_{2D} at lower temperatures. Yet, the mobility continues to rise, explaining the amplified conductivity in this temperature bracket.

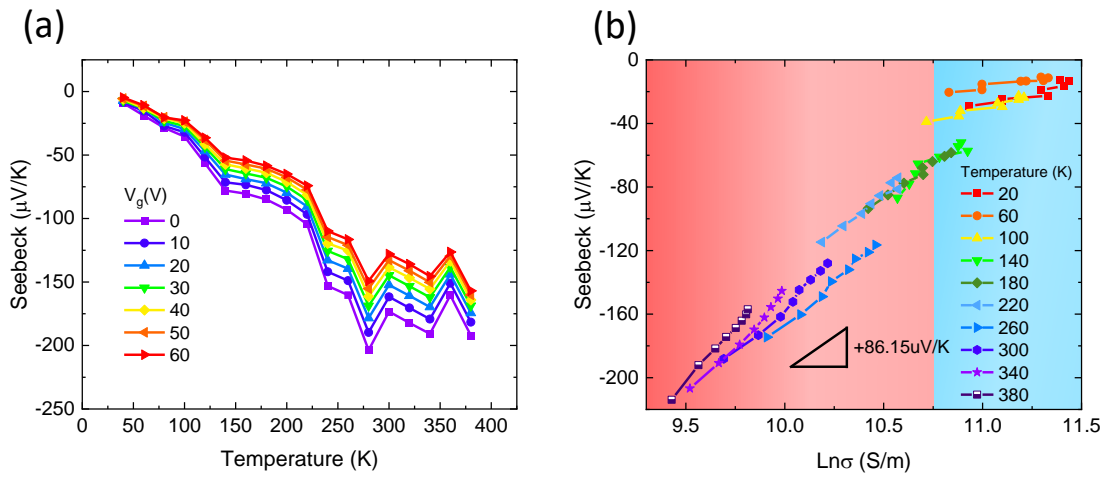


Figure 3. Thermoelectric transport measurement of $\text{Bi}_2\text{O}_2\text{Se}$. a) Seebeck coefficient plotted against temperature for various V_g . b) Typical Jonker plot where Seebeck coefficient is plotted against $\ln\sigma$. $\frac{k_B}{q} = 86.15 \mu\text{V}/\text{K}$ is shown as guide for the eye. Red region indicates the slope of the line aligning to the standard slope $86.15 \mu\text{V}/\text{K}$ above 140 K while blue region indicates the slope of the line deviating from it below 140 K.

In the realm of thermoelectric materials, the interplay between σ and S is crucial, especially when analyzing the power factor $PF = S^2\sigma$, which fundamentally drives the efficiency of a material's ability to convert thermal energy into electrical energy. As shown in Figure 4a, while the σ rises with an increase in carrier density, the magnitude of S follows the opposite trend. This is a typical behavior since having more charge carriers available in a material aid in the conduction of electrical currents. On the other hand, the S decreases when the carrier density is high as individual carriers have a reduced average energy difference between the hot and cold ends[38]. In other words, with more carriers sharing the thermal energy, each carrier contributes less to the total voltage difference,

causing a decrease in S . This effect is accentuated as the gate voltage increases; as E_F shifts closer to CBM and the net contribution from carriers with energy beyond and below the vicinity of E_F diminishes, leading to a lower S [39]. To obtain a more comprehensive understanding of the material's overall thermoelectric performance, we consider the dependence of PF on temperature. As σ increases with temperature, the influx of additional charge carriers into the channel initially boosts the PF , reaching a peak at 280 K, corresponding to a carrier mobility of $\sim 450 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$. This point aligns with where S begins to plateau (Supplementary Figure 8) even as conductivity declines due to reduced carrier density. Beyond 280 K, PF starts to decrease as the μ enters the POP regime leading to reduced μ . Given that the power factor (PF) is defined as ($PF = S^2\sigma$), even if the S remains constant or changes insignificantly, the overall power factor will decrease due to the diminished conductivity resulting from reduced μ in the POP regime. The PF is plotted against the temperature in Figure 4b for each gate voltage. A peak PF value of $860 \mu\text{W m}^{-1}\text{K}^{-2}$ is achieved at 280 K when no gate voltage is applied. Figure 4c was plotted to explicitly display the effectively conditions where high PF can be obtained. Remarkably, the PF above $500 \mu\text{W m}^{-1}\text{K}^{-2}$ can be maintained from 230 K to 330 K without a need to apply gate voltage. It implies that the material inherently possesses optimal thermoelectric properties, reducing the need for external control or adjustments, which can complicate device design and implementation. At room temperature (300 K), our $\text{Bi}_2\text{O}_2\text{Se}$ sample displays a record high PF of $578 \mu\text{W m}^{-1}\text{K}^{-2}$ as compared to other reports on few-layer[24], bulk[40-44], nanostructured and doped $\text{Bi}_2\text{O}_2\text{Se}$ [45] (Figure 4d).

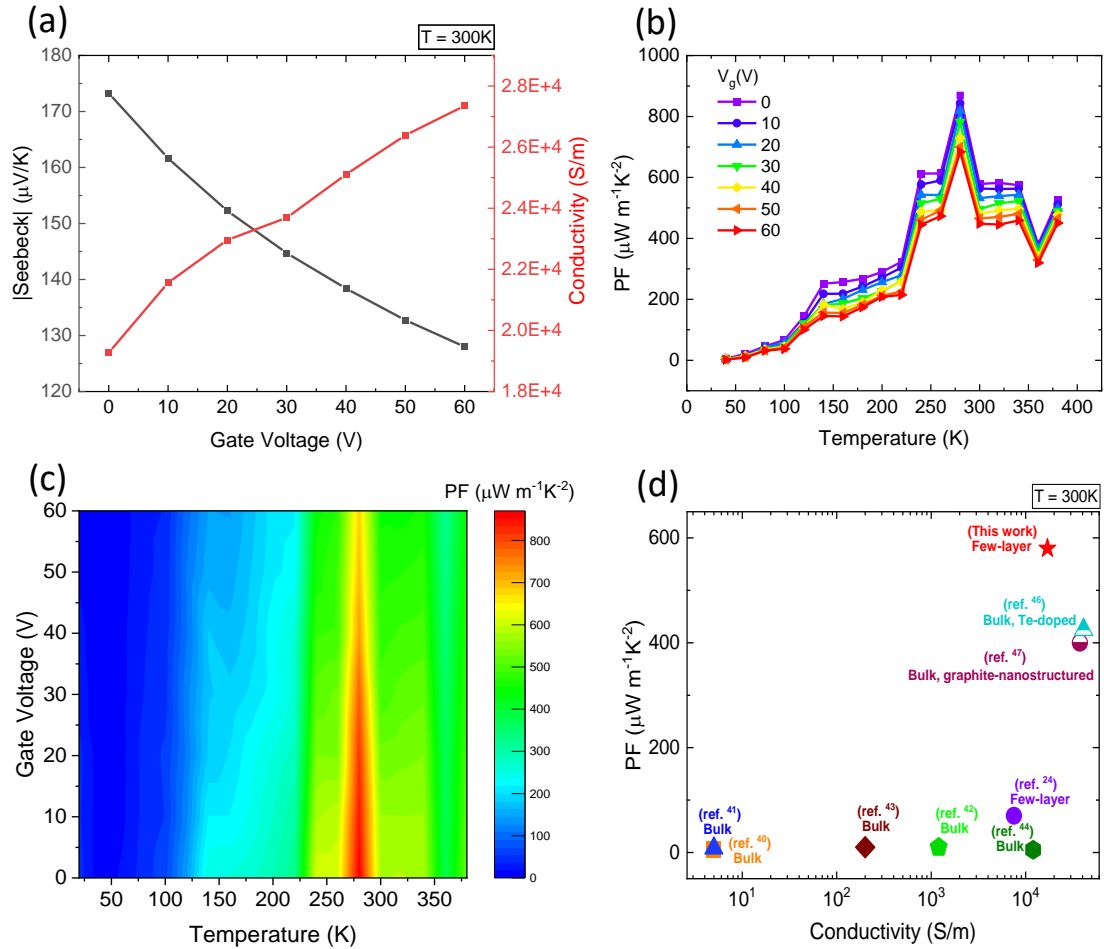


Figure 4. Thermoelectric performance of Bi₂O₂Se. a) Power factor plotted against temperature for various V_g . b) Seebeck coefficient and electrical conductivity plotted against V_g at 300K. c) Colour contour plot of power factor versus V_g and temperature. d) Comparison of power factor with other works on few-layer[24], bulk[40-44], Bi₂O_{1.96}Te_{0.04}Se[46] and Bi₂O₂Se-0.5% mol graphite[47] at 300K.

4. Summary

In summary, our extensive investigation into the thermoelectric properties of few-layer Bi₂O₂Se, facilitated by modulating charge densities via back gate voltage across a wide temperature range (20 – 380 K), has been greatly enhanced using the PS-assisted transfer method. This method has ensured the high quality of our Bi₂O₂Se samples, which is a critical factor contributing to their impressive thermoelectric performance.

Our study identified two distinct scattering regimes: piezoelectric scattering, which is predominant below 140 K, and POP scattering above this temperature. Despite a $T^{-1.25}$ temperature dependence observed above 140 K, we recorded a high mobility of $450 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at 280 K, and the Seebeck coefficient plateauing at a high value of approximately $-200 \mu\text{V/K}$. This culminated in achieving the

highest reported PF of $860 \mu\text{W m}^{-1}\text{K}^{-2}$ for $\text{Bi}_2\text{O}_2\text{Se}$ at temperatures near room level, notably achieved without the need for electrostatic gating.

The significance of this high PF , particularly at near-room temperatures, positions $\text{Bi}_2\text{O}_2\text{Se}$ as an exceptionally promising candidate for practical applications in room-temperature waste heat harvesting. This high PF near ambient temperatures opens new possibilities to efficiently convert waste heat into electrical energy, which is highly desirable, particularly in areas such as temperature sensing and thermoelectric generators.

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Data availability statement

All data needed to evaluate the conclusions in the paper are present in the paper and/or the Supplementary Materials. Additional data related to this paper may be requested from the authors.

Credit authorship contribution statement

W.Y., Q.F., X.W, M.B. and B.T. conceived and designed the project. W.Y. and Q.F. performed material synthesis, device fabrication, electrical and thermoelectric measurements. Q.F. contributed to material characterization. W.Y., Q.F., X.W., M.B., G.Z. and J.W. analyzed the data. W.Y., Q.F., M.B. and X.W. drafted the manuscript. All authors discussed and commented on the manuscript.

Declaration of competing interest

The authors declare no competing interests.

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