



## ICLs à 3,3 μm sur substrats de GaSb, GaAs et Si (001)

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# MIR Sb-based Interband cascade lasers on metamorphic substrates

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## Introduction:

The production of **MIR gas sensors** on PICs requires **Sb-based light sources**. The growth on inexpensive and large-sized **mismatched substrates** introduces dislocations. They create **deep levels** in the gap and act as trap defects, impacting laser performances. It's crucial to have an active zone **tolerant to dislocations**, which is achievable with interband cascade lasers (ICL).

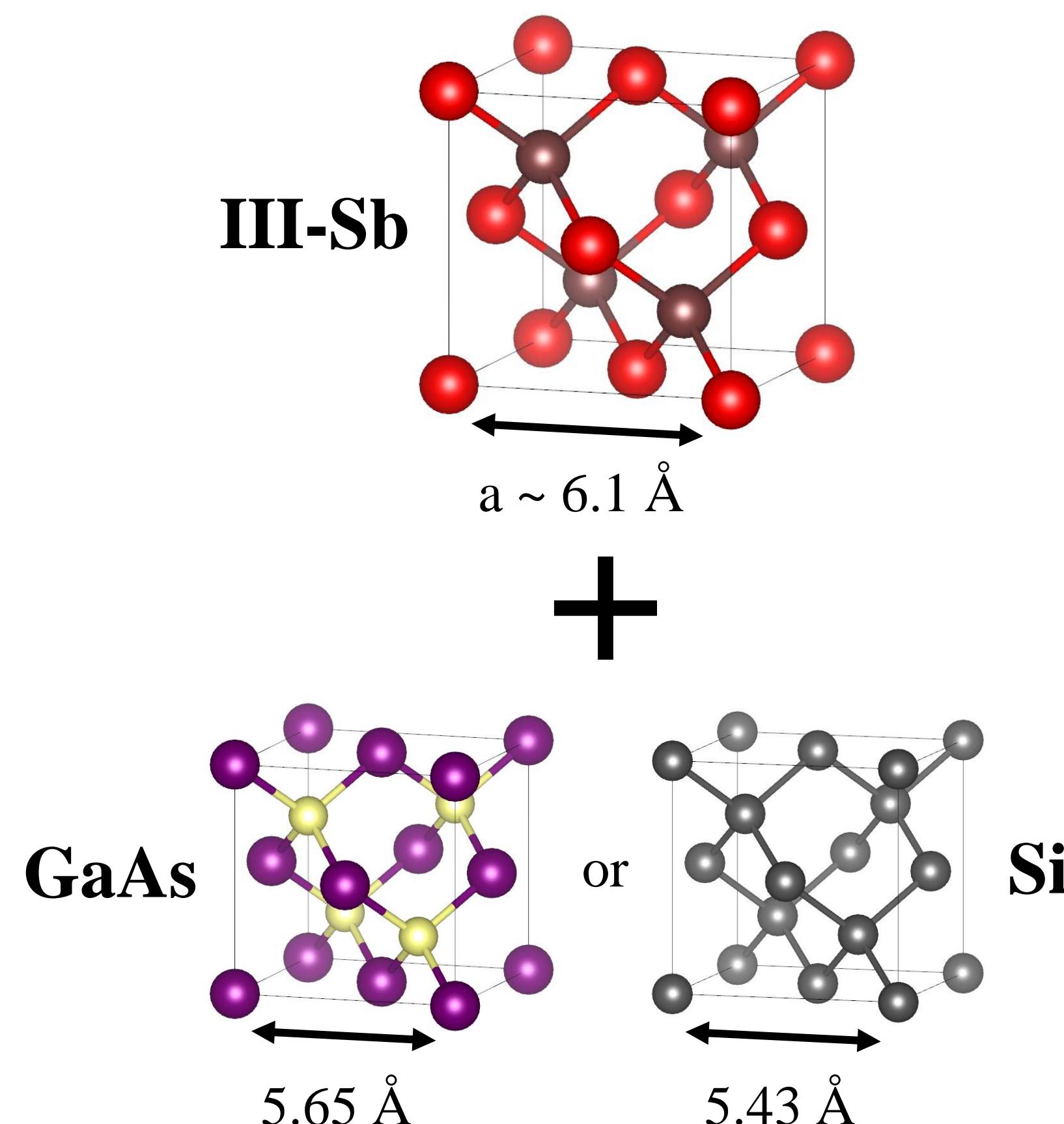


Fig. 1. Impact of dislocations in a type-I QW<sup>1</sup>

<sup>1</sup> L. Cerutti et al., Optica, vol. 8, no 11, p. 1397, nov. 2021

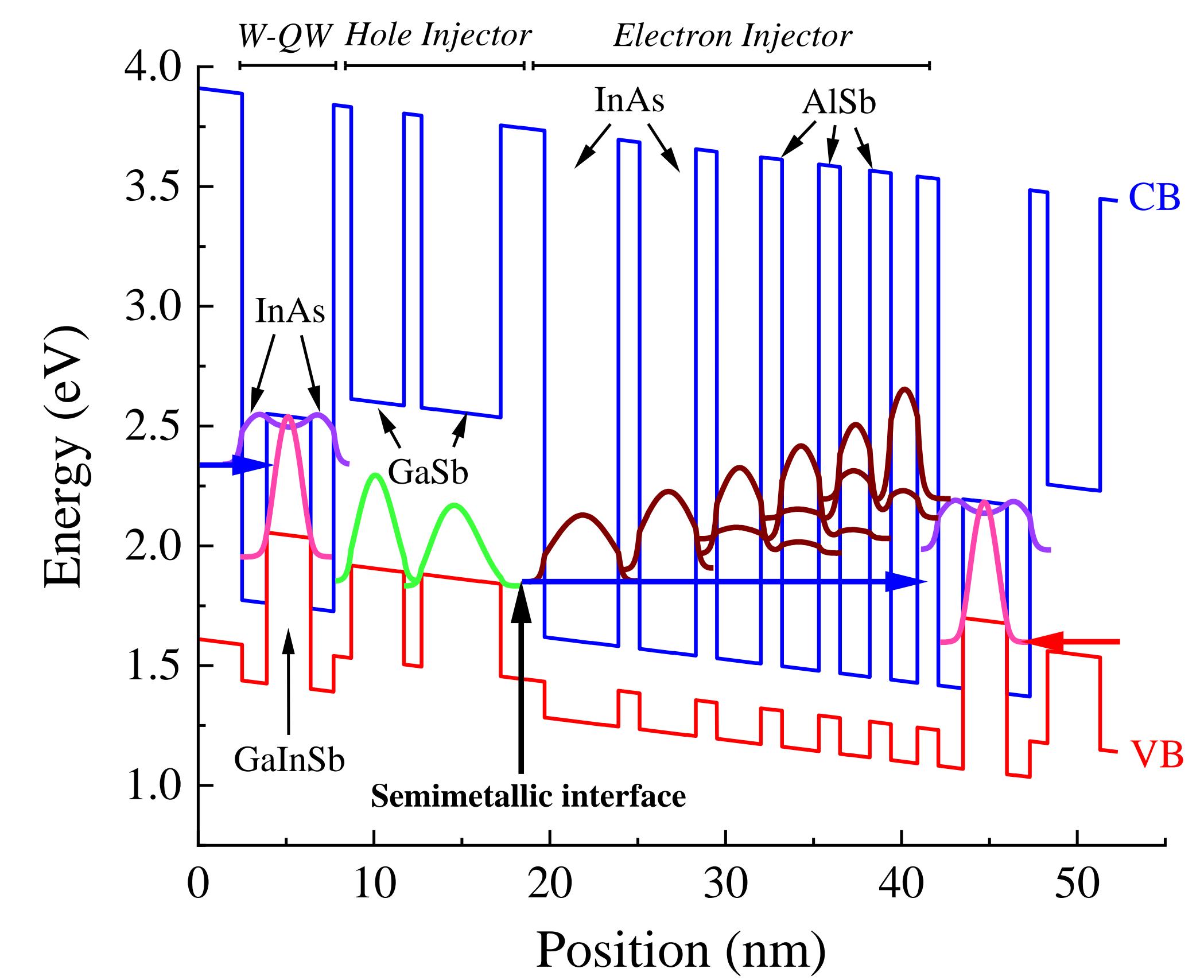


Fig. 2. Band structure of 1 stage of the ICL active zone

## Structural characterizations:

Cap – 20 nm InAs
Top cladding – 1.8 µm AlSb / InAs SLs
SCH – 500 nm GaSb:Te
Active zone – x5 or x7
SCH – 500 nm GaSb:Te
Bottom cladding – 3 µm AlSb / InAs SLs
Substrate

Fig. 3. ICL structure

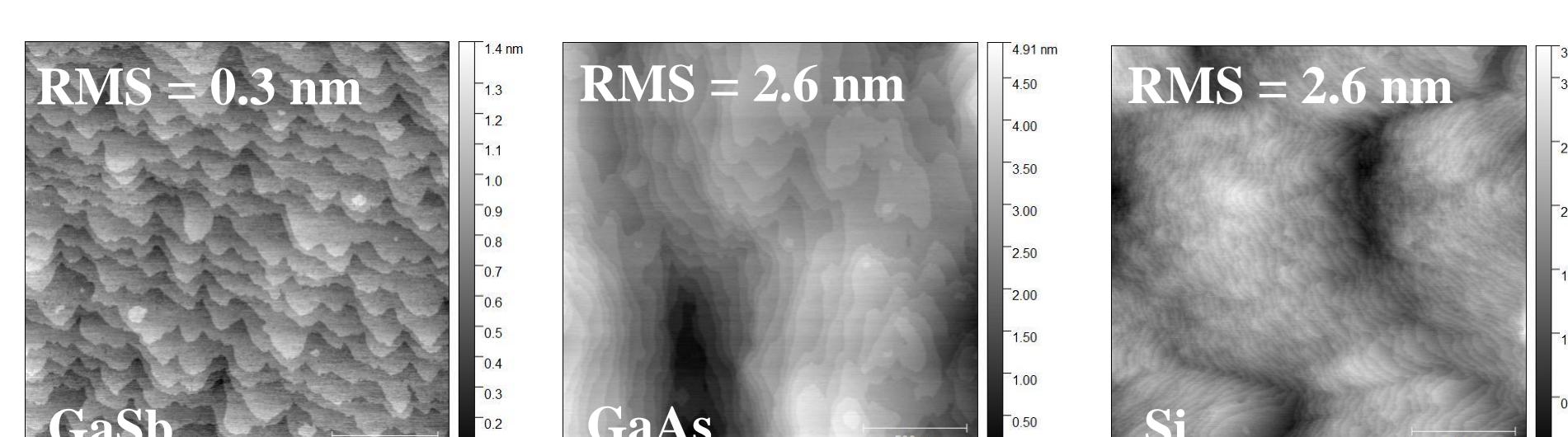


Fig. 4. 2 µm x 2 µm AFM images for ICLs emitting at 4.2 µm

Higher RMS on GaAs and Si

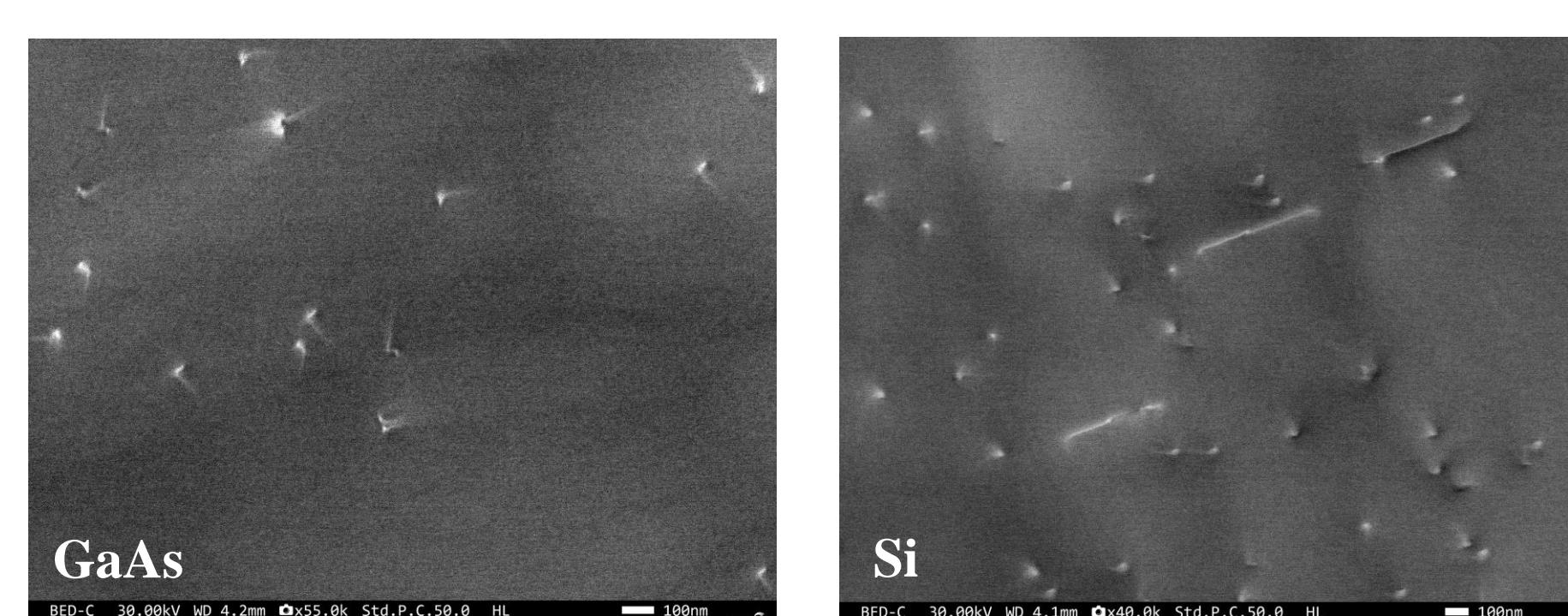


Fig. 5. ECCI for ICLs at 3.3 µm on GaAs and Si

TDD  $\sim 5 \cdot 10^8$  cm $^{-2}$

Growth on **mismatched substrates** → **High dislocation density** ( $>10^8$  cm $^{-2}$ )

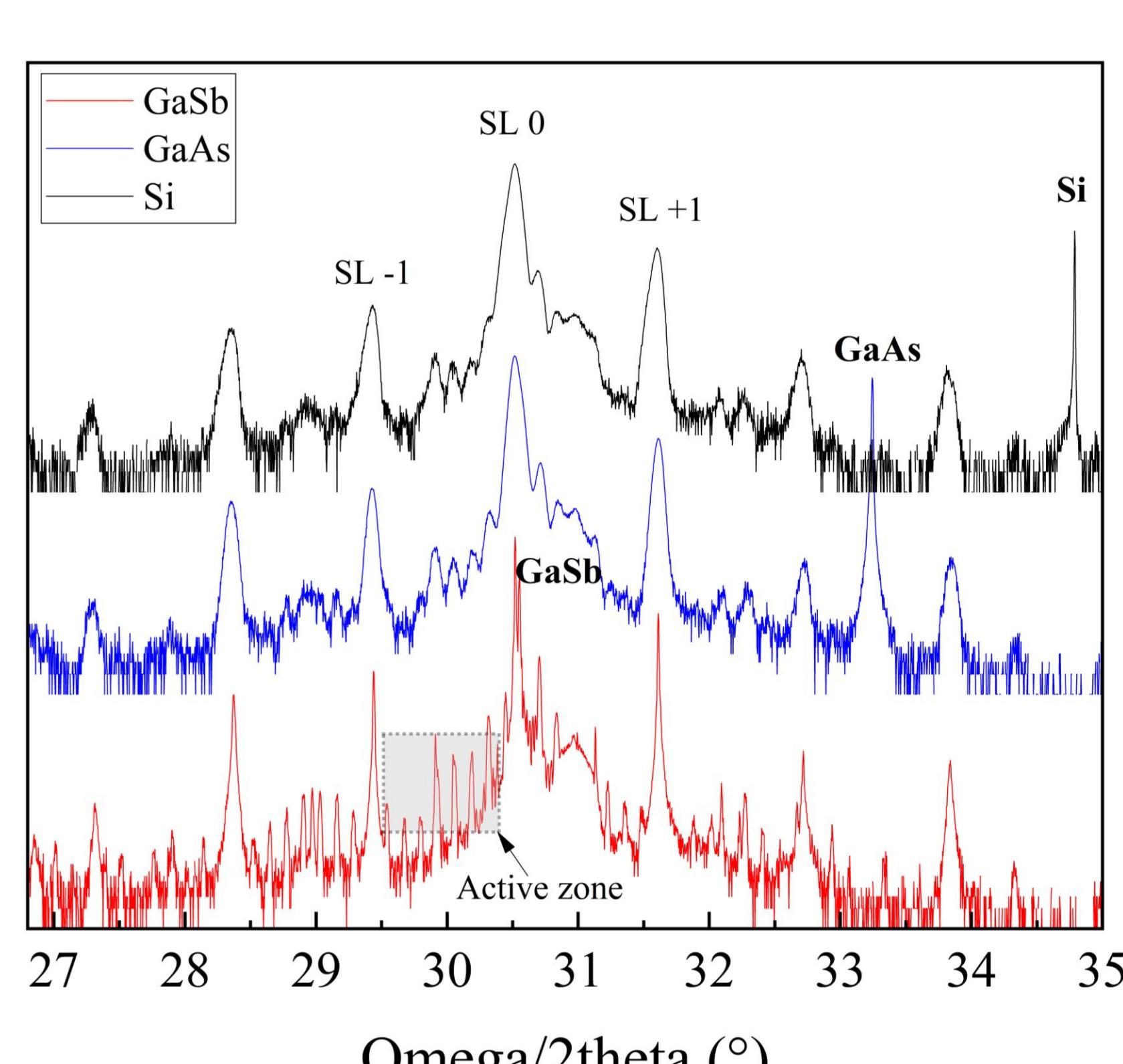


Fig. 6. Comparison of XRD patterns for ICLs grown on different substrates

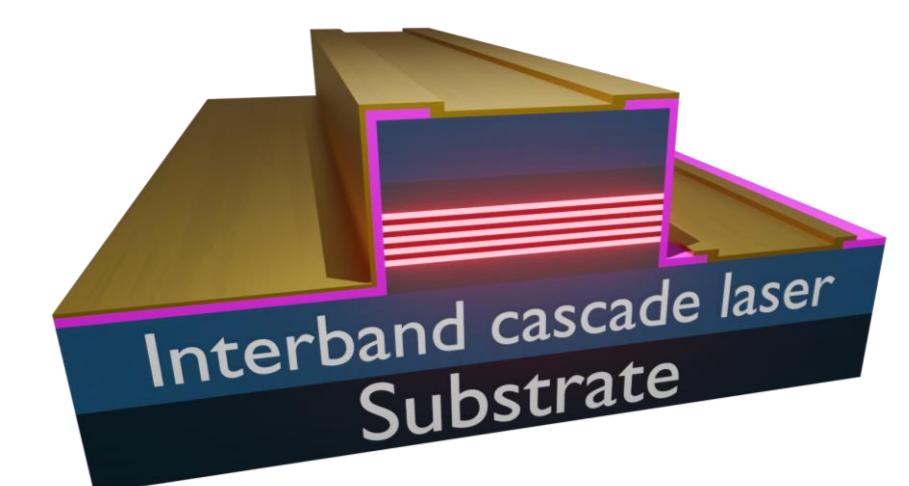
Dislocations  
↓  
Lattice distortion  
↓  
Broad satellite peaks on GaAs and Si

## Funding sources:

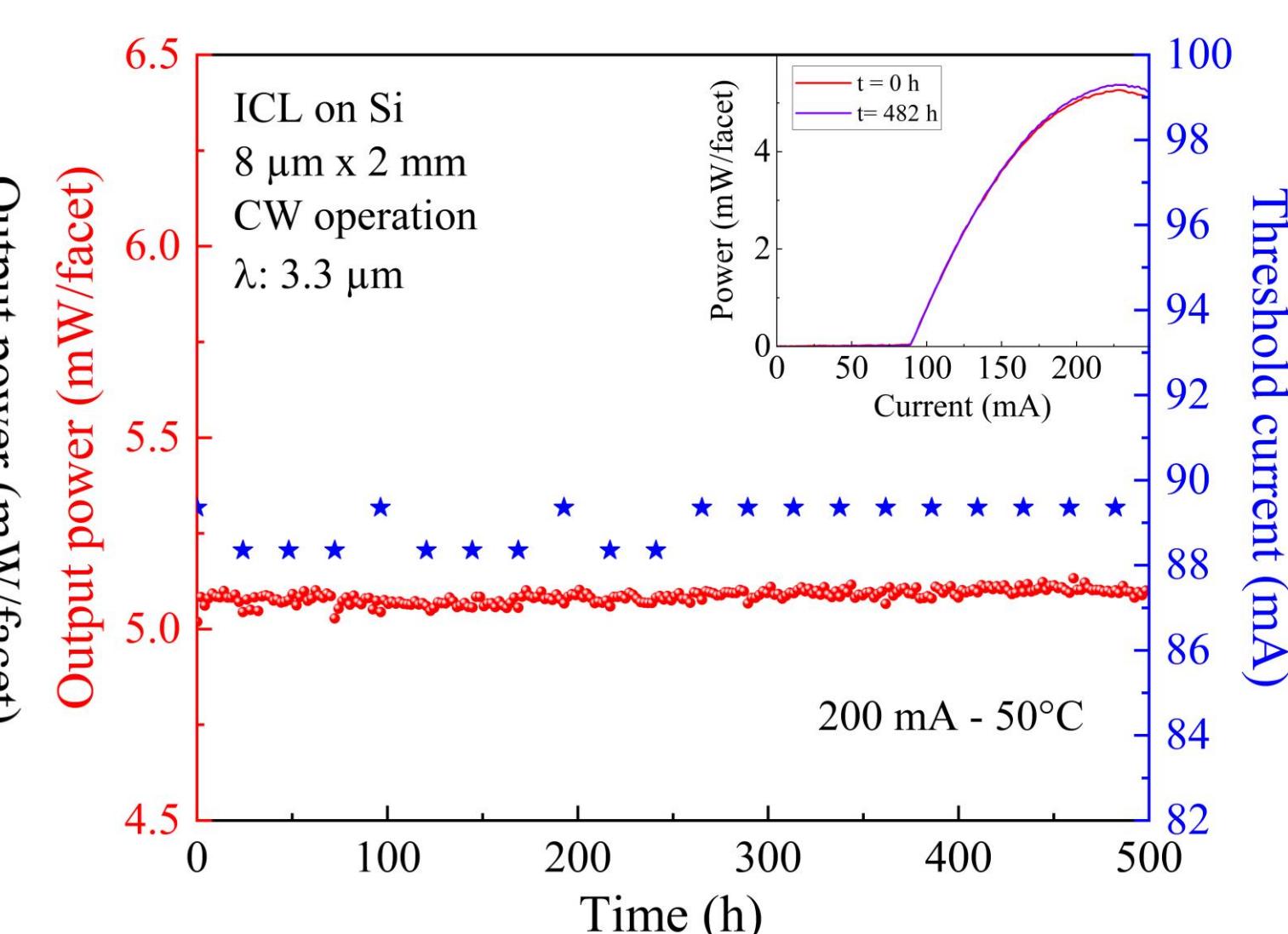
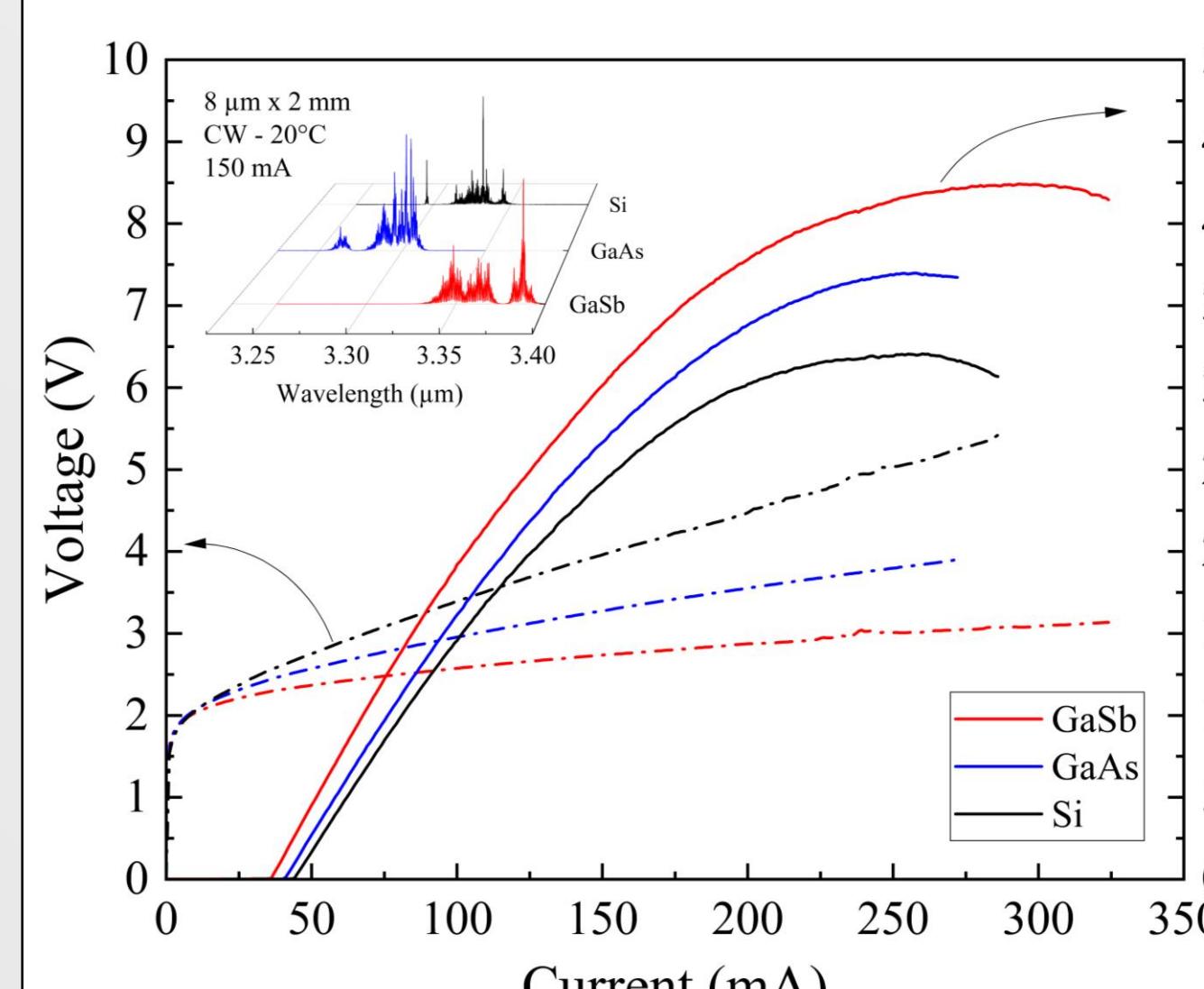


## Results:

ICLs **epi-side-up** with **uncoated facets** and both contacts taken on the epitaxial side

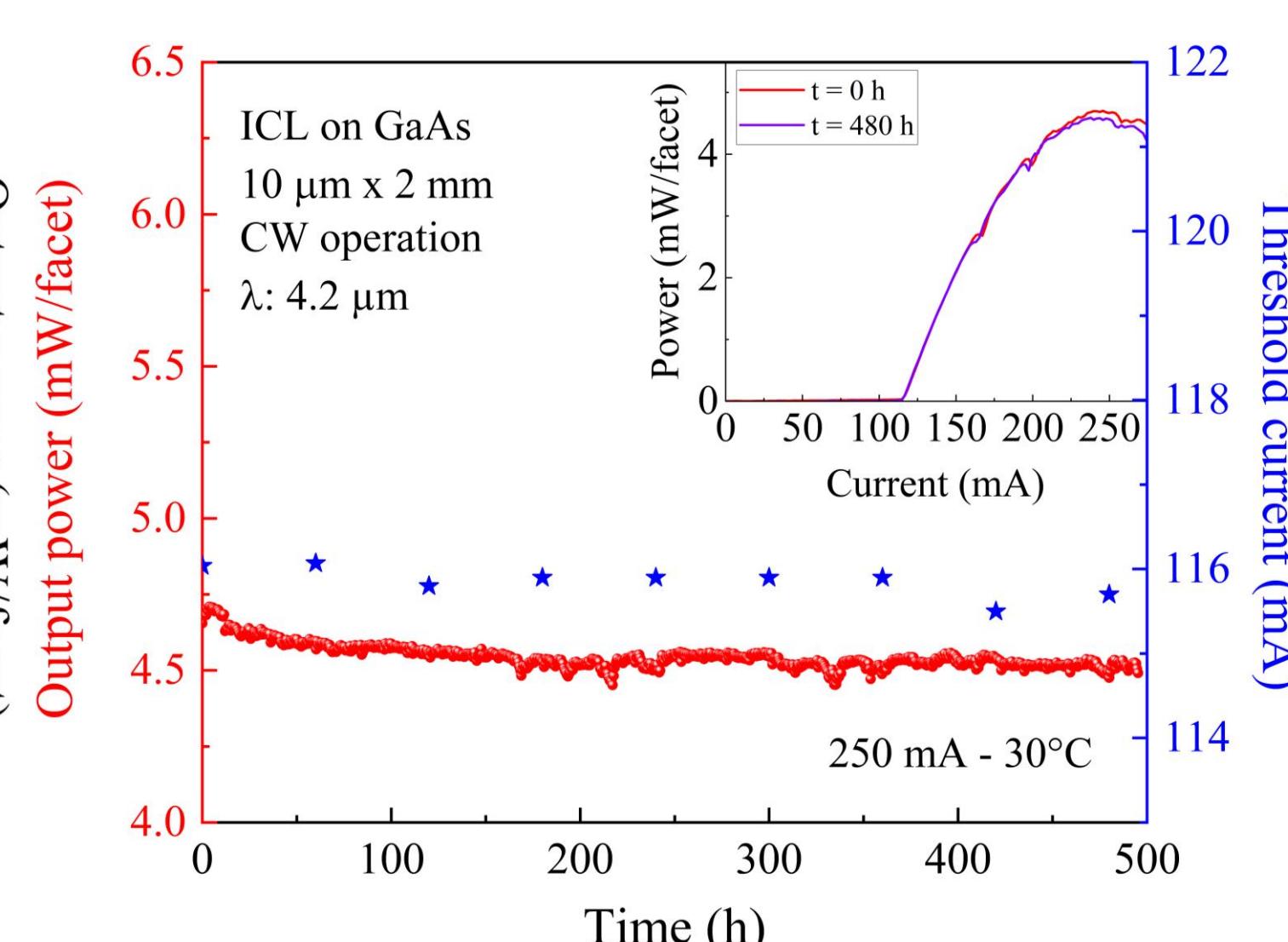
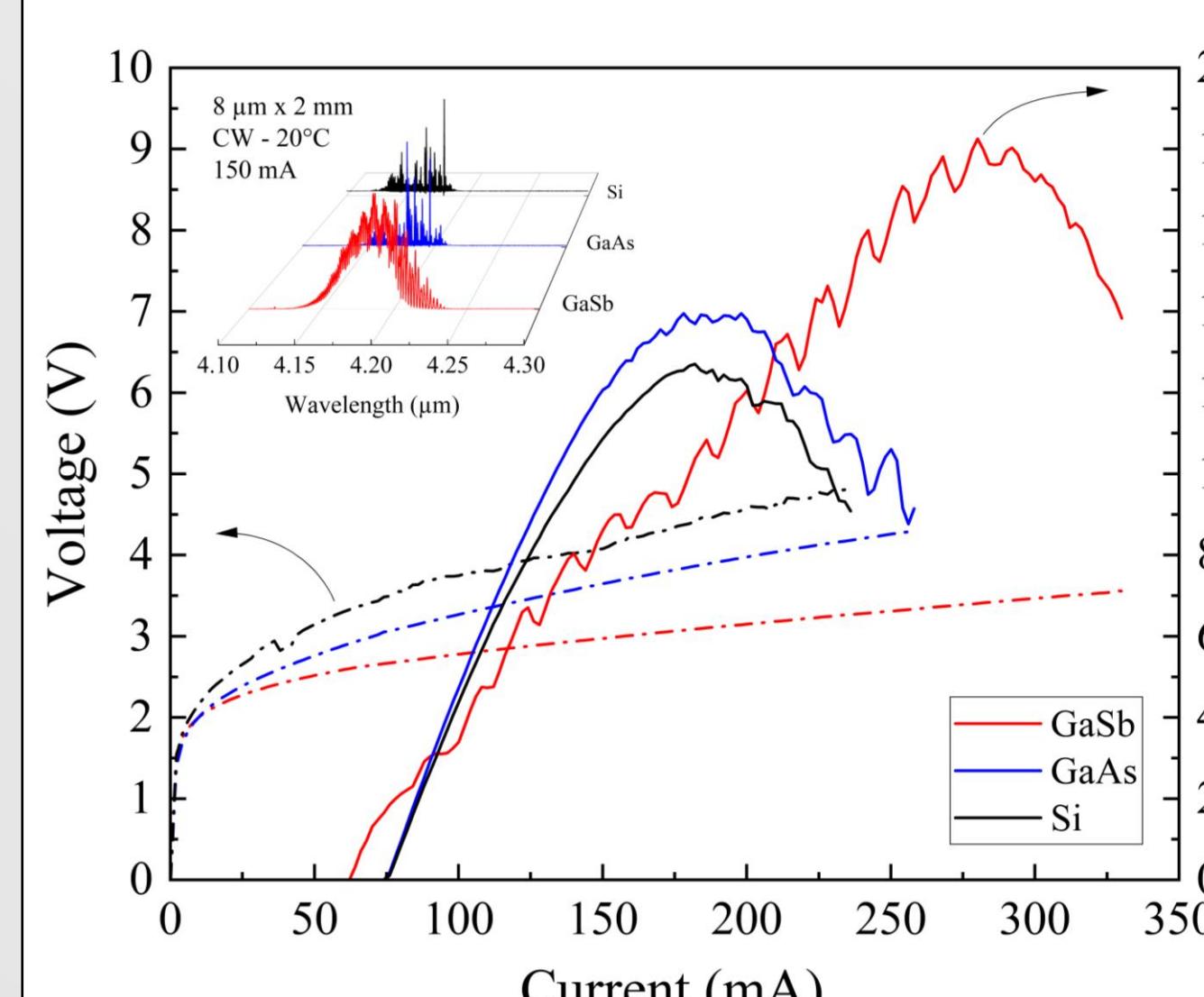


➤ Lasers at 3.3 µm



Similar  $I_{th}$ : 40 mA • CW operation up to  $\sim 65^\circ\text{C}$  • Same  $T_0$ : 39K

➤ Lasers at 4.2 µm



$I_{th}(\text{GaSb}) \sim I_{th}(\text{GaAs/Si})$  • CW operation up to  $40^\circ\text{C}$  • Same  $T_0$ : 32K

## Conclusion:

No degradation during the aging

ICLs tolerant to dislocations between 3.3 and 4.2 µm

Towards integration on PICs