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# Optimality and Approximation Ratios of Demodulator Allocation Strategies in LoRa Multi-Gateway Networks 

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#### Abstract

Long Range (LoRa) is a wireless modulation used to provide long-range connectivity to low-power devices of the Internet of Things (IoT). LoRa gateways use hardware chips called demodulators in order to demodulate concurrent frames on different channels or spreading factors. In this paper, we establish several theoretical results on the performance of fundamental demodulator allocating strategies. In the single gateway scenario, we first prove the optimality of the basic preemptive allocation strategy. Next, in the multi-gateway scenario, the greedy noncollaborative strategy, which is currently implemented in LoRa gateways, is shown to achieve a very large approximation ratio. As an alternative, we demonstrate that a preemptive non-collaborative strategy has an approximation ratio of 2 , and furthermore, that a preemptive smart-collaborative strategy reaches an approximation ratio of $3 / 2$, for two gateways. These seminal results imply that the current performance of multigateway LoRa deployments can be largely improved by the proposed collaborative demodulator allocation strategies.


## I. Introduction

Low-Power Wide Area Networks (LPWANs) are networks where low-power end-devices can communicate wirelessly over long distances. LPWANs are largely used in the Internet of Things (IoT), thanks to their long-range capabilities in environmental monitoring applications, or to their low-cost and scalability in smart cities. Long Range (LoRa) is one of the main technologies for LPWANs: it is a very robust wireless modulation that can achieve a communication range of tens of kilometers outdoor. LoRa is often used in combination with Long Range Wide Area Network (LoRaWAN), which defines a simple medium access control protocol and network topology on top of LoRa.

In LoRaWAN, the end-devices communicate to a network server through gateways, as shown in Figure 1. Each gateway in range of an end-device might intercept its frames, decode them, and forward them to the network server. The network server is in charge of removing frame duplicates. The gateways have the ability to decode several incoming transmissions at the same time, provided that they are sent on different channels or that they use different spreading factors (SFs). To do so, each gateway listens for preambles on all channels and SFs at all time, and allocates a demodulator to each incoming frame. The total number of demodulators per gateway is limited, typically to eight [1].

Several researchers have shown that the limited number of demodulators reduces the throughput of LoRa [2], [3], [4]. Thus, there have been a few attempts to allocate the demodulators efficiently [5], [6], [7]. This is a challenging issue, as the demodulator allocation is an online scheduling problem with specific constraints.


Figure 1. End-devices (EDs) communicate to all gateways (GWs) in range through LoRa links (represented with dashed lines). The gateways are all connected to the network server (NS) through an IP backhaul (represented with solid lines).

In this paper, we investigate the theoretical performance of demodulator allocating strategies, in terms of the number of demodulated frames. We demonstrate the following results, which are also summarized in Table I:

- In the single gateway scenario, with a single demodulator, the approximation ratio of the greedy algorithm is equal to $f_{\max }+1=72$, where $f_{\max }$ is the maximum number of short frames (i.e., with SF7) that can fit during a single long frame (i.e., with SF12). Meanwhile, a simple preemptive algorithm is optimal.
- In the single gateway scenario with $D=2$ demodulators, the simple preemptive algorithm is still optimal.
- In the multi-gateway scenario with $M \geq 2$ gateways, the approximation ratio of the non-collaborative greedy algorithm is at least $M f_{\max }$, and at least $f_{\max }$ with simple collaboration. Meanwhile, the preemptive algorithm achieves an approximation ratio of 2 , even without collaboration, for $M=2$ gateways. By using a smart collaboration, the preemptive algorithm reaches an approximation ratio of 1.5 for $M=2$ gateways.
Finally, these theoretical findings have been validated and illustrated through extensive computer simulation evaluations.

Table I
SUMMARY OF OUR THEORETICAL RESULTS.

| Number of <br> gateways | Number of <br> demodulators | Greedy | Preemptive |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
|  | non-collaborative | simple collabora- <br> tion | non-collaborative | simple collabora- <br> tion | smart collabora- <br> tion |
| 1 | 1 | $=f_{\max }+1$ (Theorem 1) | $=1$ (Theorem 2) |  |  |
| 1 | $D \geq 2$ | $\geq f_{\max }+1$ (corollary of Theorem 1) | $=1$ for $D=2$ (Theorem 3) |  |  |
| $M \geq 2$ | 1 | $\geq M \times\left(f_{\max }+1\right)$ (corollary of Theorem 1) | $=2$ for $M=2$ <br> (Theorem 4) | $=2$ for $M=2$ <br> (Theorem 5) | $\leq 1.5$ for $M=2$ <br> (Theorem 6) |

The remainder of this paper is organized as follows. Section II presents the existing works on demodulator allocation strategies, as well as similar scheduling problems. Section III presents our system model. Section IV presents the four algorithms we consider, as well as an optimal solution using a mixed integer linear program (MILP). Section V focuses on the single gateway scenario, with either one or two demodulators for the gateway. Section VI focuses on the multigateway scenario, with a single demodulator per gateway. Section VII-B describes our simulation results. Finally, Section VIII concludes our work.

## II. State-of-THE-ART

Several researchers have shown that the limited number of demodulators in LoRa gateways reduces the overall network performance. In [2], the authors included the number of demodulators as a parameter of their mathematical model of LoRaWAN, and showed that this parameter has a significant impact on the throughput. In [3], the authors also considered the limited number of demodulators as a practical hardware limitation. In [4], the authors studied the impact of several parameters, and notably the number of parallel reception paths which is equal to the number of demodulators, on the performance of LoRaWAN.

## A. Related work on demodulator allocation strategies

Few research works attempted to improve the usage of demodulators, probably due to the fact that the strategy implemented in the firmware of existing LoRaWAN gateways is not documented.

In [5], the authors described the conventional first-in-firstout (FIFO) greedy strategy, as well as two improved strategies called FIFO-RR1 and FIFO-RR2, for a single gateway scenario. FIFO-RR1 and FIFO-RR2 make use of the unused time between the detection of the preamble and the beginning of the payload in order to demodulate short frames. The FIFORR2 strategy can also plan in advance the demodulation of a long frame before the end of the demodulation of another frame.

In [6], the authors proposed two strategies for a multigateway scenario: an explicit collaboration strategy, and a random strategy. The explicit collaboration strategy enables a gateway to ignore the demodulation of frames that are planned to be demodulated by another gateway. This is achieved through control messages, but requires long SFs. The random strategy attempts to make gateways randomly choose different frames to demodulate, and is intended for small SFs.

In [7], an auction-based optimization approach was designed in order to pre-assign each IoT device to a unique gateway, so as to maximize the total amount of demodulated transmissions without redundancy at the network server, given the constraint of the limited number of demodulators per gateway. This optimization method was shown to significantly outperform benchmark algorithms, and to be suitable for mobile IoT applications.

## B. Similar scheduling problems

The demodulator allocation can be seen as a special form of online scheduling problem with strict deadlines, where each task is an incoming frame, and each gateway can process up to eight tasks at the same time, given that existing LoRaWAN gateways are equipped with only eight demodulators [1]. Tasks have varying durations (depending on the SF and on the payload length), and are released at the time when the preamble is detected by the gateway.

Compared to usual scheduling problems, the main specificity of the demodulator allocation problem is that the processing of our tasks cannot be postponed. Indeed, our tasks have to be processed exactly from the preamble start to the preamble end. To the best of our knowledge, this specificity has not been studied deeply by the research community on scheduling problems.

However, some results from the literature are still related to ours. For instance, in [8], the authors prove that the problem is NP-hard on a single processor (that is, with a single gateway). However, they show that it becomes polynomial if the task have to be scheduled upon arrival and if there is no preemption, which is similar to a case we consider. In [9], the author proves the NP-hardness of the problem when tasks have deadlines (in addition to due dates). Finally, our problem is also related to soft real-time scheduling [10], where the real-time comes from the fact that tasks cannot be postponed, and the softness comes from the fact that some tasks are allowed to be dropped. Unfortunately, we did not find algorithms that can solve our problem, nor fundamental results on the performance of such algorithms.

## III. SYSTEM MODEL AND NOTATIONS

According to the specifications of the hardware component of LoRaWAN gateways (that is, either the SX1301 or the SX1302 chip) [1], we consider that each gateway listens for preambles on all channels and spreading factors, at all times. Each time a new frame is detected (which occurs during the preamble of the frame), the gateway decides
whether it allocates a demodulator for this frame or not. To be successfully demodulated, at least one gateway has to demodulate the whole payload of a frame, without interruption nor demodulator change.

In order to simplify the proofs, we take a number of assumptions as follows:

- different frames cannot start or finish at exactly the same time, which corresponds to the reality.
- in the algorithms, the preamble detection time is equal to the payload starting time (although in practice, the preamble is detected at about half of the preamble duration [11], [12]).
- there is no loss of payload for a frame whose preamble was detected, hence a frame is successfully decoded as long as at least one gateway has allocated a demodulator during the whole frame payload.
- for collaborative strategies, we consider that two gateways can determine whether two preambles received by two gateways correspond to the same frame or not ${ }^{1}$.
Notations: Let $\mathcal{F}$ be a set of frames. The payload of each frame $f \in \mathcal{F}$ starts at $t_{f}^{(\mathrm{s})}$ and ends at $t_{f}^{(\mathrm{e})}$. Given an algorithm $A$, let us denote by $\Gamma_{t}(A)$ the total number of frames that have been demodulated by $A$ until time $t$ (included). Note that if $t$ is omitted, it is assumed to be equal to $\max _{f \in \mathcal{F}} t_{f}^{(\mathrm{e})}$. Finally, let $\Gamma_{t^{\prime}, t}(A)$ be the number of frames successfully demodulated by an algorithm $A$ and whose ending times are within $\left.] t^{\prime} ; t\right]$.

In the following, we will show that some algorithms are optimal in some conditions.
Definition: An algorithm $O P T$ is said to be optimal if, for all sets of frames $\mathcal{F}$ and for all algorithms $A$, we have

$$
\begin{equation*}
\Gamma(O P T(\mathcal{F})) \geq \Gamma(A(\mathcal{F})) \tag{1}
\end{equation*}
$$

We will also compute the approximation ratio of some algorithms in some conditions.
Definition: An algorithm $A$ is said to have an approximation ratio $r \geq 1$ for a maximization problem if for all sets of frames $\mathcal{F}$, we have

$$
\begin{equation*}
\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma(A(\mathcal{F}))} \leq r \tag{2}
\end{equation*}
$$

This means that the solution obtained by $A$ for any set of frames $\mathcal{F}$ cannot be $r$ times lower than the optimal solution. This notion is very important as it gives a worst bound on the quality of the solution computed by $A$, compared to the optimal solution.

## IV. Algorithms

In the following, we will consider the following algorithms:

- $G$ (see Algorithm 1) is a greedy algorithm: demodulators are allocated using a FIFO strategy. Algorithm $G$ models the method implemented in current LoRaWAN gateways.

[^0]- $P$ (see Algorithm 2) is a greedy algorithm with preemption: a demodulator can drop its current frame $c$ in order to demodulate $f$, if $f$ ends earlier than $c$.
- $P^{C}$ (see Algorithm 3) is a greedy algorithm with preemption, which takes into account a simple form of collaboration among gateways (through the network sever): each frame is demodulated by a single gateway (chosen arbitrarily).
- $P^{S}$ (see Algorithm 4) is a greedy algorithm with preemption, which takes into account a more complex form of collaboration among gateways: when a frame is demodulated by several gateways, these gateways (except one) can later drop the common frame to accept a new frame.
- OPT denotes the optimal algorithm, namely the one with maximal gain $\Gamma$.

```
Algorithm 1 Algorithm \(G\)
    Upon detecting the preamble of a new frame \(f\) :
    if a demodulator is idle then
        demodulate \(f\)
    end if
```

```
Algorithm 2 Algorithm \(P\)
    Upon detecting the preamble of a new frame \(f\) :
    if a demodulator is idle then
        demodulate \(f\)
    else
        if there exists a demodulator with a frame \(c\) such that
        \(t_{f}^{(\mathrm{e})}<t_{c}^{(\mathrm{e})}\) then
            \(\delta \leftarrow\) the demodulator demodulating the frame \(c\) with
            the largest \(t_{c}^{(\mathrm{e})}\)
            make demodulator \(\delta\) drop its frame
            demodulate \(f\) (with \(\delta\) )
        end if
    end if
```

```
Algorithm 3 Algorithm \(P^{C}\)
    Upon detecting the preamble of a new frame \(f\) :
    apply algorithm \(P\)
    if several gateways demodulate \(f\) then
        drop \(f\) for all but one gateway (chosen arbitrarily)
    end if
```

The optimal algorithm is defined as a mixed-integer linear programming (MILP) problem, as follows. The inputs are the number of frames $F$, the number of demodulators $D$, the number of gateways $M$, an array $\operatorname{Col}$ such that $\operatorname{Col}\left[f_{1}, f_{2}\right]$ is true if and only if frames $f_{1}$ and $f_{2}$ overlap in time (i.e., there is a collision between $f_{1}$ and $f_{2}$ ), and an array $R x$ such that $R x[f, g]$ is true if and only if frame $f$ is received by gateway $g$. Note that $\mathcal{F}$ is modeled indirectly through the array Col.

The MILP uses three arrays of binary variables: $\operatorname{Dem}[g, d, f]$ is true if and only if the $d$-th demodulator of gateway $g$ demodulates frame $f, \operatorname{Dem}_{1 d}[g, f]$ is true if and only if at least one demodulator of gateway $g$ demodulates

```
Algorithm 4 Algorithm \(P^{S}\)
    Upon detecting the preamble of a new frame \(f\) :
    if a demodulator is idle then
        demodulate \(f\)
    else
        if a demodulator is demodulating a frame \(c\) which is also
        demodulated by at least another gateway then
                drop \(c\)
                demodulate \(f\)
        else
            if there exists a demodulator with a frame \(c\) such that
            \(t_{f}^{(\mathrm{e})}<t_{c}^{(\mathrm{e})}\) then
                    \(\delta \leftarrow\) the demodulator demodulating the frame \(c\) with
                the largest \(t_{c}^{(\mathrm{e})}\)
                make demodulator \(\delta\) drop its frame
                demodulate \(f\) (with \(\delta\) )
            end if
        end if
    end if
```

frame $f$, and $\operatorname{Dem}_{1 g}[f]$ is true if and only if at least one gateway demodulates frame $f$.

The MILP is defined by the following equations. Equation (3) is the objective function: the goal of $O P T$ is to maximize the number of frames demodulated by any gateway. Equation (4) states the constraint that a demodulator $d$ of a gateway $g$ cannot demodulate a frame $f$ if $f$ is not received by gateway $g$. Equation (5) constrains that it is not possible for two frames $f_{1}$ and $f_{2}$ to be demodulated by the same demodulator $d$ if $\operatorname{Col}\left[f_{1}, f_{2}\right]$ is true, that is if $f_{1}$ and $f_{2}$ overlap in time. Indeed, if $f_{1}$ is not chosen by $d$, then the right part of the equation is equal to $F$, which does not impose any constraint on the left part of the equation. However, if $f_{1}$ is chosen by $d$ (and thus, $\operatorname{Dem}\left[g, d, f_{1}\right]=1$ ), all other frames $f_{2}$ chosen by $d$ have to be such that $\operatorname{Col}\left[f_{1}, f_{2}\right]=0$. Equations (6) and (7) ensure that $\operatorname{Dem}_{1 d}[g, f]$ is true when at least one demodulator of $g$ demodulates $f$, based on the value of $D e m$. Equations (8) and (9) ensure that $\operatorname{Dem}_{1 g}[f]$ is true when at least one gateway demodulates $f$, depending on the value of $D e m_{1 d}$.
demodulator (see Theorem 2), and $P$ is also optimal for $D=2$ demodulators (see Theorem 3).

Theorem 1 (Approximation ratio of Algorithm $G$ for $D=1$ ). For all $\mathcal{F}, \frac{\Gamma(O P T(\mathcal{F}))}{\Gamma(G(\mathcal{F}))} \leq f_{\text {max }}+1$ for $D=1$, with $f_{\max }$ the maximum number of short frames that can fit in a single long frame. This bound is tight.

Proof. Let $\mathcal{F}$ be any set of frames, and let us compare the solution found by algorithm $G$ with the optimal solution found by $O P T$. Let us denote by $n_{1}$ the number of frames of $O P T$ that start while $G$ is not demodulating, and by $n_{2}$ the number of frames of $O P T$ that start while $G$ is demodulating. Let us further refine $n_{2}$ by having $n_{3}$ denote the number of frames of $O P T$ that start and finish while $G$ is demodulating the same frame, and $n_{4}$ denote the other frames of $n_{2}$. If we denote by $f_{\text {max }}$ the maximum number of short frames that can fit during a single long frame (with the assumption that two frames cannot start exactly at the same time), then for each frame of $G$, there can be at most $f_{\max }$ frames in $O P T$. In other words, $n_{3} \leq f_{\max } \Gamma(G(\mathcal{F}))$. Moreover, $n_{4} \leq \Gamma(G(\mathcal{F}))$ since each frame counted in $n_{4}$ corresponds to at most one starting frame in $G$. Finally, $n_{1}=0$, as it is not possible for $O P T$ to choose a frame while $G$ is not demodulating, since $G$ is a greedy algorithm. Thus, we have $\Gamma(O P T(\mathcal{F}))=n_{1}+$ $n_{2}=n_{1}+n_{3}+n_{4} \leq\left(f_{\max }+1\right) \Gamma(G(\mathcal{F}))$, and consequently $\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma(G(\mathcal{F}))} \leq f_{\max }+1$.

This bound is tight, and can be obtained with the following set of frames $\mathcal{F}$ : an SF12-frame starts first, and then $f_{\max }+1$ SF7-frames follow shortly after. Note that the $\left(f_{\max }+1\right)$ th SF7-frame has to start before the single SF12-frame finishes. Then, $G$ selects the single SF 12 -frame (resulting into $\Gamma(G(\mathcal{F}))=1$ ), and $O P T$ selects the $\left(f_{\max }+1\right)$ SF7-frames (resulting into $\left.\Gamma(O P T(\mathcal{F}))=f_{\text {max }}+1\right)$.

Note that $f_{\max }=71$ with the European LoRaWAN regional settings. Indeed, the payload duration of a short frame (payload of 10 bytes with SF7) is 28.68 ms , and the payload duration of a long frame (payload of 51 bytes with SF12) is 2064.38 ms , as shown on Table II. In both cases, we considered a coding rate of $4 / 5$ and a bandwidth of 125 kHz .

Table II
Pay $\left(\vec{Z} \phi_{\text {Ad }}\right.$ duration of LoRa Frames as a Function of the payload SIZE.
s.t. $\operatorname{Dem}[g, d, f] \leq R x[f, g], \forall g \in[1 ; M], d \in[1 ; D], f \in[1 ; F](4)$

| $\sum_{f_{2} f_{1}} \operatorname{Dem}\left[g, d, f_{2}\right] \operatorname{Col}\left[f_{1}, f_{2}\right] \leq\left(1-\operatorname{Dem}\left[g, d, f_{1}\right]\right) F$, | SF | ) |  | d duration |
| :---: | :---: | :---: | :---: | :---: |
| $f_{2} \neq f_{1}$ | SF | ) | (10 bytes) | (largest payload) |
| $\forall g \in[1 ; M], d \in[1 ; D], f_{1} \in[1 ; F]$ | (5) | 242 | 28.68 ms | 366.60 ms |
| $\operatorname{Dem}_{1 d}[g, f] \geq \operatorname{Dem}[g, d, f], \forall g \in[1 ; M], d \in[1 ; D], f \in[1 ;$ | $F_{9}^{8}(6)$ | 242 115 | $\begin{aligned} & 47.10 \mathrm{~ms} \\ & 94.20 \mathrm{~ms} \end{aligned}$ | $\begin{aligned} & 641.02 \mathrm{~ms} \\ & 565.24 \mathrm{~ms} \end{aligned}$ |
| $\operatorname{Dem}_{1 d}[g, f] \leq \sum_{d \in[1 ; D]} \operatorname{Dem}[g, d, f], \quad \forall g \in[1 ; M], f \in[1 ;$ | 14 11 | 51 51 | 188.42 ms 376.84 ms | 516.10 ms <br> 1114.12 ms |
| $\operatorname{Dem}_{1 g}[f] \geq \operatorname{Dem}_{1 d}[g, f], \forall g \in[1 ; M], f \in[1 ; F]$ | 188) | 51 | 589.82 ms | 2064.38 ms |

(9)

Theorem 2. Algorithm $P$ is optimal for $D=1$.

## V. Single gateway scenario

In this section, we consider a single gateway. We will show three results: algorithm $G$ has a large approximation ratio for $D=1$ demodulator (see Theorem 1), $P$ is optimal for $D=1$

Lemma 1. Let $\mathcal{F}$ be a set of frames and $A$ be an algorithm. Let $t$ be a time such that $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(A(\mathcal{F}))$, and let $t^{\prime}<t$. If $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F})) \leq \Gamma_{t^{\prime}, t}(A(\mathcal{F}))$, then $t^{\prime}$ is a time where $\Gamma_{t^{\prime}}(O P T(\mathcal{F}))>\Gamma_{t^{\prime}}(A(\mathcal{F}))$.

Proof. By assumption, we have $\Gamma_{t}(O P T(\mathcal{F})) \geq \Gamma_{t}(A(\mathcal{F}))+$ 1. We also have $\Gamma_{t}(O P T(\mathcal{F}))=\Gamma_{t^{\prime}}(O P T(\mathcal{F}))+$ $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F}))$ by definition of $\Gamma_{t^{\prime}, t}$. Then, we have $\Gamma_{t^{\prime}}(O P T(\mathcal{F}))=\Gamma_{t}(O P T(\mathcal{F}))-\Gamma_{t^{\prime}, t}(O P T(\mathcal{F})) \geq$ $\Gamma_{t}(O P T(\mathcal{F}))-\Gamma_{t^{\prime}, t}(A(\mathcal{F})) \geq \Gamma_{t}(A(\mathcal{F}))+1-\Gamma_{t^{\prime}, t}(A(\mathcal{F}))=$ $\Gamma_{t^{\prime}}(A(\mathcal{F}))+1$, with $t^{\prime}<t$. Thus, $\Gamma_{t^{\prime}}(O P T(\mathcal{F}))>$ $\Gamma_{t^{\prime}}(A(\mathcal{F}))$.

Proof of Theorem 2. Let us reason by contradiction, and assume that for a given $\mathcal{F}$, there is a first time $t$ such that $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(P(\mathcal{F}))$. This means that at $t, O P T$ finished demodulating a frame $x$ that was not chosen by $P$. Either $P$ was busy demodulating another frame $y$ when $x$ started, or $P$ also chose $x$ but dropped it later by preemption for another frame $y$. Note that since $P$ is a greedy algorithm, it is not possible for $P$ to have an available demodulator and not choose $x$. In both cases, $y$ is a frame ending before $t_{x}^{(\mathrm{e})}$. Let $t^{\prime}=t_{x}^{(\mathrm{s})}<t$. We have $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F}))=1$ since $D=1$ and $O P T$ demodulates only $x$ during $\left.] t^{\prime} ; t\right]$. We also have $\Gamma_{t^{\prime}, t}(A(\mathcal{F})) \geq 1$ since $P$ demodulates at least $y$ during $\left.] t^{\prime} ; t\right]$. Thus, Lemma 1 holds, which brings a contradiction since $t$ is the first time such that $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(P(\mathcal{F}))$. Consequently, there is no time $t$ where $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(P(\mathcal{F}))$, which means that $P$ is optimal.

Theorem 3. Algorithm $P$ is optimal for $D=2$.
Proof. Let $t$ be the first time where $\Gamma_{t}(O P T(\mathcal{F}))>$ $\Gamma_{t}(P(\mathcal{F}))$. Let $x$ be the last demodulated frame of $O P T$, and $x^{\prime}$ be the one before ${ }^{2}$.

If $x$ was chosen by $P$, then let $t^{\prime}=t_{x^{\prime}}^{(\mathrm{e})}$. In this case, $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F}))=1$ (corresponding to frame $x$, since $x^{\prime}$ is the last demodulated frame before $x$ ) and $\Gamma_{t^{\prime}, t}(P(\mathcal{F})) \geq 1$ (corresponding to $x$ and possibly others). Thus, Lemma 1 holds and we have a contradiction with the definition of $t$. Note that this requires $P$ to apply the best preemption, that is to preempt with the demodulator having the frame finishing the latest. Indeed, a badly-designed preemptive algorithm could select a very long frame $y$ on one demodulator (with $t_{y}^{(\mathrm{e})}>t$ ), choose $x$ on the second demodulator, then drop $x$ (instead of $y$ ) to preempt frame $x^{\prime}$, as shown on Figure 2. This badly-designed preemptive algorithm would not have finished demodulating $y$ at $t$, and thus would be such that $\Gamma_{t^{\prime}, t}(P(\mathcal{F}))=0$.


Figure 2. Comparison of a badly-designed preemptive algorithm (left) and $P$ (right) in the proof of Theorem 3. Frames are depicted with colored rectangles. Demodulated frames are highlighted with thick edges.

[^1]If $x$ was not chosen by $P$, let us consider the following cases.

- Case 1: $x^{\prime}$ was chosen by $P$ and $t_{x^{\prime}}^{(\mathrm{s})}<t_{x}^{(\mathrm{s})}$, as shown on the left of Figure 3. Let $t^{\prime}=t_{x}^{(\mathrm{s})}$. $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F})) \leq$ 2 (one frame is $x$, and the other is $x^{\prime}$ if and only if $t_{x}^{(\mathrm{s})}<t_{x^{\prime}}^{(\mathrm{e})}$ ). At $t^{\prime}$, both demodulators of $P$ were busy with frames finishing before $t_{x}^{(\mathrm{e})}$ (one of them possibly being $x^{\prime}$ ) since $x$ was not chosen, even by preemption. Thus, $\Gamma_{t^{\prime}, t}(P(\mathcal{F})) \geq 2$ (corresponding to these two frames). Thus, Lemma 1 holds.


Figure 3. Illustration of Case 1 (left) and Case 3 (right) of the proof of Theorem 3.

- Case 2: $x^{\prime}$ was chosen by $P$ and $t_{x}^{(\mathrm{s})}<t_{x^{\prime}}^{(\mathrm{s})}$, as shown on Figure 4. Let $t^{\prime}=t_{x}^{(\mathrm{s})}$. Since $x$ was not chosen by $P$ at $t^{\prime}$, the two demodulators of $P$ were busy demodulating frames whose ending time are before $t_{x}^{(\mathrm{e})}$ : let us denote them $y_{1}$ and $y_{2}$. Let us denote by $n$ the number of frames demodulated by $O P T$ between $t_{x}^{(\mathrm{s})}$ and $t_{x^{\prime}}^{(\mathrm{s})}$, and let us call them $x_{1}, \ldots, x_{n}$. For each frame $x_{i}$, either $x_{i}$ is also demodulated by $P$, or $P$ is busy demodulating a frame starting before $t_{x_{i}}^{(\mathrm{s})}$ (due to the greediness of $P$ ) and finishing before $t_{x_{i}}^{(\mathrm{e})}$ (due to the preemptiveness of $P)^{3}$. Thus, during $\left.] t^{\prime} ; t\right], P$ demodulates $y_{1}$ and $y_{2}$, as well as $x^{\prime}$ and at least $n-1$ other frames corresponding to each $x_{i}$ except $x_{1}$. Overall, $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F}))=n+2$ (including all the $x_{i}$ frames, as well as $x$ and $x^{\prime}$ ), and $\Gamma_{t^{\prime}, t}(P(\mathcal{F}))=(n-1)+3=n+2$ (including $x^{\prime}, y_{1}$ and $y_{2}$ ). Thus, Lemma 1 holds.


Figure 4. Illustration of Case 2 of the proof of Theorem 3.

- Case 3: $x^{\prime}$ was not chosen by $P$, as shown on the right side of Figure 3. Let $t^{\prime}=\max \left(t_{x}^{(\mathrm{s})}, t_{x^{\prime}}^{(\mathrm{s})}\right)$, $\Gamma_{t^{\prime}, t}(O P T(\mathcal{F})) \leq 2$. Since neither $x$ nor $x^{\prime}$ were chosen by $P$, it means that when $x$ and $x^{\prime}$ started, the two demodulators of $P$ were used. Thus, at $t^{\prime}$, the two demodulators are busy with frames finishing before the

[^2]end of the frame starting at $t^{\prime}$. Since $\max \left(t_{x}^{(\mathrm{e})}, t_{x^{\prime}}^{(\mathrm{e})}\right) \leq t$, we have $\Gamma_{t^{\prime}, t}(P(\mathcal{F})) \geq 2$, and Lemma 1 holds.
In all cases, we have a contradiction with the definition of $t$ as the earliest time where $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(P(\mathcal{F}))$. In other words, there is no such time $t$, and thus $P$ is optimal.

## VI. Multi-Gateway scenario

In this section, we consider $M \geq 2$ gateways. We will show three results: the non-collaborative algorithm $P$ has an approximation ratio of 2 for $D=1$ demodulator (see Theorem 4), the collaborative algorithm $P^{C}$ also has an approximation ratio of 2 for $D=1$ demodulator (see Theorem 5), and the collaborative algorithm $P^{S}$ has an approximation ratio of 1.5 for $D=1$ demodulator (see Theorem 6).

Theorem 4 (Approximation ratio of algorithm $P$ for $M=2$ and $D=1$ ). For all $\mathcal{F}, \frac{\Gamma(O P T(\mathcal{F}))}{\Gamma(P(\mathcal{F}))} \leq 2$ for $M=2$ and $D=1$. This bound is tight.

Proof. Let $\mathcal{F}$ be an arbitrary set of frames. From $P(\mathcal{F})$, we will build the solution $\operatorname{OPT}(\mathcal{F})$ with the largest gain, and show that $\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma(P(\mathcal{F}))} \leq 2$. Let us divide the time into intervals depending on $P(\mathcal{F})$ : a new interval $\delta_{i}$ starts each time $P$ finishes demodulating a new frame on any gateway, for $i \in[1 ; F]$. Let $t_{\delta_{i}}^{(\mathrm{s})}$ be the starting time of $\delta_{i}$, and $t_{\delta_{i}}^{(\mathrm{e})}$ the ending time of $\delta_{i}$. Let us denote by $\Gamma_{\delta_{i}}(A(\mathcal{F}))$ the number of frames that finish their demodulation within $\left.] t_{\delta_{i}}^{(\mathrm{s})} ; t_{\delta_{i}}^{(\mathrm{e})}\right]$ for algorithm $A$. By definition of $\delta_{i}$, we have $\Gamma_{\delta_{i}}(P(\mathcal{F}))=1$ (for all gateways), for every $\delta_{i}$. This equality to one is obtained thanks to the assumption that two different frames cannot start or finish at exactly the same time, as stated in Section III.

Let us introduce the following three notations concerning $O P T(\mathcal{F})$ :

1) $n_{\delta_{i}}^{\left(\mathrm{s}_{\text {bef }}\right)}$ is the number of frames that started strictly before $t_{\delta_{i}}^{(\mathrm{s})}$ and are demodulated within $\left.] t_{\delta_{i}}^{(\mathrm{s})} ; t_{\delta_{i}}^{(\mathrm{e})}\right]$,
2) $n_{\delta_{i}}^{\left(\mathrm{e}_{\mathrm{dur}}\right)}$ is the number of frames that started and ended within $\left.] t_{\delta_{i}}^{(\mathrm{s})} ; t_{\delta_{i}}^{(\mathrm{e})}\right]$,
3) $n_{\delta_{i}}^{\left(\mathrm{e}_{\mathrm{aft}}\right)}$ is the number of frames that started before or at $t_{\delta_{i}}^{(\mathrm{e})}$ and that will end strictly after $t_{\delta_{i}}^{(\mathrm{e})}$.
Let us consider any interval $\delta_{i}$, for $i \in[1 ; F]$. For each of the following cases, let us show that $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq$ $2 \Gamma_{\delta_{i}}(P(\mathcal{F}))=2$.

- Case 1: both gateways demodulate the same frame $f$. This means that all other frames $f^{\prime}$ starting during $\delta_{i}$ are such that $t_{f^{\prime}}^{(\mathrm{e})}>t_{f}^{(\mathrm{e})}$. If $f$ is chosen by $O P T$, we have $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}=1$ and $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq 1$. Otherwise, we have $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}=0$ and $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq 2$.
- Case 2: the frame $f$ finishing in $P$ is demodulated by $G W_{1}$, but not by $G W_{2}$. If $O P T$ chooses $f$ on $G W_{1}$, then $n_{\delta_{i}}^{\left(\mathrm{e}_{\mathrm{dur}}\right)}=1$ and $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq 1$ for $G W_{2}$. If $O P T$ does not choose $f$ on $G W_{1}$, then there can be another frame $f^{\prime}$ starting on $G W_{1}$. For this frame, $t_{f^{\prime}}^{(\mathrm{e})}>t_{f}^{(\mathrm{e})}$ otherwise $P$ would have chosen $f^{\prime}$ by preemption. There might also be another frame $f^{\prime \prime}$ starting on $G W_{2}$, but then $t_{f^{\prime \prime}}^{(\mathrm{e})}>t_{f}^{(\mathrm{e})}$, otherwise either $P$ would have chosen $f^{\prime \prime}$, or $P$ would
have dropped its potential frame on $G W_{2}$ for this one. Thus, $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}=0$ and $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq 2$.
- Case 3: the frame $f$ finishing in $P$ is demodulated by $G W_{2}$, but not by $G W_{1}$. This is similar to Case 2.
- Case 4: no more frames are demodulated during $\delta_{i}$, which occurs only for $i=n$. This means that no frame can start for $O P T$ from $t_{\delta_{n}}^{(\mathrm{s})}$. Thus, $n_{\delta_{n}}^{\left(\mathrm{e}_{\text {dur }}\right)}=0$ and $n_{\delta_{n}}^{\left(\mathrm{e}_{\text {aft }}\right)}=0$.
In all cases, we have $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq 2=2 \Gamma_{\delta_{i}}(P(\mathcal{F}))$.
Finally, we have:

$$
\begin{aligned}
\Gamma(O P T(\mathcal{F})) & =\sum_{\delta_{i}, i \in[1 ; F]} \Gamma_{\delta_{i}}(O P T(\mathcal{F})) \\
& =\sum_{\delta_{i}, i \in[1 ; F]}\left(n_{\delta_{i}}^{\left(\mathrm{s}_{\text {bef }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}\right) \\
& =\sum_{\delta_{i}, i \in[2 ; F]} n_{\delta_{i}}^{\left(\mathrm{s}_{\text {bef }}\right)}+\sum_{\delta_{i}, i \in[1 ; F]} n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)} \\
& \leq \sum_{\delta_{i}, i \in[1 ; F-1]} n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)}+\sum_{\delta_{i}, i \in[1 ; F]} n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)} \\
& \leq \sum_{\delta_{i}, i \in[1 ; F]}\left(n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)}\right) \\
& \leq \sum_{\delta_{i}, i \in[1 ; F]} 2 \Gamma_{\delta_{i}}(P(\mathcal{F}))=2 \Gamma(P(\mathcal{F})),
\end{aligned}
$$

since $n_{\delta_{1}}^{(\text {Sbef })}=0, n_{\delta_{i+1}}^{\left(\mathrm{s}_{\text {bef }}\right)} \leq n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)}$ and $n_{\delta_{F}}^{\left(\mathrm{e}_{\text {aft }}\right)}=0$. This completes the proof.

This bound is tight, and can be obtained with the following set $\mathcal{F}$. A frame $x$ is received by both gateways, and another frame $y$ such that $t_{x}^{(\mathrm{s})}<t_{y}^{(\mathrm{s})}<t_{x}^{(\mathrm{e})}<t_{y}^{(\mathrm{e})}$ is received by $G W_{2}$ only. $P$ chooses $x$ on both gateways, and does not choose $y$ (as $y$ does not preempt $x$ ). In the meanwhile, $O P T$ chooses both $x$ on $G W_{1}$ and $y$ on $G W_{2}$.

Theorem 5 (Approximation ratio of algorithm $P^{C}$ for $M=2$ and $D=1$ ). For all $\mathcal{F}, \frac{\Gamma(O P T(\mathcal{F}))}{\Gamma\left(P^{C}(\mathcal{F})\right)} \leq 2$ for $M=2$ and $D=1$. This bound is tight.

Proof. The proof of the approximation ratio is the same as the proof of Theorem 4, without the possibility of Case 1.

This bound is tight, and can be obtained with the following set $\mathcal{F}$. A frame $x$ is received by both gateways, and another frame $y$ such that $t_{x}^{(\mathrm{s})}<t_{y}^{(\mathrm{s})}<t_{x}^{(\mathrm{e})}<t_{y}^{(\mathrm{e})}$ is received by $G W_{2}$ only. $P^{C}$ sees $x$ on both gateways, and decides to demodulate $x$ on $G W_{2}$. Then, $P^{C}$ can not choose $y$, as $y$ is not received by $G W_{1}$ and $y$ does not preempt $x$ on $G W_{2}$. In the meanwhile, $O P T$ chooses both $x$ and $y$, on $G W_{1}$ and $G W_{2}$ respectively.

Theorem 6 (Approximation ratio of algorithm $P^{S}$ for $M=2$ and $D=1$ ). For all $\mathcal{F}, \frac{\Gamma(O P T(\mathcal{F}))}{\Gamma\left(P^{S}(\mathcal{F})\right)} \leq 1.5$ for $M=2$ and $D=1$.

Lemma 2. Let $x_{1}, x_{2}, \ldots, x_{n}$ and $y_{1}, y_{2}, \ldots, y_{n}$ be two nonempty sequences of $n$ strictly positive integers. Let $X_{1}^{(n)}=$ $\sum_{i \in[1 ; n]}\left(x_{i}+1\right), X_{2}^{(n)}=\sum_{i \in[1 ; n]}\left(x_{i}+2\right)$ and $Y^{(n)}=$
$\sum_{i \in[1 ; n]} y_{i}$. $\sum_{i \in[1 ; n]} y_{i}$.

Then, $\frac{X_{2}^{(n)}+Y^{(n)}}{X_{1}^{(n)}+Y^{(n)}} \leq \frac{X_{2}^{(n)}}{X_{1}^{(n)}}$.

Proof.

- Case 2: the last demodulated frame $f$ finishing in $P^{S}$ is $\frac{X_{2}^{(n)}+Y^{(n)}}{X_{1}^{(n)}+Y^{(n)}}-\frac{X_{2}^{(n)}}{X_{1}^{(n)}}=\frac{\left(X_{2}^{(n)}+Y^{(n)}\right) X_{1}^{(n)}}{\left(X_{1}^{(n)}+Y^{(n)}\right) X_{1}^{(n)}}-\frac{X_{2}^{(n)}\left(X_{1}^{(n)}+Y_{\text {the }}^{(n)} \text { the construction of } \delta_{i}, \text { this means that } G W_{1} \text { demodulated }\right.}{X_{1}^{(n)}\left(X_{1}^{(n)}+Y^{(n)}\right) \geq 1 \text { frames with } P^{S} \text {, denoted } a_{1}, a_{2}, \ldots, a_{x} \text {, while }}$ $=\frac{Y^{(n)}\left(X_{1}^{(n)}-X_{2}^{(n)}\right)}{X_{1}^{(n)}\left(X_{1}^{(n)}+Y^{(n)}\right)}$ $\leq 0$.
Indeed, $X_{1}^{(n)}\left(X_{1}^{(n)}+Y^{(n)}\right)>0, Y^{(n)} \geq 0$ and $X_{1}^{(n)} \leq X_{2}^{(n)}$.

Lemma 3. Let $x_{1}, x_{2}, \ldots, x_{n}$ be a non-empty sequence of $n$ strictly positive integers. Let $X_{1}^{(n)}=\sum_{i \in[1 ; n]}\left(x_{i}+1\right)$ and $X_{2}^{(n)}=\sum_{i \in[1 ; n]}\left(x_{i}+2\right)$. Then, $\frac{X_{2}^{(n)}}{X_{1}^{(n)}} \leq \frac{3}{2}$.
Proof. Let us prove this lemma by recursion on $n$. When $n=$ 1, we have:

$$
\frac{X_{2}^{(1)}}{X_{1}^{(1)}}-\frac{3}{2}=\frac{x_{1}+2}{x_{1}+1}-\frac{3}{2}=\frac{1-x_{1}}{2\left(x_{1}+1\right)} \leq 0
$$

Indeed, $x_{1} \geq 1$ since each integer of the sequence is strictly positive.
Let us now assume that the property is true for $n-1 \geq 1$, that is $\frac{X_{2}^{(n-1)}}{X_{1}^{(n-1)}} \leq \frac{3}{2}$. Let us show that the property is true for $n$ too. We have:

$$
\begin{aligned}
& \begin{aligned}
\frac{X_{2}^{(n)}}{X_{1}^{(n)}}-\frac{3}{2} & =\frac{2 X_{2}^{(n)}-3 X_{1}^{(n)}}{2 X_{1}^{(n)}} \\
& =\frac{2\left(X_{2}^{(n-1)}+x_{n}+2\right)-3\left(X_{1}^{(n-1)}+x_{n}+1\right)}{2 X_{1}^{(n)}} \\
& =\frac{1-x_{n}+2 X_{2}^{(n-1)}-3 X_{1}^{(n-1)}}{2 X_{1}^{(n)}} \leq \frac{1-x_{n}}{2 X_{1}^{(n)}} \leq 0 .
\end{aligned} \\
& \text { This comes from the fact that } 2 X_{2}^{(n-1)}-3 X_{1}^{(n-1)} \leq 0, \\
& \text { according to the recursion hypothesis, and that } x_{n} \geq 1 .
\end{aligned}
$$

Proof of Theorem 6. Let $\mathcal{F}$ be an arbitrary set of frames. From $P^{S}(\mathcal{F})$, we will build the solution $\operatorname{OPT}(\mathcal{F})$ with the largest gain, and show that $\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma\left(P^{S}(\mathcal{F})\right)} \leq 3 / 2$. Let us divide the time into new intervals depending on $P^{S}(\mathcal{F})$ : a new interval starts as soon as both gateways $G W_{1}$ and $G W_{2}$ demodulated at least a frame each. Then, for any time interval $\delta_{i}$, we use the same notations as in Theorem 4 for $t_{\delta_{i}}^{(\mathrm{s})}, t_{\delta_{i}}^{(\mathrm{e})}, \Gamma_{\delta_{i}}(A(\mathcal{F}))$, $n_{\delta_{i}}^{\left(\mathrm{s}_{\text {bef }}\right)}, n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}$ and $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)}$. Note that, unlike for intervals defined for $P$, it is here possible to have $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)>1$ since both gateways should decode at least one frame each during $\delta_{i}$.

Let us consider any interval $\delta_{i}$, for $i \in[1 ; n]$. For each of the following cases, we will show that $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+$ $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)$ in some cases, that $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq$ $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)+1$ in the other cases, and that we always have $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right) \geq 1$.

- Case 1: both gateways demodulate the same frame $f$. This means that no other frame started during $\delta_{i}$, otherwise one of the gateway would have dropped $f$ with $P^{S}$. Thus, $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)} \leq 1, n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)}=0$ and $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)=1$.
$G W_{2}$ demodulated only one, denoted $f$.
- On $G W_{1}$, there can be no frame $f^{\prime}$ in $O P T$ such that $t_{f^{\prime}}^{(\mathrm{s})}<t_{a_{j}}^{(\mathrm{e})}$ and $t_{f^{\prime}}^{(\mathrm{e})}<t_{a_{j+1}}^{\mathrm{s})}$, nor such that $t_{a_{j}}^{(\mathrm{s})}<$ $t_{f^{\prime}}^{(\mathrm{s})}$ and $t_{f^{\prime}}^{(\mathrm{e})}<t_{a_{j}}^{(\mathrm{e})}$, for all $j \in[1 ; x]$. However, for each $j \in[1 ; x], O P T$ can choose either $a_{j}$ or a frame starting after $t_{a_{j}}^{(\mathrm{s})}$ and ending after $t_{a_{j}}^{(\mathrm{e})}$. Thus, $n_{\delta_{i}, G W_{1}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}, G W_{1}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq x$.
- On $G W_{2}$, there can be no frame $f^{\prime}$ such that $t_{f}^{(\mathrm{s})}<t_{f^{\prime}}^{(\mathrm{s})}$ and $t_{f^{\prime}}^{(\mathrm{e})}<t_{f}^{(\mathrm{e})}$, otherwise $P^{S}$ would have dropped $f$ for this $f^{\prime} . O P T$ can choose either to demodulate $f$ on $G W_{2}$, or a frame starting after $t_{f}^{(\mathrm{s})}$ and finishing after $t_{f}^{(\mathrm{e})} . O P T$ can also demodulate a frame $a_{j}$ of $P^{S}$, if this frame was received by both $G W_{1}$ and $G W_{2}$, provided that this frame was dropped by $P^{S}$ on $G W_{2}$ in order to start frame $f$. Thus, $n_{\delta_{i}, G W_{2}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}, G W_{2}}^{\left(\mathrm{e}_{\text {att }}\right)} \leq 2$.
- Overall, when considering both $G W_{1}$ and $G W_{2}$, we have: $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq x+2$, with $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)=$ $x+1$ and $x \geq 1$.
- Case 3: the last demodulated frame $f$ finishing in $P^{S}$ is demodulated by $G W_{1}$, but not by $G W_{2}$. This is similar to Case 2.
- Case 4: no more frame is demodulated on $G W_{1}$ during $\delta_{i}$, which occurs only when $i=n$. This means that no frame can start for $O P T$ from $t_{\delta_{n}}^{(\mathrm{s})}$ on $G W_{1}$. In this case, $P^{S}$ works on the single gateway $G W_{2}$, and is thus optimal according to Theorem 2. Thus, $\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)=$ $\Gamma_{\delta_{i}}(O P T(\mathcal{F}))$.
- Case 5: no more frame is demodulated on $G W_{2}$ during $\delta_{i}$, which occurs only when $i=n$. This is similar to Case 4.

Let $\mathcal{C}_{1,4,5}$ be the indexes such that $\delta_{i}$ is in Case 1 , Case 4 or Case 5 , and let $\mathcal{C}_{2,3}$ be the indexes such that $\delta_{i}$ is in Case 2 or Case 3. We have

$$
\begin{aligned}
\Gamma\left(P^{S}(\mathcal{F})\right) & =\sum_{i \in \mathcal{C}_{1,4,5}} \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)+\sum_{i \in \mathcal{C}_{2,3}} \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right) \\
& =Y^{(n)}+\sum_{i \in \mathcal{C}_{2,3}}\left(x_{i}+1\right)
\end{aligned}
$$

where we define $Y^{(n)}=\sum_{i \in \mathcal{C}_{1,4,5}} \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)$, and it was shown that $x_{i}=\Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)-1$. With the same argument as in Theorem 4, we also have

$$
\Gamma(O P T(\mathcal{F}))=\sum_{i \in[1 ; n]}\left(n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {eft }}\right)}\right)
$$

From the fact that $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)$ for Cases 1,4 and 5 , and that $n_{\delta_{i}}^{\left(\mathrm{e}_{\text {dur }}\right)}+n_{\delta_{i}}^{\left(\mathrm{e}_{\text {aft }}\right)} \leq \Gamma_{\delta_{i}}\left(P^{S}(\mathcal{F})\right)+1$ for

Cases 2 and 3, we have:
$\Gamma(O P T(\mathcal{F}))=\sum_{i \in \mathcal{\mathcal { C } _ { 1 , 4 , 5 }}} \Gamma_{\delta_{i}}(O P T(\mathcal{F}))+\sum_{i \in \mathcal{C}_{2,3}} \Gamma_{\delta_{i}}(O P T(\mathcal{F}))$

$$
\leq Y^{(n)}+\sum_{i \in \mathcal{C}_{2,3}}\left(x_{i}+2\right)
$$

Then, setting $X_{1}^{(n)}=\sum_{i \in \mathcal{C}_{2,3}}\left(x_{i}+1\right)$ and $X_{2}^{(n)}=$ $\sum_{i \in \mathcal{C}_{2,3}}\left(x_{i}+2\right)$, we get

$$
\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma\left(P^{S}(\mathcal{F})\right)} \leq \frac{Y^{(n)}+X_{2}^{(n)}}{Y^{(n)}+X_{1}^{(n)}} \leq \frac{X_{2}^{(n)}}{X_{1}^{(n)}}
$$

Finally, we obtain $\frac{\Gamma(O P T(\mathcal{F}))}{\Gamma\left(P^{S}(\mathcal{F})\right)} \leq \frac{3}{2}$, according to Lemma 3 .

## VII. Numerical Evaluations

This section describes our numerical evaluations.

## A. Simulation settings

We consider the following six configurations corresponding to various pairs $(M, D)$ :

- one gateway and one demodulator (condition of application of Theorem 1 and Theorem 2),
- one gateway and two demodulators per gateway (for Theorem 3),
- one gateway and three demodulators per gateway,
- two gateways and one demodulator per gateway (for Theorem 4, Theorem 5 and Theorem 6),
- two gateways and three demodulators per gateway,
- three gateways and three demodulators per gateway.

For each configuration, we generated a given number of frames, either equal to $100 \times M \times D$ (in order to have a charge proportional to the overall demodulation capacity) or to 100 (in order to evaluate the impact of adding more gateways, or of more demodulators per gateway). Each frame has a random payload size varying from 10 to 51 bytes (where 51 bytes is the maximum payload size in LoRaWAN) and corresponds to a random SF from 7 to 12, as shown in Table II [13], [14]. We considered a bandwidth of 125 kHz and a coding rate of $4 / 5$. The starting time of each frame is chosen randomly. When $M \geq 2$, the frame is received by at least one gateway chosen at random, and by every other gateway with a probability of $30 \%$. Each simulation lasts for 100 seconds. For each repetition, we use the MILP solver GLPK [15] to solve Problem (3)-(9), in order to obtain the optimal solution (or an interval in which the optimal solution lies). GLPK is used with a timeout of 60 s , on a computer with an $\operatorname{Intel}(\mathrm{R})$ Core(TM) i7-4790 CPU at 3.60 GHz . When the solver does not reach an optimal solution, it still outputs an interval $[L B ; U B]$ (hereafter referred to as the optimality interval), where $L B$ is a lower bound, namely the gain of a good solution (possibly sub-optimal), and $U B$ is an upper bound of the optimal solution (possibly unreachable). We generated 100 repetitions in order to obtain average values.

## B. Simulation results

Figure 5 shows the percentage of demodulated frames with a traffic load proportional to the demodulation capacity (and computed as $100 \times M \times D$ frames). Overall, the algorithms can always be ranked by performance as follows : $G<P<$ $P^{C}<P^{S}$.

For $M=1, P, P^{C}$ and $P^{S}$ yield the same results, as expected. Moreover, it can be seen that they achieve an excellent percentage of demodulated frames. They appear to be optimal for $D=1$ (which is expected from Theorem 2), and are within the optimality interval for $D=2$ (which is expected from Theorem 3), and $D=3$. Please note that more simulations results are provided for $M=1$ and $D=2$ in the sequel.
For $M=2$ and $D=1$, the algorithms are clearly suboptimal since they are below the $L B$ level, and decode from about $70 \%$ to about $75 \%$ of the frames, while $L B$ is slightly above $80 \%$. This shows that while the approximation ratio of these algorithms is between 1.5 and 2, they still perform well. For the two configurations with multiple gateways and $D=3$, the solver was not able to obtain a tight interval, and was even rarely able to find a solution before the timeout of 60 s expired. However, all algorithms are able to demodulate a very large number of frames, namely around $85 \%$ or more.


Figure 5. Performance of all algorithms with a traffic load proportional to the demodulation capacity.

Figure 6 shows the performance of all algorithms, with a constant traffic load, in two cases:

- with $M=1$ gateway and a varying number of demodulators (on the left),
- with a varying number of gateways and $D=3$ demodulators (on the right).
Obviously, as the demodulation capacity increases (either when $M$ or $D$ increases), the performance of all algorithms increases. On the left side of Fig. 5 (that is, when $M=1$ ), $P$, $P^{C}$ and $P^{S}$ have the same performance, and are all within the optimality interval. On the right side of Fig. 5 (that is, when $D=3$ ), the solver was not able to obtain a tight interval.

Moreover, it can be seen that algorithm $P^{S}$ ensures the best performance among all algorithms, in all cases.


Figure 6. Performance of all algorithms, with a varying number of demodulators (left) or with a varying number of gateways (right), with a constant traffic load.

Note that we also explicitly tested Theorem 3 by running many repetitions comparing $O P T$ and $P$ with $M=1$ and $D=2$. We used relatively small instances of 20 frames and a simulation time of 20 seconds in order to obtain optimal solutions faster, and we set a longer timeout of 10 minutes for the solver. Repetitions for which the optimal solution was not found before the timeout were discarded, until we obtained 1000 repetitions with the optimal solution. For each repetition, we verified that the optimal gain was equal to the gain of $P$. The average gain was $80.08 \%$ for both algorithms.

## VIII. Conclusion

Given their limited number of demodulators, when LoRaWAN gateways detect more than eight incoming frames, they have to decide which ones to demodulate and which ones to discard. We showed that the default behavior of LoRaWAN gateways, which is to demodulate the earliest frames in a FIFO manner, is far from optimal as it is possible to demodulate up to 72 times more frames (in the worst case). In the single gateway case, the simple preemptive algorithm $P$ is optimal for one and two demodulators, and reaches good performance with a larger number of demodulators. In the multi-gateway case, both the non-collaborative algorithm $P$ and the simplecollaborative algorithm $P^{C}$ yield an approximation ratio of 2 (for two gateways and one demodulator per gateway), while the smart-collaborative algorithm $P^{S}$ has a lower approximation ratio of 1.5 (under the same conditions). We believe that designing simple collaborative strategies for demodulator allocation can help to enhance LoRaWAN throughput, with minimal additional overhead in order to implement this collaboration framework. The optimality or approximation ratios of the proposed collaborative algorithms have not only been analyzed theoretically, but also validated through extensive simulations. The proposed approach thus appears as an effective solution towards the future challenges of massive IoT
connectivity, by smartly leveraging the increasing density of LoRaWAN gateways.

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[^0]:    ${ }^{1}$ We believe this is possible by identifying the channel, spreading factor, preamble detection time, and Carrier Frequency Offset (CFO). Notably, each gateway can estimate the CFO of each node through a training period, and use this CFO later to help determine whether two preambles received by two gateways correspond to the same frame. A more detailed discussion on frame identification is in [7].

[^1]:    ${ }^{2}$ Note that if $\Gamma_{t}(O P T(\mathcal{F}))>\Gamma_{t}(P(\mathcal{F}))$, then there are at least two frames demodulated by $O P T$. If there was only one frame demodulated by $O P T, P$ would have chosen this short frame (possibly by preemption).

[^2]:    ${ }^{3}$ For $x_{1}$, this frame could be $y_{1}$ or $y_{2}$.

