

Impact of surface passivation of III-V elements on Si (001) substrate based on absolute surface and barrier energy calculations

Sreejith Pallikkara Chandrasekharan, Divishth Gutpa, Laurent Pedesseau, Charles Cornet

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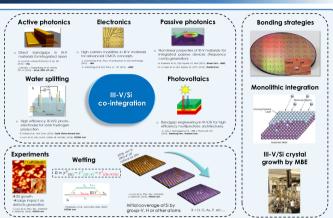


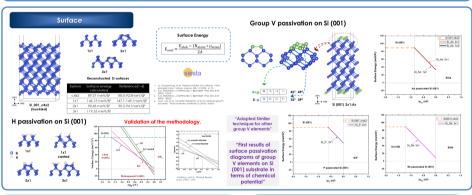
Sreejith Pallikkara Chandrasekharan^{1,*}, Divishth Gupta¹, Laurent Pedesseau¹, Charles Cornet¹

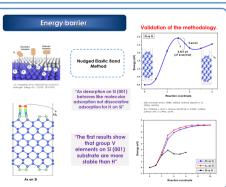
(1) Univ Rennes, INSA Rennes, CNRS, Institut FOTON-UMR 6082, F-35000, Rennes, France

Introduction

III-V semiconductors on (001) Si substrates is a long-standing challenge, which has driven many researches for the past 40 years. A large portfolio of passive photonic devices and their mature fabrication technology are available on the Si platform. This gives greater flexibility to design photonics integrated circuits (PICs) compared to the pure technologies and opens a number of possibilities for e.g. lasers, photovoltaic energy production, or energy storage via solar hydrogen production.







Conclusions

- Absolute surface energies of Si (001) surfaces with mono or sub-atomic layers of H and group V elements.
- Energy barrier calculation to study the desorption of different passivating elements from the Si surface.

Prospects

- The surface and interface energies calculations for III-V/Si.
- The influence of dislocations and antiphase boundaries.

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