

A framework for modeling and analyzing cyber-physical systems using statistical model checking

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- A Framework for Modeling and Analyzing
- Cyber-Physical Systems using Statistical Model
- Checking
- Abdel-Latif Alshalalfah
- Otmane Ait Mohamed · Samir Ouchani

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 Abstract The trustworthiness of a cyber-physical system is essential for it to be qualified for utilization in most real-life deployments. This is espe- cially critical for systems that deal with precious human lives. Although these safety-critical systems can be investigated using both experimental testing and model-based verification, accurate models have the potential to permit risk- free mimicking of the system behavior even in the most extreme scenarios. To overcome the CPS modelling and design challenges, the INCOSE/OMG standard System Modeling Language (SysML) is utilized in this work to ac- curately specify cyber-physical systems. For that, a bounded set of SysML constructs are defined to precisely capture the semantics of continuous-time and discrete-time system behaviors. Then, the SysML constructs are substi- tuted by developing a new algebra, called Enhanced Activity Calculus (EAC). So, EAC helps construct equivalent priced timed automata models by develop- ing a new systematic procedure to correctly translate the SysML models into the statistical model checking tool UPPAAL-SMC inputs. The latter checks whether the system is correct and safe or not. Moreover, the soundness of the developed translation mechanism has been proved and its effectiveness has been shown on a real use case, namely the artificial pancreas.

- Keywords System Modeling Language · Enhanced Activity Calculus ·
- Cyber-Physical Systems · Model Transformation · Model-Based Verification ·
- Safety-Critical · Statistical Model Checking · Priced Timed Automata

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Nomenclature

- Cyber-Physical Systems
- ³¹ EAC Enhanced Activity Calculus
- HA Hybrid Automata
- MITL Metric Interval Temporal Logic
- 34 ODE Ordinary-Differential Equations
- ODESCD ODE of SysML Constraint Diagram
- 36 PID Proportional-Integral-Derivative
- 37 PTA Priced Timed Automata
- SMC Statistical Model Checking
- SysML Systems Modeling Language

1 Introduction

 Whether human-operated or autonomous, embedded systems are designed to ⁴² improve the quality of life for people. From embedded computing to distributed systems, Cyber-Physical Systems (CPS) refer to computing systems that in- teract with control and management objects [53]. As technology advances, CPS is being used in a wide range of applications [30]. With the reduction in size and cost of hardware, along with accelerating innovation and advance- ment in sensor and computational technologies, CPS has been able to spread to all types of applications. Through horizontal expansion, CPS has gained popularity in all types of application. Also, CPS flourished vertically to find a foot in more complex and dependable applications From daily applications, the various success stories have encouraged designers to develop CPS for au- tonomous control compared to the early systems which required some degree of human interaction [23, 41]. Nowadays, wireless body area networks are uti-⁵⁴ lized to connect devices that observe the status of the physiological dynamics [16]. As a result, health conditions can be monitored and treated in a timely manner. Patients with chronic diseases will particularly benefit from this. For example, with around half a billion diabetes worldwide [42], an automatic glu- cose controller is necessary for them to live a normal life while still avoiding the health complications related to their situation. In order to get approval certificates from the appropriate authorization entities, these systems must prove their safety and robustness under all sce- narios [11]. However, for real-life deployments, only qualified systems must meet these safety requirements. From the first prototype to the final fabri-

 cated product, verifying the safety of CPS is a vital step in the development process. The system-level analysis provides feedback early in the design pro- cess, and by identifying safety issues early, time and resources are not wasted [29]. Additionally, the system-level analysis helps understand CPS limitations

and define the requirements of CPS components for safe operation. Further-

more, CPS can be verified under extreme scenarios that would be impossible

 to conduct in real life without taking extraordinary risks by using appropriate realistic models.

 Analyzing systems at the system level is either accomplished through sim-ulation testing or through formal methods. In the former approach, specific

 input scenarios can be used to evaluate CPS behavior. Yet, it does not give confidence on the state space coverage. On the other hand, formal methods such as model checking [10] provide exhaustive coverage for the whole state π space. Unfortunately, formal techniques do not scale well for realistic hybrid systems and suffer from the infamous state space explosion problem [21].

 As a compromise between these two approaches, Statistical Model Check-⁸⁰ ing (SMC) can be used for verification. Although it does not provide exhaustive 81 coverage for the state space, SMC can be used to introduce statistical guaran-⁸² tees for safety properties with feasible computational resources. In a nutshell, the following are the main contributions of this work.

⁸⁴ – Proposing a novel systematic procedure to capture the semantics of SysML-

 based diagrams and to construct its equivalent PTA models for SMC anal-ysis.

– The effectiveness of the proposed framework to analyze a medical CPS is

demonstrated on an artificial pancreas case study. In particular, the safety

 of the system is verified using SMC to evaluate the ability of three control configurations to mitigate message errors.

 Below is an outline of the remainder of the article. The literature review is presented in Section 2, and then Section 3 demonstrates, through an artificial pancreas example, the SysML graphical and textual modeling. Afterwards, Section 4 introduces the new proposed automatic construction of equivalent Priced Timed Automata (PTA) models and proves the soundness of the de- veloped approach. Section 5 illustrates the experiments conducted for model validation and safety verification procedure by an example experiment, and section 6 concludes the article.

2 Literature Review

With the growing demand for CPS applications, several research works have

 investigated the verification and safety analysis problems related generally to CPS. Based on our surveyed initiatives, we have identified two main categories:

Formal verification and Simulation based approaches.

2.1 Simulation based approaches

Even before the advent of modern computer systems, the term Simulation is

known as the process of designing a model of a real system to conduct exper-

iments [52]. These experiments aim at understanding the system's behavior

or evaluating a strategy associated with the system. Simulation software tools

 Connected and autonomous vehicles and their impact on road safety are discussed in [47]. Initially, the simulation software VISSIM is utilized to study a test-bed that mimics a three-lane motorway with traffic statistics measured from a real one in England. A lateral and longitudinal control algorithm is then tested for its ability to reduce traffic conflicts at different market penetration rates.

 From a healthcare perspective, a falsification approach is presented in [48, 9] to simulate and verify the artificial pancreas controller in a simulation environment. The S-Taliro tool which applies falsification simulations termi- nates with either finding a safety violation or failure to find, without the explicit guarantee that such one does not exist. Instead, the tool uses robust- ness metric to predict the distance between simulation outcomes and safety margins.

2.2 Formal based approaches

Unlike the numerical simulation approaches which mimic the behavior of real

systems, formal methods apply analytical reasoning to derive mathematically-

 proven properties that characterize the system behavior. These characteristics are not always attainable, but when achieved they provide guaranteed out-

comes which is an asset that helps verify safety-critical systems.

 In [28], piece-wise affine hybrid automata was used to analyze the wind turbine dynamics in SpaceEx verification platform [20]. Even though Kekatos et al. reduced some blocks for better scalability, the resulting model contained around 16 million locations, which would hinder the ability to analyze more elaborate systems. However, classical hybrid automata (HA) tools and method-ologies suffer from this limitation [49].

 The problem of formally analyzing a swarm of robots is handled by Schupp et al. [50]. The cooperative decentralized robots are modeled as a hybrid sys-¹⁵⁹ tem and investigated by *flowpipe* analysis where the sets of reachable states are iteratively over-approximated [19]. Although the work in [50] deals with a simple model of distributed synchronization, it still causes some scalabil- ity challenges that are partially encountered by compositional analysis and optimized transition emulation.

 Using a combination of simulations and formal analysis, [46] examines patient-controlled analgesia's safety. So, to analyze the resulting CPS, its de- tailed behavior is modeled in Simulink. Then, to qualify the CPS for model checking, the continuous dynamics are abstracted away from the system model and then replaced by simple timing constraints with the target to be analyzed in UPPAAL model checker [7]. Additionally, UPPAAL is also used in [26] to verify control algorithms in a dual chamber implantable pacemaker. Mean- while, a timed automata representation of the heart and the pacemaker are used to specify the ability of the algorithms to avoid unsafe regions of the state space. The proposed approach covers the whole state space, yet only the state space that is modeled. Thus, this excludes certain control and physiological behaviors that are beyond the expressive power of the modeling language and the computational feasibility of the verification technique. In fact, these be- haviors can be skipped in some systems but are essential to correctly analyze hybrid systems with continuous-time variables.

2.3 Statistical model checking based approach

 SMC consists of observing a number of simulation runs or system executions and using statistical methods to reason about formal properties [35].

 After some preliminary works such as the hypothesis testing of modal prop- erties in process algebra [33], initial results for SMC had witnessed progress since 2002 [58] with the corresponding term introduced for the first time in 2004 [51]. Reasoning about reachability problems with SMC algorithms pro- vides mainly guarantees on the probability error bound. Depending on the type of reachability expression being dealt with, the error bound can be calculated by utilizing the appropriate classical mathematics such as Monte Carlo with Chernoff-Hoeffding error bounds [43, 24] or hypothesis testing using Wald's sequential analysis [56].

Different tools exist that implement SMC algorithms such as PRISM [31],

UPPAAL-SMC [12, 15], BIP [39], and Ymer [57]. Since their inception, SMC

tools have been utilized to study many discrete-time and continuous-time sys-

tems. To list a few: airplane cabin communication system [6], distributed sen-

 sor network [36], energy-aware house heating [13], biological mechanisms of the genetic oscillator [14], real-time streaming protocol [44], artificial pancreas

[2, 4], anesthesia control [3], and coordinated emergency braking system [1].

2.4 Model Construction

 In order to analyze the system, it is necessary to first convert the specifications into the modeling language used by the analysis tool. Furthermore, an ade- quate level of expertise is required to model the system properly when done manually. Furthermore, formal modeling languages tend to be more error- prone due to their low readability. Therefore, the need arises to facilitate the process of constructing formal models by automatically translating high-level models that incur better readability.

 In [28], the system modeled in Simulink is translated into SpaceEx mod- eling language in four steps. After the Simulink model is modified to comply with the verification standards, the tool SL2SX [40] is employed to handle the main translation step and construct a SpaceEx model. Afterwards, composi- tional syntactic hybridization [27] and validation are conducted to achieve a model ready to be analyzed.

 An approach to transform Simulink models into UPPAAL-SMC is proposed in [18]. The work is employed on two automotive use cases for brake-by-wire and an adjustable speed limiter. The Simulink models are first reduced by the flattening procedure. Then, each block is replaced by an equivalent timed automaton composed of three locations: start, offset, and operate. Still, their approach does not implement complex real-valued blocks in UPPAAL-SMC but addresses them in Simulink instead.

 Instead of commercial modeling tools, System Modeling Language (SysML) $_{220}$ [54] can be used to specify CPS. SysML is the defacto standard modeling lan- guage for systems engineering with rich semantics and expressive power suffi- cient to describe system structures and behaviors at various levels of abstrac- tion [25]. Ouchani et al. [45] constructed probabilistic automata by converting SysML models. The resulting models were incurred to analyze security prop- erties of the real-time streaming protocol using the probabilistic model checker PRISM [31].

 Compared to the studied initiatives, the main objective of this work is to develop a framework that enables efficient modeling and analysis for CPS. The proposed framework takes system behavior specified using SysML diagrams as input. The novelty of this proposed work is summarized by the following contributions.

 – Defining a bounded set of SysML constructs that are sufficient to capture the behaviors of the CPS discrete-time and continuous-time dynamics.

 – Defining textual specification language for SysML by extending the seman-tics initially developed in [17, 45].

 – Proposing a novel systematic procedure to transform the SysML behavioral specifications into PTA. Compared with the previous works that processed models specified in the commercial tool Simulink [28, 40, 18] or did not support modeling physical processes [18, 45], this new proposed approach defines a systematic procedure to process SysML models for the CPS and to construct an equivalent PTA model for analysis by supporting more features and expressive powers to specify physical properties like time, rate and real-numbers related measurements.

- The soundness of the proposed approach has been proven and its effective-
- ness to analyze CPS is demonstrated on an artificial pancreas system.

3 The Proposed Framework

 $_{247}$ Fig. 1 provides a brief overview of the proposed framework that runs on the following steps.

- ➀ The process starts with the initial identification of the CPS to explore the nature of its application. This step helps specify the system's requirements including the safety properties that have to be met.
- ➁ The topology of the system is defined by specifying the functional compo- nents of the system which are used to create the SysML block definition diagram. Also, the interactions between the CPS components are used to define the SysML flow diagram. Integrated CPS are formed from continu- ous real-time components describing physical processes and discrete-time components describing cyber processes.
- ➂ By relying on the existing topologies, behavioral models for the physical components are imported in the form of Ordinary-Differential Equations (ODE). Similarly, the cyber components of the system are imported from design specifications in the form of discrete variations.
- ➃ Physical and cyber components are represented using SysML parametric constraint diagrams and activity diagrams, respectively.
- ➄ To automate further processing, each of the SysML diagrams are written in textual format. For a constraint diagram describing the physical dynamics, the representation is done using the proposed syntax named Ordinary- Differential Equations of SysML Constraint Diagram (ODESCD). For an activity diagram describing a component's behavior, the representation is done using the proposed Enhanced Activity Calculus (EAC).
- ➅ A new systematic algorithm is proposed to convert ODESCD and EAC blocks into equivalent PTA blocks. The SysML block definition diagram, describing the system's structure, specifies the input/output connections of each PTA block.
- ➆ The various PTA blocks for physical dynamics and component behaviors are mapped as described by the flow diagram. The parallel composition of
- all the PTA blocks form the integrated CPS that is processed.

Fig. 1: The Proposed Framework Workflow.

 ($\circled{2}$) The analysis tool UPPAAL-SMC is used to analyze the system and verify the safety properties. The framework is demonstrated on an artificial pan- creas system alongside a proposed representation of continuous-time and discrete-time dynamics.

 ➈ For safety properties that are beyond the expressive power of the query language in use, dedicated monitor blocks are employed to observe specific phenomena. An observer block is then added to the system by developing a behavioral model for that block which is specified using a SysML activity diagram. The new block is then processed as component of the CPS to construct an equivalent PTA model. By adding these monitor blocks, more complex safety properties are simplified and expressed easily in order to

be examined for safely.

²⁸⁹ 3.1 SysML Graphical and Textual Modeling

²⁹⁰ 3.1.1 SysML for continuous-time dynamics

 The dynamics of physical processes describe the flow of physical quantities in the real world. These quantities are represented by real-valued real-time variables where the derivative of a variable is equivalent to the change on its associated physical quantity. Therefore, it is common for continuous-time dynamics to be specified by a system of ODEs. SysML constraint diagrams can be used to model ODEs.

²⁹⁷ Notation 1 (ODE of SysML Constraint Diagram (ODESCD)). ODESCD 298 is defined as a tuple $(X, X⁰, K, P, R, F, I, O)$, where:

- $299 X$ is a set of real-time real-valued differentiable variables,
- $_{300}$ X^0 is a set of initial values,
- $301 K$ is a set of real-valued equation coefficients,
- $302 P$ is a set of constant real-valued parameters,
- $303 R$ is a set of real-time real-valued variables,
- $_{304}$ $F(X,R)$ is a set of real-valued functions,
- 1_{305} $I \in X \cup K \cup P \cup R$ is a set of input variables, and
- $306 O \in X$ is a set of output variables.

Definition 1 (Semantics of ODESCD). Let $(X, X^0, K, P, R, F, I, O)$ be a ODESCD, its semantics is defined as the dynamics of a physical system described by a set of ODEs as follows (in this context a subscript in the form of $a_1 \times a_2$ indicates the matrix dimensions).

$$
X'_{n\times 1}(t) = K_{n\times n}(X, P, R, t) X_{n\times 1} + F_{n\times 1}(X, R)
$$
 (1)

$$
X_{n\times 1}(t=0) = X_{n\times 1}^0
$$
 (2)

³⁰⁷ $X'_{n\times1} = [x_1 \ x_2 \dots x_n]$ is the set of differential variables to be solved, ³⁰⁸ $X_{n\times1}^0 = [x_1^0 x_2^0 \dots x_n^0]$ is the set of initial values for the differential variables, $K_{n\times n}(X, P, R, t)$ is the set of differential equation coefficients which can be ³¹⁰ constants or functions of constant parameters, real-time variables or time, P $_{311}$ is the set of additional constant parameters for the equation, R is the set of 312 additional real-time variables, $F_{n\times 1}(X,R)$ is the additional terms of the ODE, 313 $I \in X \cup K \cup P \cup R$ is the set of input variables which can be parameters or $_{314}$ real-time variables, and $O \in X$ is the set of output variables which is a subset ³¹⁵ of the ODE solution.

³¹⁶ In this system, I is defined to utilize variables and parameters that are ³¹⁷ provided as input to the ODESCD definition, and O is used to export the ³¹⁸ desired variables from the solution of ODESCD.

❖ ODESCD example: meal glucose absorption model

 X is a vector representing carbo-hydrate measures in the stomach where Q_{sto1} and Q_{sto2} are the stomach glucose amounts in solid state and liquid state, respectively, and rag is the blood glucose rate of appearance. These physical quantities are initially nulled as assigned in $X⁰$. Fig. 2 depicts the SysML constraints block diagram for meal absorption variations measures.

$$
X = [Q_{sto1} Q_{sto2} \, rag]^T \tag{3}
$$

$$
X^0 = [0 \ 0 \ 0]^T \tag{4}
$$

$$
[K] = \begin{bmatrix} -k_{gri} & 0 & 0\\ k_{gri} & -k_{empt}(Q_{sto1}(t) + Q_{sto2}(t), D_{meal}) & 0\\ 0 & \frac{f.k_{abs}}{BW}k_{empt}(Q_{sto1}(t) + Q_{sto2}(t), D_{meal}) & -k_{abs} \end{bmatrix}
$$

 $k_{empt}(Q, D_{meal}) = \begin{cases} k_{min} + \frac{k_{max}-k_{min}}{2}(tanh(\alpha(Q-b.D_{meal})) - tanh(\beta(Q-c.D_{meal})) + 2) & D_{meal} > 0 \\ 0 & D_{meal} > 0 \end{cases}$ 0 $D_{\text{metal}} = 0$

$$
\alpha = \frac{5}{2.D_{\text{med}}.(1-b)}\tag{5}
$$

$$
\beta = \frac{5}{2.D_{meal}.c} \tag{6}
$$

$$
P = \{k_{gri}, k_{abs}, f, BW, b, c, k_{min}, k_{max}\}\tag{7}
$$

$$
R = \{cur_Mcal, D_{meal}\}\tag{8}
$$

$$
F(X, R) = [cur_Mel(t) 0 0]^T
$$
\n(9)

$$
I = \{cur_Med, D_{meal}\}\tag{10}
$$

$$
O = \{rag\} \tag{11}
$$

❖ ODESCD example: glucose-insulin dynamics

X is a vector representing the various physical quantities for the glucose and insulin dynamics all over the body compartments. I_{sc1} and I_{sc2} are the insulin levels in the subcutaneous tissues, X_1 is the insulin in the interstitial fluid, $\{G, G_s, G_t\}$ are the glucose levels in the blood, subcutaneous tissues, and slowly equilibrating tissues respectively. I_p is the plasma insulin, I_l is the portal vein insulin, and I_d is the delayed insulin signal. These physical quantities are initialized as in the vector X^0 . Fig. 3 depicts the SysML Constraint Block diagrams for Glucose-Insulin variations measures.

$$
X = [I_{sc1} I_{sc2} X_1 G_s I_1 I_d I_l I_p G G_t]^T
$$
\n(12)

$$
X^{0} = [I_{sc1_{ss}} I_{sc2_{ss}} \ 0 \ G_{i} \ I_{b} \ I_{b} \ I_{b} \ I_{p_{b}} \ G_{i} \ G_{t_{i}}]^{T}
$$
(13)

Parameters:

Input: cur_Meal [*mg/min*] (real time glucose intake), D_{med} [g] (meal carbs)
Output: rag [*mg/Kg/min*] (real time glucose rate of appearance in the blood)

Fig. 2: SysML Constraint Block for Meal Absorption

$$
\begin{bmatrix}\n-[k_d + k_{a1}) \quad 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
k_d & -k_{a2} & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & -p_{2u} & 0 & 0 & 0 & 0 & \frac{p_{2u}}{V_I} & 0 & 0 & 0 \\
0 & 0 & 0 & -\frac{1}{T_s} & 0 & 0 & 0 & 0 & \frac{k_i}{V_I} & 0 & 0 \\
0 & 0 & 0 & 0 & -k_i & 0 & 0 & 0 & \frac{k_i}{V_I} & 0 & 0 \\
0 & 0 & 0 & 0 & k_i & -k_i & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & -(m_1 + \frac{m_6 m_1}{1 - m_6}) & m_2 & 0 & 0 & 0 \\
k_{a1} & k_{a2} & 0 & 0 & 0 & 0 & m_1 & -(m_2 + m_4) & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -(k_{p2} + k_1) \frac{k_2}{V_G} \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & K_1.V_G & -k_2\n\end{bmatrix}
$$

$$
P = \{I_{sc1_{ss}}, I_{sc2_{ss}}, G_i, I_{l_b}, I_{p_b}, G_{t_b}, G_b, k_d, k_{a1}, k_{a2}, p_{2u}, V_I, I_b, T_s, k_i, V_G, m_1, m_6, m_2, m_4, k_{p1}, k_{p2}, k_{p3}, F_{cns}, k_{e1}, k_{e2}, k_1, k_2, V_{m0}, V_{mx}, K_{m0}, K_{mx}\}
$$

$$
(14)
$$

$$
R = \{rag, IIR\} \tag{15}
$$

$$
F(X, R) = [IIR, 0, -p_{2u}.I_b, 0, 0, 0, 0, 0, \frac{rag + k_{p1} - F_{cns}}{V_G} \t\t(16)- k_{e1}.max(0, G - \frac{k_{e2}}{V_G}), -\frac{(V_{m0} + V_{mx}.X_1).G_t}{K_{m0} + K_{mx}.X_1 + G_t}]^T
$$

$$
I = \{rag, IIR\}
$$
(17)

$$
O = \{G_s, G\} \tag{18}
$$

«constraint» Glucose-Insulin Dynamics : Equality				
$I_{\rm sd}$ '== - ($k_{\rm d}$ + $k_{\rm a1}$) . $I_{\rm sd}$ + IIR I_{5c2} = K_d . I_{5c1} - K_{a2} . I_{5c2} $X_1' == -p_{21}$, $X_1 + p_{21}$, $(I_n / V_1 - I_h)$ $G_s' == - (G_s - G) / T_s$ $I_1' == -k_i$. $(I_1 - I_n / V_1)$ $I_n' == -k_i$. $(I_n - I_1)$ $I_1' == -(m_1 + (m_6, m_1 / (1 - m_6)))$. $I_1 + m_2$. I_n $I_0' = - (m_2 + m_4)$. $I_0 + m_1$. $I_1 + k_{a1}$. $I_{sc1} + k_{a2}$. I_{sc2} $G' = (k_{01} - k_{02} - G \cdot V_G - k_{03} - l_d - F_{cm} - k_{e1} - max(0, G \cdot V_G - k_{e2}) - k_1 \cdot G \cdot V_G + k_2 \cdot G_t + rag) / V_G$ $G_t' == - (Gt \cdot (V_{m0} + V_{mx} \cdot X_1)) / (K_{m0} + K_{mx} \cdot X + G_t) + k_1 \cdot G \cdot V_G - k_2 \cdot G_t$				
Parameters: Input: IIR [pmol/Kg/min] (subcutaneous insulin infusion rate) Input: rag $[mg/Kg/min]$ (meal glucose rate of appearance in the plasma) Output: G_s [<i>mg/dL</i>] (real time subcutaneous glucose level) Output: G [mg/dL] (real time blood glucose level)				

Fig. 3: SysML Constraint Block for Glucose-Insulin Dynamics

3.1.2 SysML for discrete-time dynamics

 Discrete-time dynamics are described by SysML activity diagrams. So, in or- der to precisely describe CPS and capture exactly its underlying semantics, we develop Enhanced Activity Calculus (EAC) to formally describe SysML activ- ity diagrams by extending NuAC presented in [17, 45]. These enhancements include redefining existing nodes as well as proposing new nodes for time- bounded delay, constraint-bounded delay, and competing events. The list of the used activity nodes and their textual EAC representation is shown in Table 1.

\bullet EAC example: artificial pancreas

 The artificial pancreas is composed of a sensor (Fig. 4) that periodically mea- sures the glucose level, sends it over wireless channel (Fig. 5) to the controller. Then, the controller (Fig. 6) calculates the required amount of insulin, and the actuator (Fig. 7) applies the control action. Lastly, the SysML activity diagram describing the meal scenario is shown in Fig. 8.

SysML Term	SysML Activity Diagram Structure	EAC Syntax
Activity Initial Node		$l \mapsto N$
Action Node	A	$l:ACT(A) \rightarrow N$
Call Procedure	$A_{\uparrow\uparrow}$	$l:CALL_P(A) \rightarrow N$
Send Node	${S,X}$	$l: \{S, X\} \rightarrow N$
Receive Node	$\{S, X_{src}, X_{dst}\}$ $\left(N \right)$	$l: \{S, X_{src}, X_{dst}\}\text{?} \mapsto N$
Merge Node		$l: Mrg \mapsto N$
Guarded Branch	N_1 True $\left\langle 6? \right\rangle$ False $\left\langle N_2 \right\rangle$	$l: B_C(l_{i_1}:(C = C_1) \mapsto N_1,$ l_{i_2} : $(C = C_2) \mapsto N_2, $
Probabilistic Branch		$l: B_P(l_{i_1}:(P = P_1) \mapsto N_1,$ l_{i_2} : $(P = P_2) \mapsto N_2, $
Time-Bounded Delay Node	$TB\{\tau_{min}:\tau_{max}, C\}$	$l : D_T B(\tau_{min} : \tau_{max}, C) \mapsto N$
Constraint-Bounded Delay Node	$CB{C_{\text{ter}}}$, C }	$l: D_{CB}(C_{ter}, C) \rightarrow N$
Competing Events		$l: Comp_Events(N_1 \rightarrow N_2,$ $N_3 \mapsto N_4, \ldots$

Table 1: SysML Enhanced Activity Calculus Nodes Syntax.

By substituting the SysML nodes with their textual equivalents following Table 1, the EAC representation of these activity diagrams is shown below.

$$
Act_Sensor = l \mapsto l_1 : Mrg \mapsto N_1
$$

$$
N_1 = l_2 : D_{TB}(T_p,) \mapsto l_3 : ACT(meas_var = phy_var) \mapsto l_4 : \{S_{et}, meas_var\}! \mapsto l_1
$$

Act-Channel_{lossy} =
$$
l \mapsto l_1 : Mrg \mapsto N_1
$$

\n
$$
N_1 = l_2 : \{S_{et1}, var_in, var_out\} \} \mapsto l_3 : B_P(l_4 : (P = P_S) \mapsto N_2, l_5 : (P = P_F) \mapsto l_6 : Mrg \mapsto l_1)
$$
\n
$$
N_2 = l_7 : \{S_{et2}, var_out\} \} \mapsto l_6
$$

Fig. 4: SysML Activity Diagram of the Sensor.

Fig. 5: SysML Activity Diagram of the Lossy Channel.

 $Act_Ctrl = l \mapsto l_1 : Mrg \mapsto N_1$ $N_1 = l_2 : Comp_Events(l_3 : \{S_{et1}, G, G_r\} ? \rightarrow N_2, l_4 : D_{TB}(Tp,) \rightarrow N_3)$ $N_2 = l_5 : CALL_P(IIR = Act_Calc_IIR(t)) \mapsto l_6 : Mrg \mapsto N_4$ $N_3 = l_7 : CALL_P(IIR = Act_Calc_IIR_missing(t)) \mapsto l_6$ $N_4 = l_8 : \{S_{et2}, IIR\}! \mapsto l_1$

 $Act_Actuator = l \mapsto l_1 : Mrg \mapsto l_2 : \{S_{et}, IIR_c, IIR_r\} ? \mapsto l_3 : ACT(IIR = IIR_r) \mapsto l_1$

Fig. 6: SysML Activity Diagram of the Controller.

Fig. 7: SysML Activity Diagram of the Actuator.

Act_meal_scenario = $l \mapsto l_1 : Mrg \mapsto l_2 : D_{TB}(inter_meal_time,) \mapsto N_1$ $N_1 = l_3 : ACT(cur_meal = 1000 * meal_carbs/meal_dur, D_meal = (Q_{sto1} + Q_{sto2})/1000) \mapsto N_2$ $N_2 = l_4 : D_{TB}(mean_dur, D_meal' == cur_meal/1000) \mapsto l_5 : ACT(cur_meal = 0) \mapsto l_1$

- ³³⁴ ❖ CPS architecture and flow for artificial pancreas
- ³³⁵ The SysML block definition diagram shown in Fig. 9 defines the blocks and
- ³³⁶ their input/output ports. Also, the mapping of the blocks and the variables

Fig. 8: SysML Activity Diagram of the Meal Scenario.

- ³³⁷ as well as the flow of information among these blocks are defined in the flow
- ³³⁸ internal block diagram shown in Fig. 10.

Fig. 9: SysML Architectural Block Definition Diagram of the Closed-Loop Glucose Control System.

³³⁹ 4 CPS Semantics

³⁴⁰ The system behavior should be represented in the suitable formality that ³⁴¹ matches the language of the analysis tool. To do so, the SysML components are

³⁴² converted into a network of equivalent PTA models. In the following, the PTA

³⁴³ is defined and the new proposed automated conversion procedure is presented.

Definition 2 (PTA). A PTA for CPS is a tuple $(L, l_0, L_{lbl}, L_{IP}, L_{OP}, E, X,$ 345 $V_q, INV(X, VAR), A(V_q), G(X, V_q), S_{et}, P_r)$, where:

- $_{346}$ L is a finite set of locations,
- $_{347}$ $l_0 \in L$ is the initial location,
- $_{348}$ L_{lbl} is a set of labels,
- $_{349}$ L_{IP} is a finite set of input ports,
- $_{350}$ L_{OP} is a finite set of output ports,
- $_{351}$ E is a finite set of edges,
- $352 X$ is a finite set of clocks.
- $353 VAR$ is a finite set of general-type variables,
- $_{354}$ INV(X, V_a) is a finite set of invariants over PTA clocks X and variables ³⁵⁵ V_q , $A(V_q)$ is a finite set of actions on the variables V_g ,
- $356 G(X, V_q)$ is a finite set of atomic propositions on PTA clocks X and vari-
- 357 ables V_q , S_{et} is a finite set of synchronization event triggers, and
- $_{358}$ P_r is a finite set of probabilistic weights.

Definition 3 (Semantics of CPS). Let $(L, l_0, L_{lbl}, L_{IP}, L_{OP}, E, X, V_g,$

 $_{360}$ INV(X, VAR), $A(V_q)$, $G(X, V_q)$, S_{et} , P_r) be a PTA for CPS. The semantics ³⁶¹ are defined as a hybrid transition system composed of a set of locations L

 362 interconnected by a set of edges E through sets of input ports IP and output

- ³⁶³ ports OP, where:
- $L = \{l_1, l_2, \ldots, l_{n1}\},$ where the i^{th} location $l_i \in L$ labelled ³⁶⁵ i label_i ∈ L_{lbl} having the invariant constraints inv_i ∈ INV and connected to
- ³⁶⁶ the input port x_{ip} and the output ports X_{op} is referred as $l_i(label_i, inv_i, x_{ip}, X_{op})$.
- ³⁶⁷ Edges $E = \{e_1, e_2, \ldots, e_{n2}\},\$ where the i^{th} edge running the action $a \in A$ 368 and triggering the synchronization event $s_{et} \in S_{et}$, and connected to the
- 369 output port x_{op} and input port x_{ip} is referred as $e_i = \{a, s_{et}, x_{op}, x_{ip}\}.$
- ³⁷⁰ Input ports $L_{IP} = \{l_{ip_1}, l_{ip_1}, \ldots, l_{ip_{n1}}\}$, where the *i*th input port $l_{ip_i} \in L_{IP}$ sourcing from incoming edges X_e towards the ith location $l_i \in L$ and
- applying the action $a \in A$ is defined as $l_{ip_i} = \{a, X_e, i\}.$
- ³⁷³ Output ports $L_{OP} = \{l_{op_1}, l_{op_1}, \ldots, l_{op_{n3}}\}$, where the k^{th} output port $l_{op_k} \in$ ³⁷⁴ L_{OP} sourcing from the ith location L_i towards the jth edge e_j , guarded by ³⁷⁵ the atomic proposition $g \in G$, triggered by the event trigger $s_{et} \in S_{et}$, and
- 376 having the probabilistic weight $p_r \in P_r$ is defined as $l_{op_k} = \{g, s_{et}, p_r, i, j\}.$

³⁷⁷ PTAs traverse sequentially through output ports towards edges, followed ³⁷⁸ by input ports towards the next location, starting at an initial location denoted b_3 by l_0 . In the case of the PTA being at location l_i , the invariant inv_i must be sso satisfied as long as the PTA is at location L_i . Similarly, an output port that has α a guard g with respect to its traversal can only be traversed if this guard g has

- been satisfied. An output port with an event trigger s_{et} is synchronized with another PTA, so that the output port is only traversed when it is activated by the corresponding event trigger on the edge of the other PTA. Furthermore,
- an output port can be traversed among other output ports in a probabilistic
- 386 manner by assigning a probability weight p_r to each of the possible candidates
- for traversal of the output port.

Fig. 10: SysML Flow Internal Block Diagram of the Closed-Loop Glucose Control System.

4.1 Converting SysML into Equivalent PTA

- In order to analyze the CPS described in SysML, it is necessary to model the
- hybrid system in PTA. So, SysML blocks are translated into equivalent PTA
- blocks which are parallel-composed to construct the hybrid system's global
- behavior. The synchronization of actions and the transfer of values are specified using shared variables.
- The template of each PTA is instantiated with its input/output parame- ters properly defined. The SysML flow internal block diagram (as in Fig. 10) is consulted to define global variables for the parameters connecting the PTA components of the system. When instantiating a PTA template, the param- eters are passed by-reference except for constant parameters that are passed by-value. Instead, those constants can be defined as local variables in the PTA. The following rules govern the definition of variables in PTA models.
- Continuous real-valued parameters are defined using clock variables.
- ⁴⁰² Discrete real-valued parameters are defined using floating point variables.
- ⁴⁰³ An event trigger should be activated whenever a discrete variable is up-
- ⁴⁰⁴ dated, so that the other PTAs are notified about the new update.
- ⁴⁰⁵ Discrete integer parameters are defined as integer variables and are passed ⁴⁰⁶ between PTAs similar to the floating point variables.
- ⁴⁰⁷ When assigning or initializing a numerical variable, it can be evaluated to ⁴⁰⁸ a single value or to a range of values for a uniformly-distributed random ⁴⁰⁹ assignment.

⁴¹⁰ ❖ Converting SysML EAC into PTA

⁴¹¹ This part presents the detailed procedure for constructing a PTA block that

- ⁴¹² represents a SysML EAC block. Alongside the description of the conversion
- steps, an illustrative example is provided for converting the $Act_Channel_{lossu}$
- ⁴¹⁴ block from EAC into PTA.

⁴¹⁵ – The first step is to merge all the EAC nodes into the main EAC con-⁴¹⁶ struct. This is done by iterating through the auxiliary constructs (N_x) and substituting for them in the main construct as depicted in Fig. 11.

Fig. 11: EAC Lossy Channel Example - Merging Nodes

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⁴¹⁸ – Connecting the EAC terms so that each arrow is uniquely identified as ⁴¹⁹ presented in Fig. 12.

- $_{420}$ Handling branching terms (B_P or Comp Events) and replicating the EAC ⁴²¹ construct, so that each branching term has only one path at a time. This ⁴²² is done by iterating through the branching terms and taking one branch ⁴²³ at a time as shown in Fig. 13.
- ⁴²⁴ Building the PTA skeleton using the procedure described in Algorithm 1.
- The resulting skeleton for $Act_Channel_{lossy}$ example is shown in Fig. 14.

$$
\Downarrow \Downarrow \Downarrow
$$

Act-Channel_{lossy} = $l \xrightarrow{1}$ $l_1 : Mrg \xrightarrow{2} l_2 : \{S_{et1}, var_in, var_out\} \xrightarrow{3} l_3 : B_P(l_4 : (P = P_S) \xrightarrow{4}$
 $l_7 : \{S_{et2}, var_out\} \xrightarrow{5} l_6, l_5 : (P = P_F) \xrightarrow{6} l_6 : Mrg \xrightarrow{7} l_1)$

Fig. 13: EAC Lossy Channel Example - Branches Handling

 – For each location node that has non-empty prev field, insert an input port. ⁴²⁷ For locations with *next* field, insert an output port per edge node that is ⁴²⁸ outgoing from the location. In the following steps, when an EAC term is linked to an output port, the one that is connected to the location where the EAC belongs is identified. In case the location is attached to two or ⁴³¹ more output ports, the sequence of EAC terms in the path construct is used to identify the corresponding output port. Moreover, an EAC node that shows up in more than one path is only converted once at its first appearance.

- ⁴³⁵ Replacing the following EAC terms with their equivalent PTA terms.
- $_{436}$ EAC term *l* signifies the location as an initial location.

 σ_{437} – $D_{TB}(\tau_{\text{min}} : \tau_{\text{max}} , C)$: Declare a clock variable t, Add a reset for the $_{438}$ clock $(t = 0)$ to the input port action, Add the following constraint ⁴³⁹ (t $\leq \tau_{max}$ && C) to the invariants *inv* of the location, and add the following $(t \geq \tau_{min})$ to the guard g of the output port.

⁴⁴¹ – {S, X_{src} , X_{dst} }?: Add the event trigger S? to the respective field s_{et} of ⁴⁴² the output port, and add the assignment $(X_{dst} = X_{src})$ to the action ⁴⁴³ of the edge outgoing from the output port.

Algorithm 1 Construction of PTA Skeleton.

for each: EAC - $Path$ 1: $prevNode = \emptyset$ > The first node of a path has no predecessor. for each: $EAC_Node \in EAC_Path$ 2: if $EAC_Node \in \{Mrg, Comp_Events, B_P, D(*), \{*,*\}?$, $(P = *)\}$ then 3: $EAC_Type = LOGATION$ 4: else if $EAC_Node \in \{\rightarrow, ACT, CALLP, \{*,*\}!\}$ then
5: $EAC_Type = EDGE$ $EAC_Type = EDGE$ 6: end if 7: if EAC_Node processed before then 8: $cur.Node = PTA.Node[EAC.Node]$ \triangleright Traverse through the node. 9: cur_Node.prev.addMember(prev_Node) \triangleright Create a new input port for the node. 10: $prev_Node.next.addMember(cur_Node)$ \triangleright Create a new output port for the node. 11: else if $EAC_Type == prev_Node_type$ then 12: $cur_Node.EAC.addMember(EAC_Node) \Rightarrow A \text{ complement for the previous node.}$ 13: else ⊳ A node not processed yet. 14: $cur_Node = create_Node(type = EAC_Type)$ \triangleright Create the node.
15: $cur_Node = EAC.addMember(EAC_Node)$ \triangleright Traverse through the node. $cur_Node.EAC.addMember(EAC_Node)$ 16: cur Node.prev.addMember(prev Node) > Create an input port. 17: $prev_Node.next.addMember(cur_Node)$ \triangleright Create an output port. 18: end if

$Node_{ID} = $	type,	prev,	next,	EAC	
$Node_1 = $	<i>LOCATION</i> ,	Ø,	2,	l	
$Node_2 = $	$EDGE$,	1,	3.	$\stackrel{1}{\mapsto}$	
$Node_3 =$	<i>LOCATION,</i>	$\{2, 11\},\$	4,	l ₁	
$Node_4 = $	EDGE,	3.	5.	$\stackrel{2}{\mapsto}$	
$Node_5 = $	<i>LOCATION,</i>	4,	6,	l_2	
$Node_6 = $	EDGE,	5,	7,	$\stackrel{3}{\mapsto}$	
$Node_7 =$	<i>LOCATION</i> ,	6,	$\{8, 10\},\$	$\{l_3, l_4, l_5\}$	
$Node_8 = $ $Node_9 = $ $Node_{10} = $ $Node_{11} = $	$EDGE$, <i>LOCATION</i> , EDGE, EDGE,	7, $\{8, 10\},\$ 7, 9,	9, 11, 9, 3,	$\{\stackrel{4}{\mapsto}, l_7, \stackrel{5}{\mapsto}\}$ l_6 $\stackrel{6}{\mapsto}$	

Fig. 14: EAC Lossy Channel Example - Building Skeleton.

- $\{S, X\}!$: Add this event trigger S! to the respective event trigger field s_{et} of the containing edge. 446 – $(P = p_x)$: Add the following probabilistic weight to the corresponding f_{447} field p_r of the output port. $448 - ACT(A)$: Add the action A to the corresponding field a of the edge.
- $\overline{CALL_P(A)}$: Add the behavior call $A()$ to the action field a of the edge.
- ⁴⁵⁰ The results shown in Fig. 15 are obtained when applying the above rules on the $Act_Channel_{lossy}$ example:

Fig. 15: EAC Lossy Channel Example - Replacing EAC with PTA Terms.

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 $452 -$ After each EAC receive node, insert a new location between the event ⁴⁵³ trigger and the signal sampling. Also, a new location is added when an ⁴⁵⁴ output port with a probabilistic weight is directly followed by an edge ⁴⁵⁵ with an EAC send node. This is done so that the send node is separated ⁴⁵⁶ from the output port. When applying this on the $Act_Channel_{lossy}$, the ⁴⁵⁷ results look like Fig. 16

Fig. 16: EAC Lossy Channel Example - Inserting Locations

 – Divide the locations into transient and regular (time-consuming) locations. A regular location is identified by having either a guard or an event trigger on the output port, or by having a non-empty invariant field. For the $Act_Channel_{lossy}$ example, all the locations are transient except location loc³ which has an event trigger on the output port.

- The rate of all local clocks should be identified on all regular locations. Therefore, if a clock is not supposed to evolve in a specific regular location, its evolution rate should be assigned to 0 in the invariants field of that location. – When exporting the PTAs into an XML file compatible with UPPAAL-
- SMC analyzer, transient locations are specified as urgent locations except for the following:
- $470 A$ location which emits output ports with probabilistic weights (location loc_4 in $Act_Channel_{lossy}$ example) is defined as an anchor point (for syntax compatibility).
- ⁴⁷³ The first location following a receive node (location loc_6 in $Act_Channel_{lossw}$ example) should be set to committed for synchronization correctness (semantic compatibility).

 The resulting PTA diagram for the above transformed lossy channel is depicted in Fig. 17. This PTA initializes at the location loc_1 . This location is urgent which means that no time progress and hence the PTA will move 479 instantly through the output port op_1 , the edge e_1 , the input port ip_2 to the μ_{480} next location loc_2 . This location is also an urgent location and hence the PTA 481 will move through the output port op_2 , edge e_2 , and the input port ip_2 towards ⁴⁸² the location loc₃. The output port op_3 is activated by the event trigger S_{et1} ? which is controlled by another PTA (the sensor in this case). Then, this sensor ⁴⁸⁴ activates the event trigger S_{et} to send a new measurement (the variable var_{in}) 485 through the wireless channel. When triggered by the event trigger S_{et1} , the lossy channel PTA moves through the output port op_3 , the edge e_7 , and the ⁴⁸⁷ input port ip_6 towards the committed location loc₆. Like the urgent location, a committed location freezes time but also synchronizes the PTAs so that the correct sequence of actions takes place. In this PTA, it is required so that the 490 up-to-date version of the measurement value var_{in} is read.

⁴⁹¹ The PTA moves through op_6 towards the edge e_3 where the measurement ⁴⁹² is sampled, and then through the input port ip_4 to the location loc₄ which is a probabilistic branching point. Then, the PTA will take a branch depending on probability weights. At one branch, the message will get lost and so the PTA 495 takes the output port $op_{(4,2)}$ towards the edge e_5 and the input port ip_5 to reach the location loc_5 . In the other branch, the measurement is successfully relayed so the other PTA (the controller in this case) is notified with the event trigger S_{et2} !, so the PTA moves through $op_{(4,1)}$, e_4 , ip_7 to the transient location towards the output port op_7 and the edge e_8 (where S_{et2} ! is activated) to $\frac{1}{500}$ the input port ip_5 while merging with the other branch in the location loc_5 . $\frac{1}{501}$ Finally, the PTA moves via the output port op_5 and the edge e_6 through the $_{502}$ input port ip_2 to merge in the location loc_2 .

❖ modeling ODESCD using PTA

The same rules apply to convert ODESCD into PTA where the ODE variables

X are defined as clock variables. The PTA is composed of one location where

 the rates of the ODE variables X are assigned using equality constraints in the invariant field of the main location. If some variables or parameters are

Fig. 17: The Resulting PTA Diagram for the Lossy Channel

initialized with random values, an additional transient initial location is added

with the variables assigned in the edge connecting the initial location to the

main operational location.

4.2 Soundness

 After presenting the semantics of CPS and PTA, we prove the soundness of the developed framework. First, lets Γ to be a function denoting Algorithm 1. Now, we prove the soundness of the transformation by showing that Γ guarantees the integrity of the CPS design, i.e. no added, modified, or excluded behavior. Thus, an equivalent PTA behavioral model is produced. Then, we show that the soundness proves the satisfiability preservation of MILT expressions when applying Γ. 519 As depicted in Fig. 18, we have to show the nature of the relation \mathscr{R} ,

⁵²⁰ that compares both PTA^{cps} and PTA^f constructed through EAC and PTA semantics rules respectively, while preserving both behaviours. Indeed, the \mathscr{F} relation \mathscr{F} could be determined by comparing the semantics of each term in EAC and the semantics of its image obtained by the function Γ. Since the goal is to guarantee the behaviour integrity of PTA^{cps} and the resulting PTA^f \mathcal{L}_{525} should not differ from $PT A^{cps}$, Lemma 1 proves that \mathcal{R} is a bisimulation relation.

 \mathbb{E}_{527} Lemma 1 The binary relation \mathscr{R} , is a bisimulation, whenever $S\mathscr{R}S$, satisfies the following.

 $\begin{array}{lll} \text{53} & \text{1. If } S \overset{\alpha}{\rightarrow} S' \text{ then } \exists \hat{S}' \text{ such that } \hat{S} \overset{\alpha}{\rightarrow} \hat{S}' \text{ and } S' \equiv_{\mathscr{R}} \hat{S}'. \end{array}$

Fig. 18: The Transformation Soundness Schema.

530 2. If $\hat{S} \stackrel{\alpha}{\rightarrow} \hat{S}'$ then $\exists S$ such that $S' \stackrel{\alpha}{\rightarrow} S'$ and $\hat{S}' \equiv_{\mathcal{R}} S'$.

⁵³¹ Proof Let's consider $A \in P T A^{cps}$ and $B \in P T A^f$ where $\Gamma(A) = B$. So, by 532 induction on EAC terms, we prove that $\mathscr R$ is a bisimulation binary relation as ⁵³³ follows.

 $\begin{array}{rcl} \text{534} & - \text{ When } A = i \to \mathcal{N}, \text{ then based on the rule } \exists S \stackrel{\alpha}{\to} S' \text{ such that } S = i \to \mathcal{N}. \end{array}$ and $S' = \overline{i \mapsto \mathcal{N}}$, we will have, $\Gamma(A) = \Gamma(i \to \mathcal{N}) = initial\ to\ i$. Thus, 536 initial∧¬i $\stackrel{\alpha}{\to}$ ¬initial∧i ∈ B^s. Then, PT A^{cps} \Re PT A^f when A = i → N. \mathcal{F}_{537} $-$ For $\{S, X\}! \rightarrow \mathcal{N}$, then $\overline{X \rightarrowtail \mathcal{N}} \rightarrow \{S, X, X'\}! \rightarrow \overline{\mathcal{N}} \in PTA^{cps}$. Also, $\Gamma (A) = resource < v> \rightarrow \mathscr{N} \text{ which means } resource_v \wedge \neg \mathscr{N} \stackrel{prt}{\rightarrow} \neg resource_v \wedge \neg \mathscr{N}$ 539 $\mathcal{N} \in B^s$. So, $PTA^{cps} \mathcal{R} PTA^f$. 540 – In the case of $A = resource? v \rightarrow \mathcal{N}$, we have $\overline{resource? v \rightarrow \mathcal{N}}$ F_{541} resource? $v \rightarrow \overline{\mathcal{N}} \in B^{sn}$. Thus, $PTA^{cps} \mathcal{R} PTA^{f}$. $\begin{array}{rcl} \text{542} & - \text{ If } A = resource! \, v \rightarrow \mathcal{N}, \text{ we have } \overline{resource! \, v \rightarrow \mathcal{N}} \rightarrow resource! \, v \rightarrow \mathcal{N} \end{array}$ $\overline{\mathscr{N}} \in B^{sn} \ R \ \hbox{resource} out_v \wedge \exists v \wedge \neg \mathscr{N} \stackrel{prt}{\rightarrow} \neg \hbox{resource} out_v \wedge \mathscr{N} \in B^s.$ 544 – By considering $A = resource \uparrow expression \rightarrow \mathcal{N}$, then $\overline{resource \uparrow expression \rightarrow \mathcal{N}} \rightarrow$ resource \uparrow expression $\rightarrow \overline{\mathcal{N}} \in B^{sn}$. As a result, we have resource_v \wedge ¬N prt → ¬resource^v [∧] ^v ⁼ newvalue [∧] ^N [∈] ^B^s ⁵⁴⁶ , which means P T Acps R $_{547}$ PTA^f . \mathcal{F}_{548} – For the decision term $A = D(g_{v1}, \mathcal{N}_1, \mathcal{N}_2)$, we differentiate two cases: 549 1. When $\neg g_{v1} \models \top$, we have $\overline{D(g_{v1}, \mathcal{N}_1, \mathcal{N}_2)} \stackrel{\neg g_{v1}}{\longrightarrow} D(g_{v1}, \mathcal{N}_1, \overline{\mathcal{N}_2}) \in B^{sn}$ 550 by relying on the decision rule. Also, we have: $\Gamma(A) = \{ \text{on } \text{prt}_i \text{ from } \}$ 551 source to $\mathcal N$ provided $g_{v_i} = eval(v_i) : i \in \{1, 2\}$. Also, since $\neg g_{v1} \models \top$, $\begin{aligned} \text{two have: } \overline{D(g_{v1}, \mathscr{N}_1, \mathscr{N}_2)} \stackrel{\neg g_{v1}}{\longrightarrow} D(g_{v1}, \mathscr{N}_1, \overline{\mathscr{N}_2}) \in B^s. \end{aligned}$

- 553 2. For the other case, when $g_{v1} \models \top$, we have shown that $D(g_{v1}, \mathcal{N}_1, \mathcal{N}_2) \equiv$ $D(\neg g_{v1}, \mathscr{N}_2, \mathscr{N}_1)$. Thus, PTA^{cps} \mathscr{R} PTA^f .
- ⁵⁵⁵ We have shown that for each EAC term, we have PTA^{cps} \mathscr{R} PTA^{f} in which 556 result that \mathscr{R} , is a bissimulation relation and it is symmetric.

⁵⁵⁷ Based on the illustration presented in Fig 18, the transformation's objec-⁵⁵⁸ tive is to verify functional properties of the generated PTA model and then ⁵⁵⁹ infer satisfiability results for the CPS design. Using Lemma 1, Proposition 1 ⁵⁶⁰ demonstrates how the properties expressed in MITL logic can be satisfied.

Proposition 1 $\forall A \in PTA^{cps}, B \in PTA^{f} \text{ s.t. } \Gamma(A) = B$, we have: $\forall \phi \in$ 562 $MITL:PTA^f \models \phi \implies PTA^{cps} \models \phi.$

- 563 Proof By induction on MITL terms, we prove that $B \models \phi \implies A \models \phi$.
- 564 1. First, let's consider the state formulae $\phi = \varphi_1 \wedge \varphi_2$ where $B \models \phi$. Now, we $\frac{565}{200}$ show the satisfiability of ϕ on A for the following EAC terms.
- 566 For $A = i \rightarrow \mathcal{N}$, we have $i \rightarrow \mathcal{N} \stackrel{\alpha}{\rightarrow} \overline{i \rightarrow \mathcal{N}}$ R initial $\wedge \neg i \stackrel{\alpha}{\rightarrow}$ $\neg initial \wedge i$. If initial $\wedge \neg i \models \varphi_1 \wedge \varphi_2$ means initial $\wedge \neg i = \varphi_1 \wedge \varphi_2$. 568 Thus, $i \to \mathcal{N} \models \phi$, and, $B \models \phi$
- 569 $-$ For $A = resource < v > \rightarrow \mathcal{N}$ when $\neg resource_v \wedge \mathcal{N} \models \varphi_1 \wedge \varphi_2$, we 570 have $resource < v > \rightarrow \overline{\mathcal{N}} \models \varphi_1 \land \varphi_2$. Then, $B \models \phi$.
- \mathcal{F}_{571} For $A = resource? v \rightarrow \mathcal{N}$, then $B \models \phi \; resourcein_v \land v = newvalue \land v$ $\mathcal{I}_{\mathcal{I}} \rightarrow \mathcal{I}_{\mathcal{I}} \rightarrow \mathcal{I}_{\mathcal{$ 573 resource? $v \rightarrow \overline{\mathcal{N}} \models \phi$. Consequently, $B \models \phi$.
- 574 2. Now, we consider the path formulae $P_{\mathbf{w}p}[\psi]$. So, since EAC does not sup- 575 port probabilistic decisions and has only deterministic ones, $P_{\geq 1}[\psi]$ means ψ else we consider the case of $P_{\leq 0}[\psi]$. Then, we prove by induction on the ⁵⁷⁷ path operators that $PTA^{cps} \models \phi$ when $PTA^f \models \phi$ as follows.
- $\begin{array}{rcl} \text{578} \end{array}$ For $\phi = N\varphi, B \models \phi \text{ means } \exists \hat{S} \stackrel{\alpha}{\rightarrow} \hat{S'} \in B^n \text{ such that } \hat{S'} \models \varphi. \text{ In }$ addition, since \Re is symmetric, then $\exists S \stackrel{\alpha}{\to} S' \in B$ such that: $S' \models \varphi$. $\begin{aligned} \mathcal{E}_{\mathsf{S}}^{\mathsf{S}} \qquad \quad & \quad - \text{ For } \phi = \varphi_1 \cup^t \varphi_2 \text{, we have } \exists \hat{S_1} \overset{\alpha}{\rightarrow} \cdots \rightarrow \hat{S_t'} \subseteq B^n \text{ such that } \hat{S}_{i:i < t'} \models \varphi_1 \end{aligned}$ and $\hat{S}_2 \models \varphi_2$. Also, $\mathscr R$ is symmetric and $\exists S_1 \stackrel{\alpha}{\to} \cdots \to S_t' \subseteq B^{sn}$ where 582 $S_i\ \mathscr{R}\ \hat{S_i}: 0 < i \leq t. \text{ Thus, } B \models \phi.$
- ⁵⁸³ Based on the previous proof, we have shown that for each EAC and MITL $_{584}$ term, $\mathscr R$ always preserves the satisfiability of MITL formulae. Consequently, ⁵⁸⁵ $B \models \phi \implies A \models \phi$ for all ϕ expressed in MITL when $PTA^{cps}\mathscr{R} PTA^f$.

⁵⁸⁶ 5 Experimentation

 This section shows the effectiveness of the proposed framework by first vali- dating the transformation algorithm. Then, the proposed approach is used to demonstrate how the safety of the obtained model can be examined by statis-tical model checking over a list of selected functional and safety requirements.

⁵⁹¹ 5.1 Validation of the Conversion Procedure

 In order to demonstrate the correctness of the proposed approach, PTA models are validated. Properties are specified for each component of the system that constrain its functional behavior. To evaluate whether the resulting PTA model meets the behavioral properties, random simulations are conducted and trace ⁵⁹⁶ log analysis is applied to the results. The resulting PTA models are more likely ⁵⁹⁷ to be valid representations of the CPS components when all the properties are ⁵⁹⁸ satisfied.

⁵⁹⁹ By comparing the values of the ODE variables with a mathematical ODE

⁶⁰⁰ solver, PTAs representing ODESCD are validated. In the case of the ODE-

⁶⁰¹ SCDs describing meal absorption and glucose-insulin dynamics, multiple sim-

⁶⁰² ulations are conducted on 10 virtual patients for 24 hours under various meal ⁶⁰³ scenarios. The PTAs for these ODESCDs that are constructed using the above

⁶⁰⁴ automatic procedure are simulated.

 The trace logs of the physical variables are compared against our ODE solver developed in Matlab and errors are recorded. The absolute errors of variable samples are divided by the variable root mean square to get the rel- ative absolute errors. The percentage mean and standard deviation (std) of these relative absolute errors are depicted in Table 2. It can be noted that the relative errors are negligible and hence demonstrate the correctness of the

⁶¹¹ proposed procedure.

Table 2: Meal and Glucose-Insulin Dynamics ODESCD Variables (Results Against a Mathematical Solver).

⁶¹² For the case of cyber components which are specified by EAC, the following ⁶¹³ steps demonstrate the model validation for this type of PTAs.

 – Sensor: The sensor PTA shown in Fig. 19-a has three locations. It periodi- cally waits in loc_3 before sampling the subcutaneous glucose measurement phy var into the variable meas var. The edge originating from loc_3 to loc_2 synchronizes the sensor with the lossy channel by means of the event trigger 618 S_{et} .

 ϵ_{619} – A new measurement is sent periodically every T_p minutes: to check on ϵ_{620} this property, a new binary flag variable is added to the PTA (chk pt₁ in ⁶²¹ the sensor PTA shown in the graph of Fig. 19-a). The variable is marked ⁶²² whenever a measurement is sent. This can be achieved by flipping the ⁶²³ value of the variable in an ACT term at the same edge as the send ϵ_{624} term (the edge goes from $loc3$ to $loc2$). The variable is monitored on ⁶²⁵ random simulations and its value should be flipped periodically every T_p minute.

Fig. 19: A Part of the Sensor's PTA Communication Network.

 ϵ_{627} – Whenever a measurement is sent, its value should be equal to the most ⁶²⁸ recent sample of the physical variable monitored. Then, the value of ⁶²⁹ the measurement is examined in particular whenever the binary flag, ⁶³⁰ defined above, is flipped. ⁶³¹ – The mapping of all the variables that are shared with other PTAs δ ₆₃₂ should be validated as well. In particular, the variables (*phy_var*, S_{et} , $meas_var$ in the Sensor PTA are examined against G_s in the glucose- \sum_{634} insulin dynamics PTA and (S_{et1}, var_in) in the Act Channel $lossy$ PTA, ⁶³⁵ respectively. For a properly mapped system, the values of the variables ⁶³⁶ in a PTA should be matched to their corresponding ones in all other ⁶³⁷ PTAs at any time. ϵ_{38} - Channel_{lossy}: The PTA shown in Fig. 19-b has seven locations where the 639 edge from loc_3 towards loc_6 synchronizes with the sensor PTA to receive

- ₆₄₀ the measurement value as an input variable var_{in} . Similarly, the edge from ϵ_{64} loc₇ to loc₅ synchronizes with the controller PTA to send the measurement ω value as an output variable var_{out} .
- ⁶⁴³ For every received measurement, the PTA will either successfully relay ϵ_{644} the measurement to the controller with probability P_S or fail with prob-

 ϵ_{49} – A measurement is sent to the controller if and only if the edge with P_S ⁶⁵⁰ probabilistic weight is traversed. This can be checked by examining the ⁶⁵¹ corresponding binary flags.

 ϵ_{652} – Whenever a measurement is sent to the controller $(S_{et2}$ is activated), ϵ_{653} the value of the measurement (*var_out*) should be equal to the value of ϵ_{654} the sample received from the sensor (var_in) .

- ϵ_{655} To validate the mapping of variables, the values of the variables $(S_{e72},$ ⁶⁵⁶ var out) should be equal to the values of the corresponding variables 657 in the controller PTA (S_{et1}, G) , respectively.
- ⁶⁵⁸ Controller: The PTA shown in Fig. 20-a has five locations where the edge ϵ_{659} from loc_3 towards loc_5 synchronizes with the lossy channel PTA to receive ϵ_{660} the glucose measurement value as an input variable G. Similarly, the edge ϵ_{661} from loc_4 to loc_2 synchronizes with the actuator PTA to send the control ⁶⁶² value as an output variable IIR.

Fig. 20: A Part of the Artificial Pancreas Control Network.

- If the time since the last delivered measurement exceeds the control ϵ_{669} period T_p , the value of the variable IIR is zeroed and the event trigger
- S_{et2} is activated to command insulin delivery suspension.

5.2 Model Verification

 PTAs are constructed for all the CPS components and are exported to a file for verification and analysis. This file is loaded into UPPAAL-SMC. A network of PTAs is created by instantiating and parallel-composing the PTA blocks using the UPPAAL-SMC. The tool performs hypothesis testing on queries specified by Metric Interval Temporal Logic (MITL). Also, monitor-based verification [8] could be used to specify more complicated queries using simpler expressions or for queries that are beyond the expressive power of MITL query language. To demonstrate the use of the proposed framework to analyze real-life systems, UPPAAL-SMC is utilized to investigate safety properties of the arti-ficial pancreas CPS that is supposed to regulate the blood glucose levels using a pre-configured closed-loop control strategy. A good control strategy would be able to satisfy safety properties under normal conditions. Moreover, it would accommodate disturbances and minimize the side effects of faults.

 Using this system, the sensor periodically transmits measurements to the controller over a wireless channel, but wireless packet transmission failure can cause measurements to be missing. Missing measurements can be handled using different control approaches. With the proposed SMC modeling and analysis, it is possible to evaluate whether each control approach can preserve safety properties at various error rates.

 Whenever the controller receives a measurement, it calculates the required insulin rate using the standard PID. For a missing measurement, the controller

will behave in one of three ways.

- Sustain: The controller will keep configuring the last valid calculated insulin rate until a new valid measurement is received.
- Suspend: The controller will stop insulin delivery until a new valid mea-surement is received.

 – Revert: The controller will revert to a low value which is equal to the PID controller basal insulin rate until a new valid measurement is received.

 The analysis is conducted on a database of 10 adult patients publicly acces- sible [38]. Each patient receives random meals of (20−50) grams carbohydrates each. Per patient, the analysis evaluates whether or not the controller satisfies safety properties for each of the three control configurations: sustain, suspend or revert. The following two safety properties are defined for analysis.

 $737 - S_A$: At all times, the blood glucose levels should not cross the boundaries ⁷³⁸ of severe minimum and maximum values of 50 mg/dL and 300 mg/dL , respectively.

 $740 - S_B$: Whenever the glucose elevates to values higher than the threshold of 180 mg/dL, it should restore its value to normal range below this threshold within a maximum of two and a half hours.

 T_{743} The first safety property S_A is straightforward and can be described using the following MITL query:

$Pr[t \le 1440]$ (\Box G ≥ 50 && G ≤ 300) ≥ 0.99

 This property specifies that throughout the test duration of one day (1440 $_{747}$ minutes) the blood glucose levels should be limited between 50 mg/dL and $_{748}$ 300 mg/dL with a probability above or equal 99%. On the other side, the $_{749}$ second safety property S_B is too elaborate to describe in a query using MITL. Instead, a monitor PTA is designed to observe the time duration for each $_{751}$ time the glucose level elevates above 180 mq/dL as shown in Fig. 21. Having this variable (tg₁₈₀) assigned, the safety property S_B is described using the following MITL property.

 $Pr[t \le 1440]$ ($\begin{bmatrix} 1 & t_{9180} <= 150 \end{bmatrix} \ge 0.99$

Fig. 21: The Duration of Time Where Glucose Exceeds 180 (mg/dL) $\{tg_{180}\}$

 This property is satisfied if and only if a high glucose incidence would recover to normal range within two and a half hours maximum with at least 99% probability. It should be noted that the monitor PTA is constructed by creating a SysML activity diagram characterizing its behavior as shown in Fig. 22 and applying the new proposed automatic procedure to convert the EAC description into a PTA component that is parallel-composed with the other PTAs in UPPAAL-SMC tool.

$$
Act_Monitor = l \mapsto l_1 : B_C(l_2 : (C = G > 180) \mapsto N_1, l_3 : (C = G \le 180) \mapsto N_2)
$$

\n
$$
N_1 = l_4 : D_{CB}(G < 180, G \ge 180 - 1 \&\& t g'_{180} == 1)
$$

\n
$$
\mapsto l_5 : Act(t g_{180} = 0) \mapsto l_6 : D_{CB}(G > 180 - 1, G \le 180) \mapsto l_4
$$

\n
$$
N_2 = l_6
$$

 The percentage of the patients with violations for each safety property is shown in Fig. 23. No violations exist in the absence of message errors. When message errors are introduced, the three control configurations result in varying behaviors. For safety property S_A , message errors result in a gradual increase of violations on sustain and suspend approaches. However, the revert approach preserves the safety property S_A on all patients with message errors up to 50%. For safety property S_B , the suspend approach fails on timely recovery of normal glucose levels in the existence of message errors. The other configurations, *sustain* and *revert*, avoid S_B violations with message errors as high as 30%. When the error rate exceeds that level, violations start to occur with the revert approach suffering more violations.

Fig. 22: SysML Activity Diagram of the Monitor

5.3 Discussion

To understand the experimental results, the following facts should be noted.

– In the absence of message errors, the three control configurations fall back

to being the same standard PID controller.

 – The analyzed artificial pancreas is a single hormone unidirectional con- troller (as opposed to dual-hormone systems [22]). This implies that it can deliver more insulin to counteract the excessive glucose levels, but it can only counteract low glucose levels by suspending the insulin delivery and waiting for the pre-delivered insulin to get consumed by the physiological processes inside the body.

 Putting this in mind can explain the results on safety property S_A (left graph in Fig. 23), where the sustain approach accidentally delivers excessive insulin amounts that can cause glucose drops below 50 mq/dL even at low message error rates. On the contrary, the suspend approach stops insulin delivery and can make it up by restarting insulin delivery when valid messages are received again. However, when the message error rate increases, there is a chance that the suspend approach might fail to prevent large glucose levels above 300 mq/dL . Instead of completely halting the insulin delivery, the revert continues delivering small amounts of insulin to make a balance between the two other approaches and avoid extreme highs and lows of glucose. The same concept explains the results in the right graph of Fig. 23 where the sustain approach provides better performance in avoiding long times with glucose levels above $796 \quad 180 \quad mg/dL$ as opposed to the *suspend* approach which fails to avoid that. The revert approach provides performance similar to the sustain approach

Fig. 23: Results for Safety Properties Violations: S_A (left) and S_B (right)

6 Conclusion

 In this work, a framework is proposed to formally model and automatically an- alyze cyber-physical systems using statistical model checking. The framework takes models specified using SysML modeling language as SysML diagrams. The latters are then represented in textual format using the proposed enhanced activity calculus and ordinary-differential equations of SysML constraint dia- grams. Then, these textual representations of the model components are fed into a new proposed conversion algorithm that automatically transforms them into equivalent priced timed automata. Thus, the resulting model is fed into UPPAAL-SMC statistical model checking tool which parallel-composes all the system components and verifies the system behaviors. The use of the proposed framework to verify safety properties is demonstrated on an artificial pancreas case study. The proposed framework can be used to verify the safety of cyber-physical

 systems and gain insight into their most critical behaviors at an early stage of the design process, thus saving valuable time and money. Ultimately, it promotes the integration of real-life problems into model-based analysis and allows experimenting a variety of scenarios without compromising participant 818 safety. This is especially crucial when dealing with systems that involve human life, whether directly as in biomedical systems or indirectly as in automotive 820 systems. In the near future, we target to improve the framework to cover more issues, mainly:

- Develop a library of different CPS components and applications.
- Model more cyber-physical systems with a focus on faults and security threats.
- Before the CPS deployment, we target also to automatically generate the source code related to the modeled and analyzed CPS.
- $\frac{1}{827}$ Provide guidance to correct the CPS whenever a property has not been satisfied.
- Establish a mechanism for defining CPS complex requirements automati-cally and easily.

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