

# A framework for modeling and analyzing cyber-physical systems using statistical model checking

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- A Framework for Modeling and Analyzing
- <sup>2</sup> Cyber-Physical Systems using Statistical Model
- <sup>3</sup> Checking
- 4 Abdel-Latif Alshalalfah
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Abstract The trustworthiness of a cyber-physical system is essential for it 8 to be qualified for utilization in most real-life deployments. This is espe-9 cially critical for systems that deal with precious human lives. Although these 10 safety-critical systems can be investigated using both experimental testing and 11 model-based verification, accurate models have the potential to permit risk-12 free mimicking of the system behavior even in the most extreme scenarios. 13 To overcome the CPS modelling and design challenges, the INCOSE/OMG 14 standard System Modeling Language (SysML) is utilized in this work to ac-15 curately specify cyber-physical systems. For that, a bounded set of SysML 16 constructs are defined to precisely capture the semantics of continuous-time 17 and discrete-time system behaviors. Then, the SysML constructs are substi-18 tuted by developing a new algebra, called Enhanced Activity Calculus (EAC). 19 So, EAC helps construct equivalent priced timed automata models by develop-20 ing a new systematic procedure to correctly translate the SysML models into 21 the statistical model checking tool UPPAAL-SMC inputs. The latter checks 22 whether the system is correct and safe or not. Moreover, the soundness of the 23 developed translation mechanism has been proved and its effectiveness has 24 been shown on a real use case, namely the artificial pancreas. 25

 $_{26}$  Keywords System Modeling Language  $\cdot$  Enhanced Activity Calculus  $\cdot$ 

 $_{27}$  Cyber-Physical Systems  $\cdot$  Model Transformation  $\cdot$  Model-Based Verification  $\cdot$ 

 $_{28}$  Safety-Critical  $\cdot$  Statistical Model Checking  $\cdot$  Priced Timed Automata

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## 29 Nomenclature

- <sup>30</sup> CPS Cyber-Physical Systems
- <sup>31</sup> EAC Enhanced Activity Calculus
- 32 HA Hybrid Automata
- <sup>33</sup> MITL Metric Interval Temporal Logic
- 34 ODE Ordinary-Differential Equations
- $_{35}$  ODESCD ODE of SysML Constraint Diagram
- <sup>36</sup> *PID* Proportional-Integral-Derivative
- 37 PTA Priced Timed Automata
- $_{38}$  SMC Statistical Model Checking
- <sup>39</sup> SysML Systems Modeling Language

### 40 1 Introduction

Whether human-operated or autonomous, embedded systems are designed to 41 improve the quality of life for people. From embedded computing to distributed 42 systems, Cyber-Physical Systems (CPS) refer to computing systems that in-43 teract with control and management objects [53]. As technology advances, 44 CPS is being used in a wide range of applications [30]. With the reduction 45 in size and cost of hardware, along with accelerating innovation and advance-46 ment in sensor and computational technologies, CPS has been able to spread 47 to all types of applications. Through horizontal expansion, CPS has gained 48 popularity in all types of application. Also, CPS flourished vertically to find a 49 foot in more complex and dependable applications. From daily applications, 50 the various success stories have encouraged designers to develop CPS for au-51 tonomous control compared to the early systems which required some degree 52 of human interaction [23, 41]. Nowadays, wireless body area networks are uti-53 lized to connect devices that observe the status of the physiological dynamics 54 [16]. As a result, health conditions can be monitored and treated in a timely 55 manner. Patients with chronic diseases will particularly benefit from this. For 56 example, with around half a billion diabetes worldwide [42], an automatic glu-57 cose controller is necessary for them to live a normal life while still avoiding 58 the health complications related to their situation. 59 In order to get approval certificates from the appropriate authorization 60 entities, these systems must prove their safety and robustness under all sce-61 narios [11]. However, for real-life deployments, only qualified systems must 62 meet these safety requirements. From the first prototype to the final fabri-63 cated product, verifying the safety of CPS is a vital step in the development 64

<sup>65</sup> process. The system-level analysis provides feedback early in the design pro-<sup>66</sup> cess, and by identifying safety issues early, time and resources are not wasted <sup>67</sup> [29]. Additionally, the system-level analysis helps understand CPS limitations

<sup>68</sup> and define the requirements of CPS components for safe operation. Further-

<sup>69</sup> more, CPS can be verified under extreme scenarios that would be impossible

 $\mathbf{2}$ 

to conduct in real life without taking extraordinary risks by using appropriaterealistic models.

Analyzing systems at the system level is either accomplished through sim-72 ulation testing or through formal methods. In the former approach, specific 73 input scenarios can be used to evaluate CPS behavior. Yet, it does not give 74 confidence on the state space coverage. On the other hand, formal methods 75 such as model checking [10] provide exhaustive coverage for the whole state 76 space. Unfortunately, formal techniques do not scale well for realistic hybrid 77 systems and suffer from the infamous state space explosion problem [21]. 78 As a compromise between these two approaches, Statistical Model Check-79

ing (SMC) can be used for verification. Although it does not provide exhaustive
 coverage for the state space, SMC can be used to introduce statistical guarantees for safety properties with feasible computational resources. In a nutshell,
 the following are the main contributions of this work.

Proposing a novel systematic procedure to capture the semantics of SysML based diagrams and to construct its equivalent PTA models for SMC anal vsis.

 $_{87}$  – The effectiveness of the proposed framework to analyze a medical CPS is

demonstrated on an artificial pancreas case study. In particular, the safety

of the system is verified using SMC to evaluate the ability of three control configurations to mitigate message errors.

Below is an outline of the remainder of the article. The literature review is 91 presented in Section 2, and then Section 3 demonstrates, through an artificial 92 pancreas example, the SysML graphical and textual modeling. Afterwards, 93 Section 4 introduces the new proposed automatic construction of equivalent 94 Priced Timed Automata (PTA) models and proves the soundness of the de-95 veloped approach. Section 5 illustrates the experiments conducted for model 96 validation and safety verification procedure by an example experiment, and 97 section 6 concludes the article. 98

### 99 2 Literature Review

<sup>100</sup> With the growing demand for CPS applications, several research works have

investigated the verification and safety analysis problems related generally to
 CPS. Based on our surveyed initiatives, we have identified two main categories:

<sup>103</sup> Formal verification and Simulation based approaches.

### <sup>104</sup> 2.1 Simulation based approaches

Even before the advent of modern computer systems, the term *Simulation* is known as the process of designing a model of a real system to conduct exper-

<sup>107</sup> iments [52]. These experiments aim at understanding the system's behavior

<sup>108</sup> or evaluating a strategy associated with the system. Simulation software tools

109	have flourished with the advent and availability of low-cost computational
110	systems.
111	Liu et al. [37] have used the open-source toolkit MATSim [55] to inves-
112	tigate large-scale transportation patterns for shared autonomous vehicles. In
113	their work, agent-based modelling is applied to estimate mode choices between
114	human-driven vehicles, shared autonomous vehicles, and public transit. Fol-
115	lowing a cost function that takes into account, the out-of-pocket, the trip time,
116	and the waiting time, each driver chooses one of the three options of travel
117	mode. The analysis is done for different fare levels, demographic settings, and
118	shared autonomous vehicles availability to give implications on sustainability.
119	In $[32]$ , an assessment of the safety of leader-follower configurations for
120	autonomous radar semi-trucks is made based on different environmental con-
121	ditions. The simulation model is developed with the commercial platforms
122	AmeSim, PreScan, and Matlab-Simulink to study the effect of environmental
123	conditions on safety margins in semi-truck convoy platooning. The autonomy
124	in their simulated vehicles is enabled by adopting sensors for radar, global
125	positioning systems, and short-range inter-vehicle communication.
126	Instead of fully autonomous vehicles, the work in [5] addressed semi-autonomous
127	vehicles implementing adaptive cruise control coexisting with regular vehicles
128	and trucks. The vehicles enter the four-lane highway with a user-predefined
129	arrival rate in the microscopic Java-based F.A.S.T. traffic simulator. Their

findings show that a high penetration of semi-autonomous vehicles can in crease traffic performance, especially under high traffic conditions.

Connected and autonomous vehicles and their impact on road safety are discussed in [47]. Initially, the simulation software VISSIM is utilized to study a test-bed that mimics a three-lane motorway with traffic statistics measured from a real one in England. A lateral and longitudinal control algorithm is then tested for its ability to reduce traffic conflicts at different market penetration rates.

From a healthcare perspective, a falsification approach is presented in [48, 9] to simulate and verify the artificial pancreas controller in a simulation environment. The S-Taliro tool which applies falsification simulations terminates with either finding a safety violation or failure to find, without the explicit guarantee that such one does not exist. Instead, the tool uses robustness metric to predict the distance between simulation outcomes and safety margins.

<sup>145</sup> 2.2 Formal based approaches

<sup>146</sup> Unlike the numerical simulation approaches which mimic the behavior of real

147 systems, formal methods apply analytical reasoning to derive mathematically-

proven properties that characterize the system behavior. These characteristics
 are not always attainable, but when achieved they provide guaranteed out-

are not always attainable, but when achieved they provide guars
 comes which is an asset that helps verify safety-critical systems.

4

In [28], piece-wise affine hybrid automata was used to analyze the wind turbine dynamics in SpaceEx verification platform [20]. Even though Kekatos et al. reduced some blocks for better scalability, the resulting model contained around 16 million locations, which would hinder the ability to analyze more elaborate systems. However, classical hybrid automata (HA) tools and methodlogies suffer from this limitation [49].

The problem of formally analyzing a swarm of robots is handled by Schupp et al. [50]. The cooperative decentralized robots are modeled as a hybrid system and investigated by *flowpipe* analysis where the sets of reachable states are iteratively over-approximated [19]. Although the work in [50] deals with a simple model of distributed synchronization, it still causes some scalability challenges that are partially encountered by compositional analysis and optimized transition emulation.

Using a combination of simulations and formal analysis, [46] examines 164 patient-controlled analysia's safety. So, to analyze the resulting CPS, its de-165 tailed behavior is modeled in Simulink. Then, to qualify the CPS for model 166 checking, the continuous dynamics are abstracted away from the system model 167 and then replaced by simple timing constraints with the target to be analyzed 168 in UPPAAL model checker [7]. Additionally, UPPAAL is also used in [26] to 169 verify control algorithms in a dual chamber implantable pacemaker. Mean-170 while, a timed automata representation of the heart and the pacemaker are 171 used to specify the ability of the algorithms to avoid unsafe regions of the state 172 space. The proposed approach covers the whole state space, yet only the state 173 space that is modeled. Thus, this excludes certain control and physiological 174 behaviors that are beyond the expressive power of the modeling language and 175 the computational feasibility of the verification technique. In fact, these be-176 haviors can be skipped in some systems but are essential to correctly analyze 177 hybrid systems with continuous-time variables. 178

<sup>179</sup> 2.3 Statistical model checking based approach

SMC consists of observing a number of simulation runs or system executions
and using statistical methods to reason about formal properties [35].

After some preliminary works such as the hypothesis testing of modal prop-182 erties in process algebra [33], initial results for SMC had witnessed progress 183 since 2002 [58] with the corresponding term introduced for the first time in 184 2004 [51]. Reasoning about reachability problems with SMC algorithms pro-185 vides mainly guarantees on the probability error bound. Depending on the type 186 of reachability expression being dealt with, the error bound can be calculated 187 by utilizing the appropriate classical mathematics such as Monte Carlo with 188 Chernoff-Hoeffding error bounds [43, 24] or hypothesis testing using Wald's 189 sequential analysis [56]. 190

<sup>191</sup> Different tools exist that implement SMC algorithms such as PRISM [31],

<sup>192</sup> UPPAAL-SMC [12, 15], BIP [39], and Ymer [57]. Since their inception, SMC

<sup>193</sup> tools have been utilized to study many discrete-time and continuous-time sys-

<sup>194</sup> tems. To list a few: airplane cabin communication system [6], distributed sen-

<sup>195</sup> sor network [36], energy-aware house heating [13], biological mechanisms of

<sup>196</sup> the genetic oscillator [14], real-time streaming protocol [44], artificial pancreas

<sup>197</sup> [2, 4], anesthesia control [3], and coordinated emergency braking system [1].

<sup>198</sup> 2.4 Model Construction

In order to analyze the system, it is necessary to first convert the specifications into the modeling language used by the analysis tool. Furthermore, an adequate level of expertise is required to model the system properly when done manually. Furthermore, formal modeling languages tend to be more errorprone due to their low readability. Therefore, the need arises to facilitate the process of constructing formal models by automatically translating high-level models that incur better readability.

In [28], the system modeled in Simulink is translated into SpaceEx modeling language in four steps. After the Simulink model is modified to comply with the verification standards, the tool SL2SX [40] is employed to handle the main translation step and construct a SpaceEx model. Afterwards, compositional syntactic hybridization [27] and validation are conducted to achieve a model ready to be analyzed.

An approach to transform Simulink models into UPPAAL-SMC is proposed in [18]. The work is employed on two automotive use cases for brake-by-wire and an adjustable speed limiter. The Simulink models are first reduced by the flattening procedure. Then, each block is replaced by an equivalent timed automaton composed of three locations: start, offset, and operate. Still, their approach does not implement complex real-valued blocks in UPPAAL-SMC but addresses them in Simulink instead.

Instead of commercial modeling tools, System Modeling Language (SysML) 219 [54] can be used to specify CPS. SysML is the defacto standard modeling lan-220 guage for systems engineering with rich semantics and expressive power suffi-221 cient to describe system structures and behaviors at various levels of abstrac-222 tion [25]. Ouchani et al. [45] constructed probabilistic automata by converting 223 SysML models. The resulting models were incurred to analyze security prop-224 erties of the real-time streaming protocol using the probabilistic model checker 225 PRISM [31]. 226

Compared to the studied initiatives, the main objective of this work is to develop a framework that enables efficient modeling and analysis for CPS. The proposed framework takes system behavior specified using SysML diagrams as input. The novelty of this proposed work is summarized by the following contributions.

Defining a bounded set of SysML constructs that are sufficient to capture
 the behaviors of the CPS discrete-time and continuous-time dynamics.

Defining textual specification language for SysML by extending the seman tics initially developed in [17, 45].

- Proposing a novel systematic procedure to transform the SysML behavioral 236 specifications into PTA. Compared with the previous works that processed 237 models specified in the commercial tool Simulink [28, 40, 18] or did not 238 support modeling physical processes [18, 45], this new proposed approach 239 defines a systematic procedure to process SysML models for the CPS and to construct an equivalent PTA model for analysis by supporting more 241 features and expressive powers to specify physical properties like time, rate 242 and real-numbers related measurements. 243

- The soundness of the proposed approach has been proven and its effective-244 ness to analyze CPS is demonstrated on an artificial pancreas system.
- 245

#### 3 The Proposed Framework 246

Fig. 1 provides a brief overview of the proposed framework that runs on the 247 following steps. 248

- ① The process starts with the initial identification of the CPS to explore the 249 nature of its application. This step helps specify the system's requirements 250 including the safety properties that have to be met. 251
- 2 The topology of the system is defined by specifying the functional compo-252 nents of the system which are used to create the SysML block definition 253 diagram. Also, the interactions between the CPS components are used to 254 define the SysML flow diagram. Integrated CPS are formed from continu-255 ous real-time components describing physical processes and discrete-time 25 components describing cyber processes. 257
- (3) By relying on the existing topologies, behavioral models for the physical 258 components are imported in the form of Ordinary-Differential Equations 259 (ODE). Similarly, the cyber components of the system are imported from 260 design specifications in the form of discrete variations. 261
- (4) Physical and cyber components are represented using SysML parametric 262 constraint diagrams and activity diagrams, respectively. 263
- To automate further processing, each of the SysML diagrams are written in (5) 264 textual format. For a constraint diagram describing the physical dynamics, 265 the representation is done using the proposed syntax named Ordinary-266 Differential Equations of SysML Constraint Diagram (ODESCD). For an 267 activity diagram describing a component's behavior, the representation is 268 done using the proposed Enhanced Activity Calculus (EAC). 269
- (6) A new systematic algorithm is proposed to convert ODESCD and EAC 270 blocks into equivalent PTA blocks. The SysML block definition diagram, 271 describing the system's structure, specifies the input/output connections 272 of each PTA block. 273
- The various PTA blocks for physical dynamics and component behaviors 274 (7)are mapped as described by the flow diagram. The parallel composition of 275
- all the PTA blocks form the integrated CPS that is processed. 276



Fig. 1: The Proposed Framework Workflow.

The analysis tool UPPAAL-SMC is used to analyze the system and verify
 the safety properties. The framework is demonstrated on an artificial pan creas system alongside a proposed representation of continuous-time and
 discrete-time dynamics.

phenomena. An observer block is then added to the system by developing a 283 behavioral model for that block which is specified using a SysML activity 284 diagram. The new block is then processed as component of the CPS to 285 construct an equivalent PTA model. By adding these monitor blocks, more

- 286 complex safety properties are simplified and expressed easily in order to 287
- be examined for safely. 288

#### 3.1 SysML Graphical and Textual Modeling 289

#### 3.1.1 SysML for continuous-time dynamics 290

The dynamics of physical processes describe the flow of physical quantities 291 in the real world. These quantities are represented by real-valued real-time 292 variables where the derivative of a variable is equivalent to the change on 293 its associated physical quantity. Therefore, it is common for continuous-time 294 dynamics to be specified by a system of ODEs. SysML constraint diagrams 295 can be used to model ODEs.

Notation 1 (ODE of SysML Constraint Diagram (ODESCD)). ODESCD 297 is defined as a tuple  $(X, X^0, K, P, R, F, I, O)$ , where: 298

- -X is a set of real-time real-valued differentiable variables, 299
- $-X^0$  is a set of initial values, 300

296

- -K is a set of real-valued equation coefficients, 301
- -P is a set of constant real-valued parameters, 302
- -R is a set of real-time real-valued variables, 303
- -F(X,R) is a set of real-valued functions, 304
- $-I \in X \cup K \cup P \cup R$  is a set of input variables, and 305
- $O \in X$  is a set of output variables. 306

**Definition 1** (Semantics of ODESCD). Let  $(X, X^0, K, P, R, F, I, O)$  be a ODESCD, its semantics is defined as the dynamics of a physical system described by a set of ODEs as follows (in this context a subscript in the form of  $a_1 \times a_2$  indicates the matrix dimensions).

$$X'_{n \times 1}(t) = K_{n \times n}(X, P, R, t) X_{n \times 1} + F_{n \times 1}(X, R)$$
(1)

$$X_{n \times 1}(t=0) = X_{n \times 1}^{0}$$
(2)

 $X'_{n \times 1} = [x_1 \ x_2 \dots x_n]$  is the set of differential variables to be solved,  $X^0_{n \times 1} = [x^0_1 \ x^0_2 \dots x^0_n]$  is the set of initial values for the differential variables, 307 308  $K_{n \times n}(X, P, R, t)$  is the set of differential equation coefficients which can be 309 constants or functions of constant parameters, real-time variables or time, P310 is the set of additional constant parameters for the equation, R is the set of 311 additional real-time variables,  $F_{n \times 1}(X, R)$  is the additional terms of the ODE, 312  $I \in X \cup K \cup P \cup R$  is the set of input variables which can be parameters or 313 real-time variables, and  $O \in X$  is the set of output variables which is a subset 314 of the ODE solution. 315

In this system, I is defined to utilize variables and parameters that are provided as input to the ODESCD definition, and O is used to export the desired variables from the solution of ODESCD.

## \* ODESCD example: meal glucose absorption model

X is a vector representing carbo-hydrate measures in the stomach where  $Q_{sto1}$  and  $Q_{sto2}$  are the stomach glucose amounts in solid state and liquid state, respectively, and *rag* is the blood glucose rate of appearance. These physical quantities are initially nulled as assigned in  $X^0$ . Fig. 2 depicts the SysML constraints block diagram for meal absorption variations measures.

$$X = [Q_{sto1} \ Q_{sto2} \ rag]^T \tag{3}$$

$$X^0 = [0 \ 0 \ 0]^T \tag{4}$$

$$\begin{bmatrix} K \end{bmatrix} = \begin{bmatrix} -k_{gri} & 0 & 0 \\ k_{gri} & -k_{empt}(Q_{sto1}(t) + Q_{sto2}(t), D_{meal}) & 0 \\ 0 & \frac{f.k_{abs}}{BW} k_{empt}(Q_{sto1}(t) + Q_{sto2}(t), D_{meal}) & -k_{abs} \end{bmatrix}$$

 $k_{empt}(Q, D_{meal}) = \begin{cases} k_{min} + \frac{k_{max} - k_{min}}{2} (tanh(\alpha(Q - b.D_{meal})) - tanh(\beta(Q - c.D_{meal})) + 2) & D_{meal} > 0 \\ 0 & D_{meal} = 0 \end{cases}$ 

$$\alpha = \frac{5}{2.D_{meal}.(1-b)} \tag{5}$$

$$\beta = \frac{5}{2.D_{meal}.c} \tag{6}$$

$$P = \{k_{gri}, k_{abs}, f, BW, b, c, k_{min}, k_{max}\}$$
(7)

$$R = \{ cur\_Meal, D_{meal} \}$$
(8)

$$F(X,R) = [cur\_Meal(t) \ 0 \ 0]^T \tag{9}$$

$$I = \{cur\_Meal, D_{meal}\}$$
(10)

$$O = \{rag\}\tag{11}$$

#### ♦ ODESCD example: glucose-insulin dynamics

X is a vector representing the various physical quantities for the glucose and insulin dynamics all over the body compartments.  $I_{sc1}$  and  $I_{sc2}$  are the insulin levels in the subcutaneous tissues,  $X_1$  is the insulin in the interstitial fluid,  $\{G, G_s, G_t\}$  are the glucose levels in the blood, subcutaneous tissues, and slowly equilibrating tissues respectively.  $I_p$  is the plasma insulin,  $I_l$  is the portal vein insulin, and  $I_d$  is the delayed insulin signal. These physical quantities are initialized as in the vector  $X^0$ . Fig. 3 depicts the SysML Constraint Block diagrams for Glucose-Insulin variations measures.

$$X = [I_{sc1} I_{sc2} X_1 G_s I_1 I_d I_l I_p G G_t]^T$$
(12)

$$X^{0} = [I_{sc1_{ss}} I_{sc2_{ss}} 0 G_{i} I_{b} I_{b} I_{l_{b}} I_{p_{b}} G_{i} G_{t_{i}}]^{T}$$
(13)

«constraint» Meal Absorption : Equality
$\begin{array}{l} Q_{sto1}' == - \ k_{gri}  .  Q_{\_sto1} + cur\_Meal(t) \\ Q_{sto2}' == - \ k_{empt}(Q_{sto1} + Q_{sto2}  ,  D_{meal})  .  Q_{sto2} + \ k_{gri}  .  Q_{sto1} \\ rag' == - \ k_{abs}  .  rag + f  .  k_{abs}  .  k_{empt}(Q_{sto1} + Q_{sto2}  ,  D_{meal})  .  Q_{sto2} / \ BW \end{array}$

# Parameters:

Input: cur\_Meal [mg/min] (real time glucose intake), D<sub>meal</sub> [g] (meal carbs) Output: rag [mg/Kg/min] (real time glucose rate of appearance in the blood)

Fig. 2: SysML Constraint Block for Meal Absorption

$$P = \{I_{sc1_{ss}}, I_{sc2_{ss}}, G_i, I_{l_b}, I_{p_b}, G_{t_b}, G_b, k_d, k_{a1}, k_{a2}, p_{2u}, V_I, I_b, T_s, k_i, V_G, m_1, m_6, m_2, m_4, k_{p1}, k_{p2}, k_{p3}, F_{cns}, k_{e1}, k_{e2}, k_1, k_2, V_{m0}, V_{mx}, K_{m0}, K_{mx}\}$$

$$R = \{rag, IIR\}\tag{15}$$

$$F(X,R) = [IIR, 0, -p_{2u}.I_b, 0, 0, 0, 0, 0, 0, \frac{rag + k_{p1} - F_{cns}}{V_G} - k_{e1}.max(0, G - \frac{k_{e2}}{V_G}), -\frac{(V_{m0} + V_{mx}.X_1).G_t}{K_{m0} + K_{mx}.X_1 + G_t}]^T$$

$$I = \{rag, IIR\}$$
(16)
(17)

$$O = \{G_s, G\}\tag{18}$$

«constraint» Glucose-Insulin Dynamics : Equality
$\begin{split} I_{sc1}' &= -(k_d + k_{a1}) \cdot I_{sc1} + IIR \\ I_{sc2}' &= k_d \cdot I_{sc1} - k_{a2} \cdot I_{sc2} \\ \chi_1' &= -p_{2u} \cdot \chi_1 + p_{2u} \cdot (I_p / V_l - I_b) \\ G_s' &= -(G_s - G) / T_s \\ I_1' &= -k_i \cdot (I_1 - I_p / V_l) \\ I_d' &= -k_i \cdot (I_d - I_1) \\ I_l' &= -(m_1 + (m_6 \cdot m_1 / (1 - m_6))) \cdot I_l + m_2 \cdot I_p \\ I_p' &= -(m_2 + m_4) \cdot I_p + m_1 \cdot I_l + k_{a1} \cdot I_{sc1} + k_{a2} \cdot I_{sc2} \\ G' &= -(K_{p1} - k_{p2} \cdot G \cdot V_G - k_{p3} \cdot I_d - F_{cns} - k_{e1} \cdot max(0, G \cdot V_G - k_{e2}) - k_1 \cdot G \cdot V_G + k_2 \cdot G_t + rag) / V_G \\ G_t' &= -(Gt \cdot (V_{m0} + V_{mx} \cdot \chi_1)) / (K_{m0} + K_{mx} \cdot \chi + G_t) + k_1 \cdot G \cdot V_G - k_2 \cdot G_t \end{split}$
Parameters: Input: IIR [ <i>pmol/Kg/min</i> ] (subcutaneous insulin infusion rate) Input: rag [ <i>mg/Kg/min</i> ] (meal glucose rate of appearance in the plasma) Output: G <sub>s</sub> [ <i>mg/dL</i> ] (real time subcutaneous glucose level) Output: G [ <i>mg/dL</i> ] (real time blood glucose level)

Fig. 3: SysML Constraint Block for Glucose-Insulin Dynamics

#### 319 3.1.2 SysML for discrete-time dynamics

Discrete-time dynamics are described by SysML activity diagrams. So, in or-320 der to precisely describe CPS and capture exactly its underlying semantics, we 321 develop Enhanced Activity Calculus (EAC) to formally describe SysML activ-322 ity diagrams by extending NuAC presented in [17, 45]. These enhancements 323 include redefining existing nodes as well as proposing new nodes for time-324 bounded delay, constraint-bounded delay, and competing events. The list of 325 the used activity nodes and their textual EAC representation is shown in Table 326 1. 327

### 328 **\*** EAC example: artificial pancreas

The artificial pancreas is composed of a sensor (Fig. 4) that periodically measures the glucose level, sends it over wireless channel (Fig. 5) to the controller. Then, the controller (Fig. 6) calculates the required amount of insulin, and the actuator (Fig. 7) applies the control action. Lastly, the SysML activity diagram describing the meal scenario is shown in Fig. 8.

SysML Term	SysML Activity Diagram Structure	EAC Syntax
Activity Initial Node		$l \mapsto N$
Action Node		$l : ACT(A) \mapsto N$
Call Procedure		$l : CALL_P(A) \mapsto N$
Send Node	{S,X}	$l : \{S, X\} ! \mapsto N$
Receive Node	$\sum \{S,X_{src},X_{dst}\} \longrightarrow N$	$l : \{S, X_{src}, X_{dst}\} ? \mapsto N$
Merge Node	$\bigwedge \longrightarrow (N)$	$l : Mrg \mapsto N$
Guarded Branch	(N <sub>1</sub> ) «True (?) False (N <sub>2</sub> )	$l : B_C(l_{i_1} : (C = C_1) \mapsto N_1, \\ l_{i_2} : (C = C_2) \mapsto N_2, \dots)$
Probabilistic Branch	$(N_1) = P_2 = (N_2)$	$l : B_P(l_{i_1} : (P = P_1) \mapsto N_1, \\ l_{i_2} : (P = P_2) \mapsto N_2, \dots)$
Time-Bounded Delay Node	TB{t <sub>mi</sub> :t <sub>max</sub> , C}	$l : D_T B(\tau_{min} : \tau_{max} , C) \mapsto N$
Constraint-Bounded Delay Node		$l : D_{CB}(C_{ter}, C) \mapsto N$
Competing Events		$l : Comp\_Events(N_1 \mapsto N_2, N_3 \mapsto N_4,)$

Table 1: SysML Enhanced Acti	ivity Calculus	Nodes Syntax.
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By substituting the SysML nodes with their textual equivalents following Table 1, the EAC representation of these activity diagrams is shown below.

$$\begin{aligned} Act\_Sensor = l \mapsto l_1 : Mrg \mapsto N_1 \\ N_1 = l_2 : D_{TB}(T_p,) \mapsto l_3 : ACT(meas\_var = phy\_var) \mapsto l_4 : \{S_{et}, meas\_var\}! \mapsto l_1 \end{aligned}$$

$$\begin{aligned} Act\_Channel_{lossy} = l \mapsto l_1 : Mrg \mapsto N_1 \\ N_1 = l_2 : \{S_{et1}, var\_in, var\_out\}? \mapsto l_3 : B_P(l_4 : (P = P_S) \mapsto N_2, l_5 : (P = P_F) \mapsto l_6 : Mrg \mapsto l_1) \\ N_2 = l_7 : \{S_{et2}, var\_out\}! \mapsto l_6 \end{aligned}$$



Fig. 4: SysML Activity Diagram of the Sensor.



Fig. 5: SysML Activity Diagram of the Lossy Channel.

$$\begin{split} Act\_Ctrl = l \mapsto l_1 : Mrg \mapsto N_1 \\ N_1 = l_2 : Comp\_Events(l_3 : \{S_{et1}, G, G_r\}? \mapsto N_2, \, l_4 : D_{TB}(Tp,) \mapsto N_3) \\ N_2 = l_5 : CALL_P(IIR = Act\_Calc\_IIR(t)) \mapsto l_6 : Mrg \mapsto N_4 \\ N_3 = l_7 : CALL_P(IIR = Act\_Calc\_IIR\_missing(t)) \mapsto l_6 \\ N_4 = l_8 : \{S_{et2}, IIR\}! \mapsto l_1 \end{split}$$

 $Act\_Actuator = l \mapsto l_1: Mrg \mapsto l_2: \{S_{et}, IIR_c, IIR_r\}? \mapsto l_3: ACT(IIR = IIR_r) \mapsto l_1$ 



Fig. 6: SysML Activity Diagram of the Controller.



Fig. 7: SysML Activity Diagram of the Actuator.

 $\begin{aligned} Act\_meal\_scenario = l \mapsto l_1 : Mrg \mapsto l_2 : D_{TB}(inter\_meal\_time,) \mapsto N_1 \\ N_1 = l_3 : ACT(cur\_meal = 1000 * meal\_carbs/meal\_dur, D\_meal = (Q_{sto1} + Q_{sto2})/1000) \mapsto N_2 \\ N_2 = l_4 : D_{TB}(meal\_dur, D\_meal' == cur\_meal/1000) \mapsto l_5 : ACT(cur\_meal = 0) \mapsto l_1 \end{aligned}$ 

- <sup>334</sup> **CPS** architecture and flow for artificial pancreas
- <sup>335</sup> The SysML block definition diagram shown in Fig. 9 defines the blocks and
- their input/output ports. Also, the mapping of the blocks and the variables



Fig. 8: SysML Activity Diagram of the Meal Scenario.

- $_{\rm 337}$   $\,$  as well as the flow of information among these blocks are defined in the flow
- <sup>338</sup> internal block diagram shown in Fig. 10.



Fig. 9: SysML Architectural Block Definition Diagram of the Closed-Loop Glucose Control System.

#### **4 CPS Semantics** 339

The system behavior should be represented in the suitable formality that 340 matches the language of the analysis tool. To do so, the SysML components are 341 converted into a network of equivalent PTA models. In the following, the PTA 342

is defined and the new proposed automated conversion procedure is presented. 343

**Definition 2** (PTA). A PTA for CPS is a tuple  $(L, l_0, L_{lbl}, L_{IP}, L_{OP}, E, X)$ 344  $V_g, INV(X, VAR), A(V_g), G(X, V_g), S_{et}, P_r)$ , where: 345

- -L is a finite set of locations, 346
- $-l_0 \in L$  is the initial location, 347
- $-L_{lbl}$  is a set of labels, 348
- $-L_{IP}$  is a finite set of input ports, 349
- $-L_{OP}$  is a finite set of output ports, 350
- -E is a finite set of edges, 351
- -X is a finite set of clocks. 352
- -VAR is a finite set of general-type variables, 353
- $-INV(X, V_q)$  is a finite set of invariants over PTA clocks X and variables 354  $V_q$ ,  $A(V_q)$  is a finite set of actions on the variables  $V_g$ , 355
- $-G(X, V_q)$  is a finite set of atomic propositions on PTA clocks X and vari-356
- ables  $V_g$ ,  $S_{et}$  is a finite set of synchronization event triggers, and 357
- $-P_r$  is a finite set of probabilistic weights. 358

**Definition 3** (Semantics of CPS). Let  $(L, l_0, L_{lbl}, L_{IP}, L_{OP}, E, X, V_q)$ 359

- $INV(X, VAR), A(V_q), G(X, V_q), S_{et}, P_r)$  be a PTA for CPS. The semantics 360 are defined as a hybrid transition system composed of a set of locations L361 interconnected by a set of edges E through sets of input ports IP and output 362 ports OP, where: 363
- Locations  $L = \{l_1, l_2, \ldots, l_{n_1}\}$ , where the  $i^{th}$  location  $l_i \in L$  labelled 364  $label_i \in L_{lbl}$  having the invariant constraints  $inv_i \in INV$  and connected to 365
- the input port  $x_{ip}$  and the output ports  $X_{op}$  is referred as  $l_i(label_i, inv_i, x_{ip}, X_{op})$ . 366 Edges  $E = \{e_1, e_2, \dots, e_{n_2}\}$ , where the  $i^{th}$  edge running the action  $a \in A$
- 367 and triggering the synchronization event  $s_{et} \in S_{et}$ , and connected to the 368
- output port  $x_{op}$  and input port  $x_{ip}$  is referred as  $e_i = \{a, s_{et}, x_{op}, x_{ip}\}$ . 369
- Input ports  $L_{IP} = \{l_{ip_1}, l_{ip_1}, \dots, l_{ip_{n_1}}\}$ , where the  $i^{th}$  input port  $l_{ip_i} \in L_{IP}$  sourcing from incoming edges  $X_e$  towards the  $i^{th}$  location  $l_i \in L$  and 370 371
- 372
- applying the action  $a \in A$  is defined as  $l_{ip_i} = \{a, X_e, i\}$ . Output ports  $L_{OP} = \{l_{op_1}, l_{op_1}, \ldots, l_{op_{n3}}\}$ , where the  $k^{th}$  output port  $l_{op_k} \in L_{OP}$  sourcing from the  $i^{th}$  location  $L_i$  towards the  $j^{th}$  edge  $e_j$ , guarded by 373 374 the atomic proposition  $g \in G$ , triggered by the event trigger  $s_{et} \in S_{et}$ , and 375 having the probabilistic weight  $p_r \in P_r$  is defined as  $l_{op_k} = \{g, s_{et}, p_r, i, j\}$ . 376

PTAs traverse sequentially through output ports towards edges, followed 377 by input ports towards the next location, starting at an initial location denoted 378 by  $l_0$ . In the case of the PTA being at location  $l_i$ , the invariant  $inv_i$  must be 379 satisfied as long as the PTA is at location  $L_i$ . Similarly, an output port that has 380 a guard g with respect to its traversal can only be traversed if this guard g has 381

- been satisfied. An output port with an event trigger  $s_{et}$  is synchronized with another PTA, so that the output port is only traversed when it is activated by the corresponding event trigger on the edge of the other PTA. Furthermore,
- an output port can be traversed among other output ports in a probabilistic
- manner by assigning a probability weight  $p_r$  to each of the possible candidates
- 387 for traversal of the output port.



Fig. 10: SysML Flow Internal Block Diagram of the Closed-Loop Glucose Control System.

#### 388 4.1 Converting SysML into Equivalent PTA

- <sup>389</sup> In order to analyze the CPS described in SysML, it is necessary to model the
- <sup>390</sup> hybrid system in PTA. So, SysML blocks are translated into equivalent PTA
- <sup>391</sup> blocks which are parallel-composed to construct the hybrid system's global
- <sup>392</sup> behavior. The synchronization of actions and the transfer of values are specified<sup>393</sup> using shared variables.
- The template of each PTA is instantiated with its input/output parameters properly defined. The SysML flow internal block diagram (as in Fig. 10) is consulted to define global variables for the parameters connecting the PTA components of the system. When instantiating a PTA template, the parameters are passed by-reference except for constant parameters that are passed by-value. Instead, those constants can be defined as local variables in the PTA. The following rules govern the definition of variables in PTA models.
- 401 Continuous real-valued parameters are defined using clock variables.

- 402 Discrete real-valued parameters are defined using floating point variables.
- $_{403}$  An event trigger should be activated whenever a discrete variable is up-
- dated, so that the other PTAs are notified about the new update.
- Discrete integer parameters are defined as integer variables and are passed
   between PTAs similar to the floating point variables.
- When assigning or initializing a numerical variable, it can be evaluated to
   a single value or to a range of values for a uniformly-distributed random
   assignment.

#### 410 Converting SysML EAC into PTA

411 This part presents the detailed procedure for constructing a PTA block that

- 412 represents a SysML EAC block. Alongside the description of the conversion
- steps, an illustrative example is provided for converting the Act\_Channel<sub>lossy</sub>
  block from EAC into PTA.
- 414 DIOCK IFOIII EAU IIITO PIA.
- <sup>415</sup> The first step is to merge all the EAC nodes into the main EAC con-<sup>416</sup> struct. This is done by iterating through the auxiliary constructs  $(N_x)$  and substituting for them in the main construct as depicted in Fig. 11.



Fig. 11: EAC Lossy Channel Example - Merging Nodes

417

- Connecting the EAC terms so that each arrow is uniquely identified as presented in Fig. 12.

- <sup>420</sup> Handling branching terms ( $B_P$  or Comp\_Events) and replicating the EAC <sup>421</sup> construct, so that each branching term has only one path at a time. This <sup>422</sup> is done by iterating through the branching terms and taking one branch <sup>423</sup> at a time as shown in Fig. 13.
- <sup>424</sup> Building the PTA skeleton using the procedure described in Algorithm 1.
- 425 The resulting skeleton for  $Act_Channel_{lossy}$  example is shown in Fig. 14.

$$\begin{split} & \Downarrow \Downarrow \\ Act\_Channel_{lossy} = l \xrightarrow{1} l_1 : Mrg \xrightarrow{2} l_2 : \{S_{et1}, var\_in, var\_out\}? \xrightarrow{3} l_3 : B_P(l_4 : (P = P_S) \xrightarrow{4} l_7 : \{S_{et2}, var\_out\}! \xrightarrow{5} l_6, l_5 : (P = P_F) \xrightarrow{6} l_6 : Mrg \xrightarrow{7} l_1) \end{split}$$





Fig. 13: EAC Lossy Channel Example - Branches Handling

For each location node that has non-empty prev field, insert an input port. 426 For locations with *next* field, insert an output port per edge node that is 427 outgoing from the location. In the following steps, when an EAC term is 428 linked to an output port, the one that is connected to the location where 429 the EAC belongs is identified. In case the location is attached to two or 430 more output ports, the sequence of EAC terms in the path construct is 431 used to identify the corresponding output port. Moreover, an EAC node 432 that shows up in more than one path is only converted once at its first 433 appearance. 434

- 435 Replacing the following EAC terms with their equivalent PTA terms.
- $_{436}$  EAC term *l* signifies the location as an initial location.

<sup>437</sup>  $- D_{TB}(\tau_{min} : \tau_{max}, C)$ : Declare a clock variable t, Add a reset for the <sup>438</sup> clock (t = 0) to the input port action, Add the following constraint <sup>439</sup>  $(t \leq \tau_{max} \&\& C)$  to the invariants *inv* of the location, and add the <sup>440</sup> following  $(t \geq \tau_{min})$  to the guard g of the output port.

<sup>441</sup> - { $S, X_{src}, X_{dst}$ }?: Add the event trigger S? to the respective field  $s_{et}$  of <sup>442</sup> the output port, and add the assignment ( $X_{dst} = X_{src}$ ) to the action <sup>443</sup> of the edge outgoing from the output port.

### Algorithm 1 Construction of PTA Skeleton.

for each: EAC\_Path  $\triangleright$  The first node of a path has no predecessor. 1:  $prev_Node = \emptyset$ for each:  $EAC\_Node \in EAC\_Path$ 2: if  $EAC\_Node \in \{Mrg, Comp\_Events, B_P, D(*), \{*, *\}?, (P = *)\}$  then  $EAC_Type = LOCATION$ 3: 4: else if  $EAC\_Node \in \{\mapsto, ACT, CALL_P, \{*, *\}\}$  then  $EAC\_Type = EDGE$ 5: 6: **end if** 7: if  $EAC_Node$  processed before then  $cur_Node = PTA_Node[EAC_Node]$  $\triangleright$  Traverse through the node. 8:  $cur\_Node.prev.addMember(prev\_Node)$  $\triangleright$  Create a new input port for the node. 9: 10: $prev_Node.next.addMember(cur_Node)$   $\triangleright$  Create a new output port for the node. 11: else if  $EAC_Type == prev_Node.type$  then  $cur\_Node.EAC.addMember(EAC\_Node)$ 12: $\triangleright$  A compliment for the previous node. 13: else  $\triangleright$  A node not processed yet.  $cur\_Node = create\_Node(type = EAC\_Type)$  $\triangleright$  Create the node. 14:15: $cur\_Node.EAC.addMember(EAC\_Node)$  $\triangleright$  Traverse through the node. 16:cur\_Node.prev.addMember(prev\_Node)  $\triangleright$  Create an input port. 17: $prev\_Node.next.addMember(cur\_Node)$  $\triangleright$  Create an output port. 18: end if

$Node_{ID} = [$ $Node_1 = [$ $Node_2 = [$ $Node_3 = [$	type, LOCATION, EDGE, LOCATION,	prev, $\emptyset,$ 1, $\{2,11\},$	next, 2, 3, 4,	$EAC \\ l \\ \stackrel{l}{\mapsto} \\ l_1 \\ l_1$	] ] ]
$Node_4 = [$ $Node_5 = [$ $Node_6 = [$ $Node_7 = [$	EDGE, LOCATION, EDGE, LOCATION,	3, 4, 5, 6,	5, 6, 7, $\{8, 10\},$	$\begin{array}{c} \stackrel{2}{\mapsto}\\ l_{2}\\ \stackrel{3}{\mapsto}\\ \{l_{3}, l_{4}, l_{5}\}\end{array}$	] ] ]
$Node_8 = [$ $Node_9 = [$ $Node_{10} = [$ $Node_{11} = [$	EDGE, LOCATION, EDGE, EDGE,	7, $\{8, 10\},$ 7, 9,	9, 11, 9, 3,	$\{\stackrel{4}{\mapsto}, l_7, \stackrel{5}{\mapsto}\}$ $\begin{array}{c}l_6\\\stackrel{6}{\mapsto}\\\stackrel{7}{\mapsto}\end{array}$	] ] ]

Fig. 14: EAC Lossy Channel Example - Building Skeleton.

144	$- \{S, X\}!$ : Add this event trigger S! to the respective event trigger field
145	$s_{et}$ of the containing edge.
46	$-(P = p_x)$ : Add the following probabilistic weight to the corresponding
147	field $p_r$ of the output port.
148	-ACT(A): Add the action A to the corresponding field a of the edge.

- $CALL_P(A)$ : Add the behavior call A() to the action field a of the edge.
- 449 The results shown in Fig. 15 are obtained when applying the above rules 450 on the Act\_Channel<sub>lossy</sub> example:



Fig. 15: EAC Lossy Channel Example - Replacing EAC with PTA Terms.

451

<sup>-</sup> After each EAC receive node, insert a new location between the event 452 trigger and the signal sampling. Also, a new location is added when an 453 output port with a probabilistic weight is directly followed by an edge 454 with an EAC send node. This is done so that the send node is separated 455 from the output port. When applying this on the  $Act\_Channel_{lossy}$ , the 456 results look like Fig. 16 457



Fig. 16: EAC Lossy Channel Example - Inserting Locations

Divide the locations into transient and regular (time-consuming) locations.
A regular location is identified by having either a guard or an event trigger
on the output port, or by having a non-empty invariant field. For the
Act\_Channel<sub>lossy</sub> example, all the locations are transient except location *loc*<sub>3</sub> which has an event trigger on the output port.

- The rate of all local clocks should be identified on all regular locations. 463 Therefore, if a clock is not supposed to evolve in a specific regular location, 464 its evolution rate should be assigned to 0 in the invariants field of that 465 location. 466
- When exporting the PTAs into an XML file compatible with UPPAAL-467 SMC analyzer, transient locations are specified as *urgent* locations except 468 for the following: 469
- A location which emits output ports with probabilistic weights (location 470  $loc_4$  in Act\_Channel<sub>lossy</sub> example) is defined as an anchor point (for 471 syntax compatibility). 472
- The first location following a receive node (location *loc*<sub>6</sub> in *Act\_Channel*<sub>lossy</sub> 473 example) should be set to *committed* for synchronization correctness 474 (semantic compatibility). 475

The resulting PTA diagram for the above transformed lossy channel is 476 depicted in Fig. 17. This PTA initializes at the location  $loc_1$ . This location 477 is urgent which means that no time progress and hence the PTA will move 478 instantly through the output port  $op_1$ , the edge  $e_1$ , the input port  $ip_2$  to the 479 next location  $loc_2$ . This location is also an urgent location and hence the PTA 480 will move through the output port  $op_2$ , edge  $e_2$ , and the input port  $ip_2$  towards 481 the location  $loc_3$ . The output port  $op_3$  is activated by the event trigger  $S_{et1}$ ? 482 which is controlled by another PTA (the sensor in this case). Then, this sensor 483 activates the event trigger  $S_{et}$  to send a new measurement (the variable  $var_{in}$ ) 484 through the wireless channel. When triggered by the event trigger  $S_{et1}$ , the 485 lossy channel PTA moves through the output port  $op_3$ , the edge  $e_7$ , and the 486 input port  $ip_6$  towards the committed location  $loc_6$ . Like the urgent location, 487 a committed location freezes time but also synchronizes the PTAs so that the 488 correct sequence of actions takes place. In this PTA, it is required so that the 489 up-to-date version of the measurement value  $var_{in}$  is read. 490

The PTA moves through  $op_6$  towards the edge  $e_3$  where the measurement 491 is sampled, and then through the input port  $ip_4$  to the location  $loc_4$  which is a 492 probabilistic branching point. Then, the PTA will take a branch depending on 493 probability weights. At one branch, the message will get lost and so the PTA 494 takes the output port  $op_{(4,2)}$  towards the edge  $e_5$  and the input port  $ip_5$  to 495 reach the location  $loc_5$ . In the other branch, the measurement is successfully 496 relayed so the other PTA (the controller in this case) is notified with the event 497 trigger  $S_{et2}$ , so the PTA moves through  $op_{(4,1)}$ ,  $e_4$ ,  $ip_7$  to the transient location 498  $loc_7$  towards the output port  $op_7$  and the edge  $e_8$  (where  $S_{et2}$ ! is activated) to 499 the input port  $ip_5$  while merging with the other branch in the location  $loc_5$ . 500 Finally, the PTA moves via the output port  $op_5$  and the edge  $e_6$  through the 501 input port  $ip_2$  to merge in the location  $loc_2$ .

#### \* modeling ODESCD using PTA 503

502

The same rules apply to convert ODESCD into PTA where the ODE variables 504

X are defined as clock variables. The PTA is composed of one location where 505 the rates of the ODE variables X are assigned using equality constraints in 506

the invariant field of the main location. If some variables or parameters are 507



Fig. 17: The Resulting PTA Diagram for the Lossy Channel

 $_{\tt 508}$   $\,$  initialized with random values, an additional transient initial location is added

<sup>509</sup> with the variables assigned in the edge connecting the initial location to the

<sup>510</sup> main operational location.

#### 511 4.2 Soundness

After presenting the semantics of CPS and PTA, we prove the soundness of the 512 developed framework. First, lets  $\Gamma$  to be a function denoting Algorithm 1. Now, 513 we prove the soundness of the transformation by showing that  $\Gamma$  guarantees 514 the integrity of the CPS design, i.e. no added, modified, or excluded behavior. 515 Thus, an equivalent PTA behavioral model is produced. Then, we show that 516 the soundness proves the satisfiability preservation of MILT expressions when 517 applying  $\Gamma$ . 518 As depicted in Fig. 18, we have to show the nature of the relation  $\mathscr{R}$ , 519

that compares both  $PTA^{cps}$  and  $PTA^{f}$  constructed through EAC and PTA semantics rules respectively, while preserving both behaviours. Indeed, the relation  $\mathscr{R}$  could be determined by comparing the semantics of each term in EAC and the semantics of its image obtained by the function  $\Gamma$ . Since the goal is to guarantee the behaviour integrity of  $PTA^{cps}$  and the resulting  $PTA^{f}$ should not differ from  $PTA^{cps}$ , Lemma 1 proves that  $\mathscr{R}$  is a bisimulation relation.

<sup>527</sup> Lemma 1 The binary relation  $\mathscr{R}$ , is a bisimulation, whenever  $S\mathscr{R}S$ , satisfies <sup>528</sup> the following.

<sup>529</sup> 1. If  $S \xrightarrow{\alpha} S'$  then  $\exists \hat{S}'$  such that  $\hat{S} \xrightarrow{\alpha} \hat{S}'$  and  $S' \equiv_{\mathscr{R}} \hat{S}'$ .



Fig. 18: The Transformation Soundness Schema.

530 2. If  $\hat{S} \xrightarrow{\alpha} \hat{S}'$  then  $\exists S$  such that  $S' \xrightarrow{\alpha} S'$  and  $\hat{S}' \equiv_{\mathscr{R}} S'$ .

<sup>531</sup> Proof Let's consider  $A \in PTA^{cps}$  and  $B \in PTA^{f}$  where  $\Gamma(A) = B$ . So, by <sup>532</sup> induction on EAC terms, we prove that  $\mathscr{R}$  is a bisimulation binary relation as <sup>533</sup> follows.

- When  $A = i \to \mathcal{N}$ , then based on the rule  $\exists S \xrightarrow{\alpha} S'$  such that  $S = i \to \mathcal{N}$ 534 and  $S' = \overline{i \to \mathcal{N}}$ , we will have,  $\Gamma(A) = \Gamma(i \to \mathcal{N}) = initial$  to *i*. Thus, 535  $initial \land \neg i \xrightarrow{\alpha} \neg initial \land i \in B^s$ . Then,  $PTA^{cps} \mathscr{R} PTA^f$  when  $A = i \to \mathscr{N}$ . 536 - For  $\{S, X\}! \to \mathcal{N}$ , then  $\overline{X \to \mathcal{N}} \longrightarrow \{S, X, X'\}! \to \overline{\mathcal{N}} \in PTA^{cps}$ . Also, 537  $\Gamma(A) = resource \langle v \rangle \rightarrow \mathcal{N}$  which means  $resource_v \land \neg \mathcal{N} \xrightarrow{prt} \neg resource_v \land$ 538  $\mathcal{N} \in B^s$ . So,  $PTA^{cps} \mathscr{R} PTA^f$ . 539 In the case of  $A = resource? v \to \mathcal{N}$ , we have  $\overline{resource? v \mapsto \mathcal{N}} \longrightarrow$ 540 resource?  $v \rightarrow \overline{\mathcal{N}} \in B^{sn}$ . Thus,  $PTA^{cps} \mathscr{R} PTA^{f}$ . 541 - If  $A = resource! v \to \mathcal{N}$ , we have  $\overline{resource! v \mapsto \mathcal{N}} \longrightarrow resource! v \mapsto$ 542  $\overline{\mathcal{N}} \in B^{sn} \ R \ resourceout_v \land \exists v \land \neg \mathcal{N} \xrightarrow{prt} \neg resourceout_v \land \mathcal{N} \in B^s.$ 543 - By considering  $A = resource \uparrow expression \rightarrow \mathcal{N}$ , then  $resource \uparrow expression \rightarrow \mathcal{N} \rightarrow \mathcal{N}$ 544 resource  $\uparrow$  expression  $\mapsto \overline{\mathcal{N}} \in B^{sn}$ . As a result, we have resource<sub>v</sub>  $\land$ 545  $\neg \mathcal{N} \xrightarrow{prt} \neg resource_v \land v = newvalue \land \mathcal{N} \in B^s$ , which means  $PTA^{cps} \mathscr{R}$ 546  $PTA^{f}$ . 547 For the decision term  $A = D(g_{v1}, \mathcal{N}_1, \mathcal{N}_2)$ , we differentiate two cases: 548 1. When  $\neg g_{v1} \models \top$ , we have  $\overline{D(g_{v1}, \mathscr{N}_1, \mathscr{N}_2)} \xrightarrow{\neg g_{v1}} D(g_{v1}, \mathscr{N}_1, \overline{\mathscr{N}_2}) \in B^{sn}$ 549 by relying on the decision rule. Also, we have:  $\Gamma(A) = \{ \text{on } \text{prt}_i \text{ from } \}$ 550 source to  $\mathscr{N}$  provided  $g_{v_i} = eval(v_i) : i \in \{1, 2\}\}$ . Also, since  $\neg g_{v_1} \models \top$ , 551 we have:  $\overline{D(g_{v1}, \mathscr{N}_1, \mathscr{N}_2)} \xrightarrow{\neg g_{v1}} D(g_{v1}, \mathscr{N}_1, \overline{\mathscr{N}_2}) \in B^s.$ 552

- <sup>553</sup> 2. For the other case, when  $g_{v1} \models \top$ , we have shown that  $D(g_{v1}, \mathscr{N}_1, \mathscr{N}_2) \equiv D(\neg g_{v1}, \mathscr{N}_2, \mathscr{N}_1)$ . Thus,  $PTA^{cps} \mathscr{R} PTA^f$ .
- We have shown that for each EAC term, we have  $PTA^{cps} \mathscr{R} PTA^{f}$  in which result that  $\mathscr{R}$ , is a bissimulation relation and it is symmetric.

Based on the illustration presented in Fig 18, the transformation's objective is to verify functional properties of the generated PTA model and then infer satisfiability results for the CPS design. Using Lemma 1, Proposition 1
 demonstrates how the properties expressed in MITL logic can be satisfied.

**Proposition 1**  $\forall A \in PTA^{cps}, B \in PTA^{f} \ s.t. \ \Gamma(A) = B, we have: \forall \phi \in MITL : PTA^{f} \models \phi \implies PTA^{cps} \models \phi.$ 

- <sup>563</sup> Proof By induction on MITL terms, we prove that  $B \models \phi \implies A \models \phi$ .
- 1. First, let's consider the state formulae  $\phi = \varphi_1 \wedge \varphi_2$  where  $B \models \phi$ . Now, we show the satisfiability of  $\phi$  on A for the following EAC terms. - For  $A = i \rightarrow \mathcal{N}$ , we have  $i \rightarrow \mathcal{N} \xrightarrow{\alpha} i \rightarrow \mathcal{N} R$  initial  $\wedge \neg i \xrightarrow{\alpha}$
- <sup>567</sup>  $\neg initial \land i.$  If  $initial \land \neg i \models \varphi_1 \land \varphi_2$  means  $initial \land \neg i = \varphi_1 \land \varphi_2.$ <sup>568</sup> Thus,  $i \to \mathcal{N} \models \phi$ , and,  $B \models \phi$
- $\begin{array}{ll} & \text{ For } A = resource < v > \rightarrow \mathscr{N} \text{ when } \neg resource_v \land \mathscr{N} \models \varphi_1 \land \varphi_2, \text{ we} \\ & \text{ have } resource < v > \rightarrow \overline{\mathscr{N}} \models \varphi_1 \land \varphi_2. \text{ Then, } B \models \phi. \end{array}$
- For  $A = resource? v \to \mathcal{N}$ , then  $B \models \phi resourcein_v \land v = newvalue \land$   $\neg \mathcal{N} \xrightarrow{prt} \neg resourcein_v \land \mathcal{N} \models \phi$ . Thus, we have  $\overline{resource? v \to \mathcal{N}} \rightarrow$  $resource? v \to \overline{\mathcal{N}} \models \phi$ . Consequently,  $B \models \phi$ .
- 2. Now, we consider the path formulae  $P_{\bowtie p}[\psi]$ . So, since EAC does not support probabilistic decisions and has only deterministic ones,  $P_{\geq 1}[\psi]$  means  $\psi$  else we consider the case of  $P_{\leq 0}[\psi]$ . Then, we prove by induction on the path operators that  $PTA^{cps} \models \phi$  when  $PTA^f \models \phi$  as follows.
- $\begin{array}{lll} & \text{For } \phi = N\varphi, \ B \models \phi \text{ means } \exists \hat{S} \xrightarrow{\alpha} \hat{S}' \in B^n \text{ such that } \hat{S}' \models \varphi. \text{ In} \\ & \text{addition, since } \mathscr{R} \text{ is symmetric, then } \exists S \xrightarrow{\alpha} S' \in B \text{ such that: } S' \models \varphi. \\ & \text{For } \phi = \varphi_1 \cup^t \varphi_2, \text{ we have } \exists \hat{S}_1 \xrightarrow{\alpha} \cdots \rightarrow \hat{S}_t' \subseteq B^n \text{ such that } \hat{S}_{i:i < t'} \models \varphi_1 \\ & \text{and } \hat{S}_2 \models \varphi_2. \text{ Also, } \mathscr{R} \text{ is symmetric and } \exists S_1 \xrightarrow{\alpha} \cdots \rightarrow S_t' \subseteq B^{sn} \text{ where} \\ & S_i \mathscr{R} \hat{S}_i : 0 < i \leq t. \text{ Thus, } B \models \phi. \end{array}$
- Based on the previous proof, we have shown that for each EAC and MITL term,  $\mathscr{R}$  always preserves the satisfiability of MITL formulae. Consequently,  $B \models \phi \implies A \models \phi$  for all  $\phi$  expressed in MITL when  $PTA^{cps}\mathscr{R} PTA^{f}$ .

#### 586 5 Experimentation

This section shows the effectiveness of the proposed framework by first validating the transformation algorithm. Then, the proposed approach is used to demonstrate how the safety of the obtained model can be examined by statistical model checking over a list of selected functional and safety requirements.

#### <sup>591</sup> 5.1 Validation of the Conversion Procedure

In order to demonstrate the correctness of the proposed approach, PTA models
are validated. Properties are specified for each component of the system that
constrain its functional behavior. To evaluate whether the resulting PTA model
meets the behavioral properties, random simulations are conducted and trace

log analysis is applied to the results. The resulting PTA models are more likely
 to be valid representations of the CPS components when all the properties are
 satisfied.

<sup>599</sup> By comparing the values of the ODE variables with a mathematical ODE

solver, PTAs representing ODESCD are validated. In the case of the ODE-SCDs describing meal absorption and glucose-insulin dynamics, multiple sim-

ulations are conducted on 10 virtual patients for 24 hours under various meal
 scenarios. The PTAs for these ODESCDs that are constructed using the above

automatic procedure are simulated.

The trace logs of the physical variables are compared against our ODE solver developed in Matlab and errors are recorded. The absolute errors of variable samples are divided by the variable root mean square to get the relative absolute errors. The percentage mean and standard deviation (std) of these relative absolute errors are depicted in Table 2. It can be noted that the relative errors are negligible and hence demonstrate the correctness of the proposed procedure.

Table 2: Meal and Glucose-Insulin Dynamics ODESCD Variables (Results Against a Mathematical Solver).

Variable Identifier	Relative Absolute Error {mean+std}
$Q_{sto1}$	$0.018\%\pm 0.007\%$
$Q_{sto2}$	$0.027\%\pm 0.012\%$
rag	$0.028\%\pm 0.012\%$
I <sub>sc1</sub>	$0.166\%\pm 0.049\%$
$I_{sc2}$	$0.117\% \pm 0.039\%$
$X_1$	$0.219\%\pm 0.029\%$
$G_s$	$0.164\% \pm 0.203\%$
$I_1$	$0.071\%\pm 0.030\%$
$I_d$	$0.047\%\pm 0.027\%$
$I_l$	$0.118\% \pm 0.040\%$
$I_p$	$0.118\%\pm 0.040\%$
G	$0.165\%\pm 0.200\%$
$G_s$	$0.180\% \pm 0.209\%$

For the case of cyber components which are specified by EAC, the following steps demonstrate the model validation for this type of PTAs.

<sup>619</sup> – A new measurement is sent periodically every  $T_p$  minutes: to check on <sup>620</sup> this property, a new binary flag variable is added to the PTA (*chk\_pt*<sub>1</sub> in <sup>621</sup> the sensor PTA shown in the graph of Fig. 19-a). The variable is marked whenever a measurement is sent. This can be achieved by flipping the value of the variable in an ACT term at the same edge as the send term (the edge goes from loc3 to loc2). The variable is monitored on random simulations and its value should be flipped periodically every  $T_p$  minute.



Fig. 19: A Part of the Sensor's PTA Communication Network.

- Whenever a measurement is sent, its value should be equal to the most 627 recent sample of the physical variable monitored. Then, the value of 628 the measurement is examined in particular whenever the binary flag, 629 defined above, is flipped. 630 The mapping of all the variables that are shared with other PTAs 631 should be validated as well. In particular, the variables  $(phy\_var, S_{et},$ 632 meas\_var) in the Sensor PTA are examined against  $G_s$  in the glucose-633 insulin dynamics PTA and  $(S_{et1}, var_in)$  in the Act\_Channel<sub>lossy</sub> PTA, 634 respectively. For a properly mapped system, the values of the variables 635 in a PTA should be matched to their corresponding ones in all other 636 PTAs at any time. 637 Channel<sub>lossy</sub>: The PTA shown in Fig. 19-b has seven locations where the 638

- edge from  $loc_3$  towards  $loc_6$  synchronizes with the sensor PTA to receive the measurement value as an input variable  $var_{in}$ . Similarly, the edge from  $loc_7$  to  $loc_5$  synchronizes with the controller PTA to send the measurement value as an output variable  $var_{out}$ .
- <sup>643</sup> For every received measurement, the PTA will either successfully relay <sup>644</sup> the measurement to the controller with probability  $P_S$  or fail with prob-

45	ability $P_F$ . To check on this, binary flags are marked (flipped) on the
46	corresponding edges for success and failure $(chk_{-}pt_{Success})$ and $chk_{-}pt_{F}$
47	in the graph of Fig. 19-b). These binary flags are monitored for random
48	simulations over various probabilistic weights.

 $_{649}$  – A measurement is sent to the controller if and only if the edge with  $P_S$ probabilistic weight is traversed. This can be checked by examining the corresponding binary flags.

<sup>652</sup> - Whenever a measurement is sent to the controller ( $S_{et2}$  is activated), <sup>653</sup> the value of the measurement ( $var\_out$ ) should be equal to the value of <sup>654</sup> the sample received from the sensor ( $var\_in$ ).

- <sup>655</sup> To validate the mapping of variables, the values of the variables ( $S_{et2}$ , <sup>656</sup>  $var\_out$ ) should be equal to the values of the corresponding variables <sup>657</sup> in the controller PTA ( $S_{et1}$ , G), respectively.
- <sup>658</sup> Controller: The PTA shown in Fig. 20-a has five locations where the edge <sup>659</sup> from  $loc_3$  towards  $loc_5$  synchronizes with the lossy channel PTA to receive <sup>660</sup> the glucose measurement value as an input variable *G*. Similarly, the edge <sup>661</sup> from  $loc_4$  to  $loc_2$  synchronizes with the actuator PTA to send the control <sup>662</sup> value as an output variable *IIR*.



Fig. 20: A Part of the Artificial Pancreas Control Network.

663	- For each measurement delivered ( $S_{et1}$ activated), the PTA will read
664	the measurement value $G$ and use it to calculate a new Insulin Infusion
665	Rate (IIR) using the standard Proportional-Integral-Derivative (PID)
666	control $[4, 34]$ . This new calculated value of $IIR$ should be sent to the
667	actuator by activating the event trigger $S_{et2}$ .

<sup>668</sup> – If the time since the last delivered measurement exceeds the control <sup>669</sup> period  $T_p$ , the value of the variable IIR is zeroed and the event trigger <sup>670</sup>  $S_{et2}$  is activated to command insulin delivery suspension.

6 6 6

671		- To validate the mapping of variables, the values of the variables $(S_{et2},$
672		IIR) should be equal to the values of the corresponding variables in
673		the actuator PTA $(S_{et}, IIR_c)$ , respectively.
674	_	Actuator: The PTA shown in Fig. 20-b has four locations where the edge
675		from $loc_3$ towards $loc_4$ synchronizes with the controller PTA to receive the
676		control value as an input variable $IIR_c$ . The actuator then modifies the
677		corresponding physical values in the glucose-insulin dynamics PTA through
678		the output variable <i>IIR</i> .
679		– Whenever a new infusion rate value $IIR_c$ control command from the
680		controller PTA is received ( $S_{et}$ activation), the actuator should update
681		the value of the physical real-time variable IIR.
682		- To verify the mapping of variables, the values for the variables $IIR$ in
683		both PTAs, actuator and glucose-insulin dynamics, should be equal at
684		all times.
685	_	Meal Scenario: This PTA is used to assign the input variables of the meal
686		absorption model such as the carbohydrate amounts and the inter-meal
687		times.
688		– Each of the variables ( <i>meal_carbs</i> , <i>meal_dur</i> , <i>inter_meal_time</i> ) takes
689		a value ranging between the configured minimum and maximum with
690		uniform distribution. Based on the histogram of the variables, this can
691		be validated.
692		$-$ The PTA should generate the values of the real-time variables ( <i>cur_meal</i> ,
693		$D_{meal}$ complying with the right amounts of insulin-carbs, meal dura-
694		tions, and inter-meal times.
695		- Validation for the mapping of the variables ( <i>cur_meal</i> , $D_{meal}$ , $Q_{sto1}$ ,
696		$Q_{sto2}$ ) with their corresponding variables in the meal absorption PTA.
697	_	Meal Absorption & Glucose-Insulin Dynamics:
698		– The variables of the ODEs for both PTAs are observed and compared
699		using our ODE simulator. The values for all variables should be identi-
700		cal to the ones calculated by the mathematical ODE solver developed
701		in Matlab except for marginal numerical computational errors, e.g. pre-
702		cision.

703 5.2 Model Verification

PTAs are constructed for all the CPS components and are exported to a file for 704 verification and analysis. This file is loaded into UPPAAL-SMC. A network of 705 PTAs is created by instantiating and parallel-composing the PTA blocks using 706 the UPPAAL-SMC. The tool performs hypothesis testing on queries specified 707 by Metric Interval Temporal Logic (MITL). Also, monitor-based verification 708 [8] could be used to specify more complicated queries using simpler expressions 709 or for queries that are beyond the expressive power of MITL query language. 710 To demonstrate the use of the proposed framework to analyze real-life 711 systems, UPPAAL-SMC is utilized to investigate safety properties of the arti-712 ficial pancreas CPS that is supposed to regulate the blood glucose levels using 713

a pre-configured closed-loop control strategy. A good control strategy would be
able to satisfy safety properties under normal conditions. Moreover, it would
accommodate disturbances and minimize the side effects of faults.

Using this system, the sensor periodically transmits measurements to the controller over a wireless channel, but wireless packet transmission failure can cause measurements to be missing. Missing measurements can be handled using different control approaches. With the proposed SMC modeling and analysis, it is possible to evaluate whether each control approach can preserve safety properties at various error rates.

Whenever the controller receives a measurement, it calculates the required insulin rate using the standard PID. For a missing measurement, the controller will behave in one of three ways.

- Sustain: The controller will keep configuring the last valid calculated insulin

 - Sustain: The controller will keep configuring the la rate until a new valid measurement is received.

- Suspend: The controller will stop insulin delivery until a new valid mea surement is received.

- Revert: The controller will revert to a low value which is equal to the PID
 controller basal insulin rate until a new valid measurement is received.

The analysis is conducted on a database of 10 adult patients publicly accessible [38]. Each patient receives random meals of (20-50) grams carbohydrates each. Per patient, the analysis evaluates whether or not the controller satisfies safety properties for each of the three control configurations: sustain, suspend or revert. The following two safety properties are defined for analysis.

 $_{737}$  –  $S_A$ : At all times, the blood glucose levels should not cross the boundaries of severe minimum and maximum values of 50 mg/dL and 300 mg/dL, respectively.

 $_{740}$  -  $S_B$ : Whenever the glucose elevates to values higher than the threshold of  $_{741}$  180 mg/dL, it should restore its value to normal range below this threshold within a maximum of two and a half hours.

The first safety property  $S_A$  is straightforward and can be described using the following MITL query:

## $_{^{745}}$ $\Pr[ ext{t}{\leq}1440]$ ( [] $ext{G}$ >= 50 && G <= 300 ) $\geq$ 0.99

This property specifies that throughout the test duration of one day (1440 746 minutes) the blood glucose levels should be limited between 50 mg/dL and 747 300 mg/dL with a probability above or equal 99%. On the other side, the 748 second safety property  $S_B$  is too elaborate to describe in a query using MITL. 749 Instead, a monitor PTA is designed to observe the time duration for each 750 time the glucose level elevates above 180 mg/dL as shown in Fig. 21. Having 751 this variable  $(tg_{180})$  assigned, the safety property  $S_B$  is described using the 752 following MITL property. 753

754  $\Pr[t \le 1440]$  ( []  $tg_{180} <= 150$  )  $\ge 0.99$ 



Fig. 21: The Duration of Time Where Glucose Exceeds 180 (mg/dL)  $\{tg_{180}\}$ 

This property is satisfied if and only if a high glucose incidence would recover to normal range within two and a half hours maximum with at least 99% probability. It should be noted that the monitor PTA is constructed by creating a SysML activity diagram characterizing its behavior as shown in Fig. 22 and applying the new proposed automatic procedure to convert the EAC description into a PTA component that is parallel-composed with the other PTAs in UPPAAL-SMC tool.

$$Act_{-}Monitor = l \mapsto l_{1} : B_{C}(l_{2} : (C = G > 180) \mapsto N_{1}, l_{3} : (C = G \le 180) \mapsto N_{2})$$
$$N_{1} = l_{4} : D_{CB}(G < 180, G \ge 180 - 1\&\&tg'_{180} = 1)$$
$$\mapsto l_{5} : Act(tg_{180} = 0) \mapsto l_{6} : D_{CB}(G > 180 - 1, G \le 180) \mapsto l_{4}$$
$$N_{2} = l_{6}$$

762

The percentage of the patients with violations for each safety property 763 is shown in Fig. 23. No violations exist in the absence of message errors. 764 When message errors are introduced, the three control configurations result in 765 varying behaviors. For safety property  $S_A$ , message errors result in a gradual 766 increase of violations on sustain and suspend approaches. However, the revert 767 approach preserves the safety property  $S_A$  on all patients with message errors 768 up to 50%. For safety property  $S_B$ , the suspend approach fails on timely 769 recovery of normal glucose levels in the existence of message errors. The other 770 configurations, sustain and revert, avoid  $S_B$  violations with message errors as 771 high as 30%. When the error rate exceeds that level, violations start to occur 772 with the *revert* approach suffering more violations. 773



Fig. 22: SysML Activity Diagram of the Monitor

774 5.3 Discussion

<sup>775</sup> To understand the experimental results, the following facts should be noted.

In the absence of message errors, the three control configurations fall back
 to being the same standard PID controller.

The analyzed artificial pancreas is a single hormone unidirectional controller (as opposed to dual-hormone systems [22]). This implies that it can deliver more insulin to counteract the excessive glucose levels, but it can only counteract low glucose levels by suspending the insulin delivery and

waiting for the pre-delivered insulin to get consumed by the physiologicalprocesses inside the body.

Putting this in mind can explain the results on safety property  $S_A$  (left graph 784 in Fig. 23), where the *sustain* approach accidentally delivers excessive insulin 785 amounts that can cause glucose drops below 50 mq/dL even at low message 786 error rates. On the contrary, the *suspend* approach stops insulin delivery and 787 can make it up by restarting insulin delivery when valid messages are received 788 again. However, when the message error rate increases, there is a chance that 789 the suspend approach might fail to prevent large glucose levels above 300 790 mq/dL. Instead of completely halting the insulin delivery, the revert continues 791 delivering small amounts of insulin to make a balance between the two other 792 approaches and avoid extreme highs and lows of glucose. The same concept 793 explains the results in the right graph of Fig. 23 where the sustain approach 794 provides better performance in avoiding long times with glucose levels above 795 180 mg/dL as opposed to the suspend approach which fails to avoid that. 796 The *revert* approach provides performance similar to the *sustain* approach 797



Fig. 23: Results for Safety Properties Violations:  $S_A$  (left) and  $S_B$  (right)

#### 800 6 Conclusion

In this work, a framework is proposed to formally model and automatically an-801 alyze cyber-physical systems using statistical model checking. The framework 802 takes models specified using SysML modeling language as SysML diagrams. 803 The latters are then represented in textual format using the proposed enhanced 804 activity calculus and ordinary-differential equations of SysML constraint dia-805 grams. Then, these textual representations of the model components are fed 806 into a new proposed conversion algorithm that automatically transforms them 807 into equivalent priced timed automata. Thus, the resulting model is fed into 808 UPPAAL-SMC statistical model checking tool which parallel-composes all the 809 system components and verifies the system behaviors. The use of the proposed 810 framework to verify safety properties is demonstrated on an artificial pancreas 811 case study. 812

The proposed framework can be used to verify the safety of cyber-physical 813 systems and gain insight into their most critical behaviors at an early stage 814 of the design process, thus saving valuable time and money. Ultimately, it 815 promotes the integration of real-life problems into model-based analysis and 816 allows experimenting a variety of scenarios without compromising participant 817 safety. This is especially crucial when dealing with systems that involve human 818 life, whether directly as in biomedical systems or indirectly as in automotive 819 systems. In the near future, we target to improve the framework to cover more 820 issues, mainly: 821

- 822 Develop a library of different CPS components and applications.
- Model more cyber-physical systems with a focus on faults and security
   threats.
- Before the CPS deployment, we target also to automatically generate the
   source code related to the modeled and analyzed CPS.

- Provide guidance to correct the CPS whenever a property has not been
   satisfied.
- Establish a mechanism for defining CPS complex requirements automati cally and easily.

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