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# Lithium-ion glass gating of HgTe nanocrystal film with designed light-matter coupling

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**Abstract:** Nanocrystals (NCs) band gap can be easily tuned over the infrared range, making them appealing for the design of cost-effective sensors. Though their growth has reached a high level of maturity, their doping remains a poorly controlled parameter raising the need for post-synthesis tuning strategies. As a result, the phototransistor device geometry offers an interesting alternative to the photoconductor one, allowing carrier density control. Phototransistors based on NCs targeting integrated infrared sensing have to (*i*) be compatible with low-temperature operation, (*ii*) avoid liquid handling, and (*iii*) enable large carrier density tuning. These constraints drive the search for innovative gate technologies beyond traditional dielectric or conventional liquid and ion gel electrolytes. Here, we explore lithium-ion glass gating and apply it to channels made of HgTe narrow band gap NCs. We demonstrate that this all-solid gate strategy is compatible with large capacitance up to  $2 \mu\text{F.cm}^{-2}$  and can be operated over a broad range of temperatures (130-300 K). In the last part of the paper, we tackle an issue often faced by NCs-based phototransistors, which is their low absorption: from a metallic grating structure, we combined two resonances and achieved a high responsivity ( $10 \text{ A.W}^{-1}$  or external quantum efficiency of 500 %) over a broadband spectral range.

**Keywords:** field effect transistor, solid electrolyte, HgTe, nanocrystals, nanophotonics, light resonator, infrared detection.

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## 1. Introduction

As colloidal growth of nanoparticles has gained maturity, nanocrystals (NCs) have become viable building blocks for optoelectronics. Using narrow band gap materials, it is possible to address the infrared (IR) part of the electromagnetic spectrum[1,2]. At such wavelengths, NCs offer an interesting alternative to epitaxially grown semiconductors to design cost-effective devices.[3] This has led to the demonstration of efficient light-emitting devices[4–7] as well as effective light sensors,[8–10] including focal plane arrays used to image near and mid-IR light.[11,12]

Though a high degree of control is achieved for the growth of this material, its doping remains[13] a challenge, and the material may not behave as an intrinsic semiconductor. This affects the final device performance. Thus, it is of utmost interest to couple NCs films with a gating method to enable an agile tunability of the carrier density. As a result, the phototransistor appears to be a remarkable evolution of the photoconductive geometry since the gate bias can be used as a knob to control the magnitude of the dark current. Phototransistors are built around a field-effect transistor[14] (FET) in which, in addition to drain/source electrodes and a channel based on a NC film, the gate appears as a central building block. The gate is used as a capacitor and, applying a bias over it, it generates charges tuning the carrier density in the FET channel.

Various technologies of gates have been coupled to NCs films.[15] The most conventional method continues to rely on dielectrics such as  $\text{SiO}_2$ . [9,16,17] The latter being commercially available and technologically mature, it presents the clear benefit of being easy to implement. Moreover, the fast gate bias sweep and the ability to operate at low temperatures are other key advantages of this technology. The low dielectric constant of silica, on the other hand, requires a large bias operation, possibly close to the material breakdown. High-k materials such as alumina,  $\text{ZrO}_x$  [18,19] or  $\text{HfO}_2$  are the natural alternatives.[20–23] They present the advantage of an all-solid strategy similar to silica but require an additional fabrication step with the deposition of an insulating layer. Despite a limited number of examples,<sup>19,20</sup> ferroelectric gating coupled to a NC film offers a strategy to achieve even higher capacitance FETs<sup>19,20</sup>. The main idea behind ferroelectric gating is to take advantage of the divergence of a ferroelectric material's dielectric constant close to its Curie temperature. However, the latter limitation tends to reduce the operating temperature ranges. When higher gate capacitances are required, the gating methods based on ion displacement become the most effective. Contrary to the previous methods, electrolytes allow for bulk gating. This ability to gate a thick film is critical for the design of phototransistors, which require a film thickness suitable with the light absorption depth.[26,27] The electrolyte is already commonly used to control the carrier density of a NC population.[28–30] The usual method is to introduce liquid or ion gel electrolyte, which may bring additional constraints related to liquid handling and moisture sensitivity: this is why a solid electrolyte appears easier to implement. The use of ionic glasses has been a step in this direction:  $\text{LaF}_3$  glass presents, for example, mobile fluoride vacancies which can be used to induce gating.[31–33]

When applied to infrared phototransistors, the gate has to fulfill some specific constraints such as being (i) compatible with operations below room temperature, (ii) solid state-based to be conveniently integrated into a device, and finally (iii) associated with a large capacitance to tune the relatively large thermally activated carrier density observed in narrow band gap NCs. In this perspective, new materials compatible with this series of constraints must be identified. For a long time, the battery field was using solid electrolytes, and their potential for field-effect transistors has been investigated for 2D materials[34–37] and superconductor based[38] FETs, but they remain unused for NC-based FETs. Here, we explore the potential of this strategy to design a FET based on HgTe NCs[39] presenting an absorption in the extended short-wave infrared. We show in particular that this lithium-ion glass substrate tends to promote hole injection over electron injection compared to what is observed with a silica gate. In the last part of the paper, we propose an update of the design to engineer the light-matter coupling in this system and achieve high absorption from

a thin NC film. This strategy allows a large responsivity ( $R > 10 \text{ A.W}^{-1}$ ), three orders of magnitude larger than the value obtained without a light management strategy.

## 2. Materials and methods

### 2.1. Nanocrystals growth

**Chemicals:** Mercury chloride ( $\text{HgCl}_2$ , Sigma-Aldrich, 99%), **Mercury compounds are highly toxic. Handle them with special care.** tellurium powder (Te, Sigma-Aldrich, 99.99%), trioctylphosphine (TOP, Alfa Aesar, 90%), oleylamine (OLA, Acros, 80-90%), dodecanethiol (DDT, Sigma-Aldrich, 98%), ethanol absolute anhydrous (VWR), methanol (VWR, >98%), isopropanol (IPA, VWR), hexane (VWR, 99%), 2-mercaptoethanol (MPOH, Merck, >99%), N,N dimethylformamide (DMF, VWR), toluene (VWR, 99.8 %), acetone (VWR), methylisobutylketone (MIBK, VWR, >98.5%)

All chemicals are used without further purification except oleylamine that is centrifuged before use.

**1 M TOP:Te precursor:** 2.54 g of Te powder is mixed in 20 mL of TOP in a three neck flask. The flask is kept under vacuum at room temperature for 5 min and then the temperature is raised to 100 °C. Furthermore, degassing of flask is conducted for the next 20 min. The atmosphere is switched to nitrogen and the temperature is raised to 275 °C. The solution is stirred until a clear orange coloration is obtained. The flask is cooled down to room temperature and the color changes to yellow. Finally, this solution is transferred to a nitrogen filled glove box for storage.

**HgTe NCs synthesis with band-edge at  $4000 \text{ cm}^{-1}$ :** In a 100 mL three neck flask, 543 mg of  $\text{HgCl}_2$  and 50 mL of oleylamine are degassed under vacuum at 110 °C. Meanwhile, 2 mL of TOP:Te (1 M) are extracted from the glove box and mixed with 8 mL of oleylamine. After the atmosphere is switched to  $\text{N}_2$  and the temperature stabilized at 82 °C, the TOP:Te solution is quickly injected. After 2 min 30 s, 10 mL of a mixture of DDT in toluene (10% of DDT) is injected and a water bath is used to quickly decrease the temperature. The content of the flask is split over 4 centrifuge tubes and MeOH is added. After centrifugation, the formed pellet is redispersed in one centrifuge tube with toluene. The solution is precipitated a second time using ethanol. Again, the formed pellet is redispersed in toluene. At this step the CQDs are centrifuged in pure toluene to get rid of the lamellar phase. The solid phase is discarded. The stable phase is transferred in a weighted centrifuge tube and finally precipitated using methanol. The solid is dried under vacuum for 30 min (in glove box vacuum chamber). Finally, CQDs are redispersed with a  $50 \text{ mg.mL}^{-1}$  concentration in toluene.

**HgTe ink:** We dissolve 30 mg of  $\text{HgCl}_2$  in a solution of 18 mL of DMF and 2 mL of mercaptoethanol. The original solution of HgTe tripods nanoparticle is treated to obtain ligand exchange. At each step the solution is stirred with a vortex and sonicated.  $900 \mu\text{L}$  of HgTe nanocrystal solution at  $50 \text{ mg.mL}^{-1}$  are mixed with 1 mL of the exchange solution. 5 mL of hexane and 2 mL of DMF are added to the solution. The top phase is then removed, and the solution is washed three more times adding 5 mL of hexane and removing the top phase each time. Finally, toluene is added up to reach a global volume of 4 mL and the solution is centrifugated for 2 min at 6000 rpm to precipitate the nanoparticles. The supernatant is discarded and the formed pellet is dried using nitrogen flux and then redispersed in  $175 \mu\text{L}$  of DMF in order to target a thickness of about 100 nm. The obtained solution is called ink. Before ink deposition, the substrate is placed in an oxygen plasma cleaner for 2 min to promote adhesion. The ink is spin-coated on the substrate at 4000 rpm (with  $2000 \text{ rpm} \cdot \text{s}^{-1}$  acceleration) for 1 minute. This gives a homogeneous film between 100 nm and 250 nm, as can be

seen in the height profile obtained from the profilometer. The speed of spin coating and ink concentration are used to tune the film thickness.

## 2.2. Material characterization

**Infrared spectroscopy:** For infrared spectroscopy, we use a Fischer Nicolet iS50 in attenuated total reflection (ATR) mode. The spectra are averaged over 32 acquisitions and have a  $4\text{ cm}^{-1}$  resolution. For photocurrent spectra, the sample is biased using the supply from a Femto DLPCA 200 current amplifier, the current is then magnified by the same amplifier and the output of the latter is fed into the electronic of the FTIR. For polarized measurements a polarizer, aligned with the electrode digits (or perpendicular to them) is added on the optical path. Background is then acquired in presence of the polarizer in both polarizations.

**Transmission electron microscopy:** A drop of the NC solution is drop-casted onto a copper grid covered with an amorphous carbon film. The grid is degassed overnight to reduce future.

## 2.3. Electrical characterization

**Electrode fabrication:** the solid electrolyte is purchased from MTI (ref EQ-CGCS-LD). It is a ceramic based on  $\text{Li}_2\text{O}-\text{Al}_2\text{O}_3-\text{SiO}_2-\text{P}_2\text{O}_5-\text{TiO}_2$ . The substrate is cleaned with acetone and rinsed with ethanol and isopropanol, before being dried by a nitrogen jet gas. First LOR 3A, a sacrificial release layer is spin-coated and baked at  $160^\circ\text{C}$  for 180 seconds. Then, AZ 1505 resist is spin-coated and annealed at  $105^\circ\text{C}$  for 90 seconds. A laser lithography (Heidelberg  $\mu\text{PG}$  101) is used to perform the lithography procedure. The resist is developed in a solution of AZ 726 for 20 s, and then rinsed in deionized water. Then 3 nm of Ti and 47 nm of Au are evaporated using an e-beam evaporator (plassys MEB 550s). For lift-off the substrate is dipped in a PG remover bath for 1 h and finally rinsed using DI water. The substrate is then dried and electrically tested to check that there is no electrical short. The electrodes are interdigitated electrodes as schematized in **Erreur ! Source du renvoi introuvable.** with 25 pairs of digits (*i.e.*, 49 spacings). Each digit is  $700\text{ }\mu\text{m}$  long and spaced from its neighbor by  $10\text{ }\mu\text{m}$ .

**Electrical measurements:** The drain and source contacts of the sample are cleaned using a cotton swab moistened with acetone. The sample is glued using a silver paste on a substrate of silicon covered with a gold layer and mounted on the cold finger of a cryostat. The gold layer is used as gate. The transfer curves and the IV curves are measured using a Keithley 2634B SourceMeter; that controls both the gate-source and the drain-source voltages, measuring the relative currents.

**Photocurrent measurements:** The time response measurements and the responsivity measurements are performed using the scope mode of a MLFI Lock-In Amplifier (Zurich Instruments). In this case both the drain-source and the gate-source voltages are controlled by the Lock-In amplifier. **Erreur ! Source du renvoi introuvable.** schematized the setup used for the responsivity measurements. A wavefunction generator is used to trigger the laser emission and the oscilloscope, the current is converted in voltage by the Femto DLPCA-200 amplifier and fed into the oscilloscope (Tektronix MDO 3102). From the time trace we can extract both the photocurrent amplitude and the time response.

**Impedance Measurements:** The MLFI Lock-In Amplifier (Zurich Instruments) is used to perform impedance measurements in the range from 0.1 Hz to 500 kHz. We used an AC voltage of 100 mV. No DC voltage was applied exploring the temperature dependence of the impedance. We first

deposit two gold layers on both sides of a Li-based substrate. The surface of the deposited gold layer is determined a posteriori using a camera and digital processing of the image. The area of the device is 8.9 mm<sup>2</sup>. The device is glued on a substrate of silicon coated with a gold layer using silver paste. The sample is then connected inside the cryostat, the top and the back sides are connected to the Lock-In amplifier.

**Noise measurement.** Current from the device (at 0.7 V bias, kept in the dark) is amplified by a Femto DLPCA-200, then fed into a SRS SR780 spectrum analyzer. The sample is mounted on the cold finger of a close cycle cryostat.

**Detectivity determination.** The specific detectivity (in Jones) of the sample is determined using the formula:  $D^* = \frac{R\sqrt{A}}{S_I}$ , where R (in A.W<sup>-1</sup>) is the responsivity, S<sub>I</sub> is the noise (A/√Hz) and A the area of the device (cm<sup>2</sup>).

## 2.4. Optical resonator

**Determination of the optical index:** We used spectrally resolved ellipsometry to determine the complex optical index of the different materials involved in the device and later conduct electromagnetic simulations.

**Spectroscopic ellipsometry:** The spectroscopic ellipsometry measures the change in the polarization state between the incident and the reflected light and this is done by measuring the angles  $\psi$  and  $\Delta$ . The following relation applies:

$$\rho = \frac{r_p}{r_s} = \left| \frac{r_p}{r_s} \right| e^{i(\delta_p - \delta_s)} = \tan \psi e^{i\Delta}$$

where  $r_p$  and  $r_s$  are the reflection coefficients of p and s polarized light respectively and where  $\delta_p$  and  $\delta_s$  are the phase shifts in reflection in p and s polarizations, respectively. The measurements are performed on a V-VASE ellipsometer (J.A. Woollam) in the 500-2000 nm range with steps of 10 nm and with angles of incidence of 50°, 60° and 70°. For the Li based substrate, the real part is shown in **Erreur ! Source du renvoi introuvable.** and is later used as a free parameter to fit experimental data and we found that  $n=1.8$  gives a reasonable fit as shown in Figure 4c. To account for the diffusive character of the substrate we use  $k=1$  for the substrate.

Complementarily, note that for gold complex optical index, we have used ref [40]. **Erreur ! Source du renvoi introuvable.** provides the imaginary part of the complex optical index of the HgTe NC film. The real part of the optical index has been taken constant and equal to 2.2 according to ref [41].

**Electromagnetic simulation:** Simulations are conducted using COMSOL, a software using Finite Element Method. The array of resonators is modelled using RF module in 2D geometry. It is one unit cell and Floquet periodic boundary conditions are used to describe the periodicity. On both sides (top and bottom), we define perfectly matched layers (PML) to absorb all outgoing waves and prevent nonphysical reflections. The absorption comes from emw. Qe, a value corresponding in COMSOL to the power density dissipated in W.m<sup>-3</sup>. In air there is no diffracted order for wavelengths above the electrode period. For shorter wavelengths, the energy propagating in the diffracted orders is absorbed by PML. On top of the resonator inside air, a port condition is used to define the incident wave, either in TE or TM polarization. An automatic mesh was used in these simulations with a predefined “Extremely fine” mesh, which means that the maximum element size is 140 nm, except for the PML where a mapped mesh is used with a distribution of 12 elements.

To enhance the device absorption, we keep the same device geometry as the one depicted in **Erreur ! Source du renvoi introuvable.** and just update some geometrical factors, see **Erreur ! Source du renvoi introuvable.**. We tune the period, the size of the digit and the film thickness so that light absorption resonances are generated. Along the TE polarization we design a guided mode resonance. This mode is dispersive and split in two modes with angle as revealed by the dispersion map along the TE polarization **Erreur ! Source du renvoi introuvable.a.** In TM mode, we observe one main resonance that we design to be red shifted compared to the TE mode. This mode is non dispersive, see TM dispersion map in **Erreur ! Source du renvoi introuvable.b,** and while it is not affected by the digit size (*i.e.* no shift of the resonance peak for various digit sizes, see **Erreur ! Source du renvoi introuvable.**), it can strongly be affected by the film thickness (*i.e.* strong shift of the resonance peak away from the exciton peak, see **Erreur ! Source du renvoi introuvable.**) which suggests a vertical Fabry-Pérot resonance.

**Fabrication of the device with resonator** The fabrication of the optimized photoresponsive device is made in two steps of lithography. First the main contact pads are fabricated using optical lithography and then the interdigitated pattern is fabricated using e-beam patterning.

**Electrodes for electrical contacts:** A lithium-ion glass substrate is initially rinsed with acetone and isopropanol to remove dust and organic contaminants. An additional O<sub>2</sub> plasma cleaning step is performed for 5 minutes. The TI Prime adhesion promoter is spin-coated onto the substrate and baked on a hot plate at 120 °C for 2 minutes. Subsequently, the substrate is spin-coated with AZ5214E photoresist and baked at 110 °C for 1 minute 30 seconds. The first UV exposure through a mask is 1 second as the substrate is very strongly scattering light and this is followed by the image reversal bake step at 125 °C for 2 minutes. A flood exposure for 40 seconds without the mask is then performed. The resist film is developed in AZ726 MIF with a development time of 30 seconds, followed by DI water rinse, N<sub>2</sub> gun and finally an oxygen plasma of 5 minutes. The next step is to deposit 5 nm of Cr and 80 nm of Au on the substrate with a thermal evaporator. Afterwards, the substrate stays 2 hours in acetone for lift-off. The obtained pattern is shown **Erreur ! Source du renvoi introuvable.a.**

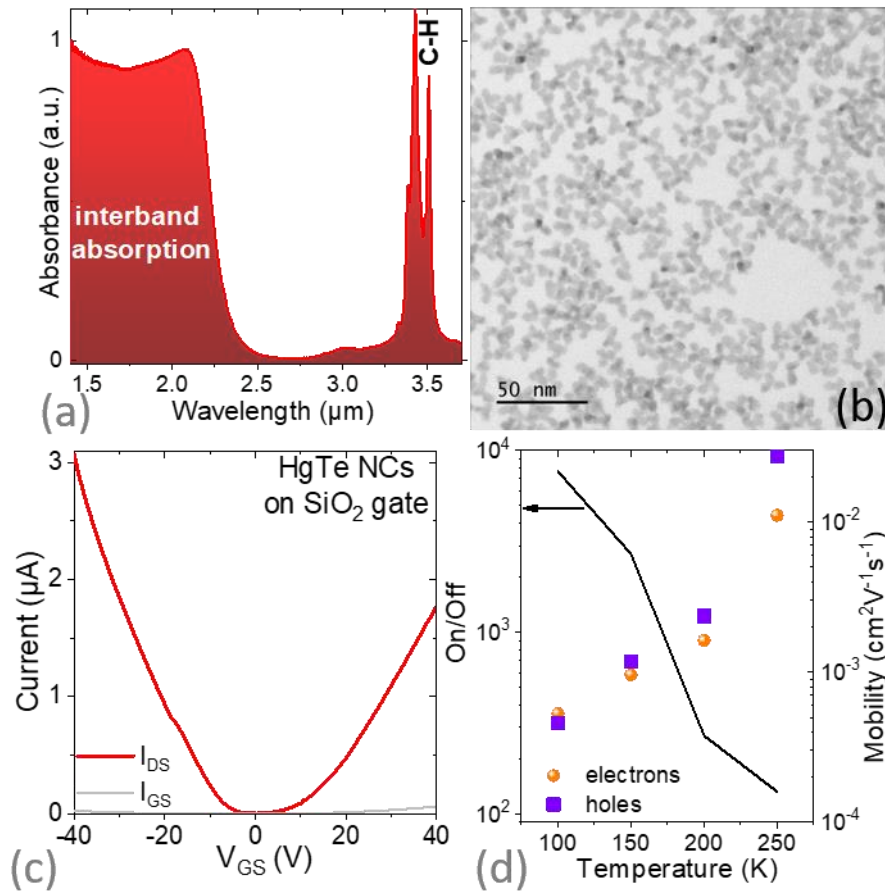
**Fabrication of gold nano-stripes:** We use Raith eLine e-beam lithography system for the fabrication of the nano-stripes. First, a 400 nm thick PMMA layer is spin-coated onto the substrate patterned with macroscopic electrodes. The resist is then baked at 180 °C for 2 minutes. We deposit 10 nm of Al with a thermal evaporator in order to reduce the charging effect. The operating bias of the electron beam is set to 20 kV, and the aperture is set to 10 µm. The dose is set to 170 µC/cm<sup>2</sup>. The resist is first dipped during 15 seconds in a KOH solution in order to remove the aluminum layer and then it is developed in a solution of MIBK:isopropanol (1:3 in volume) for 45 seconds and rinsed with isopropanol. An oxygen plasma of 5 minutes is done to remove traces of resist. 5 nm Cr and 80 nm Au are then deposited with a thermal evaporator. The substrate stays in hot acetone (45°C) for 2 hours after letting it overnight for lift-off. The obtained pattern is shown in **Erreur ! Source du renvoi introuvable.b-d.**

For resonator measurements a thicker film compared to transistor measurements was needed (>200 nm). Thicker films can be obtained mostly by updating the spin coating condition which is conducted at reduced speed. The ink is spin-coated on the substrate at 1500 rpm (at 500 rpm.s<sup>-1</sup> acceleration) during 5 minutes (after initial DMF deposition on the substrate with the same speed in order to help adhesion) followed by a fast spin coating step to dry edges (3000 rpm for 2 min, with 500 rpm.s<sup>-1</sup>). This gives a homogeneous deposition around 225 nm, see **Erreur ! Source du renvoi introuvable.** for an image of the electrode after functionalisation with the NC film.

### 3. Results and discussion

#### 3.1 Material of interest with short wave infrared band gap

To design a short wave-infrared (SWIR) absorbing phototransistor, we proceed in two informative steps. In the first step, we rely on a doped silica substrate. We use HgTe NCs as the material for the FET channel. The nanocrystals are obtained using the Keuleyan's procedure.[42] HgTe NCs present a tripodic shape according to electronic microscopy (**Figure 1b**) and a band edge at 2  $\mu\text{m}$  with a cut-off wavelength around 2.5  $\mu\text{m}$ , see the absorption spectrum in **Figure 1a**. In addition of the electronic interband transition attributed to HgTe, the absorption spectrum depicts a doublet that is attributed to the C-H bond resonance at around 2900  $\text{cm}^{-1}$ . Several steps of cleaning are conducted in order to reduce the relative magnitude of the C-H resonance close to the one of the NC exciton. When a similar magnitude is obtained, thin conductive and photoconductive films of such NCs are obtained by preparing an ink.[16,43] In this case, the native insulating long ligands (dodecanethiol) are stripped off and replaced by a mixture of a short thiol (mercaptoethanol) and ions ( $\text{HgCl}_2$ ) dissolved in dimethylformamide (DMF). The obtained ink can be spin-coated to form a thin film. The film thickness can be tuned with the deposition conditions in the 100 to 300 nm range for the device under investigation in this study.



**Figure 1** a. Absorption spectrum of the HgTe NCs measured in attenuated total reflexion mode. The doublet at around 3.4  $\mu\text{m}$  is related to the resonance of the C-H bond. b Transmission electron microscopy image of the HgTe NCs. c. Transfer curve (drain and gate current as a function of applied gate bias under the constant drain-source bias of 100 mV), measured at 150 K, for a field-effect transistor which channel is made of a HgTe NC film while the gate is  $\text{SiO}_2$  (300 nm  $\text{SiO}_2$  on top of highly doped Si). d. On/Off ratio and carrier mobility as a function of the temperature for a FET which channel is made of HgTe NCs and the gate of 300 nm  $\text{SiO}_2$ .

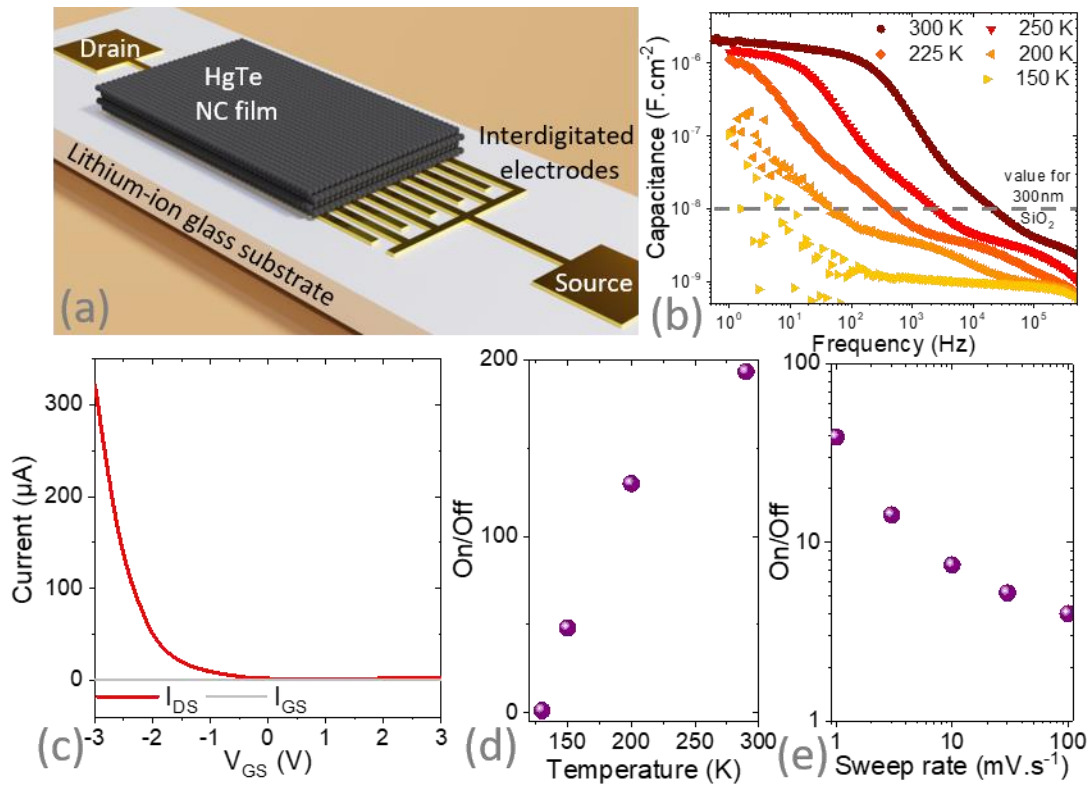


Once deposited on a doped Si/SiO<sub>2</sub> substrate on which gold interdigitated electrodes have been fabricated, the material presents an ambipolar character. We observe both holes (under negative bias) and electrons (under positive gate bias), as shown in the transfer curve, see **Figure 1c**. The current modulation is around two orders of magnitude at 250 K and increases to almost four decades at 100 K, see **Figure 1d**. This increase in current modulation is mostly due to a reduction in the off current caused by reduced carrier density activation when the temperature drops.

The carrier mobility can be determined from the transfer curve with  $\mu = \frac{L}{WC_{\Sigma}V_{DS}} \frac{\partial I_{DS}}{\partial V_{GS}}$ , where  $L$  is the electrode spacing (10  $\mu\text{m}$ ),  $W$  is the electrode length (49  $\times$  2.5 mm),  $C_{\Sigma}$  the surface capacitance as measured in **Figure 2b** and  $V_{DS}$  the drain source bias. We notice a clear thermal activation of the mobility (**Figure 1d**) which is consistent with the hopping conduction occurring in a disordered array of polydisperse NCs. Electron and hole mobilities are similar in magnitude and drop by a factor 50 as the temperature is reduced from 250 to 100 K.

### 3.2 Gating using solid electrolyte

In the second step, we design a FET using a lithium-ion glass substrate. The latter is commercially available from MTI and is a ceramic of oxide materials (Li<sub>2</sub>O-Al<sub>2</sub>O<sub>3</sub>-SiO<sub>2</sub>-P<sub>2</sub>O<sub>5</sub>-TiO<sub>2</sub>) in which the small Li<sup>+</sup> cations can be displaced under the application of a gate bias. On such substrates, we fabricate the same set of interdigitated electrodes as the one used for silica gating, see a schematic of the device in **Figure 2a** and S2. The benefit of such gates is best revealed by impedance measurements, see **Figure 2b**. At high frequency, the capacitance is low. In this regime, ions cannot move, and only the dielectric behavior of the substrate is observed. The substrate being thick (several hundred  $\mu\text{m}$ ), the resulting capacitance is even weaker than in thin silica films. At low frequency, on the other hand, ions can move to form an ionic interfacial layer, and the obtained capacitance becomes high (up to 2  $\mu\text{F.cm}^{-2}$ ). The transition frequency between these two regimes depends on the temperature and reflects the thermally activated transport of Li<sup>+</sup> ions within the substrate. In practice, ion displacement limits the gate sweep rate range, and the strongest modulations are obtained with low bias sweep rates (1 mV.s<sup>-1</sup> typically, see **Figure 2e**). Note that the capacitance value obtained is similar to the one obtained with a liquid or ion gel electrolyte, while the device can be air-operated without handling any liquid.



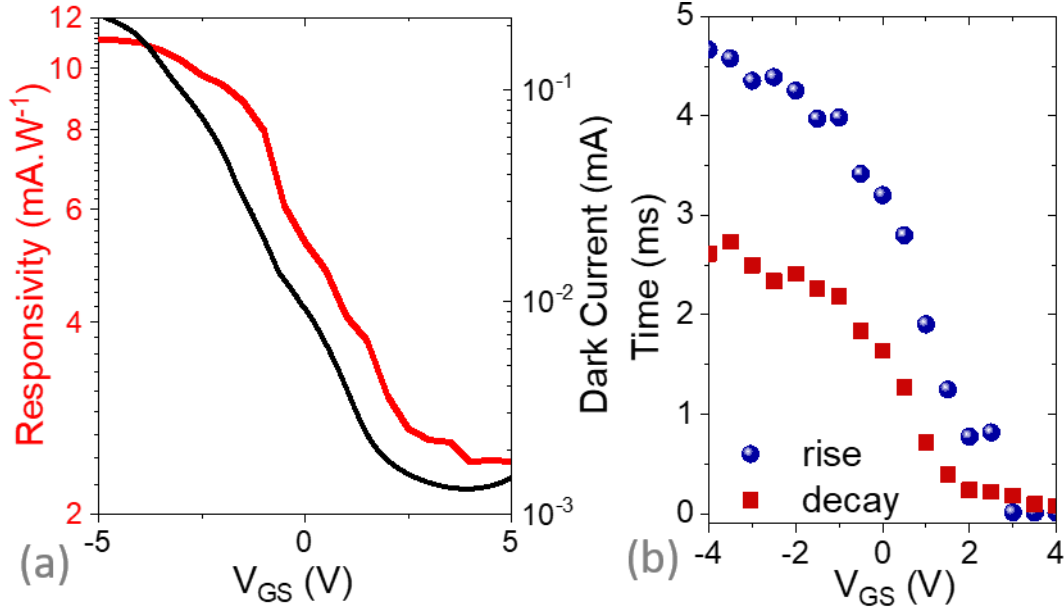
**Figure 2a.** Schematic of the FET which channel is made of HgTe NC film and the gate is made of a lithium-ion glass substrate. The substrate is used as gate and the gate electrode is taken on the back side of the substrate. **b.** Capacitance of the lithium-ion glass substrate as a function of the signal frequency for various temperatures. **c.** Transfer curve for the FET depicted in part a, at room temperature ( $V_{DS}=500\text{mV}$ ). On/Off ratio (i.e. ratio of the maximum current over the minimal current in the transfer curve) for the same FET as a function of temperature (**d**) and gate bias sweep rate at 200 K (**e**).

As bias is applied over the lithium-ion glass substrate, current modulation is observed in the HgTe NC film, see **Figure 2c** and S3-4. Current modulation is observed for temperatures down to 130 K (see **Figure 2d**); below this value, ions freeze. This enables the operation of the FET from room temperature down to this value, which is a broader range than the typical one of liquid electrolyte (acetonitrile often used as solvent freezes at  $-45^\circ\text{C}$ ) or ionic glass[31–33] (operating range 180-300 K). However, despite the higher capacitance than with silica, the on off ratio is not improved due to higher leakage inherent to the gating method based on ions displacement.

A striking feature is the observation of *p*-type only conduction (**Figure 2c**), whereas the material was ambipolar when coupled to silica gate. This means that the lithium-ion glass substrate behaves as a carrier selective charge injector. This behavior likely results from an asymmetry in the mobility of  $\text{Li}^+$  cations and their vacancies in the lithium-ion glass substrate. We have estimated the hole mobility at 250 K to be  $6 \times 10^{-3} \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$ . This is typically 3 times lower than the value measured for  $\text{SiO}_2$  gate, this reflects a carrier density dependence of the mobility.

The purpose of this device being its use as a phototransistor, we tested its potential under illumination at  $1.55 \mu\text{m}$ . As expected for such NCs films, the material is photoresponsive and we observe a responsivity around  $R=5 \text{ mA} \cdot \text{W}^{-1}$  (floating gate) which can be tuned by applying a gate bias, see **Figure 3a**. Though higher responsivity (up to  $R=11 \text{ mA} \cdot \text{W}^{-1}$ ) can be obtained under negative gate bias, the strongest photocurrent modulation is obtained under positive gate bias since this operating condition makes the material more intrinsic. As often observed in NC-based phototransistors, the change of the photocurrent magnitude comes with a change of the response

dynamics[27], see **Figure 3b**. Faster responses are obtained, when majority carriers are removed (*i.e.*, positive gate bias here). These changes in dynamics reflect the change in trap filling when the gate bias is applied[44]. With Li gating, the response time spans from 500  $\mu\text{s}$  to 5 ms depending on the applied gating bias. The former value is itself a decade longer than the value obtained with the silica gate, also associated with a much weaker photoresponse. In other words, the FET configuration enables to tune the device photoresponse while leaving the gain bandwidth product almost unaffected.



**Figure 3a.** Dark current and responsivity for a device depicted in **Figure 2a** as a function of the applied gate bias. **b.** Rise and decay time (defined as the time for the signal to change from 10 % to 90 % of its final value) as a function of applied gate bias, see Figure S 5 for a time trace. Illumination is ensured by a 1.55  $\mu\text{m}$  laser diode at  $110 \text{ mW} \cdot \text{cm}^{-2}$ , spectra are averaged 500 times. The drain-source bias is set at 500 mV. Measurements are conducted at 250 K.

### 3.3 Introduction of a photonic structure to enhance the absorption

As most NC-based light sensors in this configuration, the device faces a limitation. Due to hopping conduction, the mobility and the diffusion length are limited. As a result, the effective light absorption only occurs over a distance much shorter than the light absorption depth ( $> \mu\text{m}$  for HgTe NC in the SWIR[45]). Electromagnetic simulations reveal that the film absorbs around 10% of the incident light at the NC band edge. To overcome this limitation, we update the design of the device to couple the NCs to a photonics structure,[46] while maintaining the same interdigitated geometry (Figure S6). The initial device relies on interdigitated electrodes with 10  $\mu\text{m}$  wide digits and 10  $\mu\text{m}$  spacing between digits. We then tune the geometrical factors (film thickness, period and size of the digits) in order to maximize the absorption in magnitude and linewidth. Compared to the previous work by Gréboval *et al*<sup>20</sup> also dealing with a phototransistor coupled to a light resonator, the change in substrate from a single crystal of SrTiO<sub>3</sub> to a Li based ceramic which strongly scatters light (Figure S4-5) raises the need for an additional design.

We first match a resonance in the TM polarization (magnetic field parallel to the electrodes ribbons) with the band-edge energy. This first resonance, at 2.2  $\mu\text{m}$ , is a vertical Fabry-Perot resonance highlighted by its lack of dispersion (see figure S7b). The energy of this resonance is driven by the film thickness (Figure S8b) but is very weakly affected by the digit width (Figure S9). The obtained mode is spatially located on top of the gold pad (**Figure 4c**) as the cavity is formed between the gold and the air interface. By doing so, we minimize the loss in the metal which is estimated to be around

5.5 % of the total absorption. Details about the electromagnetic design can be found in the supporting information.

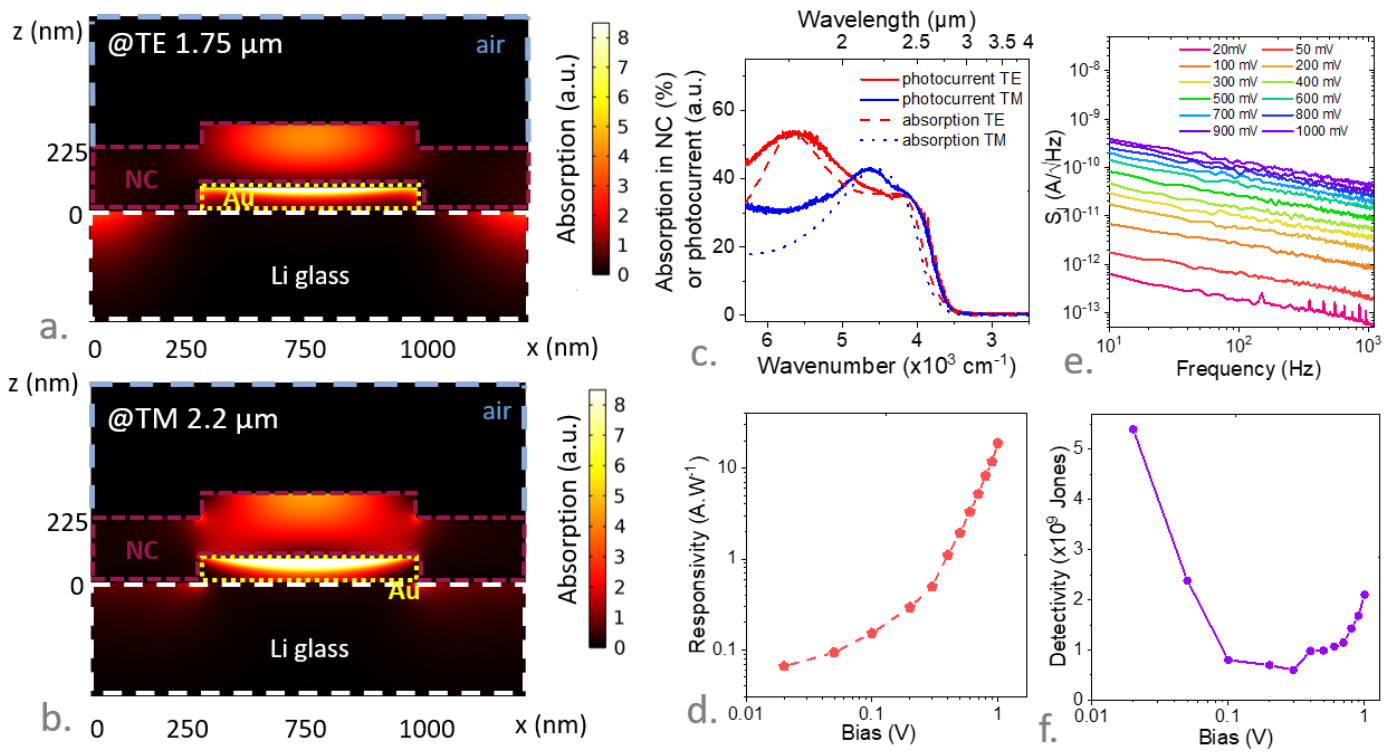
Furthermore, by tuning the period of the grating we can induce a second resonance in TE polarization (electric field parallel to the electrodes ribbons). We choose the period of the grating and digit size to slightly blue-shift this resonance with respect to the band edge (around 1.75  $\mu\text{m}$ ). By doing so, we broaden the global response of the device. This mode is a guided mode resonance[47–49] (GMR), which energy is driven by the period of the grating. This mode is dispersive, as revealed by the dispersion map, see figure S7a, and spatially located at the top of the gold electrode as revealed by the simulated absorption map (**Figure 4a**). The obtained absorption within the NCs (*i.e.* leading to the generation of photocurrent) is expected to reach 40% for non-polarized light, see **Figure 4c**.

The period of the grating being set at 1.5  $\mu\text{m}$  for a 750 nm wide digit, we use e-beam lithography for its fabrication, see Figure S10. The obtained photoresponse spectrum is now strongly polarized, see **Figure 4c**, with a TE mode clearly blue shifted with respect to the TM mode. Compared to the simulation, the photocurrent presents a good match both in energy and linewidth as long as we account for the scattering of the Li substrate. It is worth noting that the photocurrent spectrum is also significantly redshifted from the absorption shown in **Figure 1a**. This shift is the combination of two effects: (i) first we have conducted a ligand exchange procedure that tends to increase inter NC coupling and leads to a redshift. In addition (ii), the photocurrent is measured at low temperature (180 K), while HgTe NC band gap redshifts upon cooling.[45,50]

The responsivity of the device is now much higher and reaches  $10 \text{ A} \cdot \text{W}^{-1}$  (**Figure 4d** and S14) under moderate electric field: this value is three orders of magnitude larger than the one reported for the same film deposited on conventional interdigitated electrodes without resonances. As it has already been pointed out by Chu *et al.*, [49] the enhanced responsivity splits into two contributions: absorption enhancement is responsible here for a factor 4 in the performance increase; the remaining factor 250 is due to photogain [51] (*i.e.* recirculation of one of the carriers, while the other one stays trapped). Indeed, the external quantum efficiency of the device is around 500% ( $R=10 \text{ A} \cdot \text{W}^{-1}$  for 2.5  $\mu\text{m}$  cut-off wavelength and under broadband blackbody radiation),

In a nanocrystal array, charge transport occurs through hopping. Under illumination, electron hole pairs get generated and one carrier can easily get trapped while the second one is transported to the electrode. To ensure the sample neutrality, the electrode will reinject this carrier that can recirculate several times until the trapped charge gets recombined.[52] The gain magnitude is the ratio of the trapped carrier lifetime to the transit time. The latter is directly connected to the device size. Thus a smaller device favors gain.[53]

The factor 250 for the gain value is further subdivided into a factor 10 that arises from an increase in the electric field (*i.e.*, a 10 times narrowing of the electrode spacing) and a factor 25 that we ascribe to a more efficient hopping transport as the device size approaches the diffusion length[49]. Noise in this device is limited by a  $1/f$  contribution,[54,55] see the inset of **Figure 4e**, which currently limits the detectivity around  $D^* \approx 5 \times 10^9$  Jones and corresponds to a noise equivalent power (NEP) at around  $1 \text{ pW} \cdot \text{Hz}^{-1/2}$  for a signal at 1 kHz. This is nevertheless a factor 5 ( $D^*=10^9$  jones) above the value obtained for the same material (same band gap and same surface chemistry) without resonator compared to that achieved for silica[49].



**Figure 4** a. (resp b.) Simulated absorption map at the resonance of the TE @1.75 μm (resp TM at 2.2 μm) polarization. c. Simulated absorption (dashed lines) and experimental photocurrent (solid lines) spectra within the NCs film (i.e., excluding losses in contact) for TE, TM polarization. d. Responsivity measured at 180 K for a signal at 1 kHz as a function of applied bias for the device with light resonators under broadband radiation resulting from a blackbody at 980°C. e. Noise current spectral density as a function of signal frequency measured under various applied biases. f. Specific detectivity measured at 180 K (for a signal at 1 kHz) as a function of applied bias for the device with light resonators.

#### 4. Conclusions

To summarize, we have demonstrated that the lithium-ion glass substrate can be used as an all-solid back gate for NC-based FET. This gate leads to a capacitance as high as 2 μF.cm<sup>-2</sup>, similar to liquid electrolyte while considerably easing the device's manipulation and its future integration. Moreover, the gating remains effective over a broad range of temperatures from room temperature down to 130 K, matching the targeted operating temperature range for short- and mid- wave infrared sensors. Interestingly the gating favors hole injection compared to silica gating. In a second step, we demonstrate a light management strategy for a phototransistor based on this lithium-ion glass. We demonstrate the combination of two light resonators, one based on a guided-mode resonance and the second one relying on a Fabry-Perot mode. This combination of two resonances enables broad band enhancement in light absorption and responsivity as high as 10 A.W<sup>-1</sup>, this value is a thousand times larger than the one obtained for interdigitated electrodes without any light management strategy. This enhancement can be split over a factor of 4 for absorption and a factor of 250 for photogating.

## ASSOCIATED CONTENT

Supporting Information include (i) procedure for NC synthesis and characterization, (ii) additional details relative to transport and phototransport measurements, (iii) design of the resonator with electromagnetic simulations and device fabrication, (iv) additional device performances

## COMPETING INTEREST

The authors declare no competing interest.

## AUTHOR CONTRIBUTIONS

J.-F. D fabricated the electrodes without resonators, C.G., A.K., H.Z. and M.C. built the setups for optoelectronic characterization, S.P., C.G. conducted the FET characterization. Y.P. and S.I. grew the nanoparticles, C.A. and B.G. conducted ellipsometry measurements. CA, T.H.D. and G.V. designed the light resonators and fabricated them. E.L. and S.S. designed, funded the project and wrote the paper with input of all authors. All authors proofread the manuscript.

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## Data Availability Statement

The data used to support the findings of this study are included within the article and its supplementary informations.

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## TOC graphic

