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Energy Optimized Task Mapping for Reliable and Real-Time Networked Systems

LEI MO, School of Automation, Southeast University, China

QI ZHOU, School of Cyber Science and Engineering, Southeast University, China

ANGELIKI KRITIKAKOU, University of Rennes, INRIA, IRISA, CNRS, France

XIANGHUI CAO, School of Automation, Southeast University, China

Energy efficiency, real-time response, and data transmission reliability are important objectives during networked systems design. This paper aims to develop an efficient task mapping scheme to balance these important but conflicting objectives. To achieve this goal, tasks are triplicated to enhance reliability and mapped on the wireless nodes of the networked systems with Dynamic Voltage and Frequency Scaling (DVFS) capabilities to reduce energy consumption while still meeting real-time constraints. Our contributions include the mathematical formulation of this task mapping problem as mixed-integer programming that balances node energy consumption, enhancing data reliability, under real-time and energy constraints. Compared with the State-of-the-Art (SoA), a joint-design problem is considered in this paper, where DVFS, task triplication, task allocation, and task scheduling are optimized concurrently. To find the optimal solution, the original problem is linearized, and a decomposition-based method is proposed. The optimality of the proposed method is proved rigorously. Furthermore, a heuristic based on the greedy algorithm is designed to reduce the computation time. The proposed methods are evaluated and compared through a series of simulations. The results show that the proposed triplication-based task mapping method on average achieves 24.84% runtime reduction and 28.62% energy saving compared to the SoA methods.

Additional Key Words and Phrases: Networked Systems, Task Triplication and Mapping, DVFS, Problem Linearization and Decomposition

1 INTRODUCTION

Networked systems, e.g., Wireless Sensor and Actuator Networks (WSAN) or Cyber-Physical Systems (CPS), monitor and control the physical world using wireless nodes, equipped with sensors, controllers, and actuators, supporting several functions of system applications [20, 30]. From the hardware perspective, wireless nodes are usually energy-constrained, especially when they are supplied by batteries. To reduce energy consumption, modern hardware platforms are enhanced with Dynamic Voltage and Frequency Scaling (DVFS) [8, 21], able to adjust the supply voltage and the clock frequency of the processors, where task execution takes place. From the software perspective, system applications usually consist of several dependent tasks that exchange data. In-network processing is typically used to reduce network traffic [35]. Following the “Fog/Edge-computing” model [1, 16, 27], instead of collecting and sending all data to a remote Base Station (BS), a part of the data processing is done on the wireless nodes, reducing the amount of data sent to the BS. Hence, sensing, processing, and controlling tasks, and data transmissions should be executed in a real-time and energy-efficient manner, meeting the time and the energy constraints of the system application, in order to improve the overall system performance (e.g., enhancing federated learning in mobile edge computing) [40, 43].

Meanwhile, wireless nodes have become susceptible to attacks [39], and data transmissions should be reliable. During a False Data Injection (FDI) attack [28], an attacker compromises the data to obtain manipulated data that can bypass the basic “faulty data” detection mechanisms. An FDI attack can be implemented by compromising physical sensors and the sensor communication network [33]. Such attacks may lead to significant costs due to unplanned failures or loss of human lives in safety-critical applications, e.g., in predictive maintenance systems used in industry 4.0 the attack can propagate from the sensor to the machine learning part and fool the system by

50 predicting a delayed asset failure or maintenance interval [33]. Common countermeasure methods
51 for FDI are based on contingency analysis [32]. To analyze the contingencies, state estimation (e.g.,
52 Kalman filter) [9] is usually used to model the system's dynamics. Thus, the estimation errors are
53 influenced by the accuracy of the system model and the length of the historical data (i.e., time
54 window size). Considering a system is executed under real-time constraints, potentially only parts
55 of the data can be analyzed within the available time. An alternative to the aforementioned methods
56 is to apply task replication. By replicating tasks, data reliability is enhanced, while knowledge of
57 the system model is not required. Executing task replicas on different wireless nodes provides
58 redundant information on the receiver node, which can correct any information obtained by a
59 compromised sensor or communication network due to the FDI.

60 Overall, *energy efficiency*, *real-time application execution* and *reliable transmission* are crucial
61 goals in the networked systems [25], and they are significantly affected by way of deploying tasks
62 over the wireless nodes. The way allocating tasks affects the number of required data transmissions.
63 The fewer data is exchanged among the nodes, the lower the energy consumption and time for
64 transmissions, and the probability of the data being attacked during the transmission. However,
65 not all tasks can be assigned on the same node, due to task requirements for specific sensors or
66 actuators, and the application's real-time constraint. Task replication can occur when tasks are not
67 assigned on the same nodes to protect the data from attacks. However, task replication also affects
68 energy consumption and real-time execution. Furthermore, an asymmetric task allocation will
69 deplete the energy budget of some nodes, leading to network connectivity issues. Therefore, task
70 replication and deployment should be jointly addressed to achieve energy balance, under real-time
71 and energy constraints.

72 Mapping tasks on a single platform, with one or several processors, are the well-known problems,
73 e.g., task mapping to minimize energy consumption considering DVFS [8, 21, 22] and to improve
74 system reliability using task replication [17, 42, 46]. However, works to address task deployment
75 over different platforms, e.g., wireless nodes, considering task mapping, DVFS, reliability, and
76 real-time constraints are rare. On the one hand, networked approaches focusing on task mapping [4,
77 12, 24, 35, 41] usually assume that the task results are reliable, as long as the task execution process
78 is finished and the task data is transmitted to the destination, before the task deadline. On the other
79 hand, networked approaches focusing on reliability use false data detection and correction, which
80 are based on state estimation [9, 28, 31, 44]. Compared with the State-of-the-Art (SoA), this work
81 addresses the problem of mapping dependent tasks on wireless nodes with DVFS to balance the
82 energy consumption, under real-time and energy constraints. The main contributions of this paper
83 are summarized as follows:

- 84
85 (1) Task triplication scheme and DVFS scheme are integrated into the task mapping process to
86 enhance data transmission reliability and to exploit the trade-off between task execution time
87 and energy consumption, respectively. We formulate the mapping of tasks on the wireless nodes
88 as a Mixed-Integer Non-Linear Programming (MINLP), taking into account the application
89 and the system constraints (e.g., task deadline, task dependencies, data routing, node type, and
90 node energy). Nonlinear items are caused by the product of optimization variables related to
91 frequency assignment, task allocation, and task triplication decisions. These items are replaced
92 by auxiliary variables and additional linear constraints, so as to transform the MINLP problem
93 into a Mixed-Integer Linear Programming (MILP), without degrading the quality of the solution.
- 94 (2) We propose an Optimal Reliability Task Mapping (ORTM) algorithm based on Benders de-
95 composition to efficiently and optimally solve the transformed problem. The ORTM algorithm
96 decomposes the original problem into two smaller easier-to-solve problems. It solves them
97 iteratively, by using the solution of one problem in the other. The first problem is an Integer
98

99 Linear Programming (ILP) for task allocation, task triplication, and frequency assignment. The
100 second problem is a Linear Programming (LP) for task scheduling. By iterating the problems,
101 the optimality of the solution is guaranteed. We also design a novel Heuristic Reliability Task
102 Mapping (HRTM) algorithm based on the greedy approach. Compared with the ORTM algo-
103 rithm, the HRTM algorithm removes the iteration process and solves the problems sequentially.
104 Therefore, the computation time of the HRTM algorithm is significantly reduced.

- 105 (3) Finally, we perform extensive simulations to analyze the solution quality, the computation
106 time, and the scalability of the proposed task mapping scheme and the proposed ORTM and
107 HRTM algorithms. The results show that the proposed task mapping scheme outperforms other
108 task mapping schemes in terms of data transmission reliability and node energy efficiency.
109 In addition, the ORTM algorithm finds the optimal solution with reduced computation time
110 (24.84% on average), compared to SoA optimal methods, and the HRTM algorithm runs ~100
111 times faster than the ORTM algorithm with an average cost of 26.35% in energy efficiency.

112 The remainder of this paper is organized as follows. Section 2 discusses the related work. Section 3
113 presents the system model, and Section 4 formulates the task mapping problem. Section 5 and
114 Section 6 design the optimal and the heuristic algorithms, respectively. Finally, Section 7 shows the
115 simulation results, and Section 8 concludes this work.

116 2 RELATED WORK

117 Table 1 depicts some representative SoA task mapping approaches targeting a single platform, i.e.,
118 an Embedded System (ES), and several distributed platforms, i.e., a Networked System (NS). The
119 tasks can be Independent (I) or Dependent (D). The optimization variables include Task Allocation
120 (TA), Task Scheduling (TS), Task Replicas (TR), and Frequency Assignment (FA). During the task
121 mapping process, constraints regarding Energy Supply (E) and Real-Time (T) can exist. To enhance
122 data reliability, methods based on task Replication (R) and State estimation (S) are used. According
123 to different problem structures and system requirements, Optimal (O) and Heuristic (H) algorithms
124 are designed to solve the corresponding problems.

125 For embedded systems, approaches exist that focus on minimizing the energy consumption or
126 minimizing the task makespan under energy supply and real-time constraints [8, 13, 21, 22, 38].
127 Since the optimization variables of task allocation and frequency assignment are binary, the
128 corresponding task mapping problems are usually described as Mixed-Integer Programming (MIP).
129 For independent tasks, the task mapping problem is modeled as an MINLP in [22]. By relaxing
130 the binary variables to continuous variables, the problem is transformed into a convex problem
131 and is solved by the interior point method. A similar task mapping problem without DVFS but
132 with task migration is studied in [38], where the MILP-based task mapping problem is first relaxed
133 to two subproblems, and then the subproblems are solved by the polynomial-time methods. For
134 dependent tasks, additional variables and constraints are introduced into the problem to describe
135 the dependency between the tasks. The task mapping problem is described by an MINLP in [21].
136 The nonlinear items, which are caused by the quadratic function, can be approximated by a linear
137 function, and then the relaxed MILP problem is solved by the B&B method [5]. DVFS is combined
138 with Dynamic Power Management (DPM) in [8] to enhance energy efficiency. The task mapping
139 problem is formulated as an MILP and is solved according to the commercial solver, such as CPLEX.
140 In [13], each processor has a fixed frequency level, and, thus, the task mapping problem is an MILP,
141 which can be optimally solved by a hybrid algorithm based on Benders decomposition [3]. However,
142 reliability is not considered in these works.

143 Task replication techniques have been widely used in embedded systems to deal with task
144 reliability problems [17, 42, 46]. By replicating the tasks, the system's reliability can be improved as
145
146
147

Table 1. Classification of some task mapping approaches

Ref.	Task		Platform		Variables				Constraints		Reliability		Solution	
	I	D	ES	NS	TA	TS	TR	FA	E	T	R	S	O	H
[8]		√	√		√	√		√	√	√			√	
[13]		√	√		√	√				√			√	
[21]		√	√		√	√		√	√	√			√	
[22]	√		√		√			√	√	√				√
[38]	√		√		√				√	√				√
[17]	√		√		√		√		√		√			√
[42]		√	√		√		√	√	√		√			√
[46]		√	√		√		√	√	√	√	√			√
[35]		√		√	√				√					√
[4]		√		√	√				√					√
[12]		√		√	√	√			√	√			√	
[24]		√		√	√				√					√
[41]		√		√	√	√		√	√	√				√
[28]		√		√								√	√	
[31]		√		√								√	√	
[44]		√		√								√	√	
[9]		√		√								√	√	
Prop.		√		√	√	√	√	√	√	√	√		√	√

it is extremely unlikely to have errors on two or more copies at the same time. In [17], full replication is used, i.e., each task is replicated once at least. With more tasks being replicated, higher reliability is achieved. To enhance task reliability and reduce energy consumption, energy-efficient fault-tolerant scheduling is designed in [42] considering parallel task execution on processors. A similar problem is considered in [46] and a bi-objective genetic algorithm is proposed to balance two conflicting objectives: low energy consumption and high task reliability. However, task mapping approaches, focusing on a single platform (embedded systems), usually assume that the data communication cost is very small compared to the task execution cost. Thus, no data communication cost is considered during task replication and mapping. However, the approaches focusing on networked systems, where the platforms are distributed, additional energy and time are required by the nodes for data transmission and reception, which restricts DVFS and task replication options.

For the network systems, since the tasks are usually dependent [4, 12, 24, 35, 41], it is crucial to reduce the communication and computation costs (energy and time) on the nodes. The energy-aware task mapping problem is considered in [4, 35] to minimize the energy consumption of the nodes, and the problem is described by an Integer Non-Linear Programming (INLP). The original problem is first transformed into an ILP, and then solved by the greedy algorithm. The problem of maximizing the overall network lifetime under energy supply or task deadline constraints is considered in [12, 24]. Based on the problem structure, a game-theoretic approach is proposed in [12] to perform distributed computing, and a heuristic is designed in [24] to enhance system response time. However, DVFS is not taken into account in the above studies. Some works consider nodes with DVFS capabilities. In [41], the node energy consumption is optimized by determining the frequency-to-task assignment and the task-to-node allocation. This problem is formulated as an MINLP and solved by heuristics. The above studies mainly focus on minimizing the energy consumption of the nodes, without considering reliability. The task results are assumed to be corrected if the execution and transmission of the tasks are finished before the deadlines.

Approaches exist that focus on detecting FDI attacks on networked systems, based on state estimation. In [28], a Kalman Filter (KF) is used to estimate system states and a χ^2 -detector is employed to evaluate the discrepancies between the estimated data and the measured data, so as to detect the existence of FDI. In [31], based on the KF and χ^2 -detector combination, a Linear-Quadratic-Gaussian (LQG) controller is used to improve the resilience of the system to against FDI. To improve the success rate of detecting the FDI attacks, a summation (SUM) detector is proposed in [44]. Compared with the χ^2 detector, the SUM detector not only utilizes the current compromise information but also collects all historical information to reveal the threat. The Probability Density Functions (PDF) of measurements and control commands, and their confidence intervals, are estimated using historical records in [9]. If the values exceed a certain level, the measurements/control commands are re-generated by a Maximum Likelihood Estimation (MLE). However, these methods are based on state estimation, requiring knowing the system model in advance (e.g., state transition matrix A , input matrix B , and measurement matrix C), and thus, the accuracy of the used system model affects the stability of the system. Furthermore, real-time and energy supply constraints are not considered, and task allocation and scheduling decisions are fixed, since a remote Base Station (BS) is employed for processing the data from the wireless nodes, with the aim to minimize the estimation/control errors.

The aforementioned works address task mapping either on single platforms, focusing on minimizing energy consumption for computation, potentially including task replication, or on distributed platforms, minimizing the computation and the communication costs of the nodes for the networked systems. This work belongs to the second category and enhances the existing approaches by proposing a task mapping method that considers task replication and DVFS, under real-time and energy constraints, without requiring knowledge of the system model.

3 APPLICATIONS AND SYSTEM MODEL

3.1 Motivational Example

We will use the example of Fig. 1 to describe and motivate our approach. Fig. 1 depicts a part of a networked control system with six wireless nodes $\{\theta_1, \dots, \theta_6\}$, used for applications such as smart grid [23], fire detection [34], home automation [45], and precision agriculture [2]. Nodes θ_1, θ_2 and θ_3 are equipped with sensors, while node θ_4 is equipped with an actuator to monitor and control the physical variables, e.g., the voltage, current or phase in the grid [28], the temperature of POIs (points of interest) of the forest region [34], the illumination intensity for the indoor environment [45], and the soil moisture in the agriculture [2]. The application needs to monitor the physical variables periodically, process the readings of the sensors to compute the required action, and adjust the outputs of the actuators to control the physical variables. In Fig. 1, τ_1 corresponds to the sensing task, τ_4 corresponds to the data processing task, and τ_7 is the control task. τ_4 requires the sensing data from τ_1 to compute the action of the actuator. τ_7 requires the output of τ_4 to adjust the actuator's output. Therefore, allocating tasks τ_1 and τ_7 is restricted to the nodes equipped with sensors and actuators, respectively. However, the data processing task τ_4 can be placed on any node. Note that, due to the network topology and the task dependencies, the task allocation decisions influence not only the communication and computation energy of the nodes but also the communication time between the tasks.

The FDI attack can be implemented by compromising the sensor communication network [33]. Therefore, when dependent tasks, e.g., τ_1 and τ_4 , are executed on different nodes, e.g., θ_1 and θ_5 , there is a possibility to attack the node and alternate τ_1 's output, during its transmission to τ_4 . This work combines the benefits of *task mapping* with *task triplication* to improve the reliability of the networked system against FDI attacks. Using task triplication, the data reliability is enhanced, and

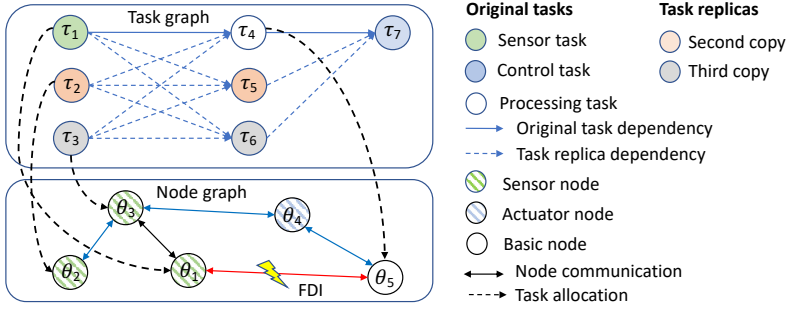


Fig. 1. Motivational example.

the influence caused by fault data can be mitigated [19], while knowledge of the system model is not required. The redundant information on the receiver node can be used to correct the information obtained by a compromised communication link. For instance, in Fig. 1, τ_2 and τ_3 are the second and the third replicas of τ_1 , respectively. Under the task allocation $\{\tau_1 \rightarrow \theta_1, \tau_2 \rightarrow \theta_2, \tau_3 \rightarrow \theta_3, \tau_4 \rightarrow \theta_5\}$, two routing paths exist $\{\theta_2 \rightarrow \theta_3 \rightarrow \theta_4 \rightarrow \theta_5$ (blue), $\theta_1 \rightarrow \theta_5$ (red) $\}$. Therefore, θ_5 will receive task data (τ_1, τ_2 and τ_3) from θ_1 and θ_4 . If θ_1 suffers from an FDI attack when it transmits the data of τ_1 to θ_4 , the data can be corrected by comparing with the results of τ_2 and τ_3 , before used as input to task τ_4 . Executing task replicas on different nodes allows sending the information through different routing paths, dealing with FDI attacks with long duration. For instance, compared with the task allocation $\{\tau_2 \rightarrow \theta_3, \tau_3 \rightarrow \theta_3\}$, the task allocation $\{\tau_2 \rightarrow \theta_2, \tau_3 \rightarrow \theta_3\}$ has a longer time interval when θ_3 transmitting the data of τ_2 and τ_3 , since a node will receive and transmit the task data in sequence.

Similarly, task τ_4 should be triplicated to enhance the reliability of the data that have to be transmitted to task τ_7 . During the task mapping process, if dependent tasks τ_4 and τ_7 are executed on the same node (e.g., θ_4), there is no need to triplicate task τ_4 , since no wireless transmission takes place between τ_4 and τ_7 . However, not all dependent task can be assigned to the same node, due to the task requirements in specific sensors and actuators, e.g., $\{\tau_1 \rightarrow \theta_1, \tau_2 \rightarrow \theta_2, \tau_3 \rightarrow \theta_3, \tau_7 \rightarrow \theta_4\}$, the energy supply of the nodes and the real-time response of the application. Based on the above discussion, the task allocation and the task triplication problems are coupled and affected by real-time and energy constraints.

3.2 System Model

3.2.1 Task Model. We consider a real-time frame-based application that consists of a task set \mathcal{T} with N periodic real-time tasks $\{\tau_1, \dots, \tau_N\}$, which includes the original task, its second and third replicas. The tasks are considered dependent and non-preemptive [8, 21]. Each task τ_i is described by a tuple $\{e_i, t_i^a, t_i^s, d_i, p_i\}$. e_i is the number of execution cycles of task τ_i , measured in Worst-Case Execution Cycles (WCEC). t_i^a, t_i^s, d_i and p_i are the arrival time, the start time, the deadline, and the period of task τ_i , respectively. Without loss of generality, task arrival times are equal to the time instance 0. We assume that the deadline and the period of task τ_i are equal to H , which is given by the frame of the application. The proposed approach can be adapted for task sets with different periods, usually consisting of independent tasks. This can be achieved by unrolling the tasks in the hyper-period (i.e., the least common multiple of all task periods $\{p_1, \dots, p_N\}$ [8, 29]), and setting the task arrival time and the task deadline, accordingly. The dependency between the tasks is given by a binary matrix $\mathbf{o} = [o_{ij}]_{N \times N}$. If $o_{ij} = 1$, τ_i precedes τ_j and τ_j is the closest task of

τ_i , else, $o_{ij} = 0$. Let s_{ij} denote the size of data that task τ_i generates for task τ_j . If tasks τ_i and τ_j are independent, we have $s_{ij} = 0$.

3.2.2 Node Model. We consider a networked system with M wireless nodes $\{\theta_1, \dots, \theta_M\}$. The nodes have different capabilities, with $\{\theta_1, \dots, \theta_{M_s}\}$ describing the nodes equipped with sensors and $\{\theta_{M_s+1}, \dots, \theta_{M_s+M_a}\}$ describing the nodes equipped with actuators, and $M_s+M_a \leq M$. The processors of the nodes, where the computation takes place, use the same Instruction Set Architecture (ISA), and they support DVFS. Each processor has L different discrete Voltage/Frequency (V/F) levels $\{(v_1, f_1), \dots, (v_L, f_L)\}$. As the relationship between voltage and frequency is almost linear [8, 10], when the supply voltage is changed, the clock frequency is changed accordingly. The execution time of task τ_i , when it is executed at frequency f_l on a processor, is given by e_i/f_l . The power consumption of a processor is modeled as the sum of static power P^s and dynamic power P^d , that is $P^c = P^s + P^d$ [8, 11]. The static power and dynamic power, under a given voltage/frequency level (v_l, f_l) , are calculated as $P_l^s = C^s v_l^\rho$ and $P_l^d = C^d f_l v_l^2$, respectively, where C^s , ρ and C^d are the constants depending on the type of processor. We assume that a processor switches to the idle mode immediately when it has no task to execute, and the idle power is P^0 . The time and energy required for the transition are incorporated into the task execution time and energy [22] since they are very small compared to the time and energy required for task execution.

4 PROBLEM FORMULATION

The aim of this work is to balance the energy consumption of the nodes, under real-time and energy constraints, exploiting task mapping, task triplication and DVFS. Therefore, we need to determine 1) which node the task should execute on (task allocation); 2) what voltage/frequency level should be used for the task (voltage/frequency assignment); 3) whether a task needs to be triplicated or not (task triplication); 4) when a task starts its execution (task scheduling). To formulate the task mapping problem, we introduce the following variables: 1) binary variable $q_{ik} = 1$ if task τ_i is allocated to node θ_k , else, $q_{ik} = 0$; 2) binary variable $c_{il} = 1$ if task τ_i is executed with the voltage/frequency (v_l, f_l) , else, $c_{il} = 0$; 3) binary variable $h_i = 1$ if task τ_i is triplicated, else, $h_i = 0$; and 4) continuous variable t_i^s represents the start time of task τ_i .

We assume that the system runs with a given routing protocol. Therefore, the routing energy cost matrix $\mathbf{r} = [r_{mnk}]_{M \times M \times M}$ and the routing time matrix $\mathbf{t} = [t_{mn}]_{M \times M}$ are known in advance [35], where r_{mnk} is the energy consumed of node θ_k , when routing a unit of data from θ_m to θ_n , while t_{mn} is the time required to transmit unit of data from θ_m to θ_n . For presentation reasons, let $\mathcal{L} \triangleq \{1, \dots, L\}$, $\mathcal{N} \triangleq \{1, \dots, N\}$, $\mathcal{M} \triangleq \{1, \dots, M\}$, $\mathcal{M}_s = \{1, \dots, M_s\}$, $\mathcal{M}_a = \{M_s+1, \dots, M_s+M_a\}$, and $\mathcal{G} \triangleq \{1, \dots, G\}$, where G is the number of original tasks. The main parameters and variables used in the problem formulation are summarized in Table 2.

4.1 Preliminary

4.1.1 Task Triplication Scheme. Since we consider task triplication and task triplication acts on original tasks, we initially analyze the relationship between the allocation and the triplication of original tasks. Let $\alpha_i = 3i - 2$ ($1 \leq i \leq G$) denote the subscript of the original task. Therefore, tasks τ_{α_i+1} and τ_{α_i+2} are the second and third replicas (duplication and triplication) of task τ_{α_i} , respectively. If $h_{\alpha_i} = 0$, task τ_{α_i} is not triplicated, and thus, we have $e_{\alpha_i+1} = e_{\alpha_i+2} = 0$. As a result, tasks τ_{α_i+1} and τ_{α_i+2} do not exist. They can be removed from the task graph and will not generate any data for the successors or receive any data from the predecessors.

To determine the value of h_{α_i} , let's consider the simple example shown in Fig. 1, where τ_1 , τ_4 and τ_7 are the original tasks. Let $\mathcal{S}_1 = \{\tau_1, \tau_4\}$ and $\mathcal{S}_2 = \{\tau_4, \tau_7\}$. Based on the elements in the task sets \mathcal{S}_1 and \mathcal{S}_2 , we get the following four cases: 1) Tasks in \mathcal{S}_1 are allocated to different nodes, while

Table 2. Symbols used in the problem formulation

Parameters	
M_a	number of actuator nodes
M_s	number of sensor nodes
M	number of wireless nodes
G	number of original tasks
N	number of original tasks and replicas
L	number of voltage/frequency levels
θ_k	the k^{th} node
τ_i	the i^{th} task
(v_l, f_l)	the l^{th} voltage/frequency level
H	scheduling horizon
P_l^s	static power of processor running on (v_l, f_l)
P_l^d	dynamic power of processor running on (v_l, f_l)
P^0	idle power of processor
E_k^r	energy supply of node θ_k
e_i	execution cycles of task τ_i
o_{ij}	$= \begin{cases} 1 & \text{if task } \tau_i \text{ proceeds } \tau_j \text{ and } \tau_j \text{ is the nearest task of } \tau_i \\ 0 & \text{else} \end{cases}$
N_{α_i}	number of tasks that dependent on task τ_{α_i}
s_{ij}	size of data that task τ_i produces for task τ_j
$r_{\beta\gamma k}$	energy consumed of node θ_k when routing unit of data from θ_β to θ_γ
$t_{\beta\gamma}$	time required to transmit unit of data from θ_β to θ_γ
Integer Variables	
q_{ik}	$= \begin{cases} 1 & \text{if task } \tau_i \text{ is allocated to node } \theta_k \\ 0 & \text{else} \end{cases}$
c_{il}	$= \begin{cases} 1 & \text{if task } \tau_i \text{ is executed with frequency } f_l \\ 0 & \text{else} \end{cases}$
u_{ij}	$= \begin{cases} 1 & \text{if task } \tau_i \text{ proceeds task } \tau_j \\ 0 & \text{else} \end{cases}$
h'_i	$= \begin{cases} 1 & \text{if task } \tau_i \text{ exists} \\ 0 & \text{else} \end{cases}$
Continuous Variables	
t_i^s	start time of task τ_i

tasks in \mathcal{S}_2 are allocated to the same node; 2) Tasks in \mathcal{S}_1 are allocated to different nodes and tasks in \mathcal{S}_2 are allocated to different nodes; 3) Tasks in \mathcal{S}_1 are allocated to the same node and tasks in \mathcal{S}_2 are allocated to the same node; 4) Tasks in \mathcal{S}_1 are allocated to the same node, while tasks in \mathcal{S}_2 are allocated to different nodes.

For $Case_1$, τ_1 is triplicated, while τ_4 is not triplicated ($h_1 = 1$ and $h_4 = 0$). For $Case_2$, τ_1 and τ_4 are triplicated ($h_1 = h_4 = 1$). For $Case_3$, τ_1 and τ_4 are not triplicated ($h_1 = h_4 = 0$). For $Case_4$, τ_4 requires triplication, since τ_4 and τ_7 are allocated to different nodes. In addition, τ_1 is triplicated, as τ_4 , τ_5 and τ_6 are allocated to different nodes. The decision regarding the tasks in \mathcal{S}_2 propagates to τ_1 , which needs to be also triplicated, even if the tasks in \mathcal{S}_1 are allocated to the same node. From the above

example, we observe that the triplication decision is not only related to the allocation of original tasks, but also to the allocation of the replicas.

To formulate the relation between task triplication and task allocation decisions, we introduce a parameter N_{α_i} and an auxiliary variable M_{α_i} for the original task τ_{α_i} , where N_{α_i} represents the number of tasks that dependent on task τ_{α_i} , while M_{α_i} represents the number of dependent tasks that are assigned to the same node as task τ_{α_i} . Based on the dependency between the tasks, the values of N_{α_i} and M_{α_i} are given by:

$$N_{\alpha_i} = \sum_{j \in \mathcal{N}} o_{\alpha_i j}, \quad \forall i \in \mathcal{G}, \quad (1)$$

$$M_{\alpha_i} = \sum_{j \in \mathcal{N}} \sum_{k \in \mathcal{M}} o_{\alpha_i j} q_{\alpha_i k} q_{j k}, \quad \forall i \in \mathcal{G}, \quad (2)$$

In Fig. 1, as τ_1 and τ_4 are original tasks, we have $N_1 = 3$ and $N_4 = 1$ for both cases due to

$$\mathbf{o} = \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}.$$

For *Case*₁, we have $M_1 \leq 2$ as τ_1 and τ_4 are assigned to different nodes, and $M_4 = 1$ as τ_4 and τ_7 are assigned to the same node. Similarly, we get $M_1 \leq 2$ and $M_4 = 0$ for *Case*₂; $M_1 = 3$ and $M_4 = 1$ for *Case*₃; $M_1 \leq 2$ and $M_4 = 0$ for *Case*₄. Since $M_{\alpha_i} \leq N_{\alpha_i}$, based on the triplication decisions regarding the tasks τ_1 and τ_4 , we can get the following conclusion: if $M_{\alpha_i} = N_{\alpha_i}$, $h_{\alpha_i} = 0$, else (i.e., $M_{\alpha_i} \leq N_{\alpha_i} - 1$), $h_{\alpha_i} = 1$. Note that this *if - else* description is not a standard constraint and M_{α_i} is changed with task allocation variable q_{ik} . We introduce the following lemma to determine the value of h_{α_i} .

LEMMA 4.1. *Let x and y denote an integer variable and a binary variable, respectively, where x is bounded by $0 \leq x \leq m$. $m \geq 0$ and $n \geq 0$ are the positive integer constants. The case: if $x = n \rightarrow y = 0$, else (i.e., $x \leq n - 1$), $y = 1$ can be given by the linear constraint $\frac{x-n+1}{m+1} \leq 1 - y \leq \frac{x+1}{n+1}$.*

PROOF. If $x = n$, we have $\frac{x-n+1}{m+1} > 0$ and $\frac{x+1}{n+1} = 1$. Therefore, we get $y = 0$. On the other hand, if $x \leq n - 1$, since $\frac{x-n+1}{m+1} \leq 0$ and $\frac{x+1}{n+1} < 1$, we obtain $y = 1$. \square

According to *Lemma 4.1*, the task triplication decision h_{α_i} is determined by the following constraint:

$$\frac{M_{\alpha_i} - N_{\alpha_i} + 1}{N + 1} \leq 1 - h_{\alpha_i} \leq \frac{M_{\alpha_i} + 1}{N_{\alpha_i} + 1}, \quad \forall i \in \mathcal{G}, \quad (3)$$

where N_{α_i} and M_{α_i} are given by Eq. (1) and Eq. (2), respectively.

4.1.2 Node Communication Cost. Let $\mathbf{h}' = [h'_1, \dots, h'_{3G}]^T = [1, h_1, h_1, \dots, 1, h_{\alpha_i}, h_{\alpha_i}, \dots, 1, h_G, h_G]^T$. With the variable h'_i , the number of execution cycles of task τ_i is $h'_i e_i$ and the size of data, that task τ_i generate for task τ_j , is $h'_i h'_j s_{ij}$. If task τ_i is assigned to node θ_m , while task τ_j ($i \neq j$) is assigned to node θ_n ($m \neq n$), the communication energy consumed by node θ_k ($k \neq m \neq n$) to send the data from τ_i to τ_j is $h'_i h'_j s_{ij} q_{im} q_{jn} r_{mnk}$. Therefore, the communication energy of node θ_k is

$$E_k^t = \sum_{i \in \mathcal{N}} \sum_{j \in \mathcal{N}} \sum_{m \in \mathcal{M}} \sum_{n \in \mathcal{M}} h'_i h'_j s_{ij} q_{im} q_{jn} r_{mnk}, \quad \forall k \in \mathcal{M}. \quad (4)$$

To execute a task τ_j , we must collect all the data generated from its previous dependent tasks. If task τ_i is assigned to node θ_m , while task τ_j is assigned to node θ_n , the time required to transmit the

data with size $h'_i h'_j s_{ij}$, from θ_m to θ_n , is $h'_i h'_j s_{ij} q_{im} q_{jn} t_{mn}$. We assume that the used communication protocol prevents concurrent reception of messages from multiple nodes, avoiding data conflicts. Hence, the time required to collect the data for the execution of task τ_j is

$$t_j^r = \sum_{i \in \mathcal{N}} \sum_{m \in \mathcal{M}} \sum_{n \in \mathcal{M}} h'_i h'_j s_{ij} q_{im} q_{jn} t_{mn}, \quad \forall j \in \mathcal{N}. \quad (5)$$

4.1.3 Task Execution Cost. Since the voltage/frequency level is adjusted in a discrete manner, similar to the previously published work [22], the time and the energy required to execute task τ_i with $h'_i e_i$ cycles are $t_i^c = \sum_{l \in \mathcal{L}} c_{il} \frac{h'_i e_i}{f_l}$ and $E_i^c = \sum_{l \in \mathcal{L}} c_{il} \frac{h'_i e_i}{f_l} P_l^c$, respectively. On this basis, considering the task allocation decision q_{ik} , the time and the energy required to execute the tasks assigned to node θ_k are

$$t_k^n = \sum_{i \in \mathcal{N}} q_{ik} t_i^c = \sum_{i \in \mathcal{N}} \sum_{l \in \mathcal{L}} q_{ik} c_{il} \frac{h'_i e_i}{f_l}, \quad \forall k \in \mathcal{M}, \quad (6)$$

$$E_k^n = \sum_{i \in \mathcal{N}} q_{ik} E_i^c = \sum_{i \in \mathcal{N}} \sum_{l \in \mathcal{L}} q_{ik} c_{il} \frac{h'_i e_i}{f_l} P_l^c, \quad \forall k \in \mathcal{M}. \quad (7)$$

Therefore, the computation energy of node θ_k is

$$E_k^d = E_k^n + (H - t_k^n) P^0, \quad \forall k \in \mathcal{M}, \quad (8)$$

where $H - t_k^n$ is the idle time of the processor during the scheduling horizon H .

4.2 Primal Problem

To balance the energy consumption of the nodes and increase the system lifetime and connectivity, we can minimize the maximum energy consumption on a single node. Thus, the objective function is set to $F = \max_{k \in \mathcal{M}} \{(E_k^t + E_k^d) / E_k^{ini}\}$. With this aim, the task mapping problem (Primal Problem, PP) has the form

$$\text{PP : } \min_{\mathbf{q}, \mathbf{c}, \mathbf{u}, \mathbf{h}', \mathbf{t}^s} F \quad (9)$$

$$\text{s.t. (3), (10) - (19).}$$

The constraints of the problem are described as follows:

(1) *Task Allocation Constraints:* Note that each task τ_i is assigned to one node (no task migration), and the tasks requiring nodes equipped with the sensor or actuator are restricted. The original task τ_{α_i} and its replicas τ_{α_i+1} and τ_{α_i+2} should not be assigned to the same node. Therefore, we have

$$\sum_{k \in \mathcal{M}} q_{ik} = 1, \quad \forall i \in \mathcal{N}, \quad (10)$$

$$q_{ik} = 1, \quad \forall (i, k) \in \mathcal{D}, \quad (11)$$

$$0 \leq q_{\alpha_i k} + q_{\alpha_i+1 k} + q_{\alpha_i+2 k} \leq 1, \quad \forall i \in \mathcal{G}, \quad \forall k \in \mathcal{M}, \quad (12)$$

where \mathcal{D} is the set of task and node indexes that have restricted task allocation.

(2) *Frequency Assignment Constraint:* Since each task τ_i is executed with one voltage/frequency level, we get

$$\sum_{l \in \mathcal{L}} c_{il} = 1, \quad \forall i \in \mathcal{N}. \quad (13)$$

(3) *Task Sequence Constraint:* For the dependent tasks (e.g., tasks τ_1 , τ_4 and τ_7 in Fig. 1), no matter if they are assigned to the same node or different nodes, the start time and the end time of tasks is bounded. Therefore, we have

$$t_j^s + (1 - o_{ij})H \geq t_i^s + o_{ij}(t_i^e - t_i^s) + t_j^r, \quad \forall i \neq j \in \mathcal{N}, \quad (14)$$

where t_i^s and t_i^e are the start time and the end time of task τ_i , respectively ($0 \leq t_i^s \leq t_i^e \leq H$). Therefore, the task execution time is given by $t_i^c = t_i^e - t_i^s$. If $o_{ij} = 1$ (i.e., τ_i precedes τ_j and τ_j is the closest task of τ_i), we have $t_j^s \geq t_i^e + t_j^r$, else (i.e., $o_{ij} = 0$), Eq. (14) is always satisfied.

- (4) *Task Non-Preemption Constraint*: Note that some tasks can be independent in the task set (e.g., tasks τ_1 , τ_2 and τ_3 in Fig. 1). When independent tasks are assigned to the same node, we need to determine their execution sequence. To this end, we introduce a binary variable u_{ij} . Since each node executes no more than one task at the same time, we get

$$t_i^e \leq t_j^s + (2 - q_{ik} - q_{jk})H + (1 - u_{ij})H, \forall i \neq j \in \mathcal{N}, \forall k \in \mathcal{M}. \quad (15)$$

If tasks τ_i and τ_j are assigned to the same node (e.g., $q_{ik} = q_{jk} = 1$), Eq. (15) is meaningful, else, Eq. (15) is always true. With $q_{ik} = q_{jk} = 1$, if $u_{ij} = 1$ (i.e., τ_i precedes τ_j), we have $t_i^e \leq t_j^s$, else (i.e., $u_{ij} = 0$), Eq. (15) is always satisfied.

- (5) *Task Deadline Constraint*: The system is periodic, i.e., it operates in rounds [35] (scheduling horizon H), where in each round, all the tasks are executed once. Therefore, the constraint related to the task deadline is

$$t_i^e \leq H, \forall i \in \mathcal{N}. \quad (16)$$

- (6) *Energy Constraints*: Let E_k^s , E_k^a and E_k^r denote the sensing energy of the sensor, the acting energy of the actuator, and the energy supply of the node at the r^{th} round, respectively. Since the total energy consumed by node θ_k , during the scheduling horizon H , should not exceed its energy supply, we get

$$E_k^t + E_k^d + E_k^s \leq E_k^r, \forall k \in \mathcal{M}_s, \quad (17)$$

$$E_k^t + E_k^d + E_k^a \leq E_k^r, \forall k \in \mathcal{M}_a, \quad (18)$$

$$E_k^t + E_k^d \leq E_k^r, \forall k \in \mathcal{M}, \forall k \notin \mathcal{M}_s, \mathcal{M}_a. \quad (19)$$

4.3 Problem Linearization

Due to the products of binary variables (e.g., $q_{im}q_{jn}$ in Eq. (3), $h'_i h'_j q_{im} q_{jn}$ and $q_{ik} c_{il} h'_i$ in Eq. (14), Eq. (16)–Eq. (19)), the PP is an MINLP problem, which makes it challenging to solve.

THEOREM 4.1. *The task mapping problem of the networked system (i.e., PP) is NP-hard.*

PROOF. Please refer to [7] for the details. \square

Linearization is an efficient way to simplify the structure of the problem. To linearize the nonlinear items, we introduce the following lemma.

LEMMA 4.2. *Let x , y , and z denote the binary variables. The nonlinear item $z = xy$ can be linearized as follows: $z \leq x$, $z \leq y$ and $z \geq x + y - 1$.*

PROOF. The inequalities $z \leq x$ and $z \leq y$ ensure that $z = 0$ if either $x = 0$ or $y = 0$. The inequality $z \geq x + y - 1$ makes sure that $z = 1$ if both x and y are set to 1. \square

According to Lemma 4.2, we first introduce an auxiliary (binary) variable β_{imjn} to replace the nonlinear term $q_{im}q_{jn}$, and then add the following constraints into the PP:

$$\{\beta_{imjn} \leq q_{im}, \beta_{imjn} \leq q_{jn}, \beta_{imjn} \geq q_{im} + q_{jn} - 1\}, \forall i \neq j \in \mathcal{N}, \forall m, n \in \mathcal{M}. \quad (20)$$

Therefore, we have

$$M_{\alpha_i} = \sum_{j \in \mathcal{N}} \sum_{k \in \mathcal{M}} o_{\alpha_i j} \beta_{\alpha_i k j}. \quad (21)$$

On this basis, to deal with the nonlinear item $h'_i h'_j q_{im} q_{jn}$, we reuse the auxiliary variable β_{imjn} . Let $\gamma_{ij} = h'_i h'_j$ and $\delta_{imjn} = \gamma_{ij} \beta_{imjn}$. Therefore, if the following constraints are added to the PP:

$$\{\gamma_{ij} \leq h'_i, \gamma_{ij} \leq h'_j, \gamma_{ij} \geq h'_i + h'_j - 1\}, \forall i \neq j \in \mathcal{N}, \quad (22)$$

$$\{\delta_{imjn} \leq \gamma_{ij}, \delta_{imjn} \leq \beta_{imjn}, \delta_{imjn} \geq \gamma_{ij} + \beta_{imjn} - 1\}, \forall i \neq j \in \mathcal{N}, \forall m, n \in \mathcal{M}, \quad (23)$$

Eq. (4) and Eq. (5) can be rewritten as follows:

$$E_k^t = \sum_{i \in \mathcal{N}} \sum_{j \in \mathcal{N}} \sum_{m \in \mathcal{M}} \sum_{n \in \mathcal{M}} s_{ij} r_{mnk} \delta_{imjn}, \forall k \in \mathcal{M}, \quad (24)$$

$$t_j^r = \sum_{i \in \mathcal{N}} \sum_{m \in \mathcal{M}} \sum_{n \in \mathcal{M}} s_{ij} t_{mn} \delta_{imjn}, \forall j \in \mathcal{N}. \quad (25)$$

Similarly, let $\varepsilon_{il} = h'_i c_{il}$ and $\eta_{ilk} = \varepsilon_{il} q_{ik}$. The nonlinear items $h'_i c_{il}$ and $h'_i c_{il} q_{ik}$ can be replaced by the auxiliary (binary) variables ε_{il} and η_{ilk} and the following constraints:

$$\{\varepsilon_{il} \leq h'_i, \varepsilon_{il} \leq c_{il}, \varepsilon_{il} \geq h'_i + c_{il} - 1\}, \forall i \in \mathcal{N}, \forall l \in \mathcal{L}, \quad (26)$$

$$\{\eta_{ilk} \leq \varepsilon_{il}, \eta_{ilk} \leq q_{ik}, \eta_{ilk} \geq \varepsilon_{il} + q_{ik} - 1\}, \forall i \in \mathcal{N}, \forall l \in \mathcal{L}, \forall k \in \mathcal{M}. \quad (27)$$

Therefore, Eq. (6) and Eq. (7) can be rewritten as follows:

$$t_k^n = \sum_{i \in \mathcal{N}} \sum_{l \in \mathcal{L}} \eta_{ilk} \frac{e_i}{f_l}, \forall k \in \mathcal{M}, \quad (28)$$

$$E_k^n = \sum_{i \in \mathcal{N}} \sum_{l \in \mathcal{L}} \eta_{ilk} \frac{e_i}{f_l} P_l^c, \forall k \in \mathcal{M}. \quad (29)$$

Based on the above analysis, the PP is linearized as follows:

$$\begin{aligned} \text{PP1 : } & \min_{\substack{\xi \\ q, c, u, h', t^s, \\ \beta, \gamma, \delta, \varepsilon, \eta, \xi}} \xi \\ & \text{s.t. } \begin{cases} \frac{E_k^t + E_k^n}{E_k^{ini}} \leq \xi, \forall k \in \mathcal{M}, \\ (3), (10) - (19), (20), (22), (23), (26), (27), \end{cases} \end{aligned} \quad (30)$$

where ξ is an auxiliary variable, and M_{α_i} , E_k^t , t_j^r , t_k^n , and E_k^n in constraints are given by Eqs. (21), (24), (25), (28) and (29), respectively.

5 OPTIMAL TASK DEPLOYMENT ALGORITHM

In this section, we propose an Optimal Reliability Task Mapping algorithm, referred to as ORTM, to find the optimal solution for PP1. The basic idea of the ORTM algorithm comes from the Benders decomposition [3]. Instead of considering all the variables and constraints simultaneously, the ORTM algorithm separates the PP1 into smaller, easier-to-solve problems, the Master Problem (MP) and the Slave Problem (SP). Then, the PP1 is solved by iteratively solving the MP and the SP. Note that the PP1 has the following abstract form

$$\begin{aligned} \text{PP2 : } & \min_{x, y} \Phi = f^T x \\ & \text{s.t. } \begin{cases} Ax \leq b_1, \\ Cx + Dy \leq b_2, \end{cases} \end{aligned} \quad (31)$$

where $x \triangleq (q, c, u, h', \beta, \gamma, \delta, \varepsilon, \eta)$ and $y \triangleq (t^s, \xi)$ denote the vectors of binary and continuous variables, respectively. The vector f represents the coefficients in the objective function. The matrices A , C and D are the coefficients in the constraints. b_1 (b_2) is a u (v)-dimensional vector.

Based on the structure of PP2, at the k^{th} iteration, the MP and the SP are formulated as follows:

$$\text{MP} : \Phi_l(k) = \min_{x, \hat{\Phi}} \hat{\Phi} \quad (32)$$

$$\text{s.t.} \begin{cases} \mathbf{Ax} \leq \mathbf{b}_1, \\ \mathbf{C}_1 : \hat{\Phi} \geq (\mathbf{f}^T + \boldsymbol{\lambda}^T(i)\mathbf{C})\mathbf{x} - \boldsymbol{\lambda}^T(i)\mathbf{b}_2, \quad 1 \leq i \leq m, \\ \mathbf{C}_2 : 0 \geq \hat{\boldsymbol{\lambda}}^T(j)(\mathbf{Cx} - \mathbf{b}_2), \quad 1 \leq j \leq n, \end{cases}$$

$$\text{SP} : \Phi_u(k) = \min_{\mathbf{y} \geq 0} \mathbf{f}^T \mathbf{x}(k) \quad (33)$$

$$\text{s.t. } \mathbf{Cx}(k) + \mathbf{Dy} \leq \mathbf{b}_2,$$

where $\hat{\Phi}$ is an auxiliary variable to facilitate the iterations between MP and SP, and $\mathbf{x}(k)$ is the MP solution at the k^{th} iteration. \mathbf{C}_1 and \mathbf{C}_2 are the sets of constraints called Benders cuts ($m + n = k$). By solving the MP and the SP, we obtain a lower bound, $\Phi_l(k)$, and an upper bound, $\Phi_u(k)$, of Φ^* [36], where Φ^* is the optimal value of Φ . The iteration stops when $\Phi_u(k) - \Phi_l(k) \leq \epsilon$.

The MP accounts for all the binary variables \mathbf{x} and the associated portion of the objective function and the constraints of the PP2. The SP includes all the continuous variables \mathbf{y} and the associated constraints in the PP2. By solving the Dual Slave Problem (DSP)

$$\begin{aligned} \text{DSP} : \max_{\boldsymbol{\lambda} \geq 0} & (\mathbf{f}^T + \boldsymbol{\lambda}^T \mathbf{C})\mathbf{x}(k) - \boldsymbol{\lambda}^T \mathbf{b}_2 \\ \text{s.t. } & \mathbf{D}'\boldsymbol{\lambda} \geq 0, \end{aligned} \quad (34)$$

we obtain the information about the SP portion of the PP2, and this information is communicated to the MP via Benders cuts, where $\boldsymbol{\lambda} \triangleq [\lambda_i]$ are the Lagrange multipliers.

Based on the solution of the DSP, we have the following three cases.

- (1) If DSP is infeasible, PP2 has no feasible solution.
- (2) If DSP has a bounded solution $\boldsymbol{\lambda}(k)$, since 1) $\hat{\Phi}(k) < (\mathbf{f}^T + \boldsymbol{\lambda}^T(k)\mathbf{C})\mathbf{x}(k) - \boldsymbol{\lambda}^T(k)\mathbf{b}_2$, where $\hat{\Phi}(k)$ is the optimal solution of MP at the k^{th} iteration, and 2) $\mathbf{x}(k)$ is not the optimal solution of PP2 due to $\Phi_u(k) - \Phi_l(k) > \epsilon$, the non-optimal solution $\mathbf{x}(k)$ is excluded by the constraint $\hat{\Phi} \geq (\mathbf{f}^T + \boldsymbol{\lambda}^T(k)\mathbf{C})\mathbf{x} - \boldsymbol{\lambda}^T(k)\mathbf{b}_2$.
- (3) If DSP has an unbounded solution, the SP has no feasible solution under the given solution $\mathbf{x}(k)$. However, the SP is feasible if positive variables $\boldsymbol{\xi} \triangleq [\xi_i]$ are introduced to relax the constraints. Based on this idea, we construct a Feasibility Check Problem (FCP) and solve its dual problem (DFCP).

$$\text{FCP} : \min_{\mathbf{y}, \boldsymbol{\xi} \geq 0} \mathbf{1}^T \boldsymbol{\xi} \quad (35)$$

$$\text{s.t. } \mathbf{Cx}(k) + \mathbf{Dy} \leq \mathbf{b}_2 + \boldsymbol{\xi}.$$

$$\text{DFCP} : \max_{\hat{\boldsymbol{\lambda}} \geq 0} \hat{\boldsymbol{\lambda}}^T (\mathbf{Cx}(k) - \mathbf{b}_2) \quad (36)$$

$$\text{s.t.} \begin{cases} \mathbf{1} - \hat{\boldsymbol{\lambda}} \geq 0, \\ \mathbf{D}^T \hat{\boldsymbol{\lambda}} \geq 0, \end{cases}$$

where $\hat{\boldsymbol{\lambda}} \triangleq [\hat{\lambda}_i]$ are the Lagrange multipliers. Let $\boldsymbol{\xi}(k)$ and $\hat{\boldsymbol{\lambda}}(k)$ be the optimal solutions of FCP and DFCP at the k^{th} iteration, respectively. If the SP is infeasible, this implies that some constraints cannot be satisfied, and thus, their related relaxation variables are non-zero. We have $\mathbf{1}^T \boldsymbol{\xi}(k) > 0$, and further, $\mathbf{1}^T \boldsymbol{\xi}(k) = \hat{\boldsymbol{\lambda}}^T(k)(\mathbf{Cx}(k) - \mathbf{b}_2) > 0$ due to the strong duality [6]. Therefore, the infeasible solution $\mathbf{x}(k)$ can be excluded by the constraint $0 \geq \hat{\boldsymbol{\lambda}}^T(k)(\mathbf{Cx} - \mathbf{b}_2)$.

638 With the constraint $\hat{\Phi} \geq (f^T + \lambda^T(k)C)x - \lambda^T(k)b_2$ added into C_1 , or the constraint $0 \geq$
 639 $\hat{\lambda}^T(k)(Cx - b_2)$ added into C_2 , at the $(k+1)^{th}$ iteration, the MP is solved again to obtain a new
 640 solution $x(k+1)$ for the next iteration.

641 **THEOREM 5.1.** *With a new constraint added to the MP at each iteration, the solution obtained by*
 642 *ORTM converges to the global optimal value Φ^* within a finite number of iterations.*

643
 644 **PROOF.** Since the non-optimal and the infeasible solutions of the binary variables x are excluded
 645 by the constraints C_1 and C_2 , and the dimension of x is finite, the solution converges to the global
 646 optimal value within a finite number of iterations. \square

647 6 HEURISTIC APPROACH

649 Note that the ORTM algorithm generates a new constraint at each iteration and adds it to the
 650 ILP-based MP. With the number of iterations increasing, the computational complexity and the size
 651 of MP also increase. Therefore, the computational complexity of the ORTM algorithm is determined
 652 by the cost of solving the MP. Although the solution provided by the ORTM algorithm is optimal,
 653 this method cannot solve problems with large network sizes efficiently. To enhance the scalability
 654 of the proposed method, we propose a novel Heuristic Reliability Task Mapping algorithm, referred
 655 to as HRTM, to efficiently solve the PP. The reasons why we design the heuristic approach for PP,
 656 rather than PP1, are the following: 1) PP and PP1 are equivalent, 2) PP contains fewer variables,
 657 which makes the problem easier to solve, and 3) the heuristic approach determines the values of
 658 the variables sequentially, and thus, some nonlinear items, which are caused by the products of
 659 variables, can be omitted.

660 Similar to the ORTM algorithm, the HRTM algorithm is also based on problem decomposition.
 661 However, the main difference is that the subproblems in the ORTM algorithm are solved iteratively,
 662 while the subproblems in the HRTM algorithm are solved sequentially without iteration. Therefore,
 663 the computation time of the HRTM algorithm can be largely reduced. Based on the structure of PP,
 664 the HRTM algorithm contains two steps. We first consider the frequency assignment problem, in
 665 order to balance the task execution energy. Based on the solution of the first step, we solve the task
 666 mapping problem to balance the nodes' computation and communication energy.

667 6.1 Frequency Assignment Problem

669 The processors of the nodes have the same voltage/frequency levels $\{(v_1, f_1), \dots, (v_F, f_L)\}$. Therefore,
 670 the frequency-to-task assignment is independent of the task-to-node allocation, and thus, it can
 671 be considered as the first step; the solutions in the next heuristic step will not violate the solution
 672 of this step. Regarding task triplication, the task allocation decision q_{ik} is unknown at this step,
 673 and according to Eq. (3), the task triplication decision h'_i is determined by the value of q_{ik} . Hence,
 674 we consider the worst case, where each original task τ_{α_i} is triplicated ($h_{\alpha_i} = 1$), i.e., the number of
 675 tasks is N .

676 Under the given frequency assignment decision c_{il} , the time and the energy required to execute
 677 task τ_i with e_i cycles are $t_i^c = \sum_{l \in \mathcal{L}} c_{il} \frac{e_i}{f_l}$ and $E_i^c = \sum_{l \in \mathcal{L}} c_{il} \frac{e_i}{f_l} P_l^c$, respectively. If a lower
 678 voltage/frequency level is used to execute the tasks, the task execution energy will be reduced.
 679 However, it will have an impact on the real-time execution, since the task execution time increases.
 680 Therefore, at the current step, we aim to balance the task execution energy, while guaranteeing
 681 that the real-time task constraint is satisfied. On the one hand, minimizing the maximum energy
 682 consumption on a single task's execution (i.e., $\min(\max_{v_i \in \mathcal{N}} E_i^c)$) facilitates the balance of node
 683 energy consumption through task allocation, which is the main purpose of next step. On the other
 684 hand, taking the task real-time constraint into account, we can choose the proper voltage/frequency
 685 to execute the tasks to avoid missing the task deadlines.

Algorithm 1: Frequency-to-task assignment

```

687
688 1: Initialize:  $S[i] = -1, \forall i \in \mathcal{N}$ ;
689 2: Sort tasks  $\{\tau_1, \dots, \tau_N\}$  according to AssSequence;
690 3: for  $\forall i \in \mathcal{N}$  do
691 4:    $MinMaxCompE = \infty$ ;
692 5:    $flag = -1$ ;
693 6:   for  $\forall l \in \mathcal{L}$  do
694 7:      $S[i] = l$ ;
695 8:     Calculate  $t_i^e$  based on  $c_{il}$  and  $o_{ij}$ ;
696 9:      $MaxComp = MaxCompE$ ;
697 10:    if  $t_i^e > H$  then
698 11:       $c_{il} = 0$ ;
699 12:      Continue;
700 13:    else
701 14:      if  $MaxComp < MinMaxCompE$  then
702 15:         $MinMaxCompE = MaxComp$ ;
703 16:         $flag = l$ ;
704 17:      else
705 18:         $MinMaxCompE = MinMaxCompE$ ;
706 19:         $flag = flag$ ;
707 20:      end if
708 21:    end if
709 22:  end for
710 23:  if  $flag == -1$  then
711 24:    Fail to assign frequency for task  $\tau_i$ ;
712 25:  else
713 26:     $S[i] = flag$ ;
714 27:  end if
715 28: end for
716 29: Calculate  $c_{il}$  according to  $S[i]$ ;

```

Based on the objective function and the constraints mentioned above, the Frequency Assignment Problem (FAP) is formulated as

$$\begin{aligned}
 \text{FAP} : \min_c & \left(\max_{\forall i \in \mathcal{N}} E_i^c \right) \\
 \text{s.t.} & (13), (16).
 \end{aligned} \tag{37}$$

By using the idea of Greedy Algorithm (GA) [26, 35], we propose Algorithms 1 and 2 to solve this problem. For presentation reasons, a frequency assignment symbol $S[i]$ is introduced for each task τ_i , where $S[i] = l$ represents frequency f_l is used to execute task τ_i .

Before executing Algorithm 1, we need to determine the frequency assignment sequence of the tasks, *AssSequence*, required for sorting the tasks (Line 2). Note that the task start time and the node communication costs (energy and time) are unknown. To take real-time constraint (16) into account, we divide the tasks into several layers; tasks in different layers are dependent, while the tasks in the same layer are independent. Each task can identify its parent tasks based on the dependency between the tasks. On this basis, we can determine the layer to which a task should belong. The sequence is obtained following the layer order, and when independent tasks exist within a layer, they are sorted in descending order of execution cycles.

Algorithm 2: Calculate *MaxCompE*

```

1: MaxCompE = 0;
2: for  $\forall i \in \mathcal{N}$  ( $S[i] \neq -1$ ) do
3:    $CompE = \frac{e_i}{f_{S[i]}^c} P^c S[i]$ ;
4:   if  $CompE > MaxCompE$  then
5:      $MaxCompE = CompE$ ;
6:   else
7:      $MaxCompE = MaxCompE$ ;
8:   end if
9: end for

```

To illustrate the above procedure, Fig. 2 shows a simple case with five dependent tasks. The task dependency is described by a binary matrix $\mathbf{o} = [o_{ij}]_{5 \times 5}$, where $o_{12} = o_{13} = o_{14} = o_{23} = o_{24} = o_{35} = o_{45} = 1$. According to the matrix \mathbf{o} , we can describe each task τ_i with the sets $PTIndex_i$ and $PTLayer_i$, where $PTIndex_i$ is the set of parent tasks and $PTLayer_i$ is the set of the layers assigned to parent tasks. For example, task τ_3 has two parent tasks τ_1 and τ_2 , and the layers of tasks τ_1 and τ_2 are L_1 and L_2 , respectively. Hence, we have $PTIndex_3 = \{1, 2\}$ and $PTLayer_3 = \{1, 2\}$, and thus, task τ_3 is assigned to layer L_3 . On the other hand, the independent tasks τ_3 and τ_4 can be assigned to the same layer L_3 . For tasks τ_3 and τ_4 , assuming that the execution cycles of τ_3 are higher than those of τ_4 (i.e., $e_3 > e_4$), the τ_3 is considered first. Therefore, we obtain a frequency assignment sequence: $AssSequence = \{\tau_1 \rightarrow \tau_2 \rightarrow \tau_3 \rightarrow \tau_4 \rightarrow \tau_5\}$. Since the original task τ_{α_i} and its replicas τ_{α_i+1} and τ_{α_i+2} are independent, they are assigned to the same layer. Note that τ_{α_i} , τ_{α_i+1} and τ_{α_i+2} have the same execution cycles (i.e., $e_{\alpha_i} = e_{\alpha_i+1} = e_{\alpha_i+2}$). We assume that the frequency assignment sequence between them is $\tau_{\alpha_i} \rightarrow \tau_{\alpha_i+1} \rightarrow \tau_{\alpha_i+2}$. On the other hand, for the original tasks τ_{α_i} and τ_{α_j} ($i \neq j$), if they are in the same layer and they have the same execution cycles (i.e., $e_{\alpha_i} = e_{\alpha_j}$), we consider that their frequency assignment priorities can be randomly chosen.

Based on the frequency assignment sequence $AssSequence$, Algorithm 1 finds the proper frequency for task τ_i under the real-time constraint, i.e., the selected frequency should cause the minimum increase in task execution energy, among the tasks that have already been assigned their frequency. During this process, Algorithm 2 is used to calculate the maximum energy required to execute a task, i.e., $MaxCompE$. If the real-time constraint cannot be satisfied under the given assignment decision $S[i] = l$, this decision will be excluded (Lines 10–12). As the task allocation decision is unknown, we omit the communication time of the nodes and assume that there is no gap between two adjacent tasks when calculating the end time of each task (Line 8). As the example shown in Fig. 2, we have $t_1^s = 0$, $t_1^e = t_1^c$, $t_2^e = t_1^e + t_2^c$, $t_3^e = \max\{t_1^e, t_2^e\} + t_3^c$, $t_4^e = \max\{t_1^e, t_2^e\} + t_4^c$, and $t_5^e = \max\{t_3^e, t_4^e\} + t_5^c$, where t_i^s , $t_i^c = \sum_{l \in \mathcal{L}} c_{il} \frac{e_i}{f_l}$ and $t_i^e = t_i^s + t_i^c$ are the start time, execution time and end time of task τ_i , respectively. By applying the above frequency selection process for each task τ_i (Lines 3–28), the solution to the FAP is found.

6.2 Task Mapping Problem

From Eq. (4) and Eq. (7), we observe that when the frequency assignment decision c_{il} is fixed, the communication cost E_k^l and the computation cost E_k^n of each node θ_k are determined by the task allocation decision q_{ik} and the task triplication decision h'_i . In addition, the task triplication decision h'_i is influenced by the task allocation decision q_{ik} . Therefore, the communication and computation energy of the nodes can be optimized by adjusting q_{ik} . The task sequence constraint (14) and the task non-preemption constraint (15) show that the value of q_{ik} is also restricted by the task start

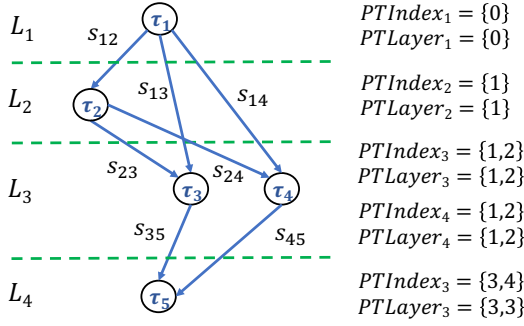


Fig. 2. Determine frequency assignment sequence based on task layer.

time t_i^s and the task sequence u_{ij} . Therefore, when making the task allocation decision q_{ik} , the constraints (14) and (15) should be taken into account.

At this step, under the given frequency assignment decision c_{il} , we aim to determine task allocation q_{ik} , task sequence u_{ij} , task triplication h'_i , and task start time t_i^s . Therefore, the Task Mapping Problem (TMP) is formulated as

$$\text{TMP} : \min_{q,u,h',t^s} \left(\max_{\forall k \in M} \left\{ \left(E_k^t + E_k^d \right) / E_k^{ini} \right\} \right) \quad (38)$$

s.t. (3), (10) – (12), (14) – (19).

Since the binary variables q_{ik} , h'_i and u_{ij} and the continuous variable t_i^s are coupled with each other linearly, and the nonlinear items $q_{im}q_{jn}$ and $h'_i h'_j q_{im}q_{jn}$ are included in constraints (3), (17)–(19), TMP is a MINLP problem. Based on the structure of TMP, we propose Algorithms 3–4 to solve it.

Algorithm 3 first initializes the allocation index $O[i]$ for each task τ_i , where $O[i] = k$ represents that task τ_i is allocated to node θ_k . Then, it allocates tasks, one by one, to their proper nodes, causing the minimum increase in energy consumption among the nodes, under real-time and energy constraints (Lines 1–2). Different from the FAP, the allocations of dependent tasks on the wireless nodes are considered in TMP. Therefore, the real-time constraint in TMP includes the communication time of the nodes. The task allocation sequence is given by *AssSequence*, as it can handle the problems of task start time and task sequence simultaneously (i.e., t_i^s and u_{ij}). To find the maximum energy consumption at a node (i.e., *MaxCostN*), under the given task allocation decision $O[i] = k$, we design Algorithm 4. During the allocation process, if tasks τ_i and τ_j ($i \neq j$) are dependent and these tasks are allocated to the same node, there is no need to triplicate task τ_i , since the data of τ_i is directly transmitted to τ_j within one node.

As shown in Fig. 1, τ_1 , τ_4 and τ_7 are the original tasks, while τ_2 , τ_3 , τ_5 and τ_6 are the corresponding replicas. Based on task dependency o_{ij} and task execution cycles e_i , we follow the sequence: *AssSequence* = $\{\tau_1 \rightarrow \tau_2 \rightarrow \tau_3 \rightarrow \tau_4 \rightarrow \tau_5 \rightarrow \tau_6 \rightarrow \tau_7\}$ to perform task allocation. Since the original task and the corresponding replicas are independent (e.g., τ_1 , τ_2 and τ_3), they can be placed in the same layer. If the tasks τ_1 , τ_4 , τ_5 and τ_6 are allocated to the same node, there is no need to triplicate τ_1 . Hence, tasks τ_2 and τ_3 can be removed from the task graph, and the execution time of tasks τ_2 and τ_3 can be set to $t_2^c = t_3^c = 0$, since $e_2 = e_3 = 0$. Based on the allocation of the original tasks τ_4 and τ_7 , we can use a similar method to decide the existence of tasks τ_5 and τ_6 .

Algorithm 3: Task-to-node allocation

```

834 1: Initialize:  $O[i] = -1, \forall i \in \mathcal{N}$ ;
835 2: Sort tasks  $\{\tau_1, \dots, \tau_N\}$  according to AssSequence;
836 3: for  $\forall i \in \mathcal{N}$  do
837 4:    $MinMaxE = \infty$ ;
838 5:   for  $\forall k \in \mathcal{M}$  do
839 6:     if  $\tau_i$  can be allocated  $\theta_k$  then
840 7:        $O[i] = k$ ;
841 8:       if  $\tau_i$  is allocated to the same node as its previous dependent tasks then
842 9:         Update  $\{t_1^e, \dots, t_{i-1}^e\}$ ;
843 10:      else
844 11:        Continue;
845 12:      end if
846 13:      Calculate  $t_i^e, E_k^t$  and  $E_k^d$  based on  $q_{ik}, o_{ij}$  and  $\{t_1^e, \dots, t_{i-1}^e\}$ ;
847 14:      Calculate  $E_k^{all}$  based on  $E_k^t, E_k^d, E_k^s$  and  $E_k^a$ ;
848 15:       $MaxE = MaxNodeE$ ;
849 16:      if  $E_k^{all} > E_k^l || t_i^e > H$  then
850 17:         $q_{ik} = 0$ ;
851 18:        Continue;
852 19:      else
853 20:        if  $MaxE < MinMaxE$  then
854 21:           $MinMaxE = MaxE$ ;
855 22:           $flag = k$ ;
856 23:        else
857 24:           $MinMaxE = MinMaxE$ ;
858 25:           $flag = flag$ ;
859 26:        end if
860 27:      end if
861 28:      else
862 29:         $q_{ik} = 0$ ;
863 30:        Continue;
864 31:      end if
865 32:    end for
866 33:    if  $flag == -1$  then
867 34:      Fail to allocate task  $\tau_i$ ;
868 35:    else
869 36:       $O[i] = flag$ ;
870 37:    end if
871 38:  end for
872 39: Calculate  $q_{ik}$  according to  $O[i]$ ;

```

6.3 Time Complexity

Finally, the time complexities of our frequency assignment and task mapping algorithms are discussed in terms of the number of variables [18]. The complexities of FAP and TMP are dominated by Algorithm 1 and Algorithm 3, respectively. Algorithm 2 is used to calculate the maximum energy required to execute a task, i.e., $MaxCompE$, under the given frequency assignment decision $S[i] = l$; and Algorithm 2 is used to calculate the maximum energy consumption at a node, i.e., $MaxNodeE$, under the given task allocation decision $O[i] = k$. In Algorithm 1 and Algorithm 3, the

Algorithm 4: Calculate *MaxNodeE*

```

883 1: MaxNodeE = 0;
884 2: for  $\forall k \in \mathcal{M}$  do
885 3:   for  $\forall i \in \mathcal{N}$  ( $O[i] \neq -1$ ) do
886 4:     Calculate  $E_k^t$  and  $E_k^d$  based on  $O[i] = k$ ;
887 5:   end for
888 6:   if  $(E_k^t + E_k^d)/E_k^{init} > \text{MaxNodeE}$  then
889 7:      $\text{MaxNodeE} = (E_k^t + E_k^d)/E_k^{init}$ ;
890 8:   else
891 9:      $\text{MaxNodeE} = \text{MaxNodeE}$ ;
892 10:  end if
893 11: end for

```

variables regarding the assignment of frequency and the allocation of the task are $\mathbf{c} = [c_{il}]_{N \times L}$ and $\mathbf{q} = [q_{ik}]_{N \times M}$. Therefore, the complexities of FAP and TMP are $O(NL)$ and $O(NM)$, respectively.

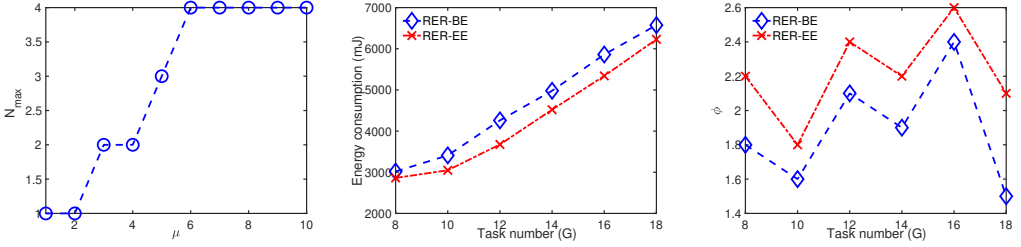
7 SIMULATION RESULTS

To evaluate the performance of the proposed approaches, we consider a networked system with 20 nodes, where 10 nodes equipped with sensors and 5 nodes connected to actuators (i.e., $M = 20$, $M_s = 10$, and $M_a = 5$). The modeling of the energy consumed by processors and wireless communication is based on [8, 41], and the characteristic of tasks is given by [42, 43], which is based on the scenario of enhancing federated learning in Fog-aided IoT.

The energy supply of node θ_k is set to $E_k^r = \eta E_k^h$, where $\eta \in [0, 1]$ is an energy efficiency factor and E_k^h is the minimum energy required to execute tasks $\{\tau_1, \dots, \tau_N\}$. In particular, $E_k^h = E_{k,\min}^t + E_{k,\min}^d$, where $E_{k,\min}^t = N \min_{\forall m,n,k} \{r_{mnk}\}$ is the minimum energy required to transmit all the task data, while $E_{k,\min}^d = \sum_{i \in \mathcal{N}} (\min_{\forall l} \frac{e_i}{f_l} P_l^c)$ is the minimum energy required to execute all the tasks. Since the nodes equipped with the sensors and the actuators consume more energy for executing sensing and control tasks, we set $E_k^r = 2\eta E_k^h$ for the sensor nodes while $E_k^r = 3\eta E_k^h$ for the actuator nodes.

The scheduling horizon (i.e., task deadline) is set to $H = \max_{\forall j} \{d_j\}$, where $d_j = \max_{\forall i, o_{ij}=1} \{d_i\} + \hat{t}_j^r + \hat{d}_j$. \hat{t}_j^r and \hat{d}_j are the temporary data receiving time and the relative deadline of task τ_j (the bound of task execution time), respectively. \hat{t}_j^r is assumed within the range $[N \min_{\forall m,n} \{t_{mn}\}, N \max_{\forall m,n} \{t_{mn}\}]$, where $N \min_{\forall m,n} \{t_{mn}\}$ and $N \max_{\forall m,n} \{t_{mn}\}$ are the minimum and the maximum time required to transmit the data of N tasks between two nodes, respectively. Similarly, \hat{d}_j is assumed within the range $[\min_{\forall l} \{\frac{e_l}{f_l}\}, \max_{\forall l} \{\frac{e_l}{f_l}\}]$, where $\min_{\forall l} \{\frac{e_l}{f_l}\}$ and $\max_{\forall l} \{\frac{e_l}{f_l}\}$ are the minimum and the maximum time required to execute a task with e_l cycles, respectively.

First, we show the influence of system parameters on the problem solution (e.g., task allocation and the value of the objective function). Then, we compare the system performance (i.e., the energy consumption of the nodes, the reliability of the system, and the schedulability of the problem) of the proposed task mapping scheme (Reliability-Energy-Realtime – RER) (i.e., PP) and other state-of-the-art mapping schemes (i.e., Energy-Efficiency (EE) [35], Task Duplication (TD) [15], and Task Triplication (TT) [19]). Finally, we evaluate the algorithm performance (i.e., the solution quality, the computation time, and the problem feasibility) of the proposed ORTM and HRTM algorithms with the Branch and Bound (B&B) [5], which can find the optimal solution for MILP problems, and the heuristics based on As-Soon-As-Possible (ASAP) [21]. The ASAP method estimates the as-soon-as-possible start time and uses it to limit the range of the task start time.



(a) The influence of μ on task allocation decision q_{ik} . (b) Total node energy consumption with RER-BE and RER-ME. (c) Node energy consumption gap with RER-BE and RER-ME.

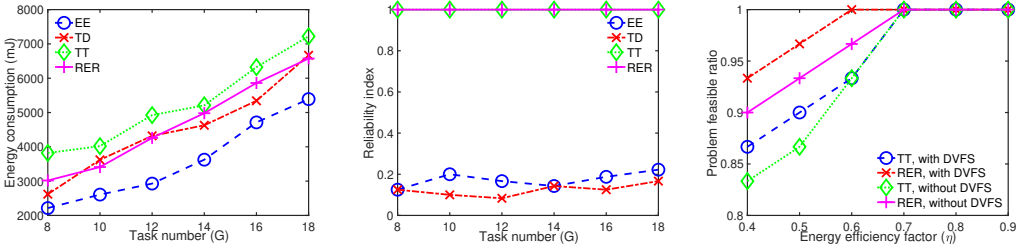
Fig. 3. System performance comparison under different parameters.

The simulations are performed on a laptop with a quad-core 2.5 GHz Intel I7 processor and 16 GB RAM, and the algorithms are implemented in Matlab 2020b. Note that different processor platforms and task graphs will lead to different parameters for the PP. However, the problem structures under different parameters are the same; thus, the proposed methods are still applicable. In addition, different algorithms can be compared under the given system parameters.

Fig. 3(a) explores the influence of energy ratio between communication and computation on task allocation decision q_{ik} . We set $G = 12$, $\eta = 0.8$, and change the value of μ from 1 to 10 with a step of 1. Let $\mu = e_{\max}^{\text{comm}} / e_{\max}^{\text{comp}}$ and $N_{\max} = \max_{\forall k} \{N_k\}$, where $e_{\max}^{\text{comm}} = \max_{\forall \beta, \gamma, k} \{e_{\beta\gamma k}\}$ is the maximum energy required for a node to relay task data, and $e_{\max}^{\text{comp}} = \max_{\forall i, l} \{\frac{e_i}{f_i} P_l\}$ is the maximum energy required for a node to execute a task. Therefore, the larger the value of μ is, the more energy is required to transmit task data, compared with the energy consumed for task execution. Since N_k is the number of tasks allocated to node θ_k , $N_{\max} = \max_{\forall k} \{N_k\}$ represents the maximum number of tasks assigned to a node. Fig. 3(a) shows that N_{\max} increases with μ . This is because, with larger communication energy compared to the computation energy, the trend is allocating dependent tasks to the same node to reduce the communication energy.

Fig. 3(b) and Fig. 3(c) compare the energy consumption of the nodes with two task mapping aims: 1) the PP with an objective function to Balance node Energy consumption (RER-BE), i.e., $\min[\max_{\forall k} \{(E_k^t + E_k^d) / E_k^{\text{ini}}\}]$, and 2) the PP with an objective function to Minimize total node Energy consumption (RER-ME), i.e., $\min[\sum_{k \in \mathcal{M}} (E_k^t + E_k^d)]$. To evaluate the performances of RER-BE and RER-ME, we introduce an index $\phi = (\max_{\forall k} \{E_k^t + E_k^d\}) / (\min_{\forall k} \{E_k^t + E_k^d\})$, where $E_k^t + E_k^d \neq 0$. Since $E_k^t + E_k^d$ is the computation and the communication energy of node θ_k , ϕ is the energy consumption gap between the nodes. The smaller the value of ϕ is, the better balance of node energy consumption can be achieved. We set $\eta = 0.8$ and change the value of G (the number of original tasks) from 8 to 18 with a step of 2. From Fig. 3(b), we observe that the total node energy consumption of RER-ME is lower than RER-BE. This is because by using RER-ME, the dependent tasks tend to be allocated to the same nodes, so as to reduce the communication energy of the nodes. On the other hand, Fig. 3(c) shows that the value of ϕ with RER-BE is lower than RER-ME. Hence, by using RER-BE, we can avoid some nodes depleting energy early, i.e., enhancing the lifetime of the networked system.

Fig. 4(a) compares the energy consumption of the nodes with EE, TD, TT, and the proposed RER scheme. The aim of the EE scheme is to minimize the energy consumption of the nodes under real-time and energy constraints. With the TD scheme, each task is duplicated, while with the TT scheme each task is triplicated. We set $\eta = 0.8$ and change the value of G from 8 to 18 with a step of 2. From Fig. 4(a), we observe that for all mapping schemes, with the task number G increasing, the energy consumption of the nodes increases as well, since more energy is required to execute



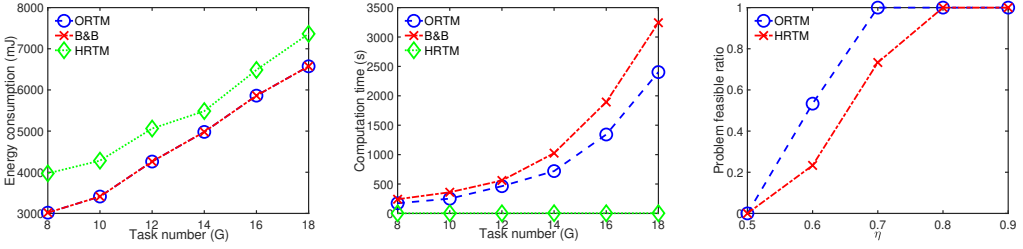
(a) Energy consumption of different task mapping schemes with N varying. (b) Reliability index of different task mapping schemes with N varying. (c) Schedulability of different task mapping schemes with η varying.

Fig. 4. System performance comparison with different task mapping schemes.

the tasks and exchange the task data between the nodes. In addition, the simulation results show that under the given task number G , the EE scheme consumes the least energy, while the TT scheme consumes more energy than the other schemes. This is because EE aims to minimize the energy consumption of the nodes, and the tasks are not duplicated/triplicated. However, each task is triplicated in TT, which leads to more tasks being required to execute. Fig. 4(a) also shows that although the approaches achieve a similar reliability level, RER consumes less energy consumption than TT (28.62% on average), since only parts of tasks are triplicated in RER.

Fig. 4(b) compares the probability of original tasks against the False Data Injection (FDI) under EE, TD, TT, and RER schemes. We introduce a reliability index $\sum_{i=1}^G w_{\alpha_i}/G$ for each task mapping scheme. With the EE scheme, since the tasks are not replicated, we have $N = G$ (G and N are the numbers of original tasks and total tasks, respectively), while with TD, TT, and RER schemes, we get $N = 2G$, $N = 3G$ and $N = 3G$, respectively. On the other hand, the value of parameter w_{α_i} is determined as follows: 1) If the original task τ_{α_i} is an entry task since τ_{α_i} will not receive data from other tasks, we set $w_{\alpha_i} = 1$; 2) If the original task τ_{α_i} is a non-entry task, and this task receives three copies (i.e., the original, duplicated and triplicated tasks) from each previous dependent task or just one copy (i.e., the original task) from each previous dependent task, which is assigned to the same node as task τ_{α_i} , we set $w_{\alpha_i} = 1$, else, we set $w_{\alpha_i} = 0$. From Fig. 4(b), we observe that under the same value of G , the reliability indexes of TT and RER are higher than EE and TD, since compared with EE and TD, the tasks are triplicated in TT and RER. In addition, because the tasks are not triplicated in EE and TD, the reliability index of TD is not always higher than EE. Although the reliability indexes of TT and RER are the same, with TT the nodes consume more energy, as more tasks are triplicated.

Fig. 4(c) evaluates the schedulability of TT and RER schemes since both of them perform task triplication. To compare their schedulabilities, the problem feasible ratio is introduced as a metric. For TT and RER schemes, we construct and solve the corresponding task mapping problem 30 times ($n_a = 30$), each time with fixed parameters G and η , and tuned parameters o_{ij} and e_i . Let n_t and n_r denote the times that the task mapping problems in TT and RER are feasible, respectively. Hence, the problem feasible ratios for TT and RER schemes can be defined as n_r/n_a and n_r/n_a , respectively. We set $G = 14$ and change the value of η from 0.4 to 0.9 with a step of 0.1. From Fig. 4(c), we observe that with η increasing, the problem feasible ratios of TT and RER both increase. In addition, the scheme with DVFS has a higher problem feasible ratio than the scheme without DVFS. This is because by introducing the frequency-to-task assignment variable c_{il} into the task mapping problem, the time and the energy regarding task execution can be further optimized. Hence, the real-time and energy constraints can be easier satisfied, especially when the energy factor η is small. Fig. 4(c) also shows that under the given G and η , RER has a higher problem feasible ratio



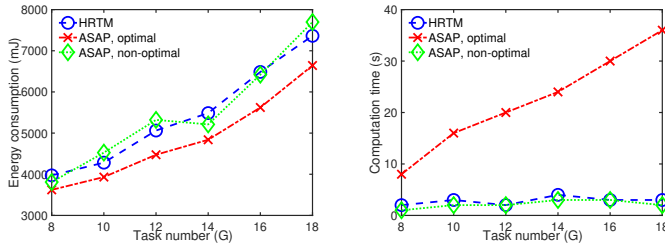
(a) Energy consumption of ORTM, HRTM and B&B with G varying. (b) Computation time of ORTM, HRTM and B&B with G varying. (c) Feasibility of task mapping problem with ORTM and HRTM.

Fig. 5. Algorithm performance comparison of ORTM, HRTM and B&B.

than TT. This is because not all original tasks are triplicated in RER, and thus, the nodes in RER consume less energy for task execution and data exchange than the nodes in TT. From Fig. 4, we can observe that compared with EE, TD, and TT schemes, the proposed RER scheme makes better use of system resources to enhance system reliability.

The solution quality and the computation time of using B&B and the proposed ORTM and HRTM algorithms to solve the PP under different task numbers G are compared in Fig. 5(a) and Fig. 5(b). The results show that the solutions found by ORTM and B&B are the same, i.e., the ORTM algorithm is also optimal. In addition, the ORTM algorithm achieves a lower objective function value than that of the HRTM algorithm (26.35% on average). With task number G increasing, more variables and constraints are added to the PP. Therefore, the computation time required by B&B and ORTM both increases. However, the computation time of ORTM is lower than that of B&B (24.84% on average). This is because the computational complexity of an optimization problem is highly related to the number of variables and constraints. Solving the smaller problems with fewer variables and constraints iteratively is more efficient than solving a single large problem [37]. In addition, compared with the ORTM, the HRTM has a negligible computation time, since this method only needs to solve two polynomial-time problems FAP and TMP in sequence. The schedulabilities of ORTM and HRTM algorithms to solve task mapping problem with the RER scheme are compared in Fig. 5(c). We use problem feasible ratio as a metric, and we set $n_a = 30$, $G = 14$, and $\eta \in [0.3, 0.8]$. Fig. 5(c) shows that the problem feasible ratios of ORTM and HRTM increase with η . Since the bounds of the constraints can be relaxed by η , the feasible region of the task mapping problem is enlarged. However, the problem feasibility ratio of ORTM is always higher than HRTM. This is because ORTM optimizes multiple variables at the same time, while HRTM optimizes these variables step by step.

Fig. 6(a) and Fig. 6(b) compare the solution quality and the computation time of HRTM and ASAP-based heuristic used to solve the PP. To solve this problem, the difficulty lies in the way of dealing with the continuous variable t_i^s (task start time). This is because when the value of t_i^s is fixed, the PP will reduce to an integer programming problem that contains only the binary variables q_{ik} , c_{il} , u_{ij} and h'_i . This problem is easier to solve than the MILP problem with continuous and binary variables. To perform the ASAP-based heuristic, in the first step, we assume that the start time of the entry task is 0. Then, for the adjacent dependent tasks, e.g., τ_i and τ_j ($o_{ij} = 1$), we add a temporary communication time θ_{ij} among them. The value of θ_{ij} is set to $(\max_{m,n} \{t_{mn}\} + \min_{m,n} \{t_{mn}\})/2$, so as to model the communication time when tasks τ_i and τ_j are assigned to the different nodes. Therefore, the start time of task τ_j can be replaced by $t_j^s = t_i^e + \theta_{ij}$. For example, as the task graph illustrated in Fig. 2, we have $t_1^s = 0$, $t_2^s = t_1^e + \theta_{12} = \sum_{l \in \mathcal{L}} c_{1l} \frac{h'_1 e_1}{f_l} + \theta_{12}$, $t_3^s = t_1^e + \theta_{13}$, $t_4^s = t_1^e + \theta_{14}$ and



(a) Energy consumption of HRTM and (b) Computation time of HRTM and ASAP with G varying.

Fig. 6. Algorithm performance comparison of HRTM and ASAP.

$t_5^s = t_3^e + \theta_{35} = t_3^s + \sum_{l \in \mathcal{L}} c_{3l} \frac{h_3^s e_3}{f_l} + \theta_{35}$. By replacing continuous variable t_i^s with binary variables c_{il} and h'_i , we next solve an integer programming problem with binary variables q_{ik} , c_{il} , u_{ij} and h'_i . Finally, when the solution is found, the temporary communication time θ_{ij} is updated by q_{ik} , and the task start time t_i^s is updated by h'_i and θ_{ij} .

From the above statement, we can see that the computation time of the ASAP-based heuristic is mainly dominated by solving the integer programming problem. To solve this problem, we can use either the optimal method (e.g., B&B) or the heuristic method (e.g., Feasibility Pump (FP) [14]). Fig. 6(a) and Fig. 6(b) show that when using the optimal method to solve the integer programming problem in ASAP, the computation time of HRTM is lower than ASAP. However, ASAP achieve a higher solution quality than HRTM. This is because the binary variables q_{ik} , c_{il} , u_{ij} , and h'_i are solved simultaneously in ASAP, while they are solved sequentially in HRTM, according to two subproblems FAP and TMP. The variable c_{il} is first determined in FAP, and then, the variables q_{ik} , u_{ij} and h'_i are determined in TMP. In addition, the binary variables q_{ik} , c_{il} and h'_i are coupled with each other nonlinearly in ASAP. To deal with the nonlinear items, more auxiliary variables and additional constraints are involved in ASAP. Hence, compared with HRTM, ASAP has a longer computation time. On the other hand, when using the heuristic approach (e.g., FP) to solve the integer programming problem in ASAP, the solution quality and the computation time both decrease, since the FP only provides a feasible solution. In that case, the gap between the computation time (the solution quality) of HRTM and ASAP is small, compared with using the optimal method to solve the integer programming problem in ASAP.

8 CONCLUSION

In this paper, we study the task mapping problem on the wireless nodes of networked systems. To enhance the reliability of data transmission among the nodes, as well as to satisfy the real-time and energy constraints, we introduce the task triplication and the DVFS scheme into the task mapping problem. The aim is to decide task-to-node allocation, frequency-to-task assignment, task triplication, and task scheduling so that the energy consumption of the nodes is balanced. The problem is first formulated as MINLP, then transformed into an MILP by adding auxiliary variables and additional constraints. Based on the structure of the problem, we propose an ORTM algorithm to find the optimal solution. In addition, to deal with large network sizes, we also design an HRTM algorithm with reduced computation time. The simulation results show that the proposed reliability-energy-time aware task mapping scheme outperforms other schemes regarding transmission reliability and energy efficiency. In addition, the solution found by ORTM is guaranteed to converge to the optimal solution, while the HRTM is able to find a proper solution within a negligible computation time compared with ORTM.

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