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OPEN Role of ambient temperature in modulation of behavior of vanadium dioxide volatile memristors and oscillators for neuromorphic applications

Stefania Carapezzi¹ , Corentin Delacour¹, Andrew Plews³, Ahmed Nejim³, Siegfried Karg⁴ & Aida Todri-Sanial^{1,2}

Volatile memristors are versatile devices whose operating mechanism is based on an abrupt and volatile change of resistivity. This switching between high and low resistance states is at the base of cutting edge technological implementations such as neural/synaptic devices or random number generators. A detailed understanding of this operating mechanisms is essential prerequisite to exploit the full potentiality of volatile memristors. In this respect, multi-physics device simulations provide a powerful tool to single out material properties and device features that are the keys to achieve desired behaviors. In this paper, we perform 3D electrothermal simulations of volatile memristors based on vanadium dioxide (VO₂) to accurately investigate the interplay among Joule effect, heat dissipation and the external temperature T_0 over their resistive switching mechanism. In particular, we extract from our simulations a simplified model for the effect of T_0 over the negative differential resistance (NDR) region of such devices. The NDR of VO₂ devices is pivotal for building VO₂ oscillators, which have been recently shown to be essential elements of oscillatory neural networks (ONNs). ONNs are innovative neuromorphic circuits that harness oscillators' phases to compute. Our simulations quantify the impact of T_0 over figures of merit of VO₂ oscillator, such as frequency, voltage amplitude and average power per cycle. Our findings shed light over the interlinked thermal and electrical behavior of VO₂ volatile memristors and oscillators, and provide a roadmap for the development of ONN technology.

The Internet of Things (IoTs)—one of the leading technologies for the twenty first century—presents an unprecedented opportunity to provide ubiquitous computing, sensing and learning capabilities at the edge. Many smart applications in key fields such as healthcare, transport, autonomous cars, or smart cities, to name a few, are fast adopting IoTs with embedded artificial intelligence (AI) on them to perform a diverse set of applications. Presently, most of the deployed AI on edge devices are pre-trained or send data to the cloud or data centers¹. But with the continuous increase in edge devices and the need for real-time data processing, such a procedure is becoming unsuitable due to data latency, privacy, bandwidth saturation, and power consumption². Alternatively, an efficient way is to train AI algorithms on IoT devices by handling data locally where they are amassed. However, executing AI algorithms on current computing architecture based on von Neumann one suffers from large power consumption, mainly due to energy loss of data transfer between memory and processor. This clashes with the limited power and computing resources available on the edge. Therefore, novel concepts are required to go beyond von Neumann computing era to enable AI learning and inference at the edge.

Both academia and industry are exploring beyond von Neumann architectures, aiming to compute by merging memory and processing of information. While many alternatives are being scrutinized, analog computing based on the principle of “let physics compute” presents a quite interesting solution³. At its core, it transforms

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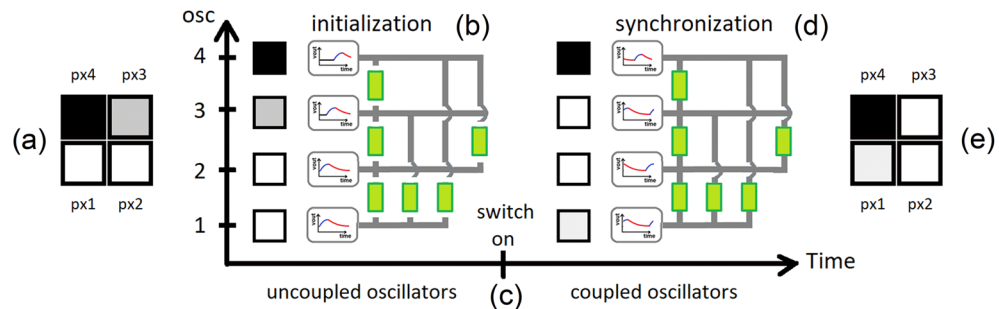


Figure 1. Example of possible ONN application for image recognition tasks. (a) Input image. (b) Initialization of ONN circuit. Each pixel of input image is mapped as oscillator's phase $\Delta\Phi_{in,j}$ by delaying of $\Delta t_{in,j}$ the biasing $V_{DD,j}$ compared to the reference (bottom-most) oscillator. The computation begins at coupling of all the oscillators (c), where the simplest coupling elements are resistances $R_{C,ij}$ (green blocks). (d) The oscillators synchronize in a stable in-phase ($\Delta\Phi_{out,j} = 0^\circ$) or out-of-phase ($\Delta\Phi_{out,j} \neq 0^\circ$) state. The phase of each oscillator is mapped back as pixel (gray scale) color to yield the (e) output image.

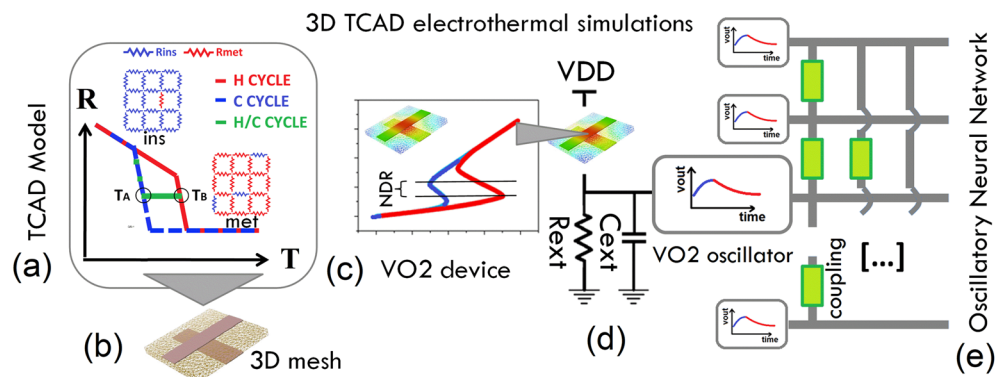


Figure 2. (a) Temperature-triggered resistive switching of VO_2 volatile memristor. (b) 3D mesh of VO_2 CB device. (c) TCAD simulated $I-V_D$ characteristics. The associated NDR region has been highlighted. (d) Scheme of VO_2 oscillator circuit. Self-oscillatory electrical behavior is associated to a suitable choice of the load-line. (e) ONN.

the dynamics of complex systems such as coupled oscillatory neural networks (ONNs)^{2,3} into an energy-efficient computing tool. ONNs take inspiration from the human brain, which is able to perform sophisticated tasks with a reduced energy budget. Specifically, ONNs aim to emulate biological neural networks, which are made of elemental units outputting periodic signals (the neurons) and interconnected into systems (the networks)^{4–8}. ONN circuits mimic neural nets as ensembles of coupled oscillators (Fig. 1), where the information is encoded in the phase difference among the oscillators (phase-based computing).

To compute with ONNs, oscillators must be initialized with different delay times compared to a reference oscillator (Fig. 1b). This establishes an initial phase difference between each oscillator and the reference one. Then, at a given time, all oscillators are connected to each other through synaptic coupling elements (Fig. 1c), where the simplest coupling elements are resistors or capacitors. These couplings mediate the ONN interaction and allow to achieve a collective phase synchronization, if any, which is the result of ONN computing (Fig. 1d)⁹.

An example of application on ONN is pattern recognition, given that the phase state of each oscillator can be mapped to a white ($\Delta\Phi_j = 0^\circ$), a black ($\Delta\Phi_j = 180^\circ$) or a gray (all other values of $\Delta\Phi_j$) pixel (Fig. 1a,e). Overall, ONNs have attracted immense interest because they have a simple structure yet fast computation and energy efficient computing paradigm^{10–13}.

Oscillators are the basic building blocks of ONNs. In this paper, we report on the realization of beyond CMOS oscillators based on vanadium dioxide (VO_2) volatile memristors. Volatile memristors are currently widely investigated for applications as neural/synaptic devices, or as random generators for hardware security^{14,15}. Their operation mode relies on a mechanism of volatile resistive switching. VO_2 is a Transition Metal Oxide that changes from a high-resistive monoclinic (M_1) crystal structure to a low-resistive tetragonal rutile-like (R) structure. Such abrupt resistive switching is related to temperature overcoming a threshold (340 K) close to room temperature. This process takes the name of insulator-to-metal transition (IMT), it is reversible (metal-to-insulator transition, MIT) and often hysteretic (Fig. 2a). The overall variation of VO_2 resistivity across IMT/MIT can be up to several orders of magnitude.

The resistive switching of VO₂ has been experimentally observed also in channels of two-terminal VO₂ devices due to current injection or voltage biasing. This gives rise to the so called volatile Mott memristors^{14–18} because, upon removal of electrical stimulus, these devices reset back spontaneously to the high resistive state (HRS) from the low resistive state (LRS). Experimental evidence^{19–22} points towards self-heating as driving this transition by increasing the device temperature.

A negative differential resistance (NDR) region in the current-voltage (*I*–*V*) characteristics (Fig. 2c) of VO₂ volatile memristors is the hallmark of such resistive switching. This provides the possibility to build VO₂ oscillators by simply inserting a VO₂ volatile memristor into an RC circuit (Fig. 2d)^{17,22,23}, without need for inductive components. The oscillatory regime is achieved provided that the circuit load-line $I_L = (V_{DD} - V)/R_{ext}$ crosses the NDR region of current *I* vs. voltage across the VO₂ (*V*_D) curve, where *V*_{DD} is the applied bias and *R*_{ext} is the external resistance. Thus, the periodic oscillation is due to cyclic charging and discharging of external capacitor *C*_{ext} related to VO₂ device being in LRS and HRS (circuit topology of Fig. 2d).

Due to the role played by temperature to regulate the LRS/HRS states of VO₂ volatile memristors, it is expected that external temperature *T*₀, as provided by an external heater or the ambient, modulates the behavior of both VO₂ device and oscillator. Consequently, the external temperature will also impact ONN dynamics, affecting ONNs as analog computing architecture. For instance, the process of finding the ground-state of Ising machines can be implemented by gradually lowering the effective temperature (annealing)^{24,25}, where such ground-state corresponds to the solution of optimization problems. Recently, an Ising Hamiltonian solver based on VO₂ oscillators has been demonstrated²⁶.

In this work, we assess the role of the external temperature over the electrical behavior of VO₂ volatile memristors and oscillators. To this aim, we use technology Computer-Aided Design (TCAD) technique to run multi-physics simulations of VO₂ devices and oscillators. Physics-based multi-scale simulations are essential tools for investigating beyond CMOS devices, where the control of materials growth and micro/nanofabrication procedures is still to be fully mastered. Given the variability of nominally identical devices, the abstraction of trends in device behavior through experiments is quite challenging. On the contrary, physics-based simulations can precisely consider material variability and/or device geometry, which captures correlations and provides insights into device behavior. We perform 3D TCAD electrothermal simulations of VO₂ volatile memristors to accurately take into account Joule effect, heat dissipation and *T*₀. We show the dependence of *I* vs. *V*_D curves from *T*₀, and in particular, of the points of occurrence of resistive switching and its resetting. We are able to provide a simplified model for the evolution of such points with *T*₀. This yields the effect of modulation with *T*₀ of associated NDR region. We assess the impact of such modulation over VO₂ oscillator, considering that the load-line of oscillatory circuit does not depend on *T*₀. It is important to observe that the sensitivity of VO₂ oscillator to *T*₀ has a number of consequences. It can bring to cross-talk effects 1) self-induced, due to the local thermal build-up of VO₂ oscillator, or 2) among nearby VO₂ oscillators during ONN operativity. However, it also *de facto* embeds into VO₂ neuron-mimicking devices a (thermal) sensory function. Thus, our work provides essential TCAD tools on one hand to ascertain the thermal cross-talk effects in VO₂ oscillators, and on the other to engineer the response characteristics of VO₂ sensory neuron devices. Finally, we determine how figures of merit of VO₂ oscillator, such as frequency, voltage amplitude and average power per cycle, vary according to *T*₀. Our findings shed light on the interlinked thermal and electrical behavior of VO₂ oscillators that are helpful for providing guidelines for the implementation of ONN technology.

TCAD electrothermal simulations of VO₂ crossbar device

We perform 3D TCAD electrothermal simulations of VO₂ crossbar (CB) devices fabricated on a silicon platform. The experimental VO₂ device is realized on a silicon substrate covered by 1 μm-thick SiO₂ layer²⁷. The bottom Pt electrode is deposited after etching a trench in SiO₂ layer. Then, the 80-nm thick VO₂ layer is grown and subsequently shaped into a square of 5 μm-side. Finally, the top electrode is laid down to complete the CB geometry (Fig. 2b). Both electrodes have a width of 250 nm.

Modeling of the VO₂ resistive switching. We use Silvaco Victory Mesh²⁸ for 3D mesh rendering of the actual geometry of VO₂ device (Fig. 2b). This allows to emulate accurately 3D effects of thermal dissipation and the real geometry of current flow. Experimental VO₂ is a polycrystalline, granular film due to the growth on SiO₂ substrate, with average size of grains of ≈ 50 nm²⁷. However, in simulations we treat VO₂ as homogeneous material, because the active device region (about where the two contacts overlap) has a cross-section area of 250 nm by 250 nm, which amounts to a few grains. Thus, to consider the device “channel” as uniform is a good approximation that helps to reduce the computational time and complexity. We perform 3D TCAD electrothermal simulations of VO₂ volatile memristor by Silvaco Victory Device²⁹. We model the VO₂ as a conductor material whose resistivity vs. temperature is described by a user-defined version of the Phase-Change Material (PCM) model^{30,31} (Fig. 2a). In static simulations, the resistivity $\hat{\rho}$ is linear with the temperature *T*, with different slopes *m*_{LT}, *m*_{HT} depending on VO₂ being insulating (*T* < *T*_{LT}) or metallic (*T* > *T*_{HT}), respectively. $\hat{\rho}(T)$ is also linear across the IMT/MIT region (*T*_{LT} < *T* < *T*_{HT}). Because experimental curves of resistance vs. temperature show hysteresis (Fig. 2a), two different sets of parameters are used for the heating/cooling cycle. In time-dependent simulations, there will be a time lag for a sufficiently fast temperature change for the phase change to occur. Therefore, the time-dependent resistivity is modeled as:

$$\rho(t + \Delta t) = \rho(t) + (\hat{\rho}(T) - \rho(t)) \times \left[1 - \exp\left(-\frac{\Delta t}{\tau}\right) \right] \quad (1)$$

where $\rho(t)$ is the resistivity at previous time step *t*, $\rho(t + \Delta t)$ is the resistivity at new time step and τ is a lifetime. The model also takes into account incomplete heating/cooling cycles, when they are reversed before achieving

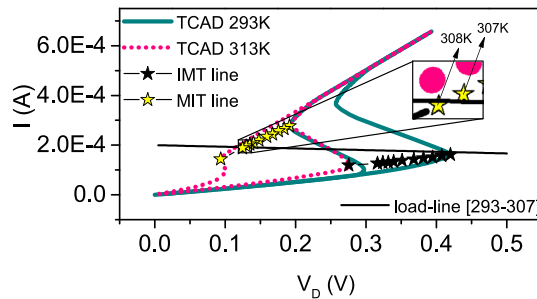


Figure 3. Simulated I - V_D at external temperature of $T_0 = 293$ K (solid dark cyan line) and $T_0 = 313$ K (pink dotted line). The IMT/MIT points for T_0 ranging from 293 K (the rightmost ones) to 313 K (the leftmost ones) are shown as black (IMT) and yellow (MIT) starred lines, respectively. The black line is the load line valid for the temperature interval [293–307] K. The inset shows the MIT points for $T_0 = 307$ K (on the right) and $T_0 = 308$ K (on the left). It is evident that the MIT point for $T_0 = 307$ K is above the load-line, while the MIT point for $T_0 = 308$ K is below it.

the full transition to the metal (or insulator) state. For example, if a cooling cycle starts at temperature T_B such that $T_{LT,H} < T_B < T_{HT,H}$, before the heating cycle is completed, then the resistivity remains constant until the temperature T_A is reached such that $\rho(T_A)$ belongs to the cooling branch (Fig. 2a). This reproduces recent experimental findings³² on the behavior of VO_2 resistivity vs. temperature.

We calibrate the static PCM resistivity $\hat{\rho}_{\text{PCM}}$ against experimental data of resistance R_{exp} vs temperature T^{31} . Given the CB geometry, the cross-section of the current flow is not limited just to the region of the overlap between the top and bottom electrodes³¹, thus we cannot derive directly $\hat{\rho}$ from $R_{\text{exp}} \times A_C/L$, with L the VO_2 thickness and A_C the cross-section area of overlapping contacts. However, most of the current is indeed flowing therein³¹, thus, we simply assume that

$$\hat{\rho}_{\text{PCM}} = R_{\text{exp}} \times A_{\text{Ceff}}/L \quad (2)$$

for $A_{\text{Ceff}} = A_C \times a$, a being a parameter. Because VO_2 is modeled as a conductor, the conduction equation is solved as $J = E/\rho = -\nabla\phi/\rho$, where J is the current density, E is the electric field and ϕ is the electric potential. The Poisson equation and the heat flow equation

$$C \frac{\partial T_L}{\partial t} = \nabla(K \nabla T_L) + \frac{(\nabla\phi)^2}{\rho} \quad (3)$$

are solved coupled self-consistently using the Finite Element Method (FEM). In Eq. (3) T_L is the local lattice temperature, K is the thermal conductivity and C is the heat capacity per unit volume. The Joule effect is accounted through the heat generation rate $\partial Q/\partial t = \rho J^2 = (\nabla\phi)^2/\rho$, where Q is the heat contributed by Joule effect. The VO_2 temperature is obtained by TCAD simulations through solution of Eq. (3). We use experimental current I vs. voltage across the VO_2 (V_D) characteristics as a benchmark to validate our approach³¹. The experimental I - V_D data have been collected at the external temperature of $T_0 = 303$ K as measured at the oxide surface close to the CB device (Fig. S10). We emulate the experimental condition by setting a temperature of $T_0 = 303$ K at the bottom surface of the VO_2 layer that we consider to be the thermal contact of TCAD simulations. We use a value of $0.06 \text{ W}/(\text{cm K})$ ³³ for the thermal conductivity of VO_2 , and of $3 \text{ J}/(\text{cm}^3 \text{ K})$ ³⁴ for its heat capacity per unit volume.

We obtain a good agreement with the experimental data³¹ (Fig. S11) provided that 1) we set $a = 2.7$ in Eq. (2). Additionally, 2) we need to calibrate the interface thermal resistance of thermal contact set at the bottom of the device to achieve the same values of voltage V_{IMT} and current I_{IMT} at which the resistive switching occurs in simulated and experimental I - V_D curves. The interface thermal resistance of thermal contact accounts for heat dissipation through VO_2 / substrate interface, and it depends from material properties of VO_2 and SiO_2 , and also from interface roughness, for example. Such interface thermal resistance is set as thermal conductance of thermal contact, which together with T_0 represent the thermal boundary condition. For simplicity sake, we consider the VO_2 / substrate interface as the main source of heat dissipation given that it is reasonable to assume that the heat dissipation across VO_2 / air interface, or VO_2 / top contact interface, can be, in comparison, neglected.

It is important to highlight that we do not fit VO_2 material parameters to match I - V_D curves, but we use physical meaningful values. This demonstrates that, overall, we are able to reproduce by simulations the salient features of the electrical behavior of VO_2 CB devices because we capture the physical mechanisms operating inside the device.

Evolution of NDR region with external temperature

We perform 3D TCAD electrothermal simulations of VO_2 crossbar (CB) devices at different external temperatures T_0 . The simulated I - V_D curves for $T_0 = 293$ K (dark cyan solid line) and $T_0 = 313$ K (pink dotted line) are plotted in Fig. 3, showing the modulation effect of T_0 . In particular, the values of voltage and current at which the resistive switching (V_{IMT} , I_{IMT}) and its reset (V_{MIT} , I_{MIT}) occur decrease with T_0 . This can be easily interpreted: by increasing the background temperature, less and less heat has to be generated by Joule effect to achieve the

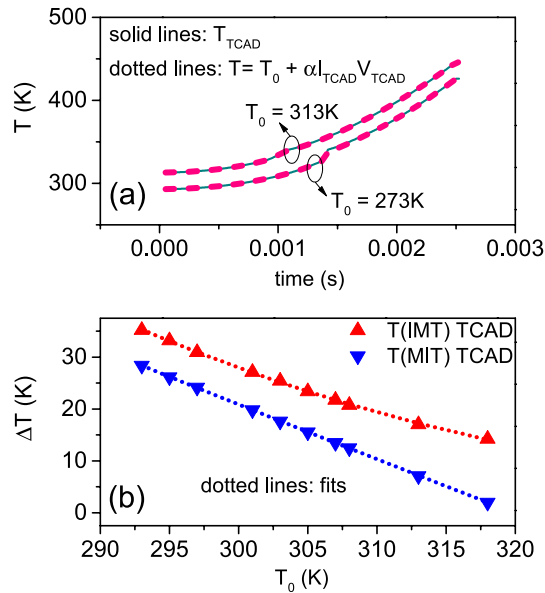


Figure 4. (a) Local temperatures probed in the geometrical center of the device as extracted by TCAD simulations (dark cyan solid lines) for T_0 equal to 293 and 313 K. We achieve a very good agreement with temperatures calculated after Eq. (4) for $\alpha = 5.15 \times 10^5$ K/W (pink dashed lines). (b) ΔT_{IMT} (red triangles) and ΔT_{MIT} (blue triangles) as determined by TCAD simulations for different T_0 . The dotted lines are the correspondent fits by 2nd order polynomials.

temperature for resistive switching inside the volatile memristor. We denote $(V_{\text{IMT}}, I_{\text{IMT}})$ as IMT point and $(V_{\text{MIT}}, I_{\text{MIT}})$ as MIT point. The black (IMT) and yellow (MIT) starred lines of Fig. 3 are the TCAD simulated IMT/MIT points for T_0 ranging from 293 K to 313 K. The IMT and MIT points define the edges of the NDR region, which then, varies with T_0 . The NDR region defines the operative regime of the oscillator circuit (Fig. 2d): it yields electrical oscillations only if the circuit load-line $I_L = (V_{\text{DD}} - V)/R_{\text{ext}}$ crosses it.

We are able to model the dependence of IMT and MIT points with T_0 as obtained by 3D TCAD electrothermal simulations. Thus, we can predict the NDR region at any given T_0 . In the following, we show the model for IMT points, the same applies to MIT points. We approximate the device as a homogeneous resistor, whose resistance is determined by $R = \hat{\rho}_{\text{PCM}}(T_{\text{ch}}) \times L/A_{\text{Ceff}}$. T_{ch} is not the local temperature inside the device but a “global” temperature such that R is the device resistance at the given time. We assume T_{ch} as the uniform temperature in the region of overlapping contacts if the current flux is confined therein. We correlate T_{ch} to the device power as:

$$T_{\text{ch}} = T_0 + \alpha \times I \times V \quad (4)$$

where the electric power is converted into heat energy that dissipates to the ambient obeying Newton’s law of cooling. This corresponds to describing the heating effects in VO_2 with the same theory used for thermistors³⁵. The validity of such approach has been experimentally demonstrated in the case of planar devices¹⁹. α is $(A \times \bar{h})^{-1}$, for A the heat transfer surface area, $\bar{h}$ the heat transfer coefficient averaged over A ³⁶, and it is related to how the VO_2 channel exchanges heat with the ambient, thus it depends on material properties and device geometry¹⁹. We consider that TCAD simulated temperature probed in the geometrical center of the device is a good approximation of T_{ch} , because most of the current flow is concentrated in the region of overlapping contacts³¹. Thus, we match the TCAD temperature to Eq. (4). We achieve a very good agreement by setting $\alpha = 5.15 \times 10^5$ K/W. Figure 4a shows the TCAD (solid lines) and the calculated temperatures (dashed lines) for T_0 equal to 293 and 313 K. Now, $T_{\text{IMT}} = T_0 + \alpha \times I_{\text{IMT}} \times V_{\text{IMT}} = T_0 + \Delta T_{\text{IMT}}$ is the temperature of resistive switching. It is noteworthy to observe that T_{IMT} depends on V_{IMT} and I_{IMT} . In turn, V_{IMT} depends from T_{IMT} , as well as I_{IMT} depends from T_{IMT} . This means that there is not a closed-form expression of the relationship between T_{IMT} and V_{IMT} , I_{IMT} . We use TCAD simulation to derive T_{IMT} at different T_0 . ΔT_{IMT} vs T_0 is plotted in Fig. 4b (red triangles). We got an excellent match by fitting these points with a 2nd-order polynomial (red dotted line). We infer $R(T_{\text{IMT}})$ from PCM model as $R(T_{\text{IMT}}) = \rho(T_{\text{IMT}}) \times L/A_{\text{Ceff}}$. Finally, $I_{\text{IMT}} = V_{\text{IMT}}/R(T_{\text{IMT}})$. However, from Eq. (4), $I_{\text{IMT}} = \Delta T_{\text{IMT}}/(\alpha \times V_{\text{IMT}})$. By combining the two above equations together, we determine

$$V_{\text{IMT}} = \sqrt{\frac{\Delta T_{\text{IMT}} \times R(T_{\text{IMT}})}{\alpha}} \quad (5)$$

and we get I_{IMT} from V_{IMT} and $R(T_{\text{IMT}})$. In Fig. 5a,b are plotted I_{IMT} and V_{IMT} against T_0 , respectively, as extracted from TCAD simulations (red symbols) and as determined by our model (dashed red lines). We apply the same procedure also to I_{MIT} and V_{MIT} vs. T_0 . The excellent agreement between simulated and calculated points in all the cases demonstrates that the behavior of IMT/MIT points with T_0 is correlated to the dependence of T_{IMT} , T_{MIT}

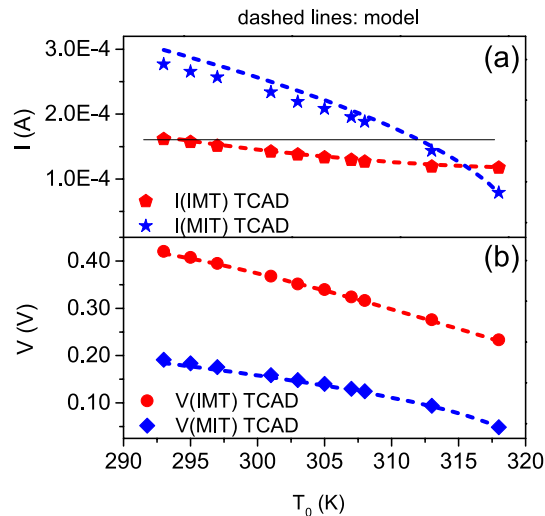


Figure 5. (a) I_{IMT} (red pentagons), I_{MIT} (blue stars) and (b) V_{IMT} (red circles), V_{MIT} (blue diamonds) as derived from TCAD simulations. The dashed lines are the currents and voltages as calculated from the proposed model. In (a), the black solid line is drawn parallel to T_0 axis. It intersects the I_{IMT} line at $T_0 = 293$ K, and the I_{MIT} line at about $T_0 = 312$ K.

from T_0 , expressed by Eq. (4). Thus, at least for the resistive switching and its resetting, the local in-homogeneity in resistivity as induced by the local temperature distribution can be ignored. The device can be reduced to a homogeneous resistor, whose resistance is determined by the temperature of the center of the device.

Behavior of VO₂ oscillator with external temperature

VO₂ oscillator (Fig. 2d) is expected to depend as well as on T_0 . For instance, figures of merit of the oscillator, such as the amplitude and period of the electrical signal at the output node, depend on V_{IMT} and V_{MIT} , and then from T_0 . This can be easily understood in the following way. In first approximation, the triggering thermal mechanism induced by Joule effect can be ignored, and VO₂ volatile memristor is just described as a two-state (LRS and HRS) variable resistor, activated by threshold voltages V_{IMT} and V_{MIT} , respectively. Either LRS/HRS, the oscillator circuit equation is expressed by Kirchhoff's law: $C_{\text{ext}} \frac{dV}{dt} = I_L - I^{30,37}$. At V_{IMT} , the VO₂ is switched on (LRS) and the external capacitor C_{ext} charges, with the voltage at the output node V_{out} varying from $V_L = V_{\text{DD}} - V_{\text{IMT}}$ to $V_H = V_{\text{DD}} - V_{\text{MIT}}$. At V_{MIT} , the VO₂ is switched off (HRS) and C_{ext} discharges, with V_{out} varying from V_H to V_L . The cycles of capacitor charging and discharging are repeated periodically and yield the voltage oscillation. Then, the amplitude of voltage oscillations is $V_H - V_L = V_{\text{IMT}} - V_{\text{MIT}}$. Moreover, the times for capacitor charging τ_{ch} and discharging τ_{dis} both depend on V_{IMT} and V_{MIT} , as well as the oscillation period $\tau = \tau_{\text{ch}} + \tau_{\text{dis}}$ ^{37,38}. We investigate by 3D TCAD electrothermal simulation the behavior of VO₂ oscillator with T_0 . It is worthwhile to remember that I) IMT/MIT points vary with T_0 , while II) the circuit parameters, that is the load-line $I_L = (V_{\text{DD}} - V)/R_{\text{ext}}$, do not depend from T_0 . This produces a number of consequences.

- There is a finite range of external temperatures for which a fixed load-line crosses the associated NDR regions. That is, there exists a finite range of T_0 within which the VO₂ oscillator can oscillate.

For instance, in Fig. 3 is plotted the load-line (black solid line) corresponding to circuit parameters $V_{\text{DD}} = 3$ V and $R_{\text{ext}} = 15$ k Ω . It 1) passes above all the IMT points for $T_0 > 293$ K, and 2) is above MIT point at $T_0 = 308$ K and below MIT point at $T_0 = 307$ K (inset of Fig. 3). This means the load-line crosses the NDR regions in the interval of external temperatures [293–307] K. Figure 6 shows the simulated V_{out} for the above load-line ($C_{\text{ext}} = 150$ nF) at $T_0 = 293$ K (dark cyan solid line), $T_0 = 303$ K (dark cyan dashed dotted line), $T_0 = 307$ K (dark cyan dotted line) and $T_0 = 308$ K (dark cyan dashed line). As expected, the oscillatory behavior is present for all the T_0 except 308 K. It also follows that there will be intervals of external temperatures for which it is impossible to find a common load-line such that it crosses all the correspondent NDRs. For instance, in Fig. 5a, the (black solid) line is drawn parallel to T_0 axis, which intercepts the I_{IMT} line at $T_0 = 293$ K and the I_{MIT} line at about $T_0 = 312$ K. This means that the NDR regions for $T_0 > 312$ K cannot overlap with NDR region at $T_0 = 293$ K. Thus, it does not exist a load-line which crosses the NDR regions from $T_0 = 293$ K to above 312 K.

- There are temperatures for which the NDR region does not exist.

For example, in Fig. 5a the I_{IMT} line crosses the I_{MIT} line at about $T_0 = 315$ K. This means that for $T_0 > 315$ K the NDR region does not exist because $I_{\text{MIT}} < I_{\text{IMT}}$.

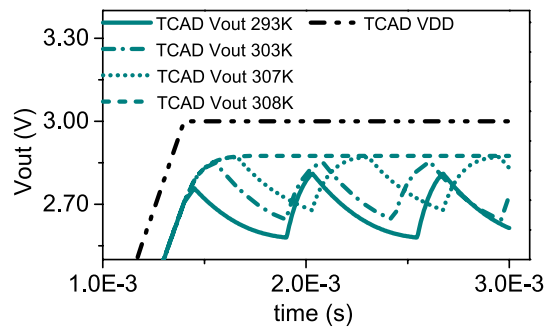


Figure 6. TCAD simulated voltage oscillations for $V_{DD} = 3$ V, $R_{ext} = 15$ k Ω and $C_{ext} = 1.5 \times 10^{-7}$ F, at external temperature of $T_0 = 293$ K (solid line), $T_0 = 303$ K (dash-dotted line), $T_0 = 307$ K (dotted line) and $T_0 = 308$ K (dashed line).

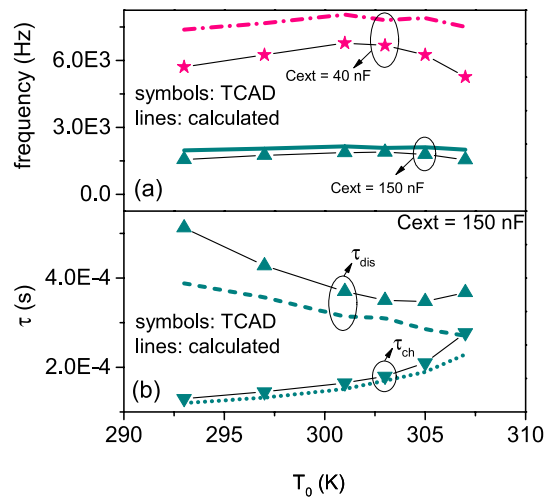


Figure 7. (a) Frequency of voltage oscillations vs the external temperature for $C_{ext} = 150$ nF (dark cyan triangles) and $C_{ext} = 40$ nF (pink stars). The curves obtained by the model of capacitor charging and discharging³⁷ (pink dashed and dark cyan solid lines) are also plotted. (b) TCAD simulated periods of charging τ_{ch} (downward triangles) and discharging τ_{dis} (upward triangles) for $C_{ext} = 150$ nF. The dotted and dashed lines are the curves for τ_{ch} , τ_{dis} by applying the circuit equations³⁷.

We perform TCAD electrothermal simulations of VO₂ oscillator at different external temperatures T_0 . The oscillator circuit is simulated by considering the external resistance R_{ext} and capacitance C_{ext} as lumped element boundaries of VO₂ memristor. Any intrinsic device capacitance is accurately accounted into TCAD simulation of device electrostatics. We extract from simulated V_{out} the frequency against T_0 in interval [293–307] K, for the load-line correspondent to $V_{DD} = 3$ V, $R_{ext} = 15$ k Ω . The results for $C_{ext} = 150$ nF (dark cyan lines) and $C_{ext} = 40$ nF (pink lines) are plotted in Fig. 7a. It can be appreciated the change of oscillator frequencies induced by variation of external temperature in the range [293–307] K. In particular, the frequency increases with T_0 , it achieves a maximum, and then it decreases again at higher T_0 . This behavior applies to all the probed values of the external capacitance. The decrease of frequency at $T_0 = 293$ K and $T_0 = 307$ K is easily interpreted with the fact that the load-line gets close to IMT point at $T_0 = 293$ K and to MIT point at $T_0 = 307$ K. Then, at these two temperatures, $I_L - I$ is very small. This, in turn, slows down the variation of voltage with time, as determined by Kirchhoff's law. This is also coherent with the fact that at $T_0 < 293$ K and $T_0 > 307$ K the load-line does not anymore cross the associated NDR regions. Thus, there is no more oscillatory behavior, which means zero frequency. We plot in Fig. 7b the periods of capacitor charging τ_{ch} (downward triangles) and discharging τ_{dis} (upward triangles) vs T_0 ([293–307] K). Their sum yields the oscillator period τ . Figure SI2 of Supporting Information (SI) shows the correspondent curves for $C_{ext} = 40$ nF. Overall, TCAD simulated τ_{ch} and τ_{dis} have opposite trends with T_0 (belonging to [293–307] K): of increasing for τ_{ch} and of decreasing for τ_{dis} . The percentage of variation between maximum and minimum values compared to the minimum one for TCAD simulated τ_{ch} varies between 113% ($C_{ext} = 150$ nF) and 87% ($C_{ext} = 40$ nF), while for TCAD simulated τ_{dis} it is comprised between 46% ($C_{ext} = 150$ nF) and 35% ($C_{ext} = 40$ nF). The percentage of variation between maximum and minimum values compared to the minimum one for TCAD simulated frequency is between 22% ($C_{ext} = 150$ nF) and 29% ($C_{ext} = 40$ nF). The

decreased variability of frequency with T_0 is explained with the partially compensating effects of the opposed trends of τ_{ch} and τ_{dis} .

We complete the survey of effects induced by T_0 over the VO₂ oscillator. We derive the variation of voltage amplitude (Fig. SI6a of SI) and average power per cycle (Fig. SI6b of SI) against the external temperature (range [293–307] K) for the fixed load-line $V_{DD} = 3$ V, $R_{ext} = 15$ k Ω . C_{ext} ranges from 150 nF to 40 nF. The voltage amplitude (Fig. SI6a) decreases with the external temperature, due to the trend of V_{IMT} and V_{MIT} with T_0 (Fig. 5b). As expected, there is no dependence from C_{ext} . Finally, we determine the average power per cycle $\langle P \rangle$. We find that it lies in the range of values from 5.1×10^{-4} W to 5.7×10^{-4} W within the temperature interval [293–307] K and for all the external capacitance values.

Conclusions

In this paper, we investigate the effect of ambient temperature over VO₂ volatile memristors and oscillators, using TCAD multi-physics simulations. We perform 3D TCAD electrothermal simulations of VO₂ volatile memristors to accurately take into account Joule effect, heat dissipation and T_0 . Since our simulations link in a physical way VO₂ material properties (for example: resistivity vs temperature relationship) and oscillator circuit parameters (load-line), we are able to extract a number of consequences. The most important is to predict and explain the variation of oscillator frequency with the external temperature T_0 . In particular, the variation of frequency is due to 1) the dynamics of variation of NDR region induced by T_0 , and, on the contrary, 2) the stability against T_0 of load-line. It is important to observe that this behavior has been recently experimentally demonstrated for VO₂ memristors in self-oscillatory regime³⁹. This modulation effect of T_0 over behavior of VO₂ oscillators has multiple repercussions. It can bring to cross-talk effects (1) self-induced, due to the local thermal build-up of VO₂ oscillator, or (2) among nearby VO₂ oscillators during ONN operativity. In this last case, the amount of cross-talk will depend on a number of factors, among them the distance among oscillators, and thus it may ultimately set a limit to integration of VO₂ oscillators. Finally, the sensitivity of VO₂ oscillator to T_0 also *de facto* embeds into VO₂ neuron-mimicking device a (thermal) sensory function. Thus, our work provides essential TCAD tools on one hand to ascertain the thermal cross-talk effects in VO₂ oscillators, and on the other to engineer the response characteristics of VO₂ sensory neuron devices.

Data availability

The datasets used and/or analyzed during the current study available from the corresponding author on reasonable request.

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Author contributions

S.C. conceived the idea and designed the simulations. S.C. performed all the TCAD simulations and analyzed the data. S.C., C.D. and A.T.S. critically interpreted the data. A.P. and A.N. implemented the customized version of the PCM model used to simulate the VO₂ material, and gave useful suggestions to implement TCAD simulations. S.K. supplied the experimental data used for the calibration of the TCAD model, and provided useful discussions to interpret them. All the authors discussed the results. S.C. drafted the manuscript and A.T.S. critically revised it. The manuscript has been read and approved by all authors.

Competing interests

The authors declare no competing interests.

Additional information

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