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Optimized Junction Termination Extension and Ring System for 11 kV 4H-SiC BJT

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Abstract: In this article, a high power 4H-SiC NPN BJT is demonstrated with a blocking voltage greater than 10 kV when its theoretical value is around 13 kV. The device design was extracted from a previous reported model and eleven fabrication process were performed. The maximum common-emitter current gain recorded is 20 at 178 A/cm².

Keywords: Bipolar Devices, 4H-SiC BJT, blocking voltage, I-V characteristics, TCAD modeling.

1. Introduction

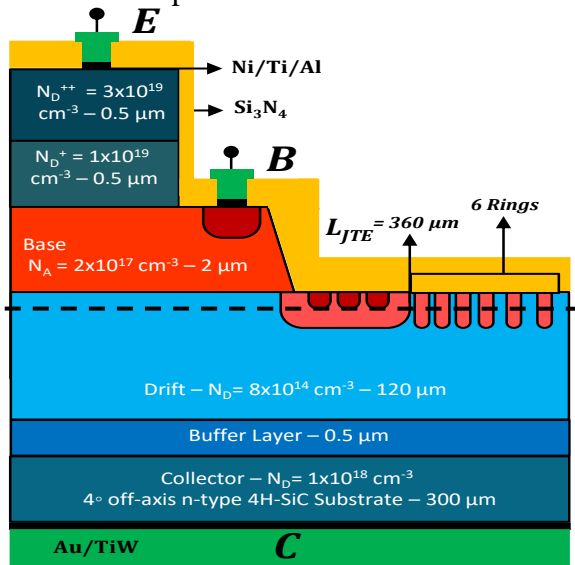
High voltage bipolar devices such as BJTs based on 4H-SiC are considered a very attractive candidate for high power applications. In particular, when compared to silicon thanks to a high breakdown electric field resulting in decreasing the ON resistance and probably avoiding a second breakdown avalanche effect [1]. In addition, a high saturation velocity enables fast switching and a wide band gap allows it to operate over high temperatures [2]. Finally, a good thermal conductivity giving it the ability to easily transfer the heat generated by Joule's effect to a heat sink [3]. The recent literature reported high voltage 4H-SiC BJTs include a 10-kV open-base breakdown voltage (BV_{CEO}) and a specific on-resistance of 130 m Ω .cm² at (RT) with a 120 μ m drift layer doped to 6×10^{14} cm⁻³ [4]. A 15 kV (BV_{CEO}) at a leakage current of 0.1 mA/cm² with 125 μ m drift layer doped to 1.1×10^{14} cm⁻³ [5]. And a 9.2 kV (BV_{CEO}) with a 50 μ m, 7×10^{14} cm⁻³ doped drift layer and $R_{SP_ON} = 78$ m Ω .cm² [6]. For high voltage applications, it is mandatory to implant a highly sophisticated base-drift junction termination extension (JTE), which serves in minimizing the electric field plate crowd generated at the MESA edge of the power

device. Several approaches were applied to optimize the extension performance such that applying variations over the length and etching depth of the JTE [7], or by implanting different doses over the total length of the JTE [8], and by the addition of extra implanted rings [9]. In this paper a ≥ 10 kV NPN BJT based on 4H-SiC is reported when the theoretical value determined by its drift layer thickness and doping concentration is almost 13 kV.

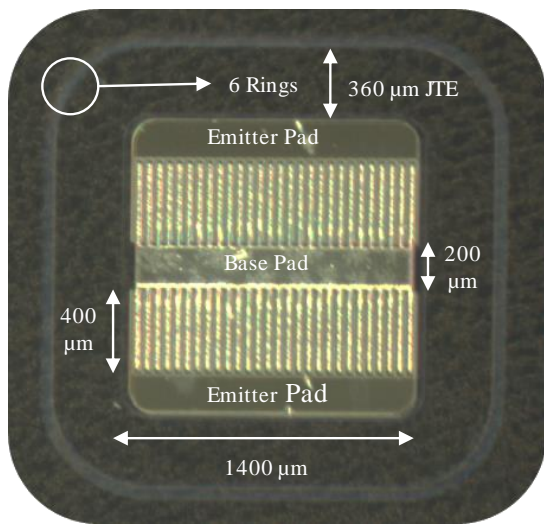
2. Device Design and Fabrication

Fig. 1-a) represents a cross-sectional view of a fabricated BJT with five epitaxial layers grown in one continuous run on a ~ 300 μ m 4° off-axis 4H-SiC substrate. Eleven photolithographic levels have been processed during the fabrication of the NPN BJT. The first two concern the 0.5 μ m plasma etching of the n-type emitter doped to 1×10^{19} cm⁻³ meeting another 0.5 μ m emitter highly doped to 3×10^{19} cm⁻³, and a 2 μ m p-type base doped to 2×10^{17} cm⁻³ in SF₆ chemistry with an ICP/RIE (Inductively Coupled Plasma-Reactive Ion Etching) Plassys MU400 reactor. The process has been optimized with high ICP density plasma (1000W) and high applied RIE bias (~ 350 V) to obtain quasi-vertical trenches with rounded sides at the bottom part (Fig. 2) to avoid equipotential line tightening and high electric field peaks in this area, under reverse bias at high voltage. After plasma etching two doping steps with Al ion implantations have been performed, under the p-type base to ensure a low resistive ohmic contact and to implant

the peripheral protection. To avoid SiC amorphization the high doped base wells have been implanted at 300°C. To activate



(a)



(b)

Fig. 1: Schematic cross-sectional view of 4H-SiC NPN BJT (a). Top microscopic view of 0.053 cm² fabricated device with 2 emitter pads (b).

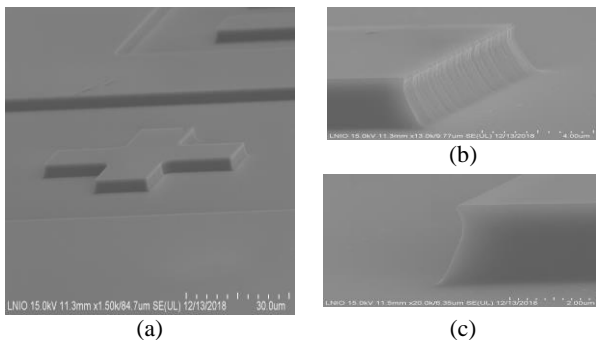


Fig. 2: Quasi-vertical trenches with rounded sides at the bottom part.

dopants, post-implantation annealing has been performed with a 1700°C/30 min plateau in an AET 6' RTP furnace with graphite resistors, specially designed for this process (Fig. 3). Previously the samples have been encapsulated on the both sides with a C-cap obtained by pyrolyzing AZ photoresist. The C-cap has been after removed by O₂ plasma. The lowly n-doped drift layer has a thickness of 120 μm and a doping concentration 8x10¹⁴ cm⁻³.

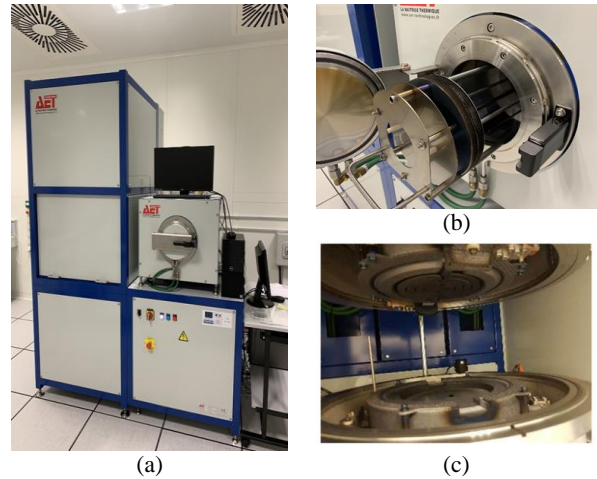


Fig. 3 AET furnace with RTP (c) and pyrolyze (b) chambers

The next technological steps have concerned the metallization with ohmic contacts formed on the high p-doped and n-doped (both sides) layers. For the n-type emitter and collector, sputtering Ni layers were patterned, and RTA annealed at 900°C and an alloy of Ni/Ti/Al 800°C RTA annealed [10] for the p-type base contacts. For making uniform potential distribution along metallized fingers, gold over-metallization

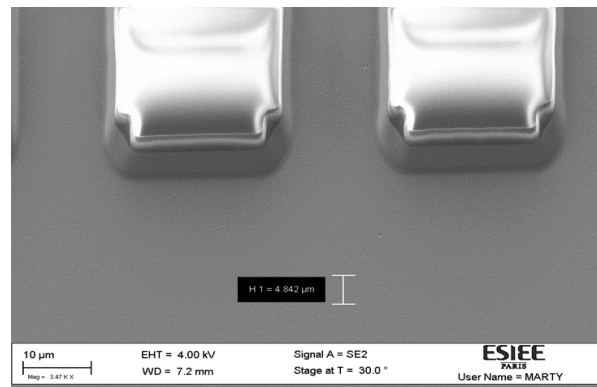
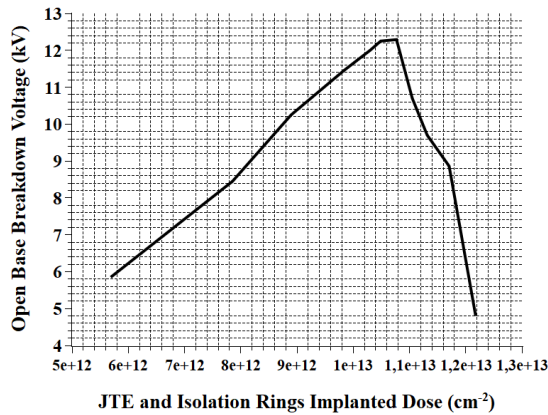
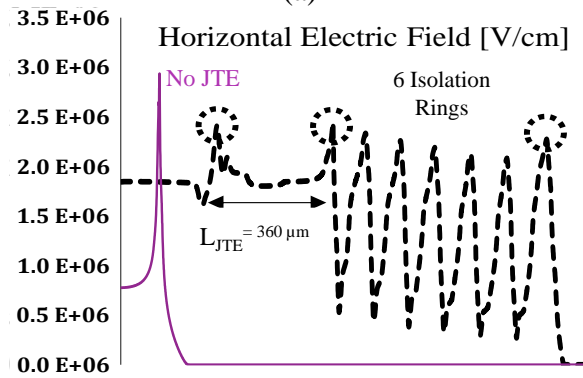


Fig. 4: An image during the technological process with the BJT defined fingers

has been also performed by sputtering and patterned. Before the metallization steps, SiC wafers have been thermal oxidized with a combined wet/dry process and the different metallic layers were isolated with LPCVD and PECVD Si_3N_4 layers locally opened by SF_6 plasma RIE. At the end a last passivation layer has been patterned in surface with a thick polyimide of 12 μm .



(a)



(b)

Fig. 5: Variation of Blocking Voltage (kV) as a function of JTE implanted Dose in cm^{-2} (a). Electric field distribution over the horizontal axis through the peripheral protection – Cross Section is shown in Fig. 1-a “Dashed Line” (b).

The peripheral protection is made up of 360 μm JTE done by Al implantation with an optimum dose of $1.1 \times 10^{13} \text{ cm}^{-2}$ (Fig. 5-a). Additional 6 rings are added each of a 5 μm length with a 4- μm space between the JTE and first ring. The peripheral protection design and parameters such as JTE length, rings number, and implanted dose were extracted from [11] upon a study done on a simulation model using TCAD from Synopsys Sentaurus [12] by performing various breakdown simulations to detect optimum technological parameters. The

electric field distribution shown in (Fig. 5-b) describes the impact of having such an optimized system to ensure a smooth flow of the high electric field crowd that gathers at the junction termination through all the device, increasing by this phenomenon the blocking voltage.

3. Results and Discussion

Electrical characterizations were performed on the fabricated devices. Reverse IV-characteristics as shown in Fig. 6 were performed using the testbench in Franco-German Institute of Saint Louis [13]. A high blocking voltage of 11 kV with 0.1 mA/cm^2 leakage current density was recorded validating our model and simulation results. The efficiency of our peripheral protection can be now clearly visualized when comparing both graphs in (Fig. 5 -b) and Fig. 6. The distribution of the electric field all over the device due to the junction termination extension (black curve) when compared to the electric field in the absence of a JTE (purple curve) lead to a great increase in blocking voltage from 1.3 kV when JTE absent and 11 kV with a JTE.

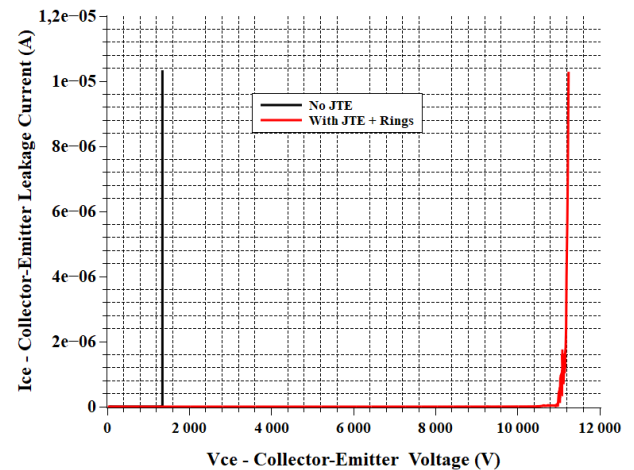


Fig. 6: Reverse IV Characteristics recording an open emitter blocking voltage of 11 kV at room temperature. The recorded results belong to the device shown in Fig. 1-b).

Fig. 7 shows forward common-emitter I - V characteristics and a Gummel plot at room temperature, of the fabricated BJT (Fig. 1-b) with an active area of 0.56 mm^2 when emitter finger width is 20 μm . The maximum current gain achieved is ($\beta = 20$) at a base

current ($I_B = 50$ mA) and $V_{CE} = 13$ V for a collector current $I_C = 1$ A (Cyan Curve) giving a current density of 178 A/cm².

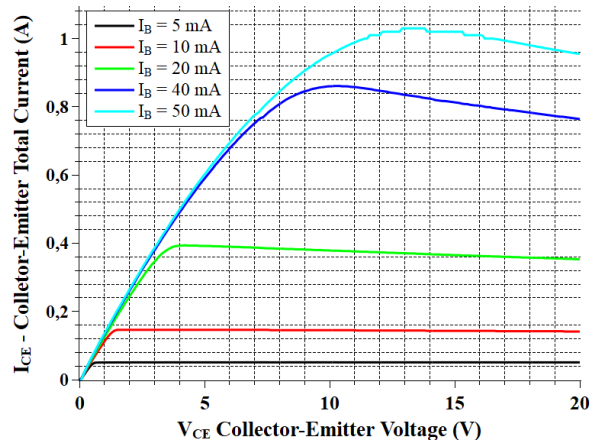


Fig. 7: Forward I - V characteristics of fabricated BJT with an active area of 0.0056 cm².

4. Summary

In this article, a 10+ kV class BJT based on 4H-SiC is reported with a well-studied junction termination extension and isolation ring system. The fabricated device attained an 11 kV as open base blocking voltage which corresponds almost to 90% of its theoretical value, at a 10 μ A leakage current. The device also shows at room temperature a maximum current gain ($\beta = 20$) at base current 50 mA and current density 178 A/cm² and a total collector current of 1 A. The next target will be to characterized optical controlled BJT designed in the project.

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