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Insights into the arsenic shell decapping mechanisms in As/GaAs nanowires by x-ray and electron microscopy

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ABSTRACT:

Nanowire heterostructures of the oxide (shell) - semiconducting (core) type are of interest for various applications in energy harvesting, such as electrodes for photocatalysis, and in sensors. Their complete synthesis often requires the deposition of shell and core in two separate reactors, with the risk of exposing the core to oxidation from atmospheric conditions during transfer. Here, we study the desorption mechanisms and protection efficiency of an arsenic shell, which was purposely deposited on the GaAs core for protection against undesirable oxidation. Using *in situ* heating in the transmission electron microscope and synchrotron radiation scanning photoelectron microscopy, we explore the morphology, the structure and the surface chemistry of GaAs nanowires capped with an arsenic shell, from room temperature up to 500 °C. A phase transformation from amorphous to polycrystalline arsenic is evidenced at a temperature about 300°C, alongside the disappearance of the surface oxidation observed at room temperature. At higher temperature, the arsenic shell desorbs with an activation energy of 2.32 eV, leading to clean facets. These results are helpful to determine pathways towards improving the efficiency of oxidation-protective layers on III-V semiconducting nanostructures.

KEYWORDS: III-V nanowires, core-shell, oxidation, annealing, *in situ* transmission electron microscopy, scanning photoelectron microscopy.

Introduction

III-V semiconducting nanowires (NWs) are promising for a large range of applications including solar energy harvesting [1-5], microelectronics [6] and integrated photonics [7-17]. In most of these applications, an epitaxial shell or a passivation layer wrapping the III-V core of the NW is needed to modify their functional properties or to avoid the formation of surface recombination traps due to undesirable oxidation. Materials such as silicon and germanium [18, 19], aluminum [20, 21], Fe₃Si [22] and SrTiO₃ [23] have been used as shell-material wrapping of III-V NWs to enhance or optimize their properties. Nevertheless, growing such heterogeneous shells is challenging; in most cases the shell and the core must be deposited in separate reactors, which implies that the oxidation of the as-deposited III-V facets must be avoided when the NWs are transferred between the deposition chambers. The strategy to avoid oxidation must preserve the quality of the NW facets from the point of view of their crystallography, their morphology and their surface chemistry. Besides the obvious use of connected ultra-high vacuum (UHV) reactors [21, 22], various methods have been suggested to obtain clean III-V facets for an epitaxial shell growth, such as for example cleaning by atomic hydrogen [24, 25]. Among them, the reversible capping by arsenic [26] presents several advantages. Firstly, As can be one of the constitutive element of III-V semiconductors, e.g., in GaAs or InAs, and consequently this approach does not require the introduction of any additional element, which could modify the nature of the surfaces. Secondly, the As decapping is obtained through heating at rather moderate temperature (less than 400°C), and can be performed in standard growth reactors or in situ in scanning tunneling microscopes (STM) [27-31]. Despite its practical importance, the physical mechanism behind the NW decapping has not been much studied, except by reflective high energy electron diffraction (RHEED) and xray photoelectron spectroscopy (XPS) [26], and STM [30]. It has been shown that even if the capping protects the NW facets well enough for subsequent epitaxial shell growth, it may

induce surface defects that require nanometer resolution analysis on a single NW, such as transmission electron microscopy (TEM), compared to characterization techniques operating at larger scales such as RHEED and XPS, which intrinsically probe a NW assembly. Therefore, the objective of this work is to combine thermal annealing using *in situ* TEM, and scanning photoelectron microscopy (SPEM) to determine how the morphology, the structure, and the surface chemistry evolve during the decapping of an As shell wrapping a single GaAs NW.

Results and Discussion

The RHEED pattern obtained after the growth of GaAs NWs of sample S1 (see experimental section) and shown in Figure 1(a) indicates that the epitaxial growth axis of the NWs, i.e., perpendicular to the surface of the substrate, is [111], confirming our previous observations [26]. The RHEED pattern exhibits spots related to both Zinc-Blende (ZB) and Wurtzite (WZ) phases, as described elsewhere [32, 33]. After the As capping, the RHEED pattern shown in Figure 1(b) does not exhibit spots related to GaAs, in agreement with the presence of an amorphous As shell that completely covers the GaAs NWs and hinders the RHEED signal from the core. The NWs are vertical with an approximate length of 1.7 μ m and a density of about 4 NWs/ μ m², as shown in the scanning electron microscopy (SEM) image of the As-capped sample S1 in Figure 1(c). These NWs are well separated and do not shadow each other, which is crucial to obtain a uniform and conformal As shell using MBE. Nearly all the As-capped GaAs NWs of sample S1 have a uniform core diameter of 80 nm. The NWs of sample S2 exhibit a similar structure with a core diameter of about 150 nm and a length of about 9.0 μ m.

The As-capped GaAs NWs were ripped from their native substrate. For TEM observations, sample S1 was deposited onto a micro-electro-mechanical system (MEMS) DENSsolutions

Wildfire chip, optimized for *in situ* annealing within the TEM column. For SPEM and XPS measurements, sample S2 was deposited on a Si substrate with its native oxide removed.

The bright field TEM image and elemental maps shown in Figure 2 confirm the core-shell NW structure with a core diameter of about 80 nm, and a conformal and smooth amorphous As shell of about 40 nm thickness. The Ga droplet at the NW tip is not visible due to its exposure to As atmosphere, which induced the formation of a crystalline WZ GaAs segment of about 150 nm length. The origin of this ZB / WZ transition has been thoroughly discussed [32, 34, 35], and is thought to be resulting from the evolution of the Ga droplet contact angle during the growth [36-39].

After NW dispersion onto Si substrate, sample S2 was rapidly sealed into the UHV chamber of the ANTARES beamline at synchrotron SOLEIL [40]. It should be noted that SPEM experiments could also be performed on vertical NW grown on their original substrate [41, 42], however we dispersed the NWs on a substrate in order to measure individual NWs. Figure 3(a) shows the XPS spectrum of sample S2 acquired with a large spot size. Only the As 3d core levels are visible; the Ga 3d photoelectrons cannot escape the shell thickness because of their shorter mean free path. The position of the As $3d_{5/2}$ core level at about 41.4 eV is that of metallic As. The spin orbit splitting is clearly evidenced. Oxidation of the exposed As shell is also detected in small quantity at about 44.4 eV due to the exposition to air prior to insertion into the XPS chamber. However, the prevalence of metallic As attests of the efficiency of the shell protective role. The beam was then focused by means of Fresnel zone plates in order to obtain a pixel size of about 400×400 nm². The SPEM image of Figure 3(b) shows that the NWs are well distributed on the surface and have the expected length. It should be noted that because the expected NW diameters (about 230 nm) are smaller than the pixel size, the NW diameter is overestimated in SPEM images. A series of four As 3d spectra have been extracted along the length of a single NW, in addition to one spectrum from the substrate as reference (Figure 3(c)).

The four spectra from the NW are similar (the doublet structure of the As *3d* peak is well resolved and the As oxide is alike) and therefore demonstrate the NW homogeneity. Comparable measurements have been obtained on several NWs.

For *in situ* TEM annealing of sample S1, the temperature of the SiN_x membrane supporting the dispersed NWs was directly increased to 200 °C, and then incrementally stepped by 5 °C every 30 s, up to 380° C. It should be mentioned that upon annealing, the structural evolution of GaAs NWs was observed to occur only in some NWs, while other NWs would undergo the same structural transformation after the temperature is raised tens or hundreds of degrees higher. It is thus apparent that the structural evolution of the NWs varies strongly as a function of the nature of the thermal contact between the nanowires and the SiN_x membrane, which depends on various factors like the NW bending, important here for NWs as long as 1.7 µm, and the NW interface with the supporting membrane (e.g., presence of amorphous hydrocarbon species at the NW surface, oxidation of the NW facets, presence of defects like atomic steps, etc.). The decapping process on NWs grown in the same conditions as sample S1 was also performed in the MBE chamber, and followed by RHEED. The in situ TEM results reported in this manuscript show structural and chemical modifications of NWs where the temperature of the SiN_x membrane, which is accurately measured directly by the 4-point probe DENSSolutions system in the TEM [43, 44], is in agreement with the temperature measured during decapping in the MBE chamber.

Above 300 °C, the NW surface became rough, as the amorphous As shell turns polycrystalline (see Supporting Information Figure S1). This crystallization was previously reported to occur at 306 °C in thick layers of amorphous As by differential thermal analysis [45], in good agreement with our observations. The homogeneous crystallization over the length of this NW suggests that it is lying flat on the SiN_x membrane, providing a homogeneous thermal contact.

This is different from the NW shown in Video V1 (see Supporting Information), where the slow progression of the crystallization front from top to bottom suggests that the thermal contact with the membrane is better at the top of the NW. While the crystallization of the NW in Video V1 seems to start from the outer part of the As shell, especially on the top left of the NW, which could be attributed to the fact that it constitutes a free surface, additional evidence should be provided to support this hypothesis. At around 310 °C, the crystalline As shell begins to disappear (Figure 4). At around 365 °C, the As shell is not visible anymore and the GaAs surface is clean and smooth. Recently, in situ heating experiments in the TEM have been realized to estimate activation energies of various phenomena in III-V NWs, such as GaAs replacement by Au [46, 47] or defect dynamics [48]. The same method has been applied here to extract the activation energy of the present As desorption mechanism. From 300 to 380 °C, the diameter D of the NW was measured in order to calculate the corresponding arsenic desorption velocity k (see Supporting Information for details concerning the data measurement). This desorption velocity can be related to an activation energy E_a (see Figure 4b) equal to 2.32 eV, a value slightly smaller than the cohesive energy of As, which varies from $E_c = 2.4 \text{ eV}$ per As atom to $E_c = 3.0 \text{ eV}$ per As atom depending on the crystalline phase [49]). Figure 5 shows the As 3d core level photoemission spectra measured on sample S2 before annealing, after a first annealing (30 min) at 400°C, and after a second annealing (30 min) at 440°C. It should be noted that this temperature is probably overestimated compared to the temperature measured during MBE growth. After the first annealing, the XPS spectrum does not display Ga 3d core level (see Figure S3), which means that the As capping is still wrapping the NWs. However, the As 3d core level does not exhibit As oxide (grey-filled area) anymore. After the second annealing at 440°C, the As 3d core levels are strongly modified. A shift toward low binding energy is clearly evidenced, and a broadening is observed. This energy shift agrees well with a modification of the As environment from As-As to Ga-As, as expected after As shell desorption. The broadening of the As 3*d* peaks could be the consequence of residual As at the surface and/or surface defects. The fitting parameters are available in Supporting Information (Table S1).

Elemental maps extracted from EELS data after the *in situ* decapping in the TEM indicate the complete desorption of the As shell, as illustrated in Figure 6. Spectra on the nanowire show both Ga L_{23} and As L_{23} contributions at ~1120 eV and ~1330 eV, respectively, as shown in Figure S4. The O-K edge at ~530 eV is not detected on the NW, suggesting that the As shell protects the nanowire from oxidation. This is in good agreement with the As 3*d* core level XPS spectra (Figure 5(c)), which indicate a GaAs surface without traces of oxidation. However, the As 3*d* peaks are broad, which could be explained by the presence of surface defects such as As_{Ga} antisite defects as observed by STM [30]). Moreover, the high resolution TEM image in Figure 6 shows that the NW surface is crystalline, and strongly facetted, comparable to as grown NWs, opening the way for further epitaxial growth. These results can be compared with NWs that have been exposed to air, and which exhibit a thin amorphous oxide shell [26].

Conclusions

In summary, we combined *in situ* heating of core(GaAs)-shell(As) NWs in the TEM and synchrotron radiation scanning photoemission microscopy to explore the structural changes of GaAs nanowires capped with an As shell upon annealing, which is a conventional step towards the growth of oxide shells on III-V cores in NW geometry. The results unambiguously show that the As decapping is preceded by the crystallization of the initially amorphous As shell at \sim 300 °C. Beyond this temperature, the arsenic shell desorbs with an activation energy of \sim 2.32 eV, leading to clean facets of the NW core, which supports the success of this approach to promote the subsequent epitaxial growth of an oxide shell. Two desorption phenomena were

identified, which could correspond to As oxide desorption and metallic As desorption. This work demonstrates that the capping / decapping process is efficient to obtain clean GaAs nanowires surfaces (stable until 600 °C [50]), and could be suitable for other arsenide NWs such as InAs (stable until 380 °C [51]). The proposed method allows to obtain clean facets in the high vacuum of a TEM column, and UHV setups equipped with a heater, thus opening the way to surface science studies on arsenide NWs.

Experimental Section

Nanowire Synthesis

Self-catalyzed GaAs NWs were grown on epiready Si(111) substrates by molecular beam epitaxy (MBE). The substrates were cleaned with ethanol and acetone for 5 min in order to remove surface contaminations. After rinsing, the substrates were outgassed at 200°C under UHV for a few minutes and then transferred into the MBE chamber. The MBE chamber is equipped with a Ga Knudsen cell with an incident angle of 27.9° [*52*] and an As valve cracked cell. GaAs NWs were grown using the VLS mechanism with Ga droplets as catalyst [*53, 54*]. Because no native silicon oxides were removed beforehand, the Si substrate temperature was increased to 530°C, and one monolayer (ML) of Ga was deposited on the substrate at an equivalent GaAs two-dimensional growth rate of 0.50 ML/s [*55, 56*]. Then, the substrate temperature was increased to 610°C to begin the growth of GaAs NWs by providing Ga and As fluxes simultaneously. The GaAs growth rate was fixed at 0.75 ML/s with a beam equivalent pressure (BEP) of Ga and As₄ equal to 3.6 x 10⁻⁷ Torr and 3.6 x 10⁻⁶ Torr, respectively. During growth, the sample holder was continuously rotated to enhance the growth homogeneity. Two samples were fabricated: sample 1 (S1) was grown during 10 minutes, while sample 2 (S2) was grown during 100 minutes. As a result, S2 NWs are longer than S1 NWs but also thicker due

to radial overgrowth during the VLS axial growth. After the Ga shutter closure, the sample was cooled down to room temperature (RT) under an As₄ atmosphere to avoid GaAs decomposition. The As capping procedure was initiated after the substrate cooled down to RT following the growth of GaAs samples S1 and S2. For the As-capping of the obtained NWs, the temperature of the As cracking-cell was increased up to 950°C to supply As₂ instead of As₄. The samples were exposed to As₂ for about 2 hours. S1 was dedicated to the TEM experiments while S2 was dedicated to the SPEM/XPS measurements.

In situ TEM characterization

The TEM experiments have been performed in a FEI Titan ETEM G2, equipped with a spherical aberration corrector of the objective lens, a Gatan imaging filter (GIF) TRIDIEM for electron energy-loss spectroscopy measurements, and operated at 300 kV. The *in situ* annealing experiments were carried out using a dedicated Wildfire heating holder from DENSsolutions. NWs were transferred to a MEMS chip with a Si₃N₄ membrane by mechanically scrapping NWs off their native silicon substrate. The EELS elemental maps were obtained after background subtraction and integration of the Ga-L₂₃ (~1120 eV) and As-L₂₃ edges (~1330 eV) using the Digital Micrograph (Gatan Microscopy Suite) software.

Synchrotron radiation scanning photoelectron microscopy

X-ray photoemission experiments were performed at the nano-XPS endstation of the ANTARES beamline of the SOLEIL synchrotron (Paris, France), equipped with Fresnel zone plates for focalization of the synchrotron radiation beam, with a spot size of ~600 nm in this work. The experiments were carried out at a photon energy of 100 eV and linear horizontal light polarization, with an angular resolution of ~0.2° and an energy resolution of ~30 meV [57]. The sample was kept at room temperature and the base pressure during all experiments

was better than 2×10^{-10} mbar. The advantage of the nano-XPS technique for the study of NWs is that the X-ray beam spot size is small enough ensuring that the XPS measured is obtained in a particular area of a single few µm-long NW.

Associated content

The Supporting Information is available free of charge at

Figure S1: TEM bright field imaging and STEM-EELS elemental mapping of arsenic crystallization during shell decapping of sample S1.
Video V1: *in situ* TEM cristallization of the As shell.
Figure S2: Evolution of the NW diameter during *in situ* TEM annealing.
Table S1: Measured binding energy.
Figure S3: STEM-EELS analysis of a NW after decapping of the arsenic shell.

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L.F., X.G., C.B., P.R. and M.G. synthesized the specimens. L.F., G.G., H.Y., J.A. and J.P.

performed the SPM experiments and data analysis. L.F., M.B. and J.P. performed the in situ

TEM experiments, and data analysis. M.B. and J.P. designed the study and wrote the

manuscript. All authors contributed to the manuscript.

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Notes

The authors declare no competing financial interest.

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Figure 1. RHEED patterns (a) before the As capping, highlighting the presence of ZB (green arrows) and WZ phases (orange arrows), and (b) after the As capping. (c) SEM image of sample S1 after the growth.



Figure 2. (a) Bright field TEM image of the as grown As/GaAs NW. (b) STEM-ADF image acquired simultaneously with a STEM-EELS spectrum image leading to the elemental maps of (c) Ga, (d) As, and (e) Ga+As. Scale bar: 50 nm



Figure 3. (a) XPS spectrum of sample S2 showing the region including As 3*d*, Ga 3*d* and valence band measured with a large spot size of $0.6 \,\mu\text{m}$. (b) SPEM image ($50 \times 50 \,\mu\text{m}^2$) obtained with a pixel size of $400 \times 400 \,\text{nm}^2$ and filtered at a binding energy of $41.5 \,\text{eV} + 1 \,\text{eV}$ corresponding to metallic As. (c) Five spectra (A, B, C, D) extracted from the map in (b) measured on a single nanowire, and a reference spectra (E) measured on the substrate. The photon energy was set to $100 \,\text{eV}$.



Figure 4. (a-f) Bright field TEM images of a single NW (sample S1) showing the desorption of the As shell, recorded at T = 300 °C (a), 315 °C (b), 330 °C (c), 345 °C (d), 360 °C (e), 375 °C (f). Scale bar: 100 nm. (g) Arrhenius plot extracted from the NW diameter evolution).



Figure 5. XPS As 3*d* core level spectra measured on a typical NW before annealing (a), after the first annealing at 400°C (b), after the second annealing at 440°C (c). The photon energy was set to 100 eV. The grey curve corresponds to As oxide, the yellow curve to metallic As and the blue curve to As in GaAs.



Figure 6. (a) STEM-ADF image acquired simultaneously with an EELS spectrum image leading to the elemental maps of (b) Ga, (c) As, and (d) Ga+As; scale bar: 100 nm. (e) High resolution TEM image of the NW after decapping of the As shell; scale bar: 1 nm. The yellow lines follow the facets and are guides for the eye. (f) FFT performed on the NW shown in (e).

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