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Design of a direct control strategy for a static shunt compensator to improve power quality in polluted and unbalanced grids

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Abstract – Many control techniques for three-phase active filters with the aim of improving the power factor and reducing the current harmonic distortion at the source level exist today and are widely reported in the scientific literature. Almost all of these methods are designed for a perfect sine-wave source and don't perform sinusoidal current when voltages are distorted or unbalanced. The work developed in this paper concerns the active filtering on a perturbed and unbalanced network. First, the proposed control method is described and validated by simulations and experimental results, and then the proposed method is compared to the classical control laws such as the original p-q theory, the modified p-q theory and the p-q-r method. Experimental results are conducted on a three-wire low power distribution network under perturbed voltage and non-linear load using a DSPACE control system. The assessment is performed on the source currents after filtering, and a comparative evaluation in terms of unbalance factor and current Total Harmonic Distortion (THD_i) is carried out for all methods.

Keywords – Active filter, voltage disturbance, voltage unbalance, three-phase power grid, harmonic distortion, unbalance factor, direct power control, instantaneous power control

1. Introduction

The increase of the non-linear loads connected to distributed electric power networks leads to an excessive injection of harmonics and the circulation of reactive power in the distribution lines. These periodic but non-sinusoidal currents circulate through the impedances of the grid and give rise to non-sinusoidal voltages that are added to the initial voltage [1]. Consequently, the

degradation of the power quality [2] has detrimental consequences on the proper functioning of electrical appliances and incurs an additional cost for the installations. This causes severe damages that must be fought forcefully [3]. The ability of shunt active filters to remove these problems has given rise to a great deal of work in recent years and many solutions have emerged. Most of these solutions are based on the concept of instantaneous reactive power and on the $p-q$ theory. This approach is particularly effective but is based on the assumption of purely sine-wave source voltages. If the voltage source is distorted, both power factor and efficiency are reduced. In this paper, the development of a novel method called **Direct Control for Active Power (DCAP)** based on the concept of instantaneous active power and on the computation of the effective value of the fundamental of the source voltages is proposed. A new definition of desired active current under perturbed and unbalanced supply voltages is developed. It is based on the active power to be delivered by the source and the fundamental of perturbed voltages obtained by a Band-pass filter rather than a PLL. The PLL extract the direct-sequence voltages system but the DCAP method explicitly takes into account the unbalance of voltages in the calculation of reference currents. PLL is commonly used in the control of SAPF [4-6] in particularly associated to the D-Q theory [7-11]. Several works investigated on the output response of a PLL under distorted and unbalanced utility voltages and demonstrated that a PLL cannot perform at its output sinusoidal waves in disturbed environment [6]. Indeed, some of its components require fine adjustments taking into account numerous parameters [12].

The DCAP method takes part of a general methodology applicable in multiphase systems particularly in three phase 3-wire, three-phase 4-wire and in single phase networks. The general approach for **polyphase system** described in [13], has been the subject of a patent application [14].

This paper is divided as follow: In section 2, the Active Power Filter APF and its control **scheme** are described. In section 3, control strategies based on the $p-q$ theory, modified $p-q$ theory, and $p-q-r$ method are presented; the proposed **DCAP** method is also detailed. In section 4, the application of the DCAP control on the three-phase shunt active power filter is studied; the setup procedure to generate a perturbed voltage source (with THD = 25%) is described, and the simulation and experimental results are analysed and discussed through comparative evaluation of all considered control methods. This work finally ends with some conclusions.

2. Shunt active power filter: Topology and control **scheme**

The shunt active power filter (SAPF) is the most common solution for the cancellation of current-type harmonics [15-17]. The conventional structure of the two-level inverter shown in Fig. 1 is highly widespread for Low Voltage applications. Fig. 2 shows the general control **scheme** applied to one phase, e.g. phase (a). It reveals 3 essential functions, first a block to extract the harmonics and reactive current $i_{r,h}$ to be compensated, a current loop and a voltage loop.

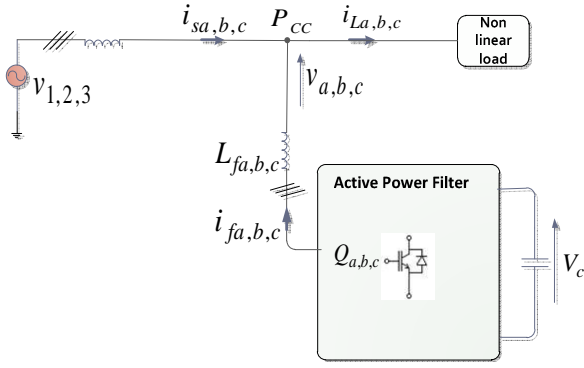


Fig.1. Three-wire grid connected Active Power Filter

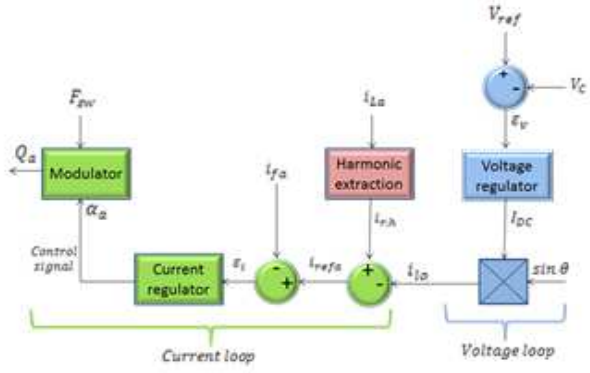


Fig. 2. General control scheme of the SAPF law

The proposed work mainly focuses on the design of harmonic extraction block. This block in its simplest version uses only the load current as input but it would be possible to use the power instead as it will be explained in the proposed control method.

3. Control strategies for APF

3.1 P-Q theory in three-phase systems

The p-q theory, initially developed for the three-phase systems by Akagi and al. [18], is one of the theories more appropriate to generate current references in current controlled systems [19-23]. It is characterized by a high dynamic response and stable steady-state, and is based on a transformation of currents and voltages vectors from the a, b, c to the α, β frame as in equations (1) and (2). The instantaneous active power $p(t)$ and an instantaneous reactive power $q(t)$ are defined expressed in (4), where C_{23} is the Concordia matrix in (3).

$$\begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} = C_{23} \cdot \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} \quad (1)$$

$$\begin{bmatrix} v_\alpha \\ v_\beta \end{bmatrix} = C_{23} \cdot \begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} \quad (2)$$

$$C_{23} = \sqrt{\frac{2}{3}} \cdot \begin{bmatrix} 1 & -1/2 & -1/2 \\ 0 & \sqrt{3}/2 & -\sqrt{3}/2 \end{bmatrix} \quad (3)$$

$$\begin{bmatrix} p \\ q \end{bmatrix} = \begin{bmatrix} v_\alpha & v_\beta \\ -v_\beta & v_\alpha \end{bmatrix} \cdot \begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} \quad (4)$$

The currents to be generated by the SAPF are given in (5) and obtained by the inversion of (4):

$$\begin{bmatrix} i_{r\alpha} \\ i_{r\beta} \end{bmatrix} = \frac{1}{v_\alpha^2 + v_\beta^2} \begin{bmatrix} v_\alpha & -v_\beta \\ v_\beta & v_\alpha \end{bmatrix} \cdot \begin{bmatrix} p_c \\ q_c \end{bmatrix} \quad (5)$$

To perform compensation without using energy storage component it is good enough to eliminate the instantaneous reactive power without altering the active power like as (6).

$$\begin{cases} q_c = \bar{q} + \tilde{q} \\ p_c = 0 \end{cases} \quad (6)$$

To eliminate harmonics and reactive power, the undesirable powers to be compensated are expressed in (7):

$$\begin{cases} q_c = \bar{q} + \tilde{q} \\ p_c = \tilde{p} \end{cases} \quad (7)$$

Finally, the current references obtained in α, β frame, which take into account the losses in the filter and the power p_f requested for the DC bus voltage regulation, are formulated in (8):

$$\begin{bmatrix} i_{r\alpha} \\ i_{r\beta} \end{bmatrix} = \frac{1}{v_\alpha^2 + v_\beta^2} \begin{bmatrix} v_\alpha & -v_\beta \\ v_\beta & v_\alpha \end{bmatrix} \cdot \begin{bmatrix} \tilde{p} - p_f \\ \bar{q} + \tilde{q} \end{bmatrix} \quad (8)$$

The bloc diagram in Fig. 3 shows the steps to extract the reference currents on the a, b, c frame.

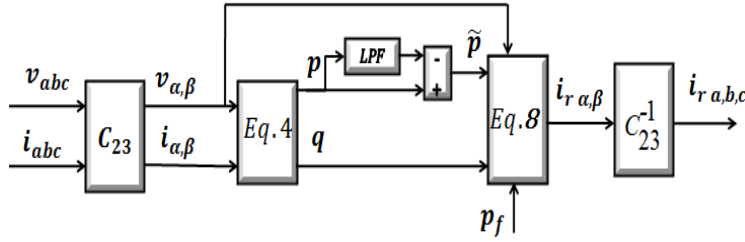


Fig. 3. Bloc diagram to extract the current references based on the p-q theory

The p_f power reference is provided by the voltage loop allowing a quasi-constant voltage V_c across the capacitor. The LPF filter that separates the DC and AC components is set at a few hertz.

3.2 Modified P-Q theory in three phase

The modified p-q theory [24,25] is based on the definition of three reactive powers and an active power, and it is dedicated in priority for 4-wire systems and formulated in (9).

$$\begin{bmatrix} p \\ q_0 \\ q_\alpha \\ q_\beta \end{bmatrix} = \begin{bmatrix} v_0 & v_\alpha & v_\beta \\ 0 & -v_\beta & v_\alpha \\ v_\beta & 0 & -v_0 \\ -v_\alpha & v_0 & 0 \end{bmatrix} \cdot \begin{bmatrix} i_0 \\ i_\alpha \\ i_\beta \end{bmatrix} \quad (9)$$

Its application in 3-wire systems is a particular case from (9) for $i_0 = 0$. In this paper, the application aims the active filtering on a perturbed 3-wire network. Accordingly, $v_0 \neq 0$ and $i_0 = 0$.

It is then possible to eliminate the first column in (9), and the reference currents are thus obtained by the inversion of (9) and are formulated in (10):

$$\begin{bmatrix} i_{r\alpha} \\ i_{r\beta} \end{bmatrix} = \frac{1}{v_{0\alpha\beta}^2} \cdot \begin{bmatrix} v_\alpha & -v_\beta & 0 & v_0 \\ v_\beta & v_\alpha & -v_0 & 0 \end{bmatrix} \cdot \begin{bmatrix} p_c \\ q_{0c} \\ q_{\alpha c} \\ q_{\beta c} \end{bmatrix} \quad (10)$$

97 With:

$$98 \quad v_{0\alpha\beta} = \sqrt{v_0^2 + v_\alpha^2 + v_\beta^2} \quad (10.a)$$

99 To compensate harmonics and reactive power, the reference compensation powers are:

$$100 \quad \begin{cases} p_c = \tilde{p} \\ q_{c0} = q_0 \\ q_{c\alpha} = q_\alpha \\ q_{c\beta} = q_\beta \end{cases} \quad (11)$$

101 The system of reference currents obtained in α, β frame is consequently formulated as:

$$102 \quad \begin{bmatrix} i_{r\alpha} \\ i_{r\beta} \end{bmatrix} = \frac{1}{v_{0\alpha\beta}^2} \begin{bmatrix} v_\alpha & -v_\beta & 0 & v_0 \\ v_\beta & v_\alpha & -v_0 & 0 \end{bmatrix} \begin{bmatrix} \tilde{p} - p_f \\ q_0 \\ q_\alpha \\ q_\beta \end{bmatrix} \quad (12)$$

103 The block diagram in Fig. 4 shows the extraction of reference currents on the a, b, c frame.

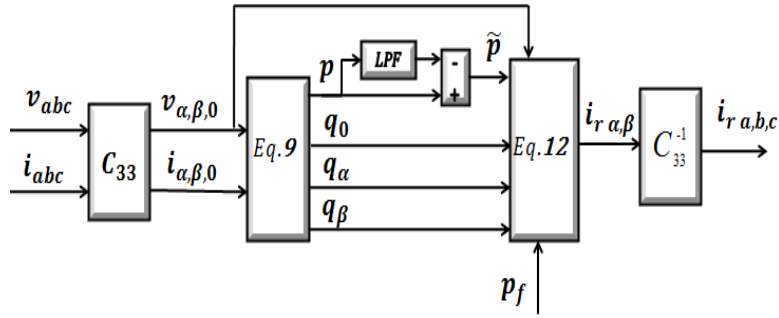


Fig. 4. Bloc diagram to extract the reference currents based on the modified p-q theory

3.3 P-Q-R theory in three phase

107 For this method [26-31], two transformations are required. Once the currents are calculated in the $\alpha, \beta, 0$ frame, another
 108 transformation to the p, q, r frame is necessary (12). The pqr-coordinate is developed as well as space-vector transformation by
 109 using Euler rotation angle. By using the Euler angle method with double rotation, the mapping matrices model results in (12):

$$110 \quad \begin{bmatrix} i_p \\ i_q \\ i_r \end{bmatrix} = T_{pqr} \begin{bmatrix} i_0 \\ i_\alpha \\ i_\beta \end{bmatrix} \quad (12)$$

111 with:

$$112 \quad T_{pqr} = \frac{1}{v_{0\alpha\beta}} \begin{bmatrix} v_0 & v_\alpha & v_\beta \\ 0 & -\frac{v_{0\alpha\beta} \cdot v_\beta}{v_\alpha\beta} & \frac{v_{0\alpha\beta} \cdot v_\alpha}{v_\alpha\beta} \\ v_{\alpha\beta} & -\frac{v_0 \cdot v_\alpha}{v_\alpha\beta} & -\frac{v_0 \cdot v_\beta}{v_\alpha\beta} \end{bmatrix} \quad (13)$$

113 And:

$$v_{\alpha\beta} = \sqrt{v_{\alpha}^2 + v_{\beta}^2} \quad (13.a)$$

Note that, in the case of 3-wire system, the zero-sequence component of the phase currents is zero, i.e. $i_0 = 0$, but all equations remain valid and it is not necessary to reduce their expression. Automatically, all components that depend on i_0 become also zero. This method is based on the definition of an active power and two reactive powers as in (14):

$$\begin{bmatrix} p \\ q_q \\ q_r \end{bmatrix} = \begin{bmatrix} v_p \cdot i_p \\ -v_p \cdot i_r \\ v_p \cdot i_q \end{bmatrix} = v_p \cdot \begin{bmatrix} i_p \\ -i_r \\ i_q \end{bmatrix} \quad (14)$$

In the p-q-r coordinate transformation two consecutive transformations are operated, the purpose of which is to cancel the voltage components on the q-axis and r-axis. So only one voltage is calculated on the p-axis that gives:

$$v_p = v_{0\alpha\beta} \quad (14.a)$$

There are two ways for the compensation in this method. The first is based on power compensation, which results are similar to those of the p-q and p-q modified methods. The second is based on the current compensation presented in the following.

The reference currents of the p, q, r frame are then in (15) and recalculated in the $\alpha, \beta, 0$ frame in (16)

$$\begin{cases} i_{rp} = \tilde{i}_p \\ i_{rq} = i_q \\ i_{rr} = i_r + \frac{i_p \cdot v_0}{v_{\alpha\beta}} \end{cases} \quad (15)$$

$$\begin{bmatrix} i_{r0} \\ i_{r\alpha} \\ i_{r\beta} \end{bmatrix} = \mathbf{T}_{pqr}^{-1} \begin{bmatrix} i_{rp} \\ i_{rq} \\ i_{rr} \end{bmatrix} \quad (16)$$

\mathbf{T}_{pqr}^{-1} is the inverse matrix of \mathbf{T}_{pqr} expressed in (16.a).

$$\mathbf{T}_{pqr}^{-1} = \frac{1}{v_{0\alpha\beta}} \cdot \begin{bmatrix} v_0 & 0 & v_{\alpha\beta} \\ v_{\alpha} & -\frac{v_{0\alpha\beta} \cdot v_{\beta}}{v_{\alpha\beta}} & -\frac{v_0 \cdot v_{\alpha}}{v_{\alpha\beta}} \\ v_{\beta} & \frac{v_{0\alpha\beta} \cdot v_{\alpha}}{v_{\alpha\beta}} & -\frac{v_0 \cdot v_{\beta}}{v_{\alpha\beta}} \end{bmatrix} \cdot \begin{bmatrix} i_{rp} \\ i_{rq} \\ i_{rr} \end{bmatrix} \quad (16.a)$$

The block diagram in Fig. 5 shows the extraction of reference currents on the a, b, c frame.

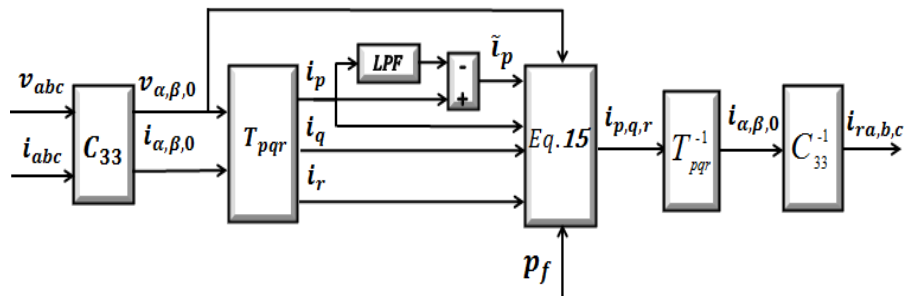


Fig.5. Bloc diagram to extract the reference currents based on the modified p-q-r theory.

3.4 DCAP theory in three phase

The proposed DCAP patented method [13,14] is based on the repartition of sinusoidal and balanced currents in the source side and not on the repartition of equal powers in the source lines. It is developed for a polyphase system and it has been demonstrated that the fundamental conductance of each phase, for a perturbed k -phase is then expressed in (17).

$$\begin{cases} G_{f1} = \frac{P_s}{V_{f1} \cdot (\sum_{j=1}^k V_{fj})} \\ G_{f2} = \frac{P_s}{V_{f2} \cdot (\sum_{j=1}^k V_{fj})} \\ \vdots \\ G_{fk} = \frac{P_s}{V_{fk} \cdot (\sum_{j=1}^k V_{fj})} \end{cases} \quad (17)$$

Where V_{fi} represent the rms voltage value of each of the phases of the network obtained by filtering.

The three phase system is a particular case of (17) for $k=3$ and the desired currents in the source side $i_{sd\ a,b,c}$ for a 3 phase system are formulated as follows:

$$\begin{cases} i_{sda} = \frac{P_s}{V_{fa} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot V_{fa} \\ i_{sdb} = \frac{P_s}{V_{fb} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot V_{fb} \\ i_{sdc} = \frac{P_s}{V_{fc} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot V_{fc} \end{cases} \quad (18)$$

Where V_f represents the fundamental voltage of the network. So, the RMS value for desired equal currents is:

$$I_{sda} = I_{sdb} = I_{sdc} = \frac{P_s}{(V_{fa} + V_{fb} + V_{fc})} \quad (19)$$

Once the sinusoidal active currents desired in the source $i_{sd\ a,b,c}$ are calculated, the reference currents are deduced:

$$i_{ri} = i_{Li} - i_{sdi} \quad i = a, b, c. \quad (20)$$

P_s is the active power to be feeded by the source and deduced from:

$$P_s = P_L + P_f \quad (21)$$

P_L is the active power required by the load and obtained at the output of a Low Pass Filter after the calculation of the instantaneous power:

$$p_L(t) = v_a \cdot i_{La} + v_b \cdot i_{Lb} + v_c \cdot i_{Lc} \quad (22)$$

P_f is the power to be absorbed by the filter and needed for the regulation of the DC bus voltage. It is expressed as:

$$P_f = I_{lo} \cdot (V_{fa} + V_{fb} + V_{fc}) \quad (23)$$

I_{lo} is the RMS value of the required sinusoidal currents $i_{lo\ abc}$ that are calculated by the same way of (18) and formulated in (24).

$$\begin{cases} i_{lo\ a} = \frac{P_f}{V_{fa} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot v_{fa} \\ i_{lo\ b} = \frac{P_f}{V_{fb} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot v_{fb} \\ i_{lo\ c} = \frac{P_f}{V_{fc} \cdot (V_{fa} + V_{fb} + V_{fc})} \cdot v_{fc} \end{cases} \quad (24)$$

The block diagram for reference current extraction is in Fig.6. The fundamental waves v_{fabc} are obtained at the output of a band-pass filter (BPF) tuned on the fundamental frequency. Afterward, the squared fundamental voltages are filtered with a low pass filter (LPF) generating thus the RMS value V_{fabc} of fundamental voltage according to the following definition where T is the period of network voltage:

$$V_{fabc} = \frac{1}{T} \sqrt{\int_0^T v_{fabc}^2 \cdot d\theta} \quad (25)$$

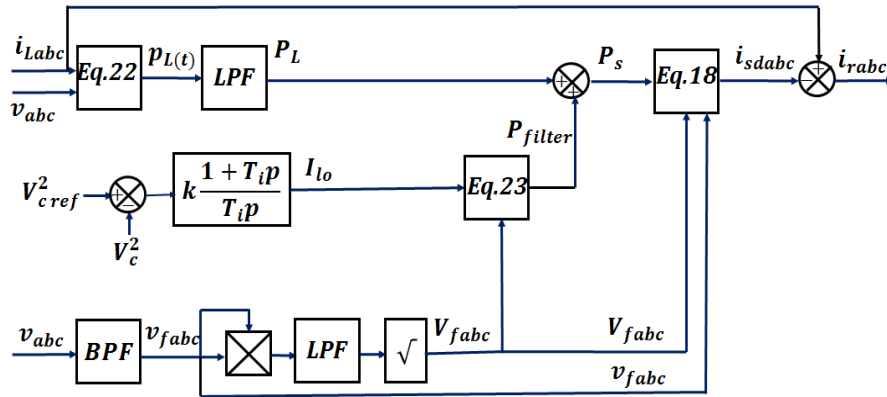


Fig.6 Block diagram for reference current extraction. DCAP method.

Once the current references $i_{r\ abc}$ to be injected by the filter are extracted, a closed-loop is then designed for the real currents injected by the filter $i_{f\ abc}$. The current control loop is presented in Fig.7 with F_{sw} being the switching frequency and Q_{abc} the switching signal.

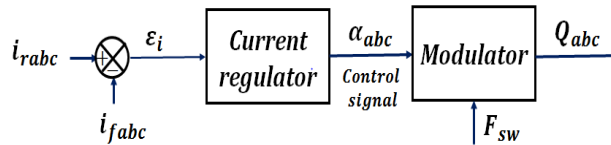


Fig.7. Bloc diagram for current loop

4. Application to three phase shunt active power filter control

Fig. 8 shows the studied three-phase shunt active power filter system configuration. It consists in a three-phase voltage source inverter, three series inductances L_{fabc} with internal resistances R_{fabc} and a DC capacitor. The shunt active power filter is connected in parallel to the non-linear load which is a three-phase diode rectifier feeding a (R_L, L_L) load. A R,L ac reactor is generally present at the input of this type of load.

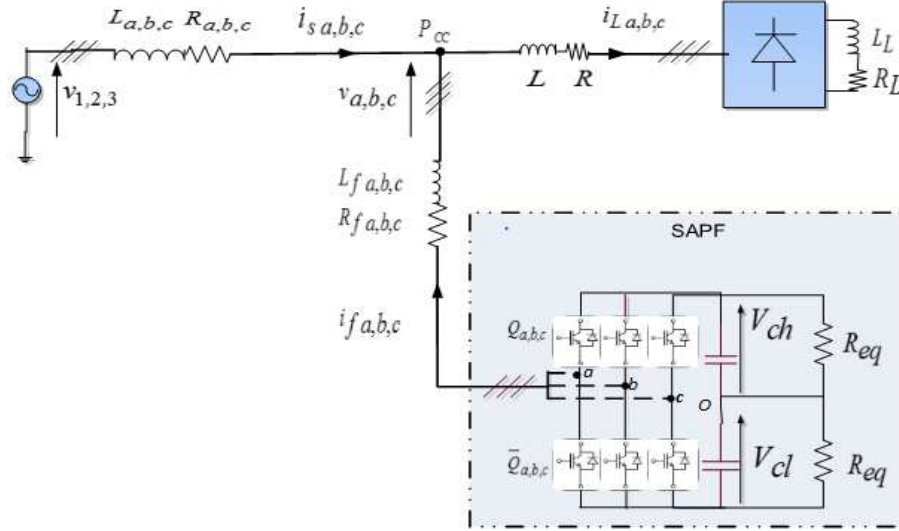


Fig.8. The three-phase three-wire power circuit.

v_1 , v_2 , and v_3 represent the voltages at the source (the output of transformer in the real case) with their harmonic expressions in (26). Three large unequal impedances $Z_{a,b,c}$ are connected between the source and the P_{CC} , ($R_a=44.4 \Omega$, $R_b=35 \Omega$ et $R_c=38.3 \Omega$) in series with ($L_a=45 \mu\text{H}$, $L_b=20 \mu\text{H}$ et $L_c=21 \mu\text{H}$). The load is constituted by a three phase diode bridge that feed a resistance $R_L=40.4\Omega$ and an inductance $L_L=27.67\text{mH}$. Three identical impedances are connected at the ac input of the bridge $L=6,7 \text{ mH}$, $R=0.5 \Omega$. The APF connected to the grid via three unequal inductances ($L_{fa}=12.81\text{mH}$, $L_{fb}=13.72 \text{ mH}$ and $L_{fc}=10.6\text{mH}$) in series with their internal resistances ($R_{fa}=0.5\Omega$, $R_{fb}=0.6\Omega$ and $R_{fc}=0.3\Omega$). The DC bus voltage is 650V. The maximum voltage for each capacitor is 400V, for it they are two capacitors in series. $C_h=C_b=0.6\text{mF}$. The switching frequency is equal to 10 kHz.

4.1 Voltage perturbation procedure

4.1.a Simulation model

Simulation is performed using Psim tools. The power system parameters are identical to those in real experimentation. Harmonic model for measured real voltages source v_1 , v_2 , and v_3 are expressed like as (26) with numerical values for fundamentals and harmonic components in table.1 for $i=1,2,3$.

$$v_i = \sum_{j=1;3;5;7;9;11} A_j \cdot \sin(j \cdot \omega \cdot t - \varphi_j) \quad (26)$$

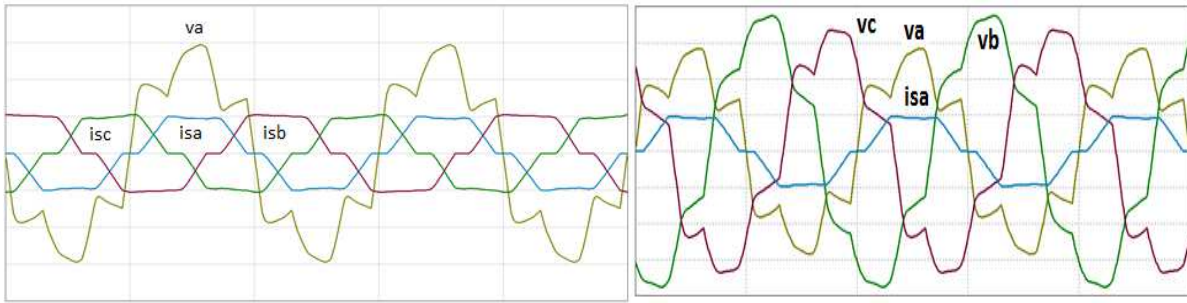
191

Table.1 Numerical values for voltages amplitudes A_j and harmonics phase shift φ_j .

	A_1	φ_1	A_3	φ_3	A_5	φ_5	A_7	φ_7	A_9	φ_9	A_{11}	φ_{11}
v_1	335.4	0	1.27	-43	7.77	171	3.39	-24	0.84	130	0.42	120
v_2	337.4	120	1.28	-2	7.9	53	2.54	104	1.41	138	0.42	-5
v_3	333.4	240	0.3	278	8	291	2.92	-216	0.84	158	0.14	212

192

193 On no load operation, voltages are initially slightly perturbed with values for THD respectively 2,6%, 2,5% and 2,5% and
 194 unbalanced with RMS values $V_{1eff}=237.1V$, $V_{2eff}= 238.5V$ et $V_{3eff}= 235.7V$. When currents before filtering, in Fig.9, pass through
 195 the large impedances $Z_{a,b,c}$ having different values, it leads to large unequal voltages drops that induce unbalance on the voltages
 196 $v_{a,b,c}$ at the P_{CC} in Fig.10.



197

Fig.9. Source currents and v_a voltage before filtering obtained by simulated model (50V-5A-5ms/div)

Fig.10. Voltages at P_{cc} and i_{sa} source current before filtering obtained by simulated model (50V-5A-5ms/div)

198 The RMS value V_f of the fundamental of each voltage decreases because of the voltage drop then the THD (27) increases for the
 199 same harmonic component V_h and voltages become highly perturbed see Table.2.

200

$$THD\% = \frac{\sqrt{\sum_{k=2}^{\infty} V_k^2}}{V_f} = \frac{V_h}{V_f} \quad (27)$$

201

4.1.b Experimental model

202 In figure 11.a below, we present the test bench which shows, in part A, the inverter with its control based on Dspace 1005 card of
 203 a DSPACE prototyping system associated with a FPGA card for the generation of the switching pattern. Next, the filter reactor in
 204 part B, afterwards the load in part C and the **measurement** and acquisition devices in part D. Results are recorded with Teledyne
 205 Lecroy (wavesurfer 3024, 200Mhz, 4Gs/s) oscilloscope and Fluke 438B device.

206 **Figure.11.b illustrates the steps for experimental test. Voltages supply $v_{a,b,c}$ and load currents $i_{L a,b,c}$ are measured as analog**
 207 **inputs and data are transferred to the simulink model via the card DS2004 A/D converter and DS4003 I/O card dedicated for**
 208 **communication with DS1005 Dspace board for real-time calculation. The control algorithm of the active filter is then designed in**
 209 **Matlab/ Simulink and the control signals obtained at the output of the simulink model are transferred to the FPGA card also via**
 210 **DS4003 card. The PWM modulation is carried out by the FPGA card and the output magnitude is adapted to command the drivers**

for IGBT semi-conductors. The regulation of current references and DC bus voltage is also performed in the simulink model compared with the real data of filter currents $i_{f\ a,b,c}$ and v_c feedback signals measured as analog inputs. The synchronization of voltages at the output of the filter and source voltages is automatically obtained because the control algorithm of the DCAP method generates reference currents synchronized with the fundamental of voltages extracted by a band-pass filter. It is no need for a PLL for this method.

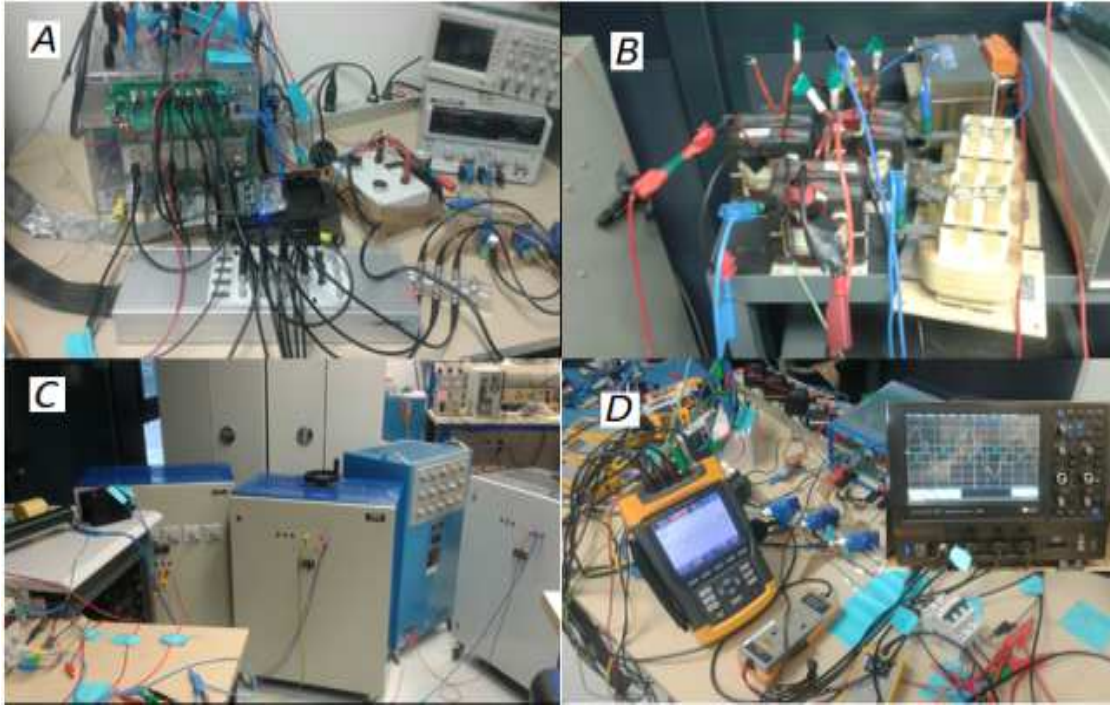


Fig.11.a Experimental test bench

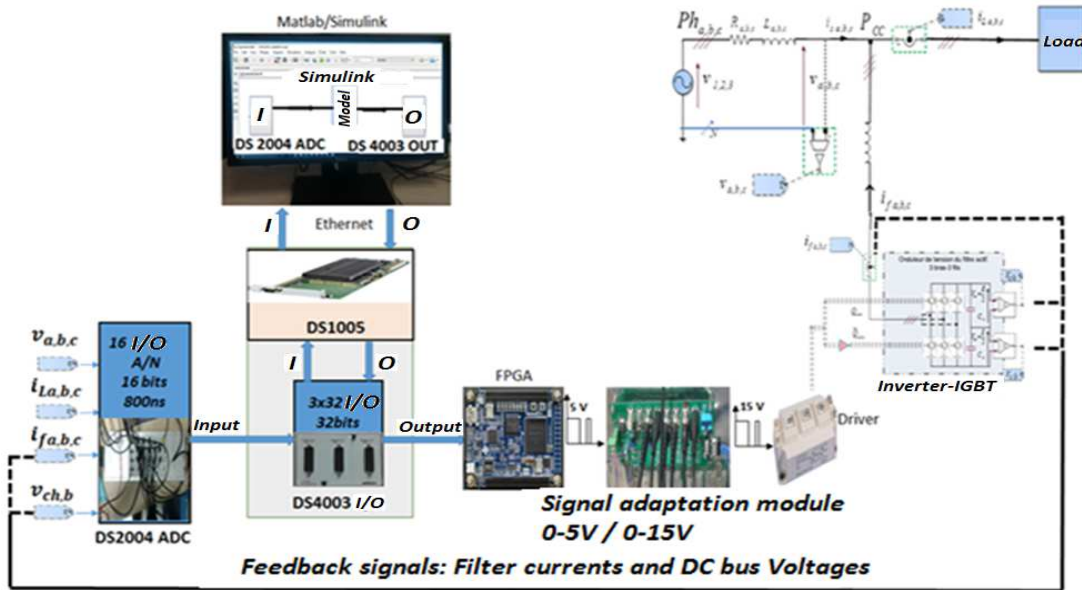


Fig.11.b Steps for experimental procedure.

Fig.12 shows results for polluted and unbalanced voltages at the P_{cc} before filtering. On its turn, the unbalanced voltage system induces unbalancing in currents system showing in Fig.13.

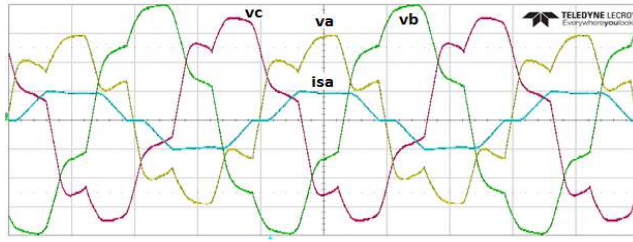


Fig.12. Experimental results before filtering for voltages at P_{cc} and i_{sa} source current (50V-5A-5ms/div).

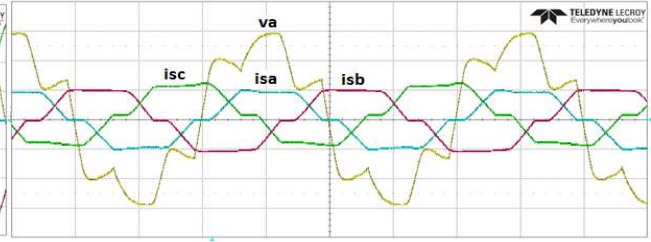


Fig. 13. Experimental results before filtering for source currents and v_a voltage (50V-5A-5ms/div)

Table.2 presents numerical values for unbalanced voltages source THD and RMS and Table.3 presents numerical values for unbalanced source current THD and current RMS for both experimental and simulated models before filtering.

Table.2: Numerical values of THD and voltages RMS before filtering obtained from experimental and simulated models

Voltages at P_{cc}		v_a	v_b	v_c
Experimental	THD %	25.8	13.3	17.8
Simulated	THD %	25.4	13.3	17.7
Experimental	RMS (V)	79	108	96.5
Simulated	RMS (V)	79	106.4	95.3

Table3: Numerical values of THD and currents RMS before filtering obtained from experimental and simulated models

Source currents		i_{sa}	i_{sb}	i_{sc}
Experimental	THD %	14.2	12.9	14.1
Simulated	THD %	14.4	13.3	14
Experimental	RMS (V)	3.6	3.953	3.764
Simulated	RMS (V)	3.617	3.953	3.788

The current unbalance factor UF before filtering is measured 4.9% on the diagram showing in Fig.14 according to the European standard EN 50160 based on $UF\% = \frac{X_i}{X_d}$ where X_d and X_i are the direct and inverse-sequence components of voltages. RMS values of source currents are also presented on this Fig.14. Measures are done with Fluke 438 device.

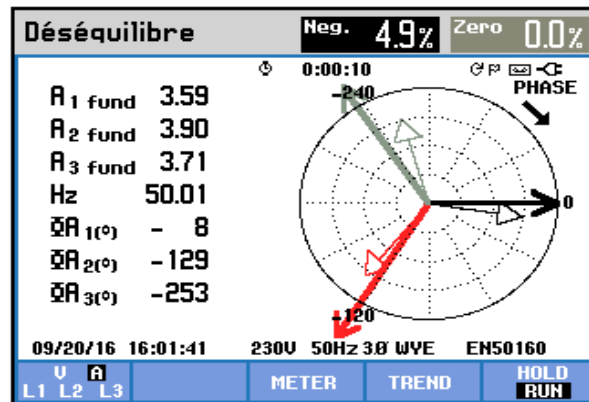


Fig.14. Current unbalance diagram before filtering

At this step of work it is shown that the simulated and experimental models before filtering ensure the same performances in terms of currents waveforms and numerical values for THD_i and RMS given in Table.2 and Table.3. Then, the effectiveness of simulated model under polluted and unbalanced supply voltages is demonstrated and that permit to conduct effective comparison after filtering between the different methods mentioned above.

4.2 Comparison of performances *after filtering* between DCAP and existing methods

Experimentation is carried out under perturbed supply voltages as shown in Fig.12 above. Fig. 15. shows the system of currents at the source referenced to the voltage v_a of the phase a. After filtering, the perturbation of voltages is reduced. Figure 15.a1, Fig.15.b1, Fig.15.c1 and Fig.15.d1 show the experimental results for the four methods after filtering compared with their associated simulation results in Fig.15.a2, Fig. 15.b2, Fig.15.c2 and Fig.15.d2.

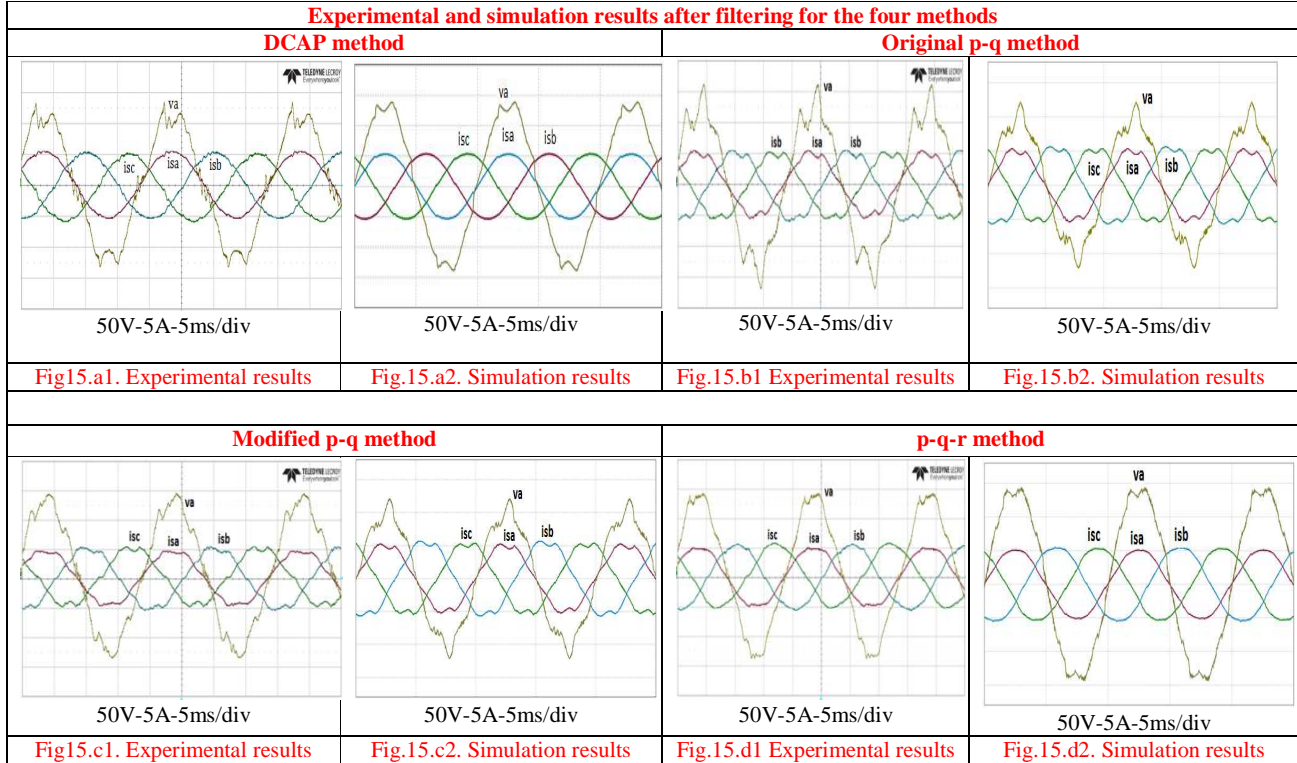


Fig. 15. System of currents at the source referenced to the voltage v_a of the phase a.

Table.4. Comparison of current THD and RMS values for all methods after filtering

Method		Experimental Source currents			Simulation Source currents		
		i_a	i_b	i_c	i_a	i_b	i_c
DCAP	THD _i %	3.2	3.3	3.9	2.8	2.7	3
	RMS (A)	3.83	3.77	3.79	3.78	3.78	3.78
Original p-q	THD _i %	7.8	8.4	8.3	7.8	8.1	8
	RMS (A)	3.61	3.9	3.7	3.62	3.95	3.8
Modified p-q	THD _i %	6.8	8.2	9.8	8	8	8
	RMS (A)	3.57	3.92	3.75	3.6	4	3.8
p-q-r	THD _i %	5.2	4.2	6.4	5	4.5	6.2
	RMS (A)	3.57	3.89	3.75	3.63	3.93	3.82

Results given by Fig.15 show that for the DCAP the currents system is sinusoidal despite the perturbation on voltages at the P_{cc} for both experimental Fig15.a1 and simulation Fig15.a2 results. For the p-q method Fig.15.b1 and Fig.15.b2 and for the modified p-q method Fig.15.c1 and Fig.15.c2 currents are distorted after filtering. These two methods do not achieve sinusoidal currents

under perturbed voltages. For the p-q-r theory in Fig.15.d1 and Fig.15.d2, currents are better than those provided by the p-q and modified p-q but there are not sinusoidal. Table.4 gives numerical values for current THD and current RMS for each phase for both experimental and simulation results.

In term of currents system harmonic perturbation, Fig.16 gives the comparison between current *THD* for the three-phase and for the four methods and that shows that the DCAP method has the lower distortion on the three phases. It is better than all methods.

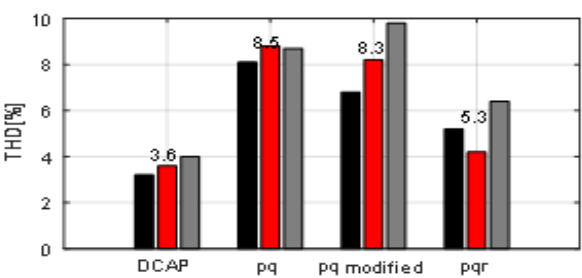


Fig.16 Comparison of performance for current THD

Regarding the balance of currents, the unbalance factor is measured according to EN 50160 with Fluke device and Fig.17 shows the unbalance diagram results after filtering for only the two methods DCAP in Fig.17.a and p-q-r in Fig.17.b. The value of the negative-sequence component is displayed and has as value of 0.8% for the DCAP method and 5.5% for the p-q-r method.

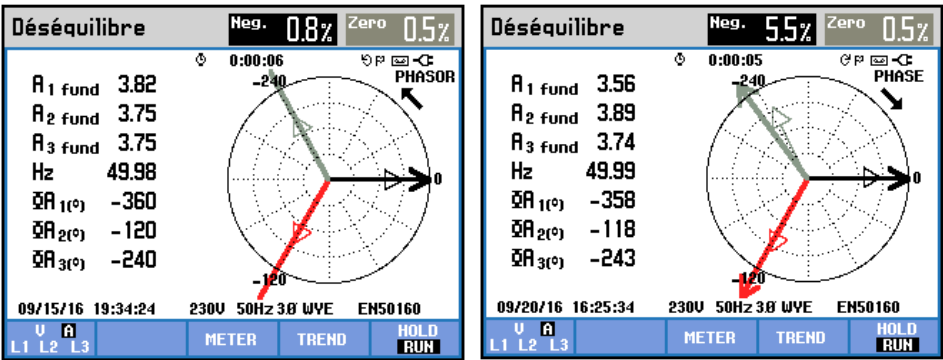


Fig.17.a) DCAP method

Fig.17.b) p-q-r Method

Fig.17. Unbalance diagrams for DCAP and p-q-r after filtering

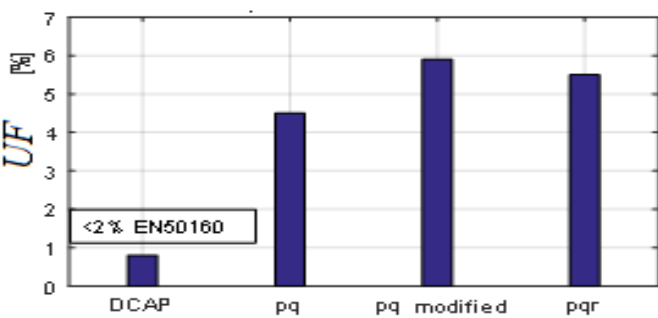


Fig. 18 Comparison of performance for Unbalance Factor

A comparison of performances regarding the unbalance factor $UF\%$ in Fig.18 is performed for all studied methods. It is shown that only the DCAP meet with standards recommendation with an $UF < 2\%$.

A summary of performances for the studied methods is presented on Tab.5

Table.5 Summary of performances for all methods based on experimental results

<i>Method</i>	<i>Sinusoidal current or low THD</i>	<i>THD_i % (Average)</i>	<i>Balance of currents system according to international standard</i>	<i>UF %</i>
<i>DCAP</i>	<i>Yes</i>	<i>3.5</i>	<i>Yes</i>	<i>0.8</i>
<i>Original p-q</i>	<i>No</i>	<i>8.2</i>	<i>No</i>	<i>4.5</i>
<i>Modified p-q</i>	<i>No</i>	<i>8.3</i>	<i>No</i>	<i>5.9</i>
<i>p-q-r</i>	<i>Medium</i>	<i>5.3</i>	<i>No</i>	<i>5.5</i>

5. Conclusion

In this work we have presented a new control method for active filter application taking into account distorted and unbalanced supply voltages. This method is based on a new definition of desired active current in the source formulated as function of the active power and the fundamental voltages. We have showing that in the presence of a highly disturbed and unbalanced network, the standard methods such as the p-q method, the modified p-q method and the p-q-r can not perform a sinusoidal current at the source side and do not make it possible to satisfy the constraints dictated by the standard. The proposed DCAP method, object of patent application, can provides sinusoidal currents on the source side and an $UF < 2\%$ that meet with the international standard in addition to the lowest THD of current compared to the standard methods . The model of the proposed control strategy is validate by simulation and experimental work and these performances are compared to three other methods by the way of simulation and experimentation. It is shown the superiority of the DCAP strategy over the existing standard ones. This general method can be deployed on single-phase or multiphase systems with 3 or 4 wires. It's easy to implement and does not require PLL. We believe that it can provide solutions in the case of isolated or embedded systems.

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