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# Reconfigurable 2D/0D *p-n* Graphene/HgTe Nanocrystal Heterostructure for Infrared Detection

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**Abstract:** Nanocrystals are promising building blocks for the development of low-cost infrared optoelectronics. Gating a nanocrystal film in a phototransistor geometry is commonly proposed as a strategy to tune the signal to noise ratio by carefully controlling the carrier density within the semiconductor. However, the performance improvement has so far been quite marginal. With metallic electrodes, the gate dependence of the photocurrent follows the gate-induced change of the dark current. Graphene presents key advantages: (i) infrared transparency that allows back-side illumination, (ii) vertical electric field transparency and (iii) carrier selectivity under gate bias. Here, we investigate a configuration of 2D/0D infrared photodetectors taking advantage of a high capacitance ionic glass gate, large scale graphene electrodes, and a HgTe nanocrystal layer of high carrier mobility. The introduction of graphene electrodes combined with ionic glass enables to reconfigure selectively the HgTe nanocrystals and the graphene electrodes between electrons (*n*) and holes (*p*) doped states. We unveil that this functionality enables to design a 2D/0D *p-n* junction that expands throughout the device, with a built-in electric field that assists charge dissociation. We demonstrate that in this specific configuration, the signal to noise ratio for infrared photodetection can be enhanced by two orders of magnitude, and that photovoltaic operation can be achieved. The detectivity now reaches  $10^9$  Jones while the device only absorbs 8% of the incident light. Additionally, the time response of the device is fast ( $<10 \mu\text{s}$ ) which strongly contrasts with the slow response commonly observed for 2D/0D mixed dimensionalities heterostructures, where larger photoconduction gains come at the cost of slower response.

**Keywords:** HgTe, narrow band gap nanocrystals, infrared detection, gate induced diode, graphene.

& these two authors have equal contributions

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Among possible applications for colloidal nanocrystals (NCs), low cost short-wave and mid-wave infrared photodetectors are raising more and more interests.<sup>1,2</sup> HgTe appears as the most tunable material to address these ranges of wavelength, thanks to the semimetal nature of the bulk material.<sup>3,4</sup> Significant progresses over the recent years have transformed the concept from an infrared (IR) absorbing material to high performance detectors with near unity absorption<sup>5,6</sup> or dual color detection.<sup>7,8</sup> With progress in mastering the fabrication and understanding of this material, HgTe has also been used as a platform for IR integrated photonics,<sup>9,10</sup> gas sensing<sup>11</sup> or electronics.<sup>12</sup> There are nevertheless many issues that still need to be addressed. Among them there are (i) the need for IR transparent electrodes and (ii) the energy band profile of HgTe-based devices which is not yet optimized for charge dissociation. In this paper, we propose a structure which addresses these two current limitations.

In the visible and near infrared ranges, oxides such as tin doped indium oxide are commonly used as transparent conductive electrodes. Such doped oxides present a plasmonic absorption<sup>13,14</sup> in the infrared that limits their use as transparent electrodes. To overcome this issue, it has been proposed to reduce the thickness of the oxide<sup>6,15</sup> but this comes at the price of a higher contact resistance. Alternatively, very thin layer of metal<sup>16</sup> or a metal grid array<sup>17</sup> have been used as charge collector. Again, there is a clear trade-off between absorption and contact resistance in these cases too. In the short and mid-wave infrared ( $\lambda < 5 \mu\text{m}$ ), the absorption of graphene is extremely limited (2%) and quickly after its discovery, graphene has appeared as a good candidate for electrode design.<sup>18–22</sup> The coupling of NCs with 2D materials has been mostly investigated in the so-called mixed dimensional Van der Waals heterostructure geometry,<sup>23</sup> revealing properties and functionalities that already lead to prospects for quantum electronics,<sup>24</sup> spintronics<sup>25,26</sup> and optoelectronics.<sup>27,28</sup>

For photodetection applications, the driving idea was to take advantage of both the high carrier mobility<sup>29</sup> of 2D materials and the strong tunable light absorption of NCs. Graphene devices functionalized with PbS NCs have thus led to exceptionally high responsivities<sup>27,30</sup> ( $> 10^6 \text{ A/W}$ ), and this concept was then extended to longer wavelengths using HgTe NCs.<sup>31</sup> To date, most of these pioneer works engineer their device architecture such that charge transport occurs only into the graphene conducting channel, while nanocrystals are simply used as light sensitizer that injects carriers into graphene. Hence, while this approach allows for large responsivity, it comes at the price of some key limitations such as the large dark current in the structure, and dramatically slow time response.<sup>32</sup> To reduce the dark current, it was later proposed to replace the gap-less graphene by MoS<sub>2</sub>.<sup>33</sup> Nevertheless, the device concept remains similar and some limitations persist, as in these mixed dimensional heterostructures the coupling between the 2D material and NCs occurs through a tunnel process and only the NCs in the very vicinity of the 2D material are actually contributing to the photoconduction.

Here, we propose a change of paradigm compared to previous 2D/0D photodetectors : we show that for 2D/0D devices, charge transport should involve the nanocrystal film to absorb more strongly the incident light. This requires to make the conduction within the nanocrystal array more efficient using a ligand exchange step to get high carrier mobility.

In this manuscript, we report about a way to induce *p-n* junction directly into the nanocrystal thin film by combining three key elements: (i) the unique electronic properties of graphene electrodes, (ii) the recent developments of HgTe NCs-based inks<sup>15</sup> leading to films with relatively high carrier mobility<sup>34</sup> ( $> 1 \text{ cm}^2\text{v}^{-1}\text{s}^{-1}$ ), and (iii) the use of high capacitance ionic glass gating technology<sup>35–37</sup> that enables to reach high carrier density and to spatially tune the doping profile through the whole 2D/0D heterostructure. Our study demonstrates that the built-in electric field of the *p-n* junction formed into the nanocrystal film allows for enhancing the charge dissociation and unlock photovoltaic functionality.

Devices based on vertical geometry have certainly led to the highest detection performance reported so far. However, the choice of the layers involved in the device currently obeys empirical laws and offers no post-processing tunability. In this sense, the planar transistor geometry offers more flexibility through gating. Gating of HgTe thin films has been obtained with a variety of gates from conventional dielectrics,<sup>11,34,38</sup> to electrolytes<sup>39,40</sup> and more recently using ionic glasses.<sup>37</sup> In the

conventional operation mode of a transistor, the drain source bias is low compared to the gate bias. This limits the charge dissociation compared to what is achieved in vertical geometry devices. In addition, even if the gate allows a certain tunability of the responsivity, the increase of the photocurrent is greatly balanced by the rise of the dark current.<sup>11,37,41</sup> There is however another operating mode for transistor, which is the diode mode. In this case, a large drain-source bias is used, inducing opposite carrier density (*ie* electron vs hole) for the drain and source electrodes. This mode has been extensively used to design gate-induced light emitting diodes,<sup>42–45</sup> in particular in organic compounds.<sup>46–48</sup> In this paper, we demonstrate that graphene-based electrodes combined with ionic glass gating offer an interesting platform for the design of gate-induced 2D/0D *p-n* junction. Graphene combines three key advantages compared to metallic electrodes: (*i*) infrared transparency which allows back-side illumination of the device. (*ii*) Graphene being a semimetal with a low density of state compared to metal, it is possible to tune the Fermi level below and above the Dirac point. This makes the design of carrier-selective electrodes (*ie* electron or hole extractor) possible. Last, (*iii*) graphene is thinner than its Thomas-Fermi screening length and thus partially transparent to the vertical electric field induced by the underneath gate. Consequently, the *p* and *n* parts of the gate-induced junction have a much larger spatial extension into the NC thin film compared to what is achieved with metallic electrodes. Such gate-induced 2D/0D *p-n* junction can be used to enhance the charge dissociation directly inside the HgTe nanocrystals array and boost the device photocurrent. We bring clear evidence that the behavior of the graphene-HgTe-graphene junction strongly differs from its equivalent based on gold contacts, and allow the design of reconfigurable heterojunctions, fast and improved photoresponse, and photovoltaic operation.

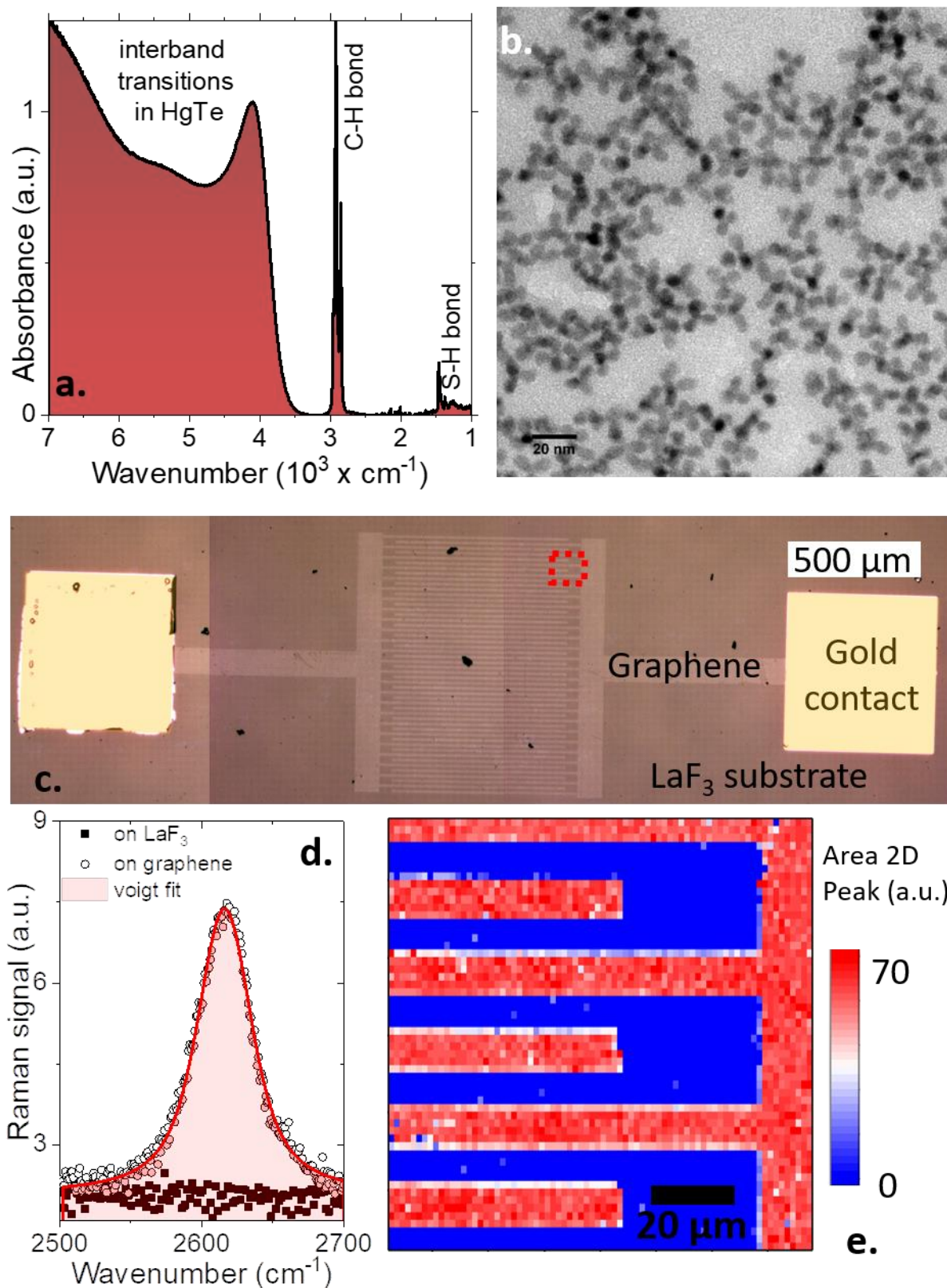
## Results and discussion

We start by designing a graphene-HgTe NC-graphene junction. To do so, we have grown HgTe NCs using the procedure provided by Keuleyan *et al.*<sup>49</sup> The obtained nanocrystals have a multipodic shape with a typical size around 8 nm, see **Figure 1b**. The particles present an interband-edge at  $4000\text{ cm}^{-1}$  ( $\approx 0.5\text{ eV}$  or  $2.5\text{ }\mu\text{m}$ ), according to their infrared spectrum, see **Figure 1a**. Previous investigation of the electronic structure of the material has revealed that the work function of the pristine material is equal to  $4.6\pm 0.1\text{ eV}$ , while the Fermi level is located in the upper part of the band gap ( $V_b - E_F = 0.3\text{ eV}$ ).<sup>50</sup>

The devices are constructed over a  $\text{LaF}_3$  substrate, which is an infrared transparent ionic glass (allowing back side illumination of the sample). This ionic glass is also used as a back-side gate dielectric. Under application of a potential difference over the two sides of the substrate, the  $\text{F}^-$  ions migrate in the bulk matrix, allowing the  $\text{LaF}_3$  substrate to present a larger gate capacitance than conventional or even high-*k* dielectrics.<sup>35,51</sup> The freezing temperature of the ions (below 180 K) remains much lower than the one of ion gel electrolyte, making it suitable for IR photodetection as recently demonstrated on HgTe NCs arrays<sup>37</sup> and atomically thin transition metal dichalcogenides layers.<sup>35,51</sup>

On the top of this  $\text{LaF}_3$  substrate, chemical vapor deposition (CVD) monolayer graphene is transferred and processed using laser lithography methods to create interdigitated electrodes (see **Figure 1c**) made of a total of 50 digits,  $750\text{ }\mu\text{m}$  long,  $10\text{ }\mu\text{m}$  wide and spaced by  $10\text{ }\mu\text{m}$  from its neighbors. The Raman 2D peak is used as an unambiguous fingerprint of the presence of graphene and allows mapping of the interdigitated pattern, see **Figure 1d** and **e**. The full width at half maximum of the 2D Raman peak is found to be  $46\text{ cm}^{-1}$  (**Figure 1d**), a value somewhat larger than in mechanically exfoliated monolayers suggesting<sup>52</sup> a contribution from nanoscale strain variations<sup>53</sup> arising from the transfer process onto the  $\text{LaF}_3$  substrate. Spatial mapping of the 2D peak is fully

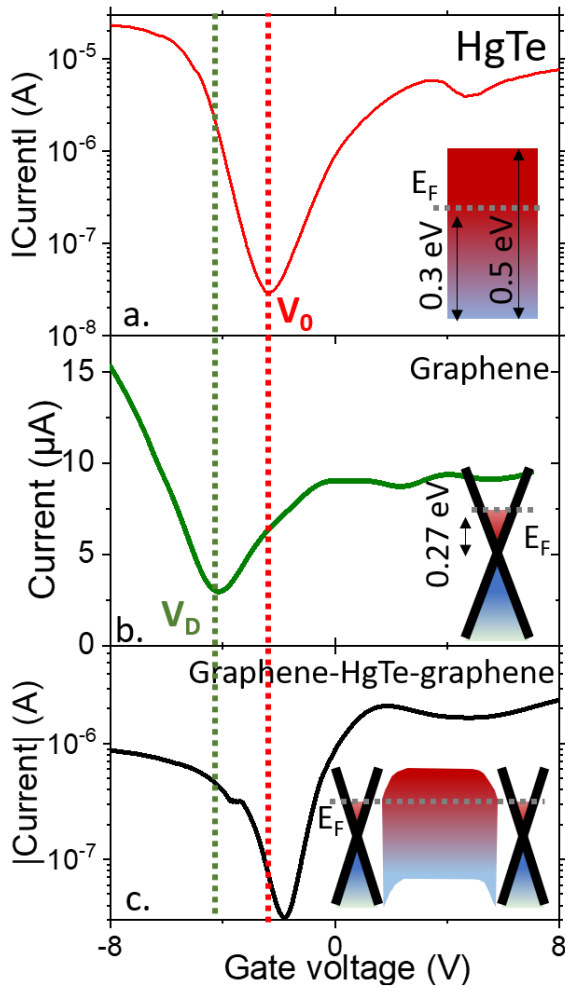
consistent with the targeted pattern (**Figure 1e**), confirming that graphene is fully etched between the digits.



**Figure 1 Material under consideration.** a. Absorption spectrum of HgTe NCs. b. Transmission electron microscopy image of HgTe NCs. c. Microscopy image of the graphene interdigitated electrodes on a  $\text{LaF}_3$  substrate. The area in the red square is mapped using Raman spectroscopy (laser wavelength = 633 nm) in part e. d. Raman spectra of the graphene electrode on  $\text{LaF}_3$ . e. Map of the intensity of the Raman 2D peak ( $2616 \text{ cm}^{-1}$ ) in the area of the graphene interdigitated electrodes.

To form a conductive array of HgTe NCs, we use a phase transfer method which aims at preparing a NC (photo)conductive ink. At the end of the procedure, HgTe nanoparticles are capped with short ligands (mercaptoethanol and HgCl<sub>2</sub> in this case<sup>15</sup>). Before building the HgTe-graphene junction, it is of utmost interest to reveal the transport properties of each material independently over the same LaF<sub>3</sub> gate to account for possible charge transfer between the material and the substrate. Metallic gold electrodes are used to build an HgTe device that presents a clear ambipolar behavior with a charge neutrality point ( $V_0$ ) appearing under negative gate bias at  $-2.3$  V, see **Figure 2a**. We can anticipate a  $n$ -doping of the material in absence of gate, which is consistent with previous photoemission measurements.<sup>50</sup> In the case of graphene deposited on LaF<sub>3</sub>, we build a Hall bar device used in four-probe configuration to measure the graphene intrinsic conductivity (Figure S1). The transfer curve of the bare graphene also reveals an ambipolar behavior with minimum of conductance at the Dirac point reached for a negative gate bias of  $V_D=V_{GS} = -4.2$  V, see **Figure 2b**. This suggests a partial electron transfer from the LaF<sub>3</sub> to the graphene which displaces the Fermi level above the Dirac point of graphene. From the gate bias associated to the Dirac point we can determine the relative position of the Fermi level with respect to the Dirac point thanks to the relation<sup>54</sup>  $E_F = \hbar v_F \sqrt{\frac{\pi C_{LaF_3}}{e} (V_G - V_D)} = 0.27$  eV with  $\hbar$  the reduced Planck's constant,  $v_F$  the Fermi velocity<sup>55</sup> equals to  $8 \times 10^5$  m.s<sup>-1</sup>,  $C_{LaF_3}$  the capacitance of the LaF<sub>3</sub> substrate<sup>37</sup> ( $0.4$   $\mu$ F.cm<sup>-2</sup>) and  $e$  the elementary charge.

Interestingly, the transfer curve of the graphene-HgTe-graphene heterojunctions (see **Figure 2c**) demonstrates a preserved ambipolar behavior with a charge neutrality point at  $V_{GS}=-1.8$  V  $\approx V_0$ . This value is very close to that of HgTe film contacted by gold electrode, see **Figure 2a**. The deposited film of HgTe NCs is typically 120 nm thick, see Figure S2. The switching of graphene electrode doping state is reflected in the transconductance curve by an inflection of the transfer curve at the Dirac point  $V_{GS}=-3.6$  V  $\approx V_D$ . From these transconductance measurements, we can describe the band alignment in the graphene-HgTe-graphene junction: the Fermi level lies above the Dirac point and in the upper part of the semiconductor nanocrystals, see **Figure 2c**. In such junction, the key difference with metallic contact comes from the gate-tunable nature of the graphene doping level. The gate will not only tune the position of the Fermi level within the HgTe film but also within the electrodes. This enables the formation of electron and hole extracting layers.



**Figure 2** Transport properties of HgTe NCs array and graphene. *a.* Transfer curve (drain current as a function of gate bias) for a HgTe NCs array on a  $\text{LaF}_3$  substrate and connected with gold electrode, at 220 K. The inset is a scheme of the pristine HgTe electronic structure *b.* Transfer curve for a graphene Hall bar on a  $\text{LaF}_3$  substrate, at 240 K. The inset is a scheme of the pristine graphene electronic structure. Both materials appear to be n-doped in absence of gate. *c.* Transfer curve for a HgTe NCs array on a  $\text{LaF}_3$  substrate and connected with graphene electrode, at 220 K. Scheme of the electronic structure of graphene/HgTe/graphene junction assuming that the Dirac point is located at 4.6 eV from the vacuum level<sup>64</sup> and that Fermi level is 0.27 eV above.

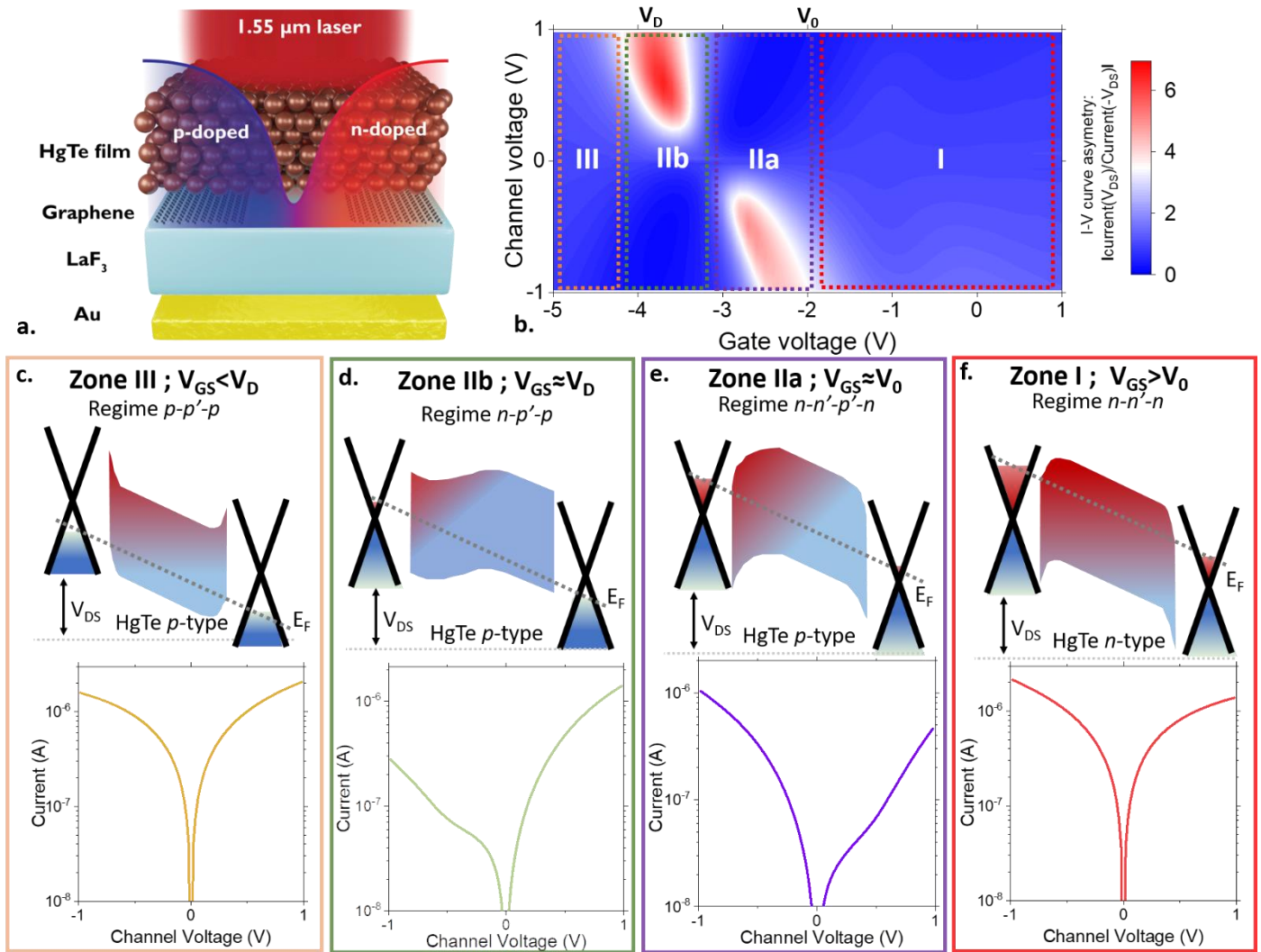
To better reveal the specificity of this 2D/0D heterostructure, we investigate in detail the graphene-HgTe-graphene current-voltage (I-V) characteristics dependence on ionic glass gate voltage  $V_{GS}$  (**Figure 3**, see also **Figure S3**). A schematic of the device is given in **Figure 3a**. We report, on **Figure 3b**, the charge transport phase diagram of the 2D/0D device. It emphasizes on the evolution of the asymmetry of the source-drain I-V curve (ratio of current at  $+V_{DS}$  bias over current under  $-V_{DS}$  bias) under the back-gate voltage  $V_{GS}$ .

Three different zones corresponding to distinct doping states of the graphene/HgTe heterojunctions can be discriminated: (i) For gate bias  $V_{GS} > V_0$  (zone I), exceeding the HgTe film charge neutrality point  $V_0$ , we observe a symmetric quasi-ohmic I-V behavior, see **Figure 3f**. Under such condition the Fermi level remains above the Dirac point for both electrodes, while the HgTe NCs film remains in the electron doping state, forming  $n-n'$  junctions at the graphene/HgTe NC interfaces. (ii) When the gate bias is in the range  $V_0 > V_{GS} > V_D$  (zone II), the I-V curve acquires a rectifying behavior, see examples on **Figure 3d** and **e**. On **Figure 3b.**, the high value of the current asymmetry (clearly



above 1) highlights the areas of occurrence (red part) of  $p$ - $n$  junctions, with two distinct heterojunction configurations that can be discriminated. Details on the origin of these two distinct  $p$ - $n$  junction regimes will be discussed below. Finally (iii), under large hole injection, when  $V_D > V_{GS}$  (zone III), the two graphene electrodes and the HgTe film become hole-rich, and the I-V curve recovers its quasi-ohmic behavior, reflecting the formation of  $p$ - $p'$  junction, see **Figure 3c**.

We now discuss the ionic glass assisted doping mechanisms that enable the formation of the 2D/0D  $p$ - $n$  junctions in the zones IIa and IIb. First, it is worth pointing that, because of the large capacitance of  $\text{LaF}_3$  substrate, applying moderate drain-source bias may lead to distinct vertical gate bias conditions near the source and the drain electrodes. For gate bias voltage close to charge neutrality points of HgTe or graphene, this induce electron and hole doping in the vicinity of each electrode.



**Figure 3** Evidence for gate induced  $p$ - $n$  junction in the graphene/HgTe/graphene junction. **a.** Scheme of the field effect transistor under consideration where the gate is made of a  $\text{LaF}_3$  ionic substrate, the drain and source electrodes are made of interdigitated graphene electrodes and the channel is made of HgTe NCs array. **b.** 2D color map of the asymmetry of the I-V curve, quantized through the ratio of the current under a given bias and the current under the opposite bias, as a function of the channel voltage between the source and drain electrodes, and gate voltage  $V_{\text{GS}}$ . The regions in red correspond to the existing domains for a  $p$ - $n$  junction within the graphene/HgTe/graphene junction. **c.** Band alignment of the graphene/HgTe/graphene junction in the  $p$ - $p'$ - $p$  configuration and the associated quasi-ohmic I-V curve, in the zone III corresponding to the orange square in the phase diagram of part b. **d.** Band alignment of the graphene/HgTe/graphene junction in the  $n$ - $p'$ - $p$  configuration and the associated rectifying I-V curve, in the zone IIb corresponding to the green square in the phase diagram of part b. **e.** Band alignment of the graphene/HgTe/graphene junction in the  $n$ - $n'$ - $p'$ - $n$  configuration and the associated rectifying I-V curve, in the zone IIa corresponding

to the purple square in the phase diagram of part b. f. Band alignment of the graphene/HgTe/graphene junction in the  $n-n'$  configuration and the associated quasi-ohmic  $I$ - $V$  curve, in the zone I corresponding to the red square in the phase diagram of part b. All data are acquired at 220 K.

A first  $p$ - $n$  junction state appears when  $V_{GS}$  gets closer to  $V_0$  (zone II.a, see figure **Figure 3e**), this corresponds to  $p$ - $n$  junction formation between electron-doped graphene electrodes and hole-doped HgTe NCs array, forming two back-to-back  $p$ - $n$  junctions. A strong current asymmetry appears while increasing  $V_{DS}$  amplitude (with value of the asymmetry clearly above 1 in the red area of the graph), highlighting the formation of a  $p$ - $n$  junction expanding throughout the HgTe film. Indeed, in the case of ambipolar semiconducting HgTe NC array, for gate bias below but close to the charge neutrality point  $V_{GS} < V_0 \approx -2$  V, applying a negative  $V_{DS}$  voltage on the drain electrode results in switching back the HgTe doping state from  $p$  to  $n$  doped state. This creates a  $p$ - $n$  junction within the HgTe thin film, with a  $p$  doped state at the source side and  $n$  doped state at the drain side.

The second  $p$ - $n$  junction state (zone IIb) is triggered once  $V_{GS}$  is decreased down to  $V_D$ . There, applying source drain bias voltage results in Fermi level positioning on different sides of the Dirac point for the drain and source graphene electrodes (see band diagram in **Figure 3d**). One electrode gets electron rich while the other is hole rich, resulting in  $n$ - $p$  diode on the drain side.

Hence, the observation of the  $p$ - $n$  junction is made possible by the combination of the high capacitance gate from the  $\text{LaF}_3$ , the tunable charge carrier polarities of graphene and HgTe NC array, and the low electric screening of graphene. Electric charge density  $\Delta q$  at the graphene/ $\text{LaF}_3$  interface of the source (resp. drain) electrode is defined as  $\Delta q = C_{TOT} \times V_{GS}$  (resp.  $\Delta q = C_{TOT} \times V_{DS}$ ). The total capacitance is defined as  $1/C_{tot} = 1/C_{\text{LaF}_3} + 1/C_Q$ . Here  $C_{\text{LaF}_3}$  is the ionic glass effective capacitance, which at low frequency has been quantified by impedance spectroscopy to lay in the range  $C_{\text{LaF}_3} \approx 0.4 \mu\text{F} \cdot \text{cm}^{-2}$  at 220 K. The quantum capacitance  $C_Q$  captures the band filling of graphene that results from its low density of states, and can be approximated by  $C_Q \approx e^2 D(E_F)$ , with  $e$  the electron charge and  $D(E_F)$  the graphene density of states at the Fermi energy.<sup>56-58</sup> Experimental studies estimated  $C_Q$  within 3 to 10  $\mu\text{F} \cdot \text{cm}^{-2}$  depending on the graphene doping level. Given these orders of magnitude for the capacitances, approximately 90% of the charge induced by the ionic substrate are present on the top of the graphene, while no charge will be present in the case of metal electrodes. This low screening propagates the spatial extension of the  $p$ - $n$  junction above the electrodes. Graphene thus plays a dual role: not only it allows carrier selectivity which first enables the formation of the  $p$ - $n$  junction, but it also favors the spatial extension of the junction, leading to a higher volume of the HgTe film exposed to the gate-induced electric field.

We have revealed in this device an interesting operating point where a 2D/0D  $p$ - $n$  junction can be formed. In this case, a large electric field should appear at the interface between the hole and electron rich areas, which should enhance the charge dissociation. To confirm such predictions, we probe in **Figure 4b** the photocurrent in the graphene-HgTe-graphene junction illuminated by a short-wave infrared laser at 1.55  $\mu\text{m}$  (*i.e.* above the band-edge of the HgTe nanocrystals). The curve presents a well-defined maximum of photocurrent in the range of gate bias corresponding to the existence of  $p$ - $n$  junction formation (same as region II, introduced previously in **Figure 3b**). This behavior dramatically differs from the one observed for the same HgTe film, with the same  $\text{LaF}_3$  gate but using gold metallic electrodes, see **Figure 4a**. In this case the responsivity is minimal at the minimum of conductance of HgTe. Conventional phototransistors actually display this kind of behavior, where photocurrent scales as the dark current. In other words, in usual (*i.e.* based on

metallic electrodes) phototransistors, applying gate bias enhances the photocurrent, but barely improves the signal-to-noise ratio. This behavior has been observed for many nanocrystals such as CdSe,<sup>59</sup> or even HgTe with various dielectrics<sup>34</sup> and electrolytic gates.<sup>41</sup> Here with graphene electrodes combined with high capacitance substrate, we can overcome this limitation. There is a gate bias window where photoresponse is maximized while the dark current is minimized, promoted by the formation of  $p$ - $n$  junction through the 2D/0D heterostructure. Such regime is extremely promising for photodetection, as later discussed in the text.

Beyond the presence of an extremum in the responsivity magnitude as a function of gate bias, matching with the minimum of dark current, we can notice that the photoresponse is much higher under negative gate bias, *ie* under hole injection, compared to the value obtained under electron injection condition. This actually results from the initial  $n$ -type nature of the HgTe. By injecting holes in the material, it gets more intrinsic which reduces the recombination pathway for the minority carriers and thus extend their lifetime, leading to a larger photoconductive gain.

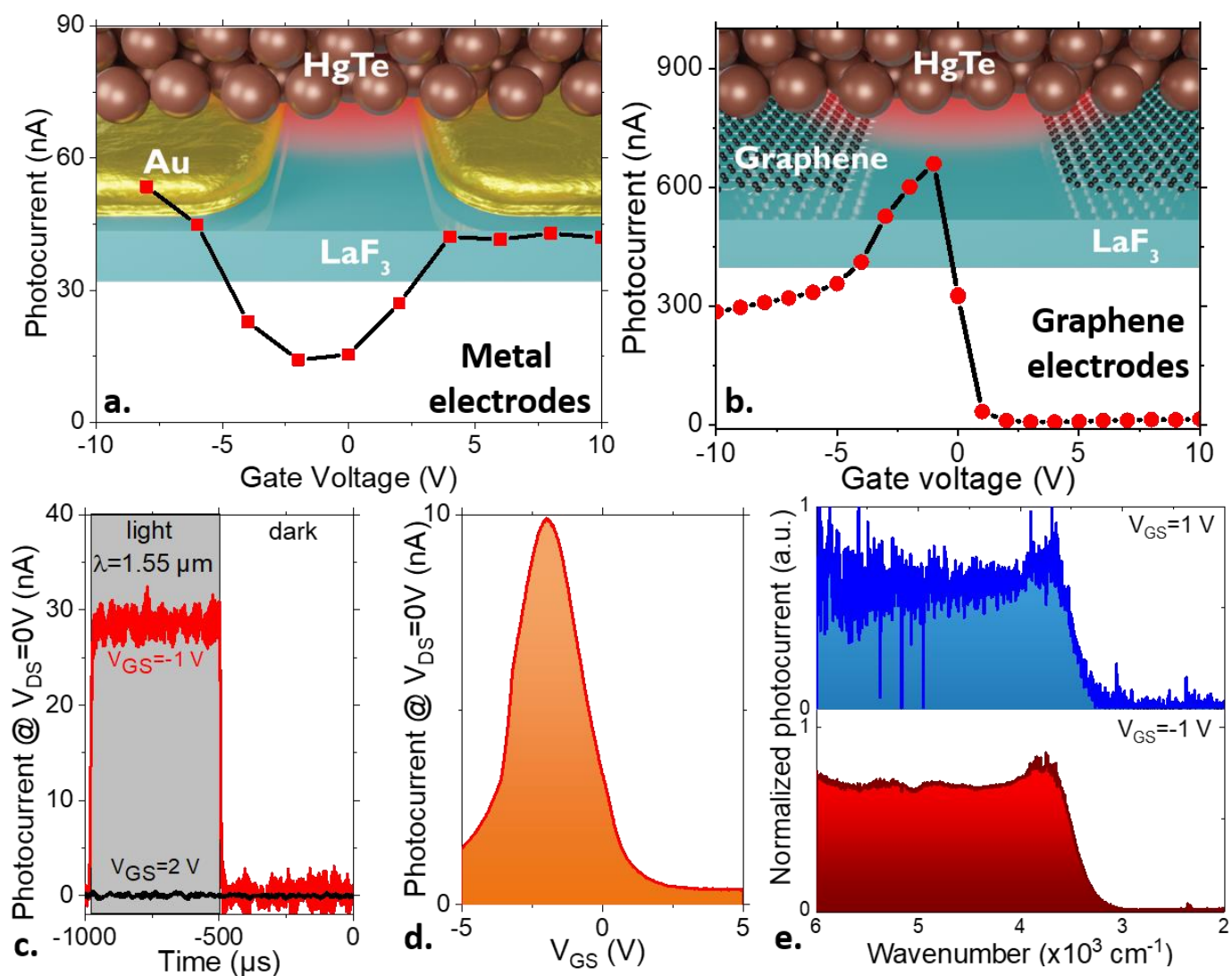


Figure 4 Photoresponse of the graphene/HgTe/graphene junction. a. Photocurrent as a function of gate bias for gold/HgTe/gold junction. The top part is a scheme of the device. b. Photocurrent as a function of gate bias for graphene/HgTe/graphene junction. The top part is a scheme of the device. c. Photocurrent under 0 V drain source bias as the light ( $\lambda=1.55 \mu\text{m}$ ) is turned on and off for two values of the gate bias. d. Photocurrent under 0 V, while the light is chopped at 1 kHz, as a function of the gate bias. e. Photocurrent spectra of the graphene/HgTe/graphene junction for two different gate bias. All data are acquired at 220 K.

The photovoltaic operation mode of the  $p-n$  junction, hallmark of the extension of the  $p-n$  junction into the NCs array, is further confirmed by the investigation of the photocurrent under zero applied electric field. Under positive gate bias, where no  $p-n$  junction exists, there is no photocurrent under zero drain-source bias, see **Figure 4c** and S5. Once the gate bias is applied within the window enabling the  $p-n$  junction formation, we observe a clear photo-signal even in absence of applied bias, which is the signature of built-in electric field. The magnitude of the photocurrent modulation at 1 kHz under zero drain source bias is given in **Figure 4d** and follows pretty well the data obtained under DC condition in **Figure 4b**. While the magnitude of the photocurrent spectrum also follows the same trend with gate bias, the shape of the spectral response remains unchanged for all gate bias, see **Figure 4e** and **S4**. In particular it is worth noticing that in spite of the large gate electric field associated with  $\text{LaF}_3$ , no bleach of the band edge transition is observed.<sup>40</sup> The  $p-n$  junction is formed while the electron and hole carrier densities are kept well below 1 carrier per dot, which is critical to avoid any excessive increase of the dark current. More qualitatively, we can estimate the carrier density in the  $n$  and  $p$  area under 1V of drain source bias, assuming a balanced  $p-n$  junction. The sheet charge density induced by the gate is given by  $C_{\text{LaF}_3} \cdot V_{\text{DS}} / 2e$  which corresponds to  $1.25 \times 10^{12}$  carrier. $\text{cm}^{-2}$ . If we assume the density is spread over the whole film thickness, this corresponds to a volume density of  $1 \times 10^{17}$  carriers. $\text{cm}^{-3}$ . We can estimate that the particle density is  $2.4 \times 10^{18}$  nanocrystals. $\text{cm}^{-3}$ , assuming a spherical shape for HgTe with a radius of 4 nm and a random close packing (film density of 0.64). This means that the average gate induced doping density is  $4 \times 10^{-2}$  carrier per nanocrystal. This is far below 1 as expected from the photocurrent spectrum (*i.e.* no evidence for intraband absorption).

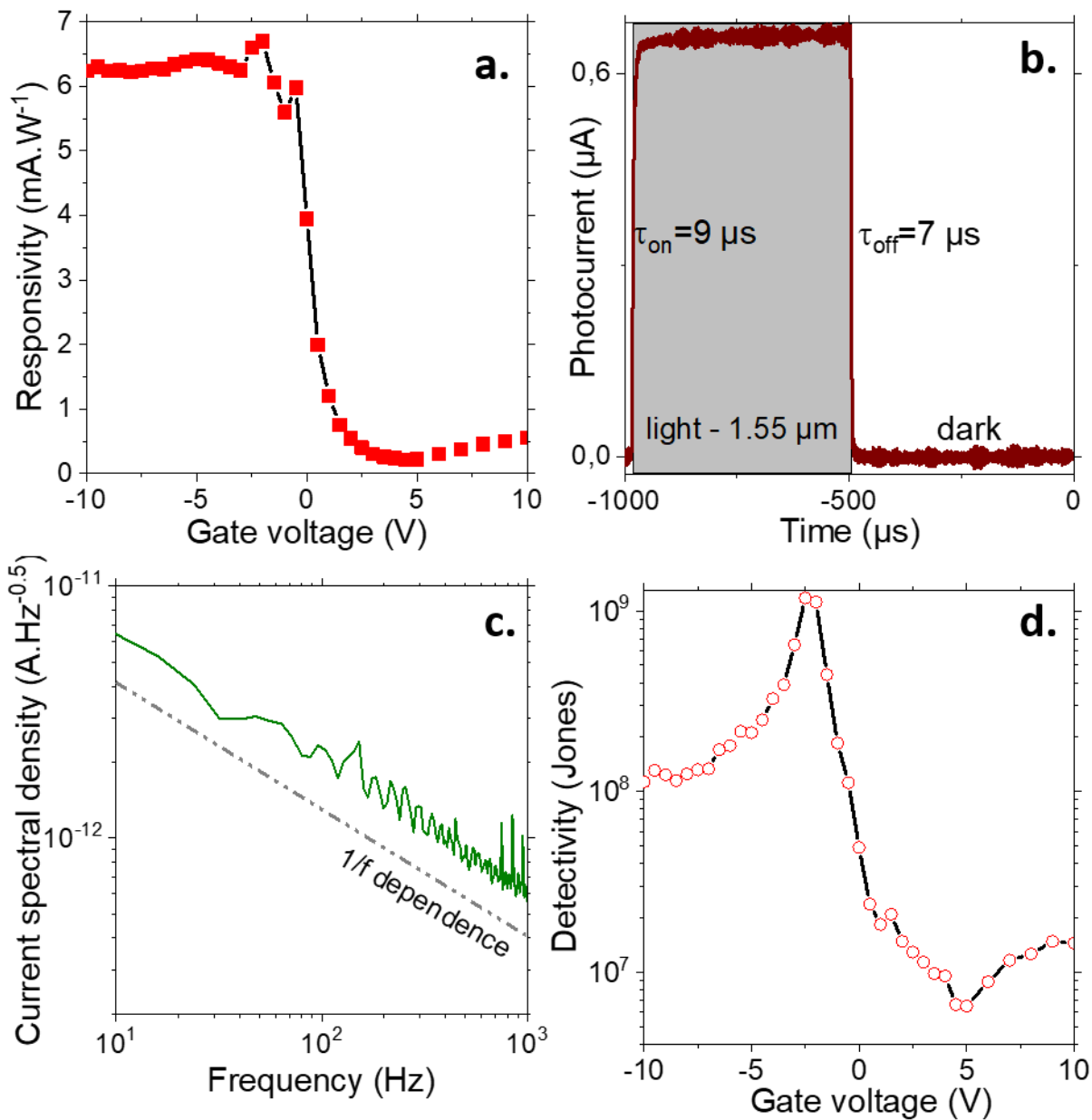


Figure 5 Infrared photodetection performance of the graphene/HgTe/graphene junction. a. Responsivity at 1.55  $\mu\text{m}$  of the graphene/HgTe/ graphene junction, as a function of the applied gate bias. b. Temporal response of the graphene/HgTe/ graphene junction, under -1 V of gate bias. Dynamics appears to be limited by experimental setup. c. Current spectral density as a function of signal frequency, under -2.5 V of gate bias. The noise appears to be  $1/f$  limited. d. Detectivity (*ie* signal to noise ratio) at 1 kHz and 220 K as a function of the applied gate bias for the graphene/HgTe/ graphene junction.

In the last section of the paper, we investigate the potential of this gate-induced  $p$ - $n$  junction for infrared photodetection. Under moderate excitation power (*ie* irradiance of  $133 \mu\text{W.cm}^{-2}$  corresponding to  $\approx 1 \mu\text{W}$  of incident light on the sample) the responsivity reaches 6 mA/W under negative gate bias, see **Figure 5a**. The noise in the  $p$ - $n$  junction appears to be  $1/f$  limited, as commonly observed for NC array,<sup>60,61</sup> see **Figure 5c**. The detectivity clearly presents a maximum  $1.2 \times 10^9$  Jones for gate bias where the  $p$ - $n$  junction is formed (see also **Figure S8**). This is typically 2 orders of magnitude larger than the value obtained for the same film in absence of  $p$ - $n$  junction. This remains one order of magnitude weaker than the best value reported for HgTe based nanocrystals in vertical geometry.<sup>6,8</sup> However, this limitation mostly results from the film thickness ( $\approx 120 \text{ nm}$ ) which only absorb 8% of the incident light. For sake of comparison, films with thickness

above 500 nm and absorbing 30 % of the incident light and based on the same particles and surface chemistry achieved a detectivity of  $3 \times 10^9$  Jones.<sup>15</sup> This indicates that charge dissociation is more efficient in the planar *p-n* junction developed here than for current stack of nanocrystals used in HgTe nanocrystals based photodiode. Introduction of plasmonic resonators may be a viable way to solve this issue.<sup>5,6</sup> The time response of the device has been found to be below 10  $\mu\text{s}$  (**Figure 5b**), with a weak incident power dependence (figure S6 and S7), which actually appears to be fully limited by the experimental setup. Note that the fast time response of our device is not driven by the large gate capacitance but rather by the capacitance of the nanocrystals film, which relates to HgTe thickness, its dielectric constant and the device area. Overall, our Graphene/HgTe nanocrystals heterostructures demonstrates a fast time response below a few  $\mu\text{s}$  which is enough for all video rate application. Such fast response strongly contrasts with the slower response commonly observed in quantum dots arrays coupled to 2D materials, usually locked in the tens of ms<sup>31</sup> or even slower.<sup>32</sup> In such usual 2D/0D device geometry, absorption usually occurs in the quantum dot, and the carrier with the lowest band offset with respect to the 2D material is transferred to the 2D layer, while the second carrier stays in the quantum dot. This photogating process leads to very large photoconduction gains and to large response but also hamper the current dynamics that gets driven by the recombination of the carrier trapped in the quantum dots array. In other words, the photogain increase is balanced by the drop of the device bandwidth. In our 2D/0D *p-n* junction geometry, the enhancement of the signal to noise ratio is obtained without degradation of the device bandwidth.

In table S1, we have compared the performances of the device proposed here with the one reported in the literature based on infrared nanocrystal using various technology for the device (hybrid 0D-2D, photoconductor, phototransistor, and photodiode). Beyond the fast time response, our device offers an interesting tradeoff in term of signal to noise ratio and bandwidth, quantized in table S1 through the ratio of the detectivity over the time response. The device performance remains on the other hand, weaker than the best photodiode and this is mostly the result of the reduced absorption. The control of the light matter coupling<sup>5,6</sup> is clearly a direction that need to be pushed for future work.

## Conclusions

We have designed a phototransistor architecture based on a graphene-HgTe nanocrystals-graphene reconfigurable *p-n* junction. The formation of the *p-n* junction is made possible by the combination of the large gate capacitance of the infrared transparent  $\text{LaF}_3$  ionic glass, the carrier selectivity of graphene and its partial transparency to vertical electric field. While previous phototransistors based on metallic electrodes were leading to marginal improvement of the signal to noise ratio under applied gate bias, the graphene electrodes/ionic glass combination enables a significant enhancement of the signal to noise ratio. The formation of a *p-n* junction, which widely expands over the nanocrystal film thickness thanks to the graphene low screening capability, allows to improve the device detectivity by more than two orders of magnitude under gate bias application, and to demonstrate extremely fast photoresponse time. The photovoltaic operation of the 2D/0D *p-n* junction is clearly exhibited by the presence of photocurrent under zero drain source bias. Hence, while combining the high doping density provided by ionic glass, the electric field transparency and tunable charge carriers of graphene, and the large scale processability of nanocrystals, our 2D/0D device offers the possibility of being switched electrostatically from an IR phototransistor to a photovoltaic device. Finally, as *p-n* junction is the most ubiquitous component of modern electronics, the prospects opened by this 2D/0D *p-n* junction technology embrace a wider scope of application including integrated circuits, detectors, photovoltaics, and appears as highly promising approach to revisit the gate-induced light emitting diode.

## Methods

**Chemicals for nanocrystal synthesis:** Mercury chloride ( $\text{HgCl}_2$ , Strem Chemicals, 99%), Tellurium powder (Te, Sigma-Aldrich, 99.99%), trioctylphosphine (TOP, Cytek, 90%), oleic acid (Sigma, 90%), oleylamine (Acros, 80-90%), dodecanethiol (DDT, Sigma-Aldrich, 98%), chloroform (Carlo Erba), ethanol absolute anhydrous (Carlo Erba, 99.9%), methanol (Carlo Erba, 99.8%), toluene (Carlo Erba, 99.3%), All chemicals are used as received, except oleylamine which is centrifuged before used. **Mercury compounds are highly toxic. Handle them with special care.**

**1 M TOP:Te precursor:** 2.54 g of Te powder is mixed in 20 mL of TOP in a three neck flask. The flask is kept under vacuum at room temperature for 5 min and then the temperature is raised to 100 °C. Furthermore, degassing of flask is conducted for the next 20 min. The atmosphere is switched to Ar and the temperature is raised to 275 °C. The solution is stirred until a clear orange coloration is obtained. The flask is cooled down to room temperature and the color switches to yellow. Finally, this solution is transferred to an Ar filled glove box for storage.

**HgTe CQD synthesis with band edge at 4000  $\text{cm}^{-1}$ :** 513 mg of  $\text{HgCl}_2$  was added to 60 mL of oleylamine in a 100 mL round flask. The solution was placed under vacuum and heated to 110 °C for 1 h. Then, the temperature is decreased to 80 °C and solution placed under Ar atmosphere. 1.9 mL of TOP:Te (1 M) with 10 mL of oleylamine are added to the mercury solution. The solution color gradually turns to dark brown and the reaction is stopped 3 min. A solution made of 1 mL of dodecanethiol and 9 mL of toluene is quickly added to quench the reaction. The nanocrystals are then precipitated with ethanol. After centrifugation, the nanocrystals are redispersed in chloroform. The washing step is repeated one more time. The solution is filtered with a 0.2  $\mu\text{m}$  filter and redispersed in 6 mL of chloroform.

**Chemicals for ink preparation:** *n*-hexane (VWR, 99%), N,N dimethylformamide (DMF, Sigma Aldrich), 1,2 ethanedithiol (EDT, Fluka, 98%), mercaptoalcohol (MPOH, Merck, >99%).

**HgTe CQD ink preparation:** 5 mg of  $\text{HgCl}_2$ , 100  $\mu\text{L}$  of MPOH and 900  $\mu\text{L}$  of DMF are mixed. 0.5 mL of this solution is added to 0.5 mL of a HgTe solution at 50  $\text{mg}\cdot\text{mL}^{-1}$  in toluene. A few mL of hexane can be added to help phase dissociation: the QDs migrate to the bottom phase (DMF), showing efficient ligand exchange. After 3 washing steps with hexane, the QDs are mixed to 2-3 drops of ethanol and centrifuged at 6000 rpm for 2 min. The clear supernatant is discarded and the QDs are redispersed in 100  $\mu\text{L}$  of fresh DMF to reach a concentration of 250  $\text{mg}\cdot\text{mL}^{-1}$ . To ensure the colloidal stability is good, the sample is then re-centrifuged at 3000 rpm for 4 min.

**Material characterization:** For TEM pictures, a drop of CQD solution is drop-casted on a copper grid covered with an amorphous carbon film. The grid is degassed overnight under secondary vacuum. A JEOL 2010F is used at 200 kV for acquisition of pictures. For infrared spectroscopy we use a Fischer Nicolet iS50 in either Attenuated Total Reflection (ATR). The spectra are averaged over 32 acquisitions and have a 4  $\text{cm}^{-1}$  resolution. Raman measurements were done with a Labram-HR confocal microscope from Horiba, under ambient conditions. We used a 633 nm laser, with a power of 3  $\mu\text{W}$  and a grating of 600  $\text{g} / \text{mm}$ . The 2D peak of graphene fit with a single Lorentz peak. This results in a uniform graphene signal on the electrode.

### Graphene electrode fabrication

Graphene interdigit contacts are formed by transferring a CVD-grown monolayer graphene onto  $\text{LaF}_3$  substrate (done by Grapheal<sup>62</sup> following fabrication process detailed in reference<sup>63</sup>). Using laser lithography ( $\mu\text{PG}$  101) and Reactive Ion Etching (Oxford instrument RIE) processes, 50

interdigits with 10  $\mu\text{m}$  width and 750  $\mu\text{m}$  length, spaced by 10  $\mu\text{m}$  were patterned. The substrate is firstly cleaned with acetone and rinsed with ethanol and isopropanol. AZ 1505 resist is then spin-coated and baked at 105 °C. The well-defined area on the substrate is exposed under UV Laser. The exposed resist is developed using a bath of AZ 726 for 20 s, before being rinsed in DI water. We then used the mixture of Argon and Oxygen plasmas (RIE) to etch the exposed area during 15 s, and before being cleaned 1 min in acetone, rinsed using isopropanol and dried by a nitrogen jet gas. Shadow mask evaporation is finally used to contact graphene with Ti (10 nm) /Au (40 nm).

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## Competing interests

The authors declare no competing interests.

## Additional information

The Supporting Information is available free of charge on the ACS Publications website at DOI:

It includes additional information regarding : 1. Graphene characterization - Transport in graphene Hall bar ; 2. Ink deposition and sample preparation ; 3. Transport measurements: 3.1. I(V) and transfer curves measurements; 3.2. I(V) measurements at different gate voltages; 3.3. FTIR photocurrent measurements; 3.4. Photocurrent measurements with an electrically chopped laser at 1.55  $\mu\text{m}$ ; 3.5.Noise measurements; 4. Comparison with other technologies; and 5. References specific to the Supporting Information.

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