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Charlie Gréboval, Corentin Dabard, Nikita Konstantinov, Mariarosa Cavallo, Sang-Soo Chee, et al.. Split Gate Photodiode based on Graphene-HgTe Heterostructure with few ns Photoresponse. ACS Applied Electronic Materials, 2021, 10.1021/acsaelm.1c00442 . hal-03418189

**HAL Id: hal-03418189**

**<https://hal.science/hal-03418189v1>**

Submitted on 6 Nov 2021

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# Split Gate Photodiode based on Graphene-HgTe Heterostructure with few ns Photoresponse

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**Abstract:** Hopping transport associated with the granular nature of nanocrystal arrays has led to the thought that nanocrystal-based devices might be incompatible with fast operations. Here we explore the design of HgTe nanocrystal-based sensors operating in the short-wave infrared and with very fast time response down to a few ns. To reach this goal, the design relies on a planar geometry to reduce the device capacitance. A strong in-built electric field is tailored via electrostatic control from two bottom split-gate electrodes, which promotes the charge extraction. While using graphene electrodes patterned over the two gate electrodes, we optimize the control on the electrostatic design of the p-n junction inside the nanocrystal array. Taking advantage of a high-k dielectric spacer, we demonstrate that the device can be operated under low gate bias (<6 V). The split-gate photodetector appears to be versatile, and can be used either in phototransistor or diode modes, upon the two gates voltages that are set to design isotype or diode-type heterojunctions. We finally highlight that time response enabled by the planar diode configuration can be made much faster than the one associated with the conventional vertical geometry.

**Keywords:** HgTe, nanocrystal, infrared, light detection, graphene, heterostructure, planar *p-n* junction.

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## INTRODUCTION

Nanocrystals (NCs) are versatile building blocks to design optoelectronic devices. In particular, the range of materials grown under colloidal nanocrystal form has been expanded, shifting their optical features from the visible to longer wavelengths, opening to NCs the market of infrared technologies (IR) where their cost effective and scalable capabilities are promising for next generations of sensors.

Naively, the polycrystalline nature of a NC thin film was considered incompatible with fast responding devices owing to hopping transport and slow carrier release resulting from trap states. This is probably why the first targeted application of infrared NCs was solar cells,<sup>1,2</sup> in which the device time response is not a relevant figure of merit. Nevertheless, the growing interest for low-cost alternatives to current technologies dedicated to IR sensing has pushed toward the development of new surface passivation to reduce the number of traps, while increasing the coupling between NCs. As a result, effective carrier mobilities in the order of  $1 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$  have been reported,<sup>3,4</sup> even if even lower mobility values remain compatible with imaging applications. Such progresses on transport have enabled the integration of NCs onto read-out-circuit to design short-wave<sup>5,6</sup> and mid-wave<sup>7</sup> IR focal plane arrays. Light Detection And Ranging (LIDAR) sensing appears as another promising application for NCs, especially in the context of autonomous vehicles for which the device cost is constrained and poorly compatible with conventional technologies. In this case, operations as fast as 10 or even 100 MHz of frequencies are required for LIDAR sensors. what remains challenging and requires an updated device geometry to overcome the RC time constant limitation.<sup>8</sup> For PbS NCs, a time response as short as 300 ps has been obtained by reducing the pixel size to decrease the associated capacitance.<sup>9,10</sup> However, when a cut-off wavelength longer than  $2 \mu\text{m}$  is targeted, very large PbS NCs are then required raising difficulties to obtain smooth films from particles with limited colloidal stability.<sup>11</sup> By contrast, for HgTe NCs<sup>12</sup>, such fast operating diode remains mostly unreported<sup>13–15</sup> and currently most devices based on this material present a time response in the 100 ns to few  $\mu\text{s}$  range.<sup>16–19</sup> Further updates are required to make the device geometry compatible with GHz operation.

So far, the most popular NC sensor geometry is the vertical photodiode.<sup>20</sup> The intrinsic electric field is used to rectify currents or to support charge dissociation of photogenerated electron–hole pairs. However, the chemical engineering of *p-n* junctions with reliable doping profile specifications is very challenging, including difficulties such as the design of ligand generating surface dipole.<sup>21</sup> Moreover, the vertical stack used for photodiodes is not favorable to achieve low capacitance devices ( and thus low RC time constant). As those devices are thin, their sheet capacitance, given by  $\epsilon/d$  with  $\epsilon$  the dielectric constant and  $d$  the thickness, turns to be large. Hence, it is highly desirable to explore alternative device concepts based on the planar geometry, which is more compatible with fast operations.

In this manuscript, we report graphene/HgTe NCs heterostructures based on a lateral device configuration to achieve NC devices with a fast photoresponse. We explore how the graphene/HgTe nanocrystals heterostructure can be used to enhance the charge dissociation through the in-built electric field resulting from *p-n* junction while the planar geometry enables low capacitance and consequently fast operation.

In order to design a planar *p-n* junction, we build our photodetector from a split-gate field-effect-transistor (FET) architecture. The latter consists of two back gates, located below the drain and source electrodes, and electrically decoupled by an  $\text{Al}_2\text{O}_3$  dielectric thin film. The role of the gates is to enable an independent and precise control on the carrier concentration of each of the two sides of the junction that they delimitate. Taking advantage of the ambipolar properties of HgTe NCs, the two gates enable injection of electrons or holes separately in the vicinity of the two top electrodes, triggering the formation of a lateral *p-n* junction inside the NC thin film. Moreover, drain and source electrodes are made of graphene<sup>22</sup> Here, not only we use graphene for its gate tunable work function, but also for its large transparency to the gate induced electric field. Thanks to this

transparency, the gate effect propagates within the nanocrystal film, as opposed to a full screening of the gate induce electric field in the case of metallic electrodes. As a result, the gate induced  $p$ - $n$  junction propagates within the NCs film and enhances the volume over which charge dissociation is occurring. Because our IR sensor architecture is planar, a lateral  $p$ - $n$  junction, whose depletion width is directly exposed to light, is built from a simple and unique NC thin film. We demonstrate that taking advantage of the lateral space charge layer of such device, a rectification ratio of the current up to 3 decades can be achieved, together with a recorded photoresponse time on HgTe NC as short as 3 ns.

## DISCUSSION

Graphene/NCs hybrid structure for photodetection has been first motivated by the large mobility of graphene.<sup>23</sup> In that case a graphene channel was photosensitized in the near IR by PbS NCs. By switching the NCs to HgTe, Grotevent *et al*<sup>24</sup> shifted the absorption of graphene in the short-wave IR, while Tang *et al*<sup>25</sup> even extended it to the long-wave IR. Whilst this geometry leads to large responsivities, it is also associated with two main drawbacks: a slow response induced by the transfer mechanisms of the charge carrier from the NC to the graphene, and a too large dark current resulting from the intrinsically high (dark) conductivity of graphene<sup>26</sup> explaining why the graphene has no longer been used as a channel, but rather as an electrode. In this case, the targeted properties are typically its optical transparency in the IR,<sup>27,28</sup> or its ability, as an electrode, to let back gate electric field penetrate through it to modulate the electronic states of a photosensitive material deposited on its surface.<sup>22,28,29</sup>

We start by fabricating a split-gate FET, as shown in Figure 1a and Figure S1-2. While such geometry is commonly used for 2D materials<sup>26</sup>, in particular with transition metal dichalcogenides,<sup>30,31</sup> its use for gate NC films remains mostly unexplored. However, electrostatic doping has revealed as an efficient approach to modulate the carrier concentration of NC, using various types of gate technologies, such as dielectric,<sup>32,33</sup> ferroelectric,<sup>34</sup> ionic glass<sup>35</sup> and electrolyte<sup>36</sup>. Hence, the use of two spatially defined gates appears as a convenient approach to overcome the limited control on the intrinsic NC doping. Here, we use Al<sub>2</sub>O<sub>3</sub> (40 nm) as high- $k$  dielectric spacer on top of the two gold bottom gate electrodes, see Figure 1c and S1-2. On the top of alumina, we fabricate the drain and source electrodes from transferred graphene monolayers grown by chemical vapor deposition (CVD) (for details, see supplementary information).

The channel of the FET is made of HgTe NCs grown using the Keuleyan's procedure.<sup>37</sup> Absorption spectrum, in Figure 1b, shows that those particles present a cut-off energy of 0.5 eV (or  $\approx 2.5 \mu\text{m}$  or  $\approx 4000 \text{ cm}^{-1}$ ) and have a tripodic shape according to transmission electron microscopy<sup>38</sup> (background of Figure 1b). To ensure a strong interparticle coupling, the native long thiol ligands are stripped off through a concentrated ink preparation,<sup>18</sup> that is finally spin-coated onto the dual gated FET device. Conductive films are obtained, see Figure 1d and e. The film has an ambipolar nature, conducting both electrons and holes, with a larger hole mobility. This ambipolarity is mandatory to later induce a  $p$ - $n$  junction. The temperature dependence of the current follows an Arrhenius behavior in the vicinity of room temperature. The activation energy is 160 meV, below the value of  $E_g/2 \approx 250 \text{ meV}$  expected for an intrinsic semiconductor, which may have been inferred by the strong  $p$ -branch observed from the FET measurements and the positioning of the neutrality point on the negative polarity side at  $V_N = -1.75 \text{ V}$ .

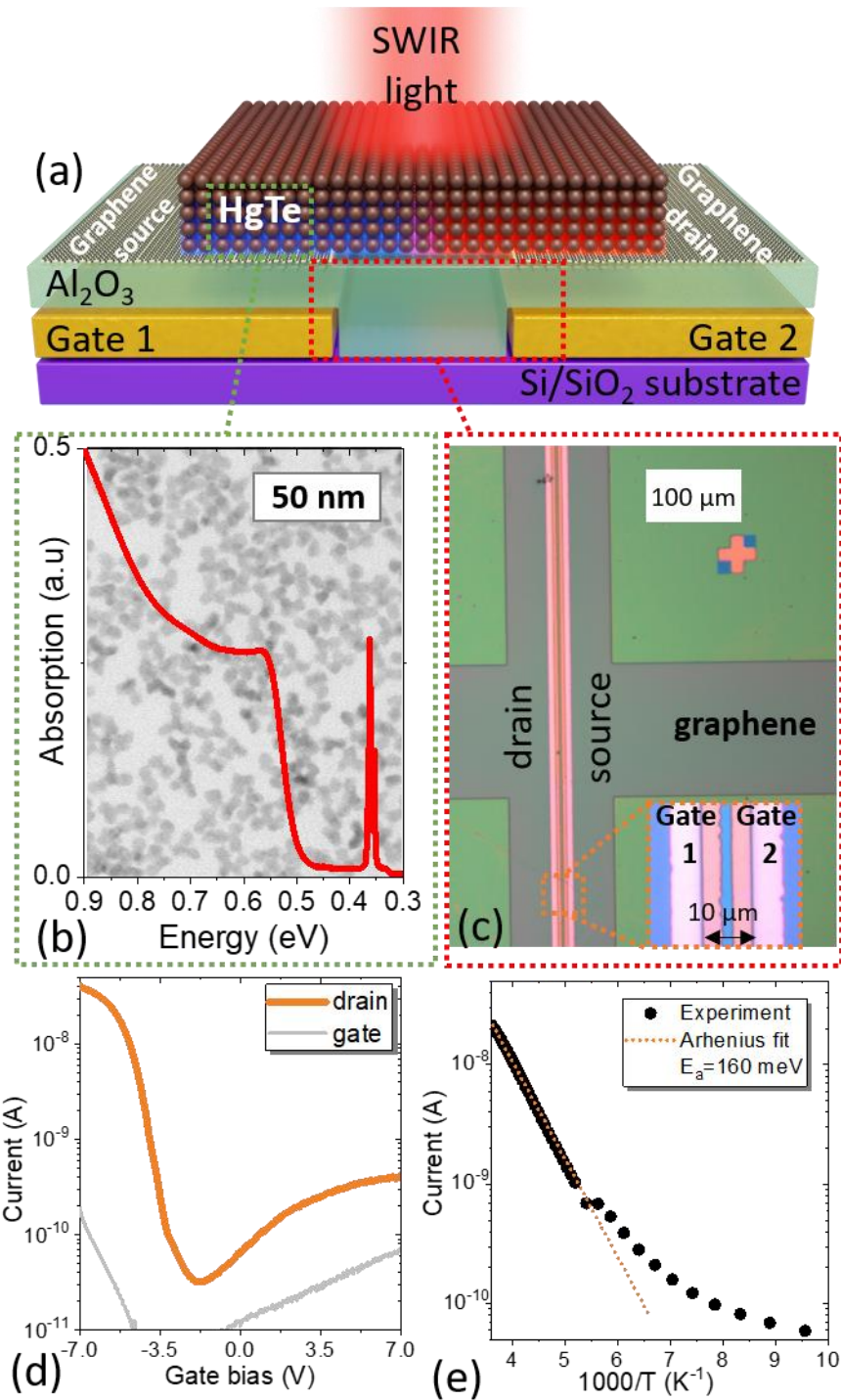


Figure 1 a. Schematic of the dual gated planar p-n graphene-HgTe heterojunction. b. Absorption spectrum of HgTe NCs with a cut-off energy of 0.5 eV ( $2.5 \mu\text{m}$  or  $4000 \text{ cm}^{-1}$ ). Background is a transmission electron microscopy image of these NCs. c. Optical microscopy image of the dual gated device (top view) with the drain source electrode made of graphene. d. Transfer curves (drain and gate currents as a function of the applied gate bias) for a HgTe NC channel connected to the two graphene electrodes. In this case, the two gates are connected together. e. Current as a function of the temperature for a HgTe NC channel connected to the two graphene electrodes.

In Figure 2a-c, we depict the band structure of the device for different operating conditions. The graphene to HgTe band alignment (Figure 2a), in absence of applied bias, has been previously determined using photoemission<sup>22</sup> and revealed a small ( $\approx 0.3 \text{ eV}$ ) Schottky barrier for the holes. When the two gates are operated with the same gate bias (Figure 2b), the system behaves as a single gated FET, resulting in identical doping on both sides of the junction. In this case the I-V curve

is linear and symmetric, see Figure 2d. When the two gates are operated with opposite biases (see Figure 2c), the two sides of the junction get inversely doped with respect to each other. Such opposite polarity leads to a  $p-n$  junction and induces a rectifying behavior of the I-V, see Figure 2e. Gate bias will play a double role: (i) tune the Schottky barrier height at the graphene-HgTe interface and (ii) induced in the volume between the two gates (*ie* within the HgTe array) the pn junction.

In a  $p-n$  junction, the in-built potential  $V_{bi}$  responsible for the asymmetric I-V curve is given by:  $V_{bi} = \frac{k_B T}{e} \ln\left(\frac{N_D N_A}{n_i^2}\right)$  with  $k_B$  the Boltzmann constant,  $T$  the temperature,  $e$  the elementary charge,  $N_D$  (resp.  $N_A$ ) the donor (resp. acceptor) dopant density and  $n_i$  the intrinsic carrier density.<sup>39</sup> The latter is related to the density of state through:  $n_i^2 = N_C N_V \exp\left(-\frac{E_G}{k_B T}\right)$ . In HgTe NCs, the density of state of the conduction band  $N_C$  (resp. valence band  $N_V$ ) is equal to  $2\rho$  (resp.  $6\rho$ ), with  $\rho$  the density of NC.  $\rho$  is given by:  $\rho = \frac{0.64}{4/3\pi R^3}$ , where the term 0.64 relates to the NC volume fraction, assuming a random close packing, and  $R$  the particle radius.

The “electrostatic doping”, also referred as gate-induced charge, is a unique feature of nano-sized structures such as two-dimensional materials<sup>40,41</sup>, nanowires<sup>42–45</sup> and fully depleted silicon-on-insulator. In electrostatically controlled  $p-n$  junctions, also called split-gate geometry, on the contrary to traditional ones where donor and acceptor densities are set by implanted or chemically doped species, the values of  $N_D$  and  $N_A$  are now replaced by gate-induced electron  $N_*$  and hole  $P_*$  charge densities. If we label  $V_N$  the gate bias associated with the neutrality point of the transfer curve,  $N_* = \frac{C_S |V_{G1} - V_N|}{et}$  while  $P_* = \frac{C_S |V_{G2} - V_N|}{et}$  with  $t$  the film thickness and  $C_S$  the sheet capacitance, assumed to be the same for the two gates.

The asymmetry of the I-V curve appears to be strongly dependent on the operating temperature, see figure S3 and Figure 2c. At high temperatures ( $>200$  K), the forward current is one order of magnitude larger than the reverse one, which is similar to what has been observed with metallic drain and source electrodes.<sup>38</sup> As the temperature is decreased, the current asymmetry drastically increases and reaches a factor 1000 at 150 K, see Figure 2f. Below this temperature, the I-V curve asymmetry drops. This drop has to be connected to the decrease of the on-current in the FET measurements, see Figure S3. This behavior relates to the thermal activation of the mobility, which magnitude reduces at low temperature.

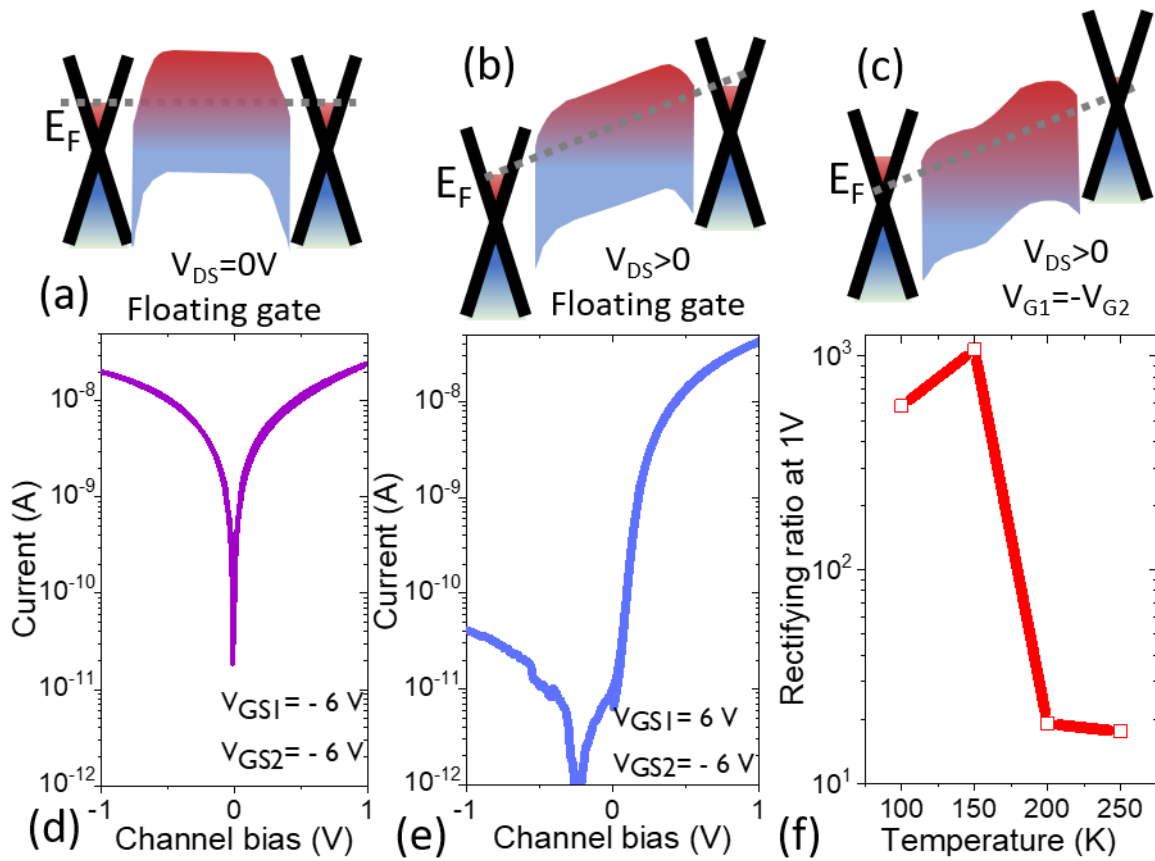


Figure 2 a. schematic of the band structure without drain or gate bias (a.), with drain bias and floating gate (b.) and with drain bias and opposite gate biases (c.). d. I-V curve at 150 K for the channel made of HgTe NCs connected to the two graphene electrodes, when the two gates are operated under strong negative bias ( $V_{G1}=V_{G2}=-6$  V). This corresponds to the transistor regime under a strong hole injection. e. I-V curve at 150 K for the channel made of HgTe NCs connected to the two graphene electrodes, when the two gates are operated under opposite gate bias ( $V_{G1}=-V_{G2}=6$  V) corresponding to the diode regime. f. Temperature dependence of the rectifying ratio  $R$  of the device set in the diode mode ( $V_{G1}=-V_{G2}=6$  V), measured at 1V and defined at  $R=|I(+1\text{ V})/I(-1\text{ V})|$

In the diode mode and under illumination, the device is generating photocurrent under a 0 V channel bias, see Figure 3a and b. On the contrary, in the transistor mode ( $n-n$  and  $p-p$  regimes), the photocurrent at 0 V is null and no open circuit voltage is observed, see Figure 3a. By fitting the open circuit voltage as a function of the logarithm of the incident light power, we can estimate a slope of  $0.8k_B T/e$ , see Figure 3c. The factor 0.8 corresponds to the diode ideality factor. Values below 1 are typically associated with non-selective contact diodes.<sup>46</sup>

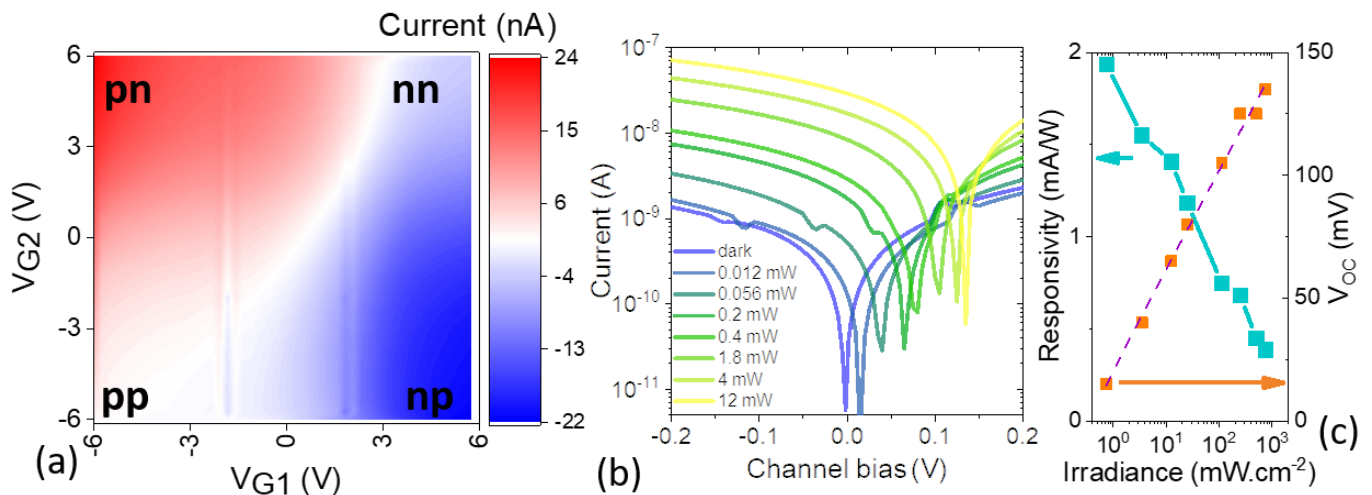




Figure 3 a. Photocurrent map, under 0 V channel bias, as a function of the applied gate biases. b. I-V curves under dark condition and under illumination by a 1.55  $\mu\text{m}$  laser diode operated under various incident powers. c. Open circuit voltage ( $V_{OC}$ ) and responsivity for the channel made of HgTe NCs connected to the two graphene electrodes. The purple dashed line is a log fit of the experimental data associated with a  $0.8k_B T/e$  slope. The two gates are operated under opposite gate bias ( $V_{G1}=-V_{G2}=6\text{V}$ ) corresponding to the diode regime. All measurements are made at 250 K.

We now study the photoresponse of our colloidal planar p-n photodiode. As it has been pointed out in the introduction, the time response of HgTe photodiodes were mostly reported above 100 ns, see table S1 and Figure 4a. For sake of comparison, we have built such a diode based on the following stack glass/FTO/HgTe/Hg:Ag<sub>2</sub>Te/Au, see supporting information for details about fabrication.<sup>19</sup> Full decay of the vertical diode photoreponse requires almost a  $\mu\text{s}$ , see Figure 4a. The split-gate FET appears to be compatible with faster operations, see Figure 4a and S4. The decay time corresponding to the drop of 90% to 10% of the signal is 3 ns and correspond to a 3dB cut-off frequency of  $\approx 110$  MHz. This value matches the targeted performance required for the LIDAR operation.

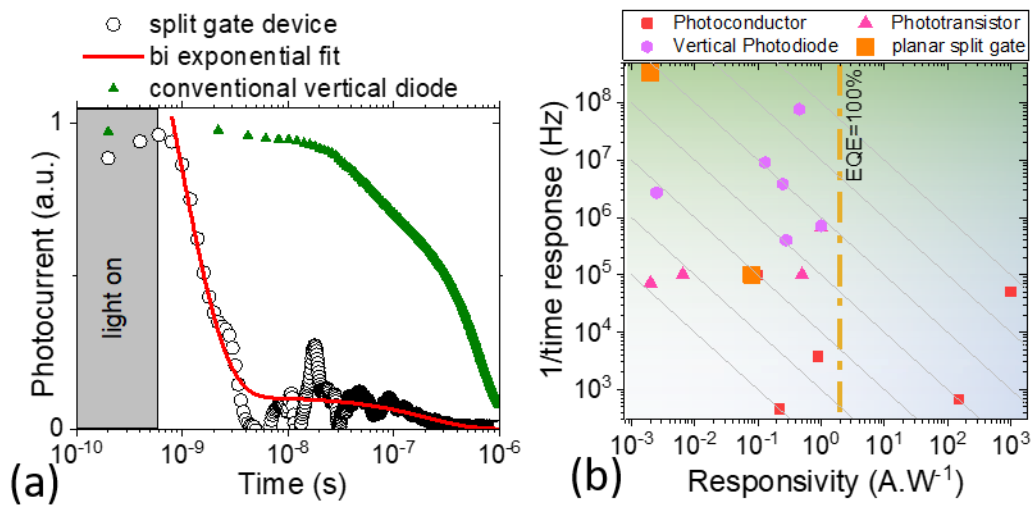


Figure 4 a. Photocurrent as a function of time after the sample gets illuminated by a 1 ns pulse at 1573 nm. The redline is a bi-exponential fit. In green we also provide the time response of a vertical geometry diode made of glass/FTO/HgTe/Hg:Ag<sub>2</sub>Te/Au. b. Inverse of the time response as a function of the responsivity of HgTe NC-based devices operated in the SWIR based on various device geometries. Grey curves are iso gain bandwidth lines. See table S1 for associated references.

To compare the performance of this dual gate device with other devices based on the same material, we present a phase diagram where the inverse of the time response is plotted as a function of the device responsivity. In this diagram (Figure 4b), all devices mentioned in table S1 are reported. This phase diagram can be split into two main parts. Along the x axis, the responsivity corresponding to an EQE =100 % (or  $R=2 \text{ A.W}^{-1}$  for 2.5  $\mu\text{m}$  cut-off wavelength) sets the maximal value than can be reached by diodes. Above this value, gain is required and only ultrashort devices<sup>47</sup> or devices based on graphene channels<sup>24</sup> have been reported.

It is interesting to point that most HgTe devices collected in Figure 4b are reported with time response shorter than ms, which suggests a very weak contribution of traps or at least a fast release from them. Such fast release is probably enabled by the relatively high operating temperature of HgTe NC-based SWIR devices, typically above 250 K. On Figure 4b, we also have plotted lines corresponding to iso-gain-bandwidth product (tilted grey line). The latter is defined as the ratio of



the responsivity over the time response. While the split gate device presents a modest responsivity, the achieved gain bandwidth product appears among the highest from literature.

## CONCLUSION

To summarize, we have explored a device geometry to design a fast time photosensor from an HgTe NC array in a dual gate FET configuration. To reach this goal, we designed a low capacitance planar device in which a diode operation is induced by two bottom split-gates. The use of a high- $k$  material makes that a  $p$ - $n$  junction can be obtained from low bias operations (6 V typically). Graphene electrodes are used to maximize the difference of potential between the two electrodes while expanding the volume of the  $p$ - $n$  junction within the NC array. Thus, the combination of graphene and split-gate enables operating condition for the sensor with enhanced current modulation. The obtained time response appears to be much shorter than the obtained from conventional geometry diode and compatible with LIDAR requirement. As regards of photodiodes, future research direction includes improvement of the device light absorption, since only 10% of the incident photons are absorbed in the current configuration. Moreover, the demonstration of fine spatial electrostatic control on the carrier density profile of NC thin films goes further than photodiodes applications, and may become an ubiquitous device structure in the next generation of logic rectifiers, photovoltaics, photodetectors, and even for light emission.

## METHODS

**Chemicals:** mercury chloride ( $\text{HgCl}_2$ , Strem Chemicals, 99%), **mercury salts are toxic, handle them with great care**, tellurium powder (Te, Sigma-Aldrich, 99.99%), trioctylphosphine (TOP, Alfa-Aesar, 90%), oleylamine (OLA, Acros, 80-90%), dodecanethiol (DDT, Sigma-Aldrich, 98%), mercaptoalcohol (MpOH, Merck, >99%), ethanol absolute anhydrous (VWR), methanol (Carlo Erba, 99.8%), octane (Carlo Erba, 99%), toluene (Carlo Erba, 99.3%), N,N-dimethylformamide (DMF, Sigma Aldrich), Methylisobutylketone (MIBK, VWR, >98.5 %). All chemicals are used as received, except OLA that is centrifuged before being used.

**1 M TOP:Te precursor:** We load a 100 mL three neck flask, with 6.35 g of Te powder with 50 mL of TOP. The content of the flask is degassed, first at room temperature and once vacuum is below 1 mbar, the degassing is continued while the flask is heated at 100°C. Then the atmosphere is switched to  $\text{N}_2$  and the temperature raised to 275 °C. The reaction is continued until the Te powder gets fully dissolved. Then the heating mantle is removed and the flask is cooled. Once temperature is below 100 °C, the flask is degassed again. This vial is then transferred into an air free glove box and used as stock solution.

**HgTe NC growth:** In a 100 mL three neck flask, 543 mg of  $\text{HgCl}_2$  and 50 mL of oleylamine are degassed under vacuum at 110 °C. 2 mL of TOP:Te (1 M) from the stock solution are mixed with 8 mL of oleylamine. The atmosphere is switched to  $\text{N}_2$  and the temperature cooled to 82 °C, the TOP:Te solution is quickly injected. A quenching mixture containing 9 mL of toluene and 1 mL of DDT is prepared and transferred to a 10 mL syringe. The latter is injected into the three-neck flask 2 min after the beginning of the reaction. The content of the flask is split over 4 tubes and precipitation is induced by addition of MeOH. The tubes are centrifuged and the formed pellets are redispersed in a single tube with toluene. The cleaning procedure is repeated a second time using EtOH as non-solvent. Again, the formed pellet is redispersed in toluene. The nanocrystals are then centrifuged in toluene (ie without non solvent) to get rid of the lamellar phase. The clear dark brown supernatant is transferred in a weighted tube and precipitated using methanol. The solid is dried under primary vacuum for 30 min. Nanocrystals are finally stored in toluene with a 50 mg.mL<sup>-1</sup> concentration.

Ag<sub>2</sub>Te NCs growth: In a 25 mL three neck flask, 34 mg AgNO<sub>3</sub> (0.2 mM), 5 mL OLA and 0.5 mL OA are degassed at 70 °C under vacuum until the AgNO<sub>3</sub> is completely dissolved and the solution becomes clear. Under nitrogen, 0.5 mL TOP is injected into the solution. Then temperature is raised to 160 °C. At 160 °C the solution becomes orange. Now 0.1 mL TOPTe (1 M) is injected into the solution and the reaction is quenched after 10 min with a water bath. The NCs are precipitated with methanol and redispersed in chlorobenzene. At this step, 500 µL of DDT are added. The washing step is repeated one more time and finally the NCs are redispersed in hexane:octane (9:1) solution.

*Material characterization:* For TEM imaging, a drop of diluted NC solution is cast on a copper grid covered with an amorphous carbon film. The grid is degassed overnight under secondary vacuum. A JEOL 2010F system is operated at 200 kV for the acquisition of images. For infrared spectroscopy, we use a Fischer Nicolet iS50 system in attenuated total reflection (ATR) mode. The spectra are averaged over 32 acquisitions and have a 4 cm<sup>-1</sup> resolution.

*Split gate device fabrication* First, gate electrodes (gap distance: 3 µm) and source/drain contact pads are patterned onto a Si/SiO<sub>2</sub> substrate (400 nm thick SiO<sub>2</sub>), followed by consecutive metal deposition using a thermal evaporator given as follow: 5 nm-thick Cr, 40 nm-thick Au, and 5 nm Al (Figure S1a). This last layer is there to promote the adhesion of the subsequent alumina layer. These evaporation steps are performed while sample is under rotation to obtain soft edges. Then, we deposited 40 nm of Al<sub>2</sub>O<sub>3</sub> onto devices by ALD, which serves as a gate dielectric (Figure S1b). Next, Al<sub>2</sub>O<sub>3</sub> areas on contact pads of gates and source/drain are patterned, and then etched by H<sub>3</sub>PO<sub>4</sub> (85%) solution at 50 °C for 6 min, to improve contact properties (Figure S1c).

The Graphene drain and source electrodes were fabricated from monolayer graphene grown by chemical vapor deposition (CVD). We used commercially available CVD Graphene on polymer film (PMMA sacrificial layer/Graphene/Polymer stack, “Easy transfer” product, from Graphenea Inc.). The following protocol was carried out: **1.** Releasing the stack in the deionized water to detach the sacrificial layer/graphene bilayer from the bottom polymer support. **2.** Fishing the sacrificial layer/graphene by cleaned glass slides. Transferring it on the substrate previously detailed. **3.** Putting substrate on hot plate at 150°C for 2 hours in glovebox. **4.** Storing it in glovebox 24 hours. **5.** Removing the sacrificial layer into successive dipping in hot acetone (50°C, 1 hour), isopropanol (1 hour), being dried by a nitrogen jet gas. Using laser lithography (Heidelberg µPG 101) and Oxygen plasma etching (Plasma Prep III) processes, electrodes spaced by 10 µm were patterned. The substrate is cleaned with acetone and rinsed with ethanol and isopropanol, before being dried by a nitrogen jet gas. AZ 1505 resist is spin-coated and annealed at 105 °C. A UV Laser setup is used to perform the lithography procedure. The resist is developed in a solution of AZ 726 for 20 s, and then rinsed in DI water. A mixture of Argon and Oxygen plasmas is used to etch the exposed graphene during 120 s. The substrate is then cleaned for 1 min in acetone, before being rinsed using isopropanol and dried by a nitrogen jet gas (Figure S1d). A last step of lithography is used to connect the graphene electrode to the contact pad (Figure S1e). we deposited a Ti (10 nm)/Au (50 nm) bilayer using electron beam evaporator (Plassys MEB 550S), followed by a lift-off in acetone, and similar cleaning, rinsing and drying of the substrate as detailed previously.

*HgTe ink preparation and deposition:* We prepare a mixture made of 15 mg of HgCl<sub>2</sub> dissolved in 9 mL of DMF, then 1 mL of mercaptoethanol is added. 500 µL of NC solution at 50 mg.mL<sup>-1</sup> is mixed with 5 mL of hexane. 1 mL of ligand exchange solution is added to the tube containing the NC solution and mixed. The NCs experience a phase transfer from the apolar phase to the DMF phase. The apolar phase is discarded using a pipette. 5 mL of hexane are added to perform a second cleaning. The apolar phase is again removed and another cleaning step is performed. The NCs solution in DMF is precipitated by addition of a few drops of toluene. NCs are redispersed in fresh DMF at a concentration of ≈ 160 mg.mL<sup>-1</sup>. 15 µL of ink solution are spin-coated on the electrodes at

3000 rpm for 60 s to form a <100 nm film. To obtain thicker film, the spin-coating speed is reduced to 1200 rpm (for 120 s) and is followed by a drying step at 2500 rpm for 60 s.

*Device characterization:* The sample is mounted on the cold finger of a closed cycle He cryostat. Unless specified, the measurements have been conducted at 250 K under secondary vacuum. Both gates are connected to the two channels of a Keithley 2634B, while the drain source electrodes are connected to another Keithley 2634B used to apply a channel voltage and measure the resulting current. Measurements under illumination are obtained while the sample is illuminated by a 1.55  $\mu\text{m}$  laser diode. The beam spot presents an area of 1.6  $\text{mm}^2$ . The responsivity under 0 V bias is determined from the photocurrent I-V curves.

*Time – resolved measurements:* For the time-resolved measurements, we use a pulsed laser (CNI laser MPL-H-1573-30 $\mu\text{J}/\text{pulse}$ -1 ns pulse duration and 100 Hz repetition rate) at 1573 nm. An oscilloscope (Rohde & Schwarz, RTE 1102) acquires the outgoing signal, which is proportional to  $I_{\text{DS}}$ , through a 50  $\Omega$  resistor connected to a Femto DUPVA-70 voltage amplifier. The pulse arrival time is determined by an InGaAs photodiode used as the oscilloscope trigger.

*Vertical diode fabrication:* For the device fabrication, we use a FTO coated glass ( $\rho_s \approx 70\text{-}90 \Omega/\text{sq}$  – 70 nm thickness) from Solems. The FTO electrodes are defined with optical UV lithography. The FTO-coated glass substrates were sequentially cleaned in acetone and isopropanol. The substrates were exposed to UV-ozone for 5 min. The HgTe CQD ink (is deposited onto the electrodes via spin-coating. The thickness of the film can be tuned with spin-coating speed and ink concentration. A  $\text{Ag}_2\text{Te}$  NCs layer is spin-coated, on the HgTe layer, at 2000 rpm. Then, 50  $\mu\text{L}$  of  $\text{HgCl}_2$  in methanol (10 mM) were dropped onto silver containing layer film and spin-dried after 15 s. the film was rinsed twice with IPA. The top layer is finally cross linked through an EDT ligand-exchange. The substrate is dipped into a 1% EDT in acetonitrile solution for 30 s and rinsed with fresh acetonitrile. A gold layer (80 nm) is deposited with thermal evaporation under secondary vacuum at the rate of 3  $\text{\AA}/\text{s}$ . To ensure an homogeneous thickness, the substrate holder was rotated during the procedure. The overlapping area between the electrodes FTO and Au defined the device area (1  $\text{mm}^2$ ).

## **ASSOCIATED CONTENT**

Supporting information include discussion about the material synthesis, procedure for the device fabrication, additional electrical characterization and table to compare the figures of merit for state-of-the-art HgTe NC based SWIR operating light sensor.

## **COMPETING INTEREST**

The authors declare no competing financial interests.

## **DATA AVAILABILITY**

The data that support the findings of this study are available from the corresponding author upon reasonable request.

## **ACKNOWLEDGEMENTS**

The project is supported by ERC starting grants blackQD (grant n° 756225). We acknowledge the use of clean-room facilities at the “Centrale de Proximité Paris-Centre”. This work has been supported by Region Ile-de-France in the framework of DIM Nano-K (grant dopQD). This work was supported by French state funds managed by the ANR within the Investissements d'Avenir programme by Labex Matisse (ANR-11-IDEX-0004-02) and Labex NIE (ANR-11-LABX-0058, and

ANR-10-IDEX-0002-02) and by grants IPER-Nano2 (ANR-18CE30-0023-01), Copin (ANR-19-CE24-0022), Frontal (ANR-19-CE09-0017), Graskop (ANR-19-CE09-0026), MIXES (ANR-19-CE09-0028), NITQuantum (ANR-20-ASTR-0008-01), Bright (ANR-21-CE24-0012-02) and MixDferro (ANR-21-CE09-0029). AC thanks Agence Innovation Defense. J.-F. D. thanks the Institut Universitaire de France (IUF) for financial support.

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