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► **To cite this version:**

Marc Margalef-Rovira, Tadeu Mota Frutuoso, Abdelhalim Saadi, Loïc Vincent, Manuel J. Barragan, et al.. Miniaturization of Transmission Lines: Meandered Slow-wave CPWs. IEEE MTT-S International Conference on Microwaves for Intelligent Mobility (ICMIM 2020), Nov 2020, Linz (virtual), Austria. 10.1109/ICMIM48759.2020.9299107 . hal-03371412

HAL Id: hal-03371412

<https://hal.science/hal-03371412v1>

Submitted on 11 Oct 2021

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Miniaturization of Transmission Lines: Meandered Slow-wave CPWs

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Abstract—This work presents novel transmission line structures based on Meandered Slow-wave CoPlanar Waveguides (MS-CPWs), aiming to achieve high miniaturization. As a proof-of-concept, these two kinds of transmission lines were designed and fabricated in the AMS 0.35 μm CMOS technology together with classical straight and meandered microstrip lines. Measurement results from 70 kHz to 100 GHz of the fabricated transmission lines are presented. At 80 GHz, all the considered transmission lines present similar quality factors, ranging between 10 and 14. On the other hand, at this frequency, one of the developed MS-CPW presents an effective dielectric constant of 88, while the meandered microstrip exhibits an equivalent effective dielectric constant of 51, thus leading to higher compactness for the MS-CPW.

Keywords—transmission line, slow-wave, CPW, mm-wave

I. INTRODUCTION

Current and future use of transceivers for diverse applications such as Internet of Things (IoT), automotive radar, 5G (future 6G) or medical applications require to operate in several frequency bands and the use of diverse communication standards. In the best scenario, the change of such telecommunication standard can be achieved through the operation of tunable elements in the transceiver. However, changing the telecommunication standard under use often means switching complete parts of the transceiver or even using a completely different transceiver.

In this scenario, the downscaling of the transistor size plays a major role in the miniaturization of complex transceivers. However, miniaturization of passive devices, needed to perform filtering, impedance matching or beam-steering functions, is a less technology-dependent issue. Passive devices are divided into two families: (i) lumped elements and (ii) distributed elements. Lumped elements (i.e. capacitors, inductors and transformers) tend to have a high Quality-factor (Q-factor) at RF frequencies (i.e. around 10 GHz) that decreases with the increase of frequency, while

distributed elements (i.e. transmission lines) tend to have a low Q-factor at RF frequencies that increases with the frequency. Moreover, the physical length of a transmission line (TLine) with a given electrical length is inversely proportional to the frequency. For these reasons, lumped elements are preferred at low frequencies (below ~ 30 -50 GHz in integrated technology). Above these frequencies, TLines are preferred thanks to their increased performance and acceptable size.

TLine footprint reduction has been addressed in the literature using two main approaches: (i) meandering [1,2] or (ii) using slow-wave concept [3]. In 2003, the first proposal of the application of the slow-wave effect on CPWs was published [3], presenting the so-called Slow-wave CPW structure (S-CPW). In 2013, the first proposal of the distributed electrical model for S-CPWs was published [4] and subsequently improved in [5]. In [5], authors provide tools for the analytical modeling of this structure, allowing the designer to accurately simulate the S-CPW in any technology, greatly reducing the effort to be devoted to time-consuming EM simulations.

In this work, innovative structures aiming to reduce the footprint of CoPlanar Waveguides (CPWs) are proposed. The proposed structures utilize the meandering and the slow-wave concept to reduce their physical length per unit of electrical length. Recently, meandering techniques together with the slow-wave concept was proposed for slow-wave microstrips, as in [6]. However, to the best of author's knowledge, this is the first time that an approach merging both techniques is proposed for CPW structures.

This work is organized as follows: section II provides a brief explanation of the slow-wave concept. In section III, the proposed structures are presented. Section IV presents the results of the fabricated TLines. Finally, section V presents the main conclusions of this work.

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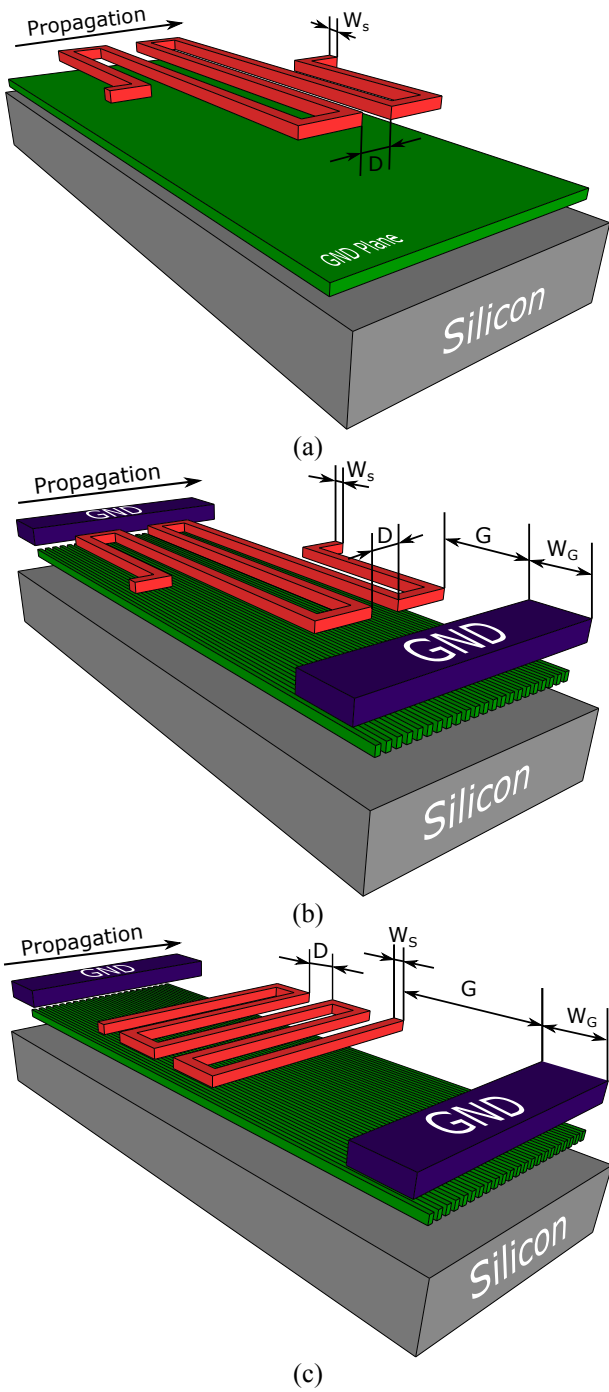


Fig. 1 (a) Meandered microstrip. (b) Meandered S-CPW type 1. (c) Meandered S-CPW type 2.

II. THE SLOW-WAVE PRINCIPLE

The slow-wave concept has been applied to multiple TLine architectures such as: microstrip [6], CoPlanar Waveguides (CPWs) [3,4,5], and CoPlanar Striplines (CPS) [7]. This technique utilizes some patterned structures to capture the electrical field of the TLine while allowing the magnetic field to flow freely. From a circuit point of view, this results in an increase of the capacitance per unit of length of the Tline, C_{lin} , and do not modify inductance per unit of length, L_{lin} . The increase of C_{lin} leads to a reduction of the propagation velocity (v_ϕ), whose expression is given by:

$$v_\phi = \frac{1}{\sqrt{C_{lin} \cdot L_{lin}}} \quad (1)$$

Hence, the physical length (L) of a TLine, with a given electrical length (θ), is reduced when the slow-wave concept is used since:

$$\theta = \frac{\omega}{v_\phi} \cdot L, \quad (2)$$

where ω is the considered angular frequency.

In addition, the Q-factor of a TLine can be expressed as:

$$Q = \frac{\omega}{2 \cdot v_\phi \cdot \alpha}, \quad (3)$$

where α is the attenuation constant of the considered TLine. Using this expression, it can be proved that the use of the slow-wave concept can lead to an improved Q-factor as compared to the classical structures.

In practice, the slow-wave concept for coplanar structures is achieved through the implementation of a floating shield beneath the signal and ground strips. This floating shield is composed of narrow metal ribbons placed transversally to the propagation direction.

III. PROPOSED ARCHITECTURE

This work explores the effect of meandering the signal strip of S-CPWs. Note that two kinds of structures can be envisioned with this consideration: (i) a structure where most of the signal strips are placed transversally to the overall propagation direction, or (ii) a structure where most of the signal strips are placed in parallel to the overall propagation direction. In this paper, these kinds of Meandered S-CPWs (MS-CPWs) are named type 1 and 2, respectively.

Fig. 1(a) presents a classical meandered microstrip line and the geometrical parameters for its design. In this work, together with a classical microstrip, this structure will be used as a reference for the evaluation of the performance of the proposed MS-CPWs. Figs. 1(b) and 1(c) show 3D views of the so-called type 1 and type 2 MS-CPWs and their geometrical parameters.

In a traditional meandered microstrip line, whether the strip is meandered transversally or parallelly to the overall propagation direction has a negligible effect on the performance of the TLine. However, the behavior of the type 1 or type 2 MS-CPWs is slightly different.

Both of the proposed MS-CPW types present two propagation modes within the structure. A first mode where the propagation occurs as in a S-CPW. This mode is found for the regions where the signal strip is placed parallelly to the overall propagation direction. A second mode of propagation is found for the regions where the signal strip is placed transversally to the overall propagation direction. In this case, the propagation mode is similar to that of a microstrip line. Even though the electrical field is still captured by the shield below, the signal strip is parallel to the metallic ribbons. Hence, return-current path is created in the metallic ribbons. Thus, behaving like a degenerated microstrip line where the return path for the signal is done through the metallic ribbons, with a capacitance existing between the signal strip and the metallic ribbons, and an inductance given by the flux of the magnetic field around the strips. As a result, type 1 MS-CPWs are expected to show a lower propagation constant as compared to their type 2 counterparts.

IV. RESULTS

As a proof-of-concept, four different TLines were designed, fabricated and measured on the AMS 0.35 μm CMOS technology. The selected architectures were: (i) a 50- Ω traditional microstrip line, (ii) a 50- Ω meandered microstrip, (iii) a 50- Ω type 1 MS-CPW, and (iv) a 50- Ω type 2 MS-CPW. Measurements were performed using an Anritsu VectorStar ME7838A4, from 70 kHz to 100 GHz, with a Line-Reflect-Reflect-Line (LRRM) [8] calibration on an Impedance Standard Substrate (ISS). Subsequently, the effect of the pads and the 200- μm access lines were de-embedded using the technique described in [9].

For a fair comparison, all the circuits were allocated in an area of 120 μm of length and 132 μm of width. In particular, meandered microstrip TLines were designed with a margin of 20 μm in each of their sides due to the fact that in a practical case any circuit placed inside this spatial margin would suffer from electromagnetic coupling. On the other hand, the confined electrical field of S-CPW-based TLines allows to place circuitry close to the TLine, while avoiding cross-talk issues.

The used technology features 3 metal layers and an optional additional thick layer, which was used for the strips design. In this technology, aluminum is the metal of choice for the Back-End-Of-Line (BEOL). It is well-known that the conductivity of aluminum is sensibly lower than the conductivity of copper. Hence, better results are expected in a technology featuring a copper BEOL.

The microstrip line was designed with a width of 5.5 μm using the top thick metal for the signal strip and metal 1 for the ground plane. The meandered microstrip line was designed using the same metal layers and width than the classical microstrip and a spacing between parallel lines, D , of 11 μm , to avoid self-coupling between meandered strips. The type 1 MS-CPW was designed using metal 4 for the signal and ground strips and metal 3 for the floating ribbons. The width of the ground, W_G , and signal strips, W_S , was set to 8 μm and 10 μm , respectively. The distance between signal and ground strips, G , was set to 13 μm . Finally, the spacing between parallel lines, D , was designed using 19 μm . Concerning the type 2 MS-CPW, the floating ribbons were placed in the metal 3 layer and the signal and ground strips in the metal 4 layer. W_S and W_G were set to 5.5 μm and 16 μm , respectively. The distance between the signal and ground strips, G , was designed using 13 μm . The distance between parallel strips, D , was set to 12 μm . Finally, for both MS-CPWs the width and spacing of the floating ribbons was set to 0.6 and 2.6 μm , respectively.

Measurement and simulation are shown in Fig. 2 using solid and dashed lines, respectively. The presented results show very good agreement throughout the considered band. Fig. 2(a) shows the attenuation constant, α , throughout the considered frequency band of the measured TLines. At 80 GHz, the traditional microstrip presents an attenuation constant of around 1.3 dB/mm, α is equal to 4 dB/mm for the meandered microstrip, around 5 dB/mm for the MS-CPW type 1, and around 6.6 dB/mm for the MS-CPW type, respectively.

Fig. 2(b) presents the propagation constant and effective dielectric constant, $\epsilon_{r,eff}$, from 70 kHz to 100 GHz for the considered TLines. At 80 GHz, the classical microstrip

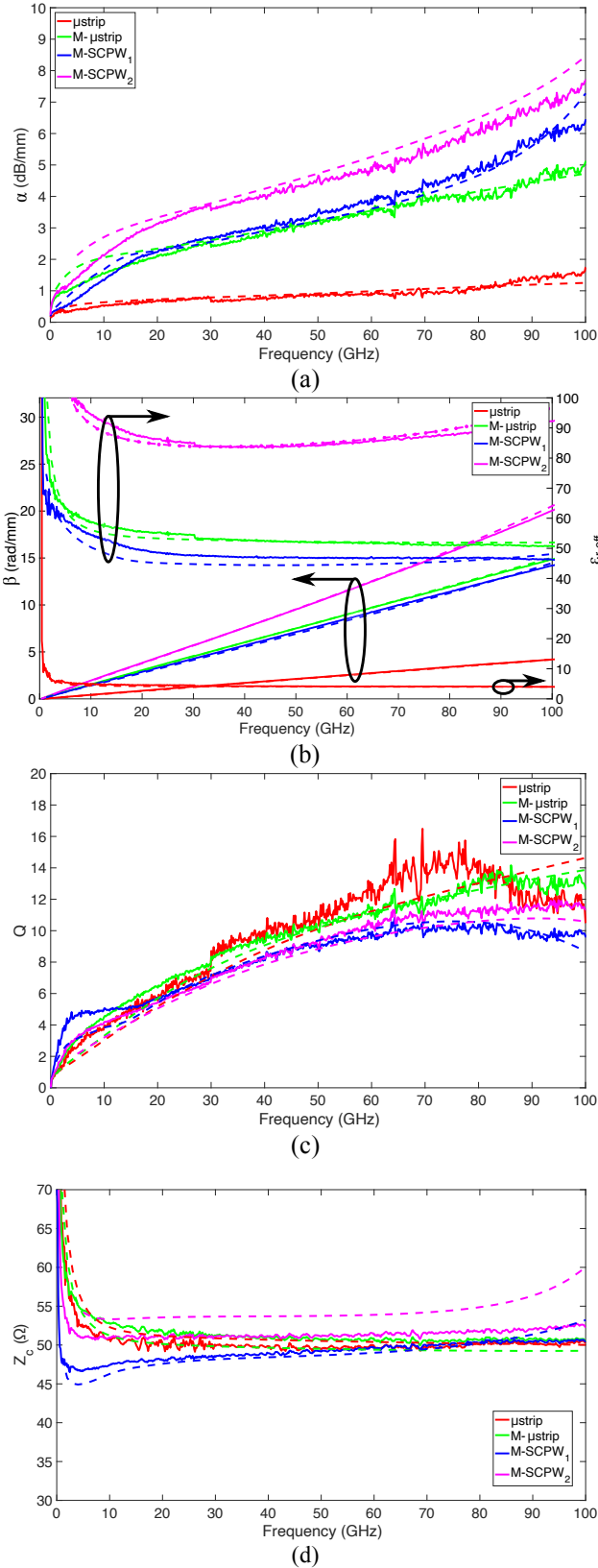


Fig. 2 Measurement (solid lines) and simulation results (dashed lines) of the considered TLines. (a) Attenuation constant. (b) Propagation constant and effective dielectric constant (c) Q-factor. (d) Characteristic impedance.

presents a β and an ϵ_{reff} of 3.5 rad/mm and 4.2, respectively. The meandered microstrip has a β and an ϵ_{reff} of 12 rad/mm and 51, respectively. The type 1 and 2 MS-CPWs present a β of around 11.5 and 16, respectively. Finally, these two TLines present an ϵ_{reff} of 47 and 88, respectively.

Note that the values presented in Figs. 2(a) and 2(b) express magnitudes per unit of length. These values were obtained using the effective length of the structure, 120 μm .

Figure 2(c) presents the Q-factor of the considered TLines. At 80 GHz, the microstrip and meandered microstrip present a Q-factor of 14 and 13, respectively. At the same frequency, the type 1 and type 2 MS-CPWs present Q-factors of 12 and 10, respectively.

Figure 2(d) presents the characteristic impedance, Z_c , of the considered TLines throughout the analyzed frequency band, with all of them achieving the desired 50 Ω . Note that the type 2 MS-CPW shows a resonance at around 120 GHz. This is due to the fact that, at this frequency this TLine reaches 180 degrees of electrical length.

Finally, Table I summarizes the performance of the considered TLines at 80 GHz. A Figure-of-Merit (FoM) is introduced, aiming to compare the miniaturization capabilities of the presented TLines. The considered FoM is obtained through the ratio between the electrical length of the TLine at the considered frequency and the occupied surface, which is equal to 0.016 mm^2 for all the considered TLines. For the microstrip and meandered microstrip, the FoM has a value of 26 and 91 rad/mm^2 , respectively. The FoM has a value of 87 and 120 rad/mm^2 for the type 1 and type 2 MS-CPWs, respectively.

Table I. TLINES PERFORMANCE COMPARISON AT 80 GHz

| TLine Type | Q | Z_c (Ω) | $\frac{\theta}{S}$ (rad/mm^2) | ϵ_{reff} |
|----------------------|----|--------------------|---|--------------------------|
| Microstrip | 14 | 50 | 26 | 4 |
| Meandered Microstrip | 13 | 50 | 91 | 51 |
| MS-CPW Type 1 | 12 | 50 | 87 | 47 |
| MS-CPW Type 2 | 10 | 52 | 120 | 88 |

V. CONCLUSIONS

In this work, four 50- Ω transmission line architectures were compared: (i) a straight microstrip line, (ii) a meandered microstrip line, (iii) a type 1 MS-CPW and (iv) a type 2 MS-CPW. The presented measurements, from 70 kHz to 100 GHz, show the capability of the type 2 MS-CPW to achieve high miniaturization. At 80 GHz, this TLine shows an ϵ_{reff} of 88, greater than the ϵ_{reff} shown by the

meandered microstrip or the type 1 M-SCPW, which is of 51 and 47, respectively. In addition, measurement results of the fabricated TLines show their equivalent performance in terms of Q-factor, with values ranging from 14 to 10 at 80 GHz.

Together with the presented TLines, other TLines were sent into fabrication to carry out a comparison between the synthesizable characteristic impedance in this technology by using these architectures. In addition, microstrip lines and MS-CPWs were fabricated in a 55-nm technology featuring a copper BEOL, aiming to show the Q-factor improvement due to the increased conductivity. Future work includes the characterization of these TLines as well as the development of a parametric electrical model.

ACKNOWLEDGMENT

This work has been funded by the European Union (ECSEL JU GA 737454: TowARds Advanced BiCMOS NanoTechnology platforms for RF applicatiOns – Taranto), and the General Directorate for Enterprises (DGE) in France.

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