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ON SYMMETRY VERSUS ASYNCHRONISM: AT THE EDGE OF UNIVERSALITY IN AUTOMATA NETWORKS. *

A PREPRINT

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ABSTRACT

An automata network (AN) is a finite graph where each node holds a state from a finite alphabet and is equipped with a local map defining the evolution of the state of the node depending on its neighbors. The global dynamics of the network is then induced by an update scheme describing which nodes are updated at each time step. We study how update schemes can compensate the limitations coming from symmetric local interactions. Our approach is based on intrinsic simulations and universality and we study both dynamical and computational complexity. By considering several families of concrete symmetric AN under several different update schemes, we explore the edge of universality in this two-dimensional landscape. On the way, we develop a proof technique based on an operation of glueing of networks, which allows to produce complex orbits in large networks from compatible pseudo-orbits in small networks.

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1 Introduction

Automata networks introduced in the 40s [24] are both a family of dynamical systems frequently used in the modeling of biological networks [29, 22] and a computational model [10, 4, 11, 33, 32]. An automata network is a (finite) graph where each node holds a state from a finite set Q and is equipped with a local transition map that determines how the state of the node evolves depending on the states of neighboring nodes. Alternatively, they can be described (in the deterministic case) through a global map $F : Q^V \rightarrow Q^V$ that defines the collective evolution of all nodes of the network. However, this global map hides two fundamental aspects at the heart of automata network literature [8, 9, 5, 2, 12, 1, 27]: the interaction graph (knowing on which nodes effectively depends the behavior of a given node) and the update schedule (knowing in which order and with which degree of synchrony are local transition maps of each node applied). The major influence of these two aspects on the dynamics of automata networks is clear, but the detailed understanding remains largely open.

The initial motivation of this paper lies in the striking interplay established in some cases between the symmetry of the local interactions and the synchrony of the update schedule: on one hand, a seminal result [13, 19] shows that symmetric threshold networks under fully synchronous updates cannot have periodic orbits of period more than 2, and that they have polynomially bounded transients; this implies the existence of a polynomial time algorithm to predict the future of a node from any given initial configuration. On the other hand, majority networks under partially asynchronous updates (precisely block-sequential update modes) were shown to have super-polynomial periodic orbits and a PSPACE-complete prediction problem [17, 4]. A similar result was obtained recently for conjunctive networks under a more general update mode called 'firing memory' [16]. It is well-known that threshold networks with synchronous updates but without the symmetry constraint are as capable as automata networks in general (essentially because they can embed any monotone Boolean circuit). Thus, the results above can be interpreted as the power of some asynchronous updates to break symmetries in the local rules in such a way that dynamical and computational complexity is recovered.

The purpose of this paper is to further study the capabilities of asynchronous update schemes to compensate for the limitation coming from constrained symmetric local interactions. Our approach is based in considering a hierarchy of symmetric local interactions and a hierarchy of update mode which allow us to precisely analyze how the dynamical and computational complexity varies depending on both dimensions. We are particularly interested in cases where the full richness of automata networks is achieved both dynamically and computationally, or more precisely, the cases where any automata network can be closely simulated, a property that we call universality and that we formalize below. The main question we ask (and partially answer) is where lies the edge of universality in this bi-dimensional landscape (local symmetry vs. asynchronism).

Detailed framework We aim at making our approach general and modular, while giving concrete and relevant examples that were considered in literature. Our framework can be decomposed as follows:

- the family of automata network we consider, called *concrete symmetric automata network* (CSAN), are described by labeled undirected graphs whose labels (both on vertices and edges) describe the local interaction maps; most of the results obtained as an application of our framework are on signed symmetric conjunctive Boolean networks and three subfamilies defined by local sign constraints: all positive (conjunctive networks), locally positive (one neighbor at least has a positive interaction), and no constraint on signs. This latter family has been much studied already (see *e.g.* [2, 28]).
- we consider four types of update modes: parallel, block-sequential and general periodic ones (well-known in the literature) and a much less studied one that we call *local clocks*, which was already considered in the setting of asynchronous cellular automata [6] and which is close to the recently introduced block-parallel mode [8]; we view all these update modes as particular local mechanisms of activation of nodes based on hidden local finite memory acting like clocks; more precisely, we formalize everything inside deterministic automata network under parallel update mode whose projection on a sub-component of states gives exactly the desired asynchronous behavior;
- the key aspect in the above choices is that considering a CSAN family X under an update mode Y is actually formalized as a new CSAN family Z, because the way update modes are coded is directly translated into constraints in the local interaction maps; our study therefore amounts to analyze the complexity of particular CSAN families;
- our complexity analysis focus on two aspects: dynamical complexity (transient, cycles, etc) and computational complexity (witnessed by various long term or short term prediction problems); we use the notion of

intrinsic simulation in order to hit two targets with one bullet: with a proof that A simulates a previously analyzed B we show complexity lower bounds on A both in dynamical and in the computational sense; in particular we have a clear formal notion of universality for a family of automata networks that implies maximal complexity in both aspects; the interest of our approach is its modularity, and the fact that it is not a priori limited to a small set of benchmark problems or properties: universality results can be used as a black box to then prove new corollaries on the complexity of other decision problems or other dynamical aspects; we stress that knowing that property/aspect X is complex for some family F of automata networks do not generally imply that property/aspect Y is also complex for family F, even in the case where Y is hard for automata networks in general (we actually give concrete examples of this below).

Our contributions This paper makes two kinds of contributions: it partially answers the main question addressed above, but it also establishes a new formalism and a general proof technique to obtain simulation and universality results suitable for automata network families with symmetric interactions.

- our main contribution is a detailed study of the trade-off between all local constraints and update modes of our framework described above (Section 6); what we obtain is a series of separation results and markers at the “edge of universality” showing that the interplay between local interactions and update modes is rich; the following table give a synthetic view of some of our results:

update mode	parallel	block sequential	local clocks	periodic
all positive	BPA	BPA	BPA	SPA
locally positive	BPA	BPA	SU	SU
free	BPA	SU	SU	SU

where BPA means *bounded period attractors* (there is a bound on the period of all attractors), SPA means *super-polynomial attractors* (attractors can be constructed whose period is super-polynomial is the number of nodes of the network) and SU means *strong universality*; strong universality (Definition 19) implies the existence of exponential attractors (Theorem 22) as well as maximal complexity for short term and long term prediction problems (Corollary 1).

- our second contribution is a method and proof technique for building complex networks, which is key in obtaining the above results. Indeed, the classical approach to obtain complex networks (both computationally and dynamically) is to design individual building blocs or gadgets that have a specific input/output behavior (e.g. some Boolean operators) and connect them, from outputs to inputs, in such a way that the desired global behavior is achieved by composition (e.g. a Boolean circuit); when the local interactions are symmetric or constrained, it is generally impossible to proceed like this because connecting the output of some gadget to the input of another one is making a two-way link that can induce feedback compromising the behavior of the first gadget. Of course this problem can be dealt with in particular cases (it was done in [16, 17]), but our approach is generic: we replace the connection between gadgets by a glueing operation of two networks on a common part identified in both (Definition 24), and the fundamental objects that compose through this glueing operation are pseudo-orbits (Lemma 25) and not directly input/output relations. At the end, we obtain a proof method that allows to show strong universality (therefore both dynamical and computational complexity) by just exhibiting a finite set of gadgets and pseudo-orbits verifying suitable compatibility conditions (Definition 29).
- besides the two main contributions above, we also develop a complete formalization of (intrinsic) simulation between families of automata networks and (intrinsic) universality. This kind of approach is well-known for other models (see e.g. [7]), and informally or indirectly present in various contributions on automata networks (see e.g. [11]). We however believe that our efforts of formalization clarify important aspects: the choice of a concrete representation when considering decision problems with automata networks as input (see Section 4.2), the existence of several natural definitions of universality (Definition 19) with different implication on dynamical complexity (Theorem 22), the fact that two widely used prediction problems are actually incomparable (one can be hard while the other is easy in some family of automata networks, and reciprocally, see Theorem 18), and the fact that some concrete families can exhibit dynamical complexity while failing to be universal (theorems 35 and 37 and 39); to put it shortly, we show that a simulation/universality approach is better than taking individual decision problems or dynamical features as benchmarks.
- finally, the local clocks update mode we consider was not studied theoretically before in automata networks (as far as we know), and proves to be useful as an intermediate one lying between block sequential and general periodic modes. More generally, we believe that our unified approach which consists in viewing

various update modes as local mechanisms of activation using some finite local information is natural and deserves further developments. For instance, it captures the 'firing memory' mode of [16] and more general modes can be proposed on this principle.

Organization of the paper We start in Section 2 by giving basic definitions about automata networks and families, including our hierarchy of concrete symmetric automata networks. We then introduce our hierarchy of update schemes in Section 3, including its formalization as asynchronous extensions. In Section 4, we detail our notions of intrinsic simulations between individual automata networks, then between families. From there, we introduce the notion of intrinsic universality and we study their consequences both in terms of dynamics and computational complexity. In Section 5, we formalize our toolbox based on glueing, \mathcal{G} -networks and gadgets. We also study various families of \mathcal{G} -networks, including the canonical universal ones that will serve as a base to establish universality results later on. In Section 6, we show our main results on local interaction rules versus update modes classification that leads to the table presented above. Finally, we conclude by discussing some research perspectives in Section 7.

2 Automata networks and families

A *graph* is a pair $G = (V, E)$ where V and E are finite sets satisfying $E \subseteq V \times V$. We will call V the set of *nodes* and the set E of *edges*. We call $|V|$ the *order* of G and we usually identify this quantity by the letter n . Usually, as E and V are finite sets we will implicitly assume that there exists an ordering of the vertices in V from 1 to n (or from 0 to $n - 1$). Sometimes we will denote the latter set as $[n]$. If $G = (V, E)$ and $V' \subseteq V, E' \subseteq E$ we say that G' is a *subgraph* of G . We call a graph $P = (V, E)$ of the form $V = \{v_1, \dots, v_n\}, E = \{(v_1v_2), \dots, (v_{n-1}, v_n)\}$ a *path graph*, or simply a *path*. We often refer to a path by simply denoting its sequence of vertices $\{v_1, \dots, v_n\}$. We denote the *length* of a path by its number of edges. Whenever $P = (V = \{v_1, \dots, v_n\}, E = \{(v_1v_2), \dots, (v_{n-1}, v_n)\})$ is a path we call the graph in which we add the edge $\{v_n, v_1\}$ a *cycle graph* or simply a *cycle* and we call it C where $C = P + \{v_n, v_1\}$. Analogously, a cycle is denoted usually by a sequence of nodes and its length is also given by the amount of edges (or vertices) in the cycle. Depending of the length of C we call it a k -cycle when k is its length. A non-empty graph is called *connected* if any pair of two vertices u, v are linked by some path. Given any non-empty graph, a maximal connected subgraph is called a connected component.

We call *directed graph* a pair $G = (V, E)$ together with two functions $\text{init} : E \rightarrow V$ and $\text{ter} : E \rightarrow V$ where each edge $e \in E$ is said to be directed from $\text{init}(e)$ to $\text{ter}(e)$ and we write $e = (u, v)$ whenever $\text{init}(e) = u$ and $\text{ter}(e) = v$. There is also a natural extension of the definition of paths, cycles and connectivity for directed graphs in the obvious way. We say a directed graph is strongly connected if there is a directed path between any two nodes. A strongly connected component of a directed graph $G = (V, E)$ is a maximal strongly connected subgraph.

Given a (non-directed) graph $G = (V, E)$ and two vertices u, v we say that u and v are neighbors if $(u, v) \in E$. Remark that abusing notations, an edge (u, v) is also denoted by uv . Let $v \in V$, we call $N_G(v) = \{u \in V : uv \in E\}$ (or simply $N(v)$ when the context is clear) the set of neighbors (or *neighborhood*) of v and $\delta(G)_v = |N_G(v)|$ to the *degree* of v . Observe that if $G' = (V', E')$ is a subgraph of G and $v \in V'$, we can also denote by $N_{G'}(v)$ the set of its neighbors in G' and the degree of v in G' as $\delta(G')_v = |N_{G'}(v)|$. In addition, we define the *closed neighborhood* of v as the set $N[v] = N(v) \cup \{v\}$ and we use the following notation $\Delta(G) = \max_{v \in V} \delta_v$ for the *maximum degree* of G .

Additionally, given $v \in V$, we will denote by E_v to its set of *incident edges*, i.e., $E_v = \{e \in E : e = uv\}$. We will use the letter n to denote the order of G , i.e. $n = |V|$. Also, if G is a graph whose sets of nodes and edges are not specified, we use the notation $V(G)$ and $E(G)$ for the set of vertices and the set of edges of G respectively. In the case of a directed graph $G = (V, E)$ we define for a node $v \in V$ the set of its *in-neighbors* by $N^-(v) = \{u \in V : (u, v) \in E\}$ and its *out-neighbors* as $N^+(v) = \{u \in V : (v, u) \in E\}$. We have also in this context the indegree of v given by $\delta^- = |N^-(v)|$ and its *outdegree* given by $\delta^+ = |N^+(v)|$.

During the most part of the text, and unless explicitly stated otherwise, every graph G will be assumed to be connected and undirected. We start by stating the following basic definitions, notations and properties that we will be using in the next sections. In general, Q and V will denote finite sets representing the alphabet and the set of nodes respectively. We define $\Sigma(Q)$ as the set of all possible permutations over alphabet Q . We call an *abstract automata network* any function $F : Q^V \rightarrow Q^V$. Note that F induces a dynamics in Q^V and thus we can see (Q^V, F) as dynamical system. In this regard, we recall some classical definitions. We call a *configuration* to any element $x \in Q^V$. If $S \subseteq V$ we define the restriction of a configuration x to V as the function $x|_S \in Q^S$ such that $(x|_S)_v = x_v$ for all $v \in S$. In particular, if $S = \{v\}$, we write x_v .

Given an initial configuration $x \in Q^V$, we define the *orbit* of x as the sequence $\mathcal{O}(x) = (F^t(x))_{t \geq 0}$. We define the set of *limit configurations* or *recurrent configurations* of F as $L(F) = \bigcap_{t \geq 0} F^t(Q^V)$. Observe that since Q is finite and F is deterministic, each orbit is eventually periodic, i.e. for each $x \in Q^V$ there exist some $\tau, p \in \mathbb{N}$ such that $F^{\tau+p}(x) = F^\tau(x)$ for all $x \in Q^V$. Note that if x is a limit configuration then, its orbit is periodic. In addition, any configuration $x \in Q^V$ eventually reaches a limit configuration in finite time. We denote the set of orbits corresponding to periodic configurations as $\text{Att}(F) = \{\mathcal{O}(x) : x \in L(F)\}$ and we call it the set of *attractors* of F . We define the *global period* or simply the *period* of $\bar{x} \in \text{Att}(F)$ by $p(\bar{x}) = \min\{p \in \mathbb{N} : \bar{x}(p) = \bar{x}(0)\}$. If $p(\bar{x}) = 1$ we say that \bar{x} is a *fixed point* and otherwise, we say that \bar{x} is a *limit cycle*.

Given a node v , its behavior $x \mapsto F(x)_v$ might depend or not on another node u . This dependencies can be captured by a graph structure which plays an important role in the theory of automata networks (see [9] for a review of known results on this aspect). This motivates the following definitions.

Definition 1. Let $F : Q^V \rightarrow Q^V$ be an abstract automata network and $G = (V, E)$ a directed graph. We say G is a communication graph of F if for all $v \in V$ there exist $D \subseteq N_v^-$ and some function $f_v : Q^D \rightarrow Q$ such that $F(x)_v = f_v(x|_D)$. The interaction graph of F is its minimal communication graph.

Note that by minimality, for any node v and any in-neighbor u of v in the interaction graph of some F , then the next state at node v effectively depends on the actual state at node u . More precisely, there is some configuration $c \in Q^V$ and some $q \in Q$ with $q \neq c_u$ such that $F(c)_v \neq F(c')_v$ where c' is the configuration c where the state of node u is changed to q . This notion of effective dependency is sometimes taken as a definition of edges of the interaction graph.

From now on, for an abstract automata network F and some communication graph G of F we use the notation $\mathcal{A} = (G, F)$. In addition, by abuse of notation, we also call \mathcal{A} an abstract automata network. We define a set of automata networks or a *abstract family* of automata networks on some alphabet Q as a set $\mathcal{F} \subseteq \bigcup_{n \in \mathbb{N}} \{F : Q^V \rightarrow Q^V : V \subseteq [n]\}$.

Note that the latter definition provides a general framework of study as it allows us to analyze an automata network as an abstract dynamical system. However, as we are going to be working also with a computational complexity framework, it is necessary to be more precise in how we represent them. In this regard, one possible slant is to start defining an automata network from a communication graph. One of the main definition used all along this paper is that of *concrete symmetric automata network*. Roughly, they are non-directed labeled graph G (both on nodes and edges) that represent an automata network. They are *concrete* because the labeled graph is a natural concrete representation upon which we can formalize decision problems and develop a computational complexity analysis. They are *symmetric* in two ways: first their communication graph is non-directed, meaning that an influence of node u on node v implies an influence of node v on node u ; second, the behavior of a given node is blind to the ordering of its neighbors in the communication graph, and it can only differentiate its dependence on neighbors when the labels of corresponding edges differ.

Definition 2. Given a non-directed graph $G = (V, E)$, a vertex label map $\lambda : V \rightarrow (Q \times 2^Q \rightarrow Q)$ and an edge label map $\rho : E \rightarrow \Sigma(Q)$, we define the tuple $\mathcal{A} = (G, \lambda, \rho)$ and we call it a *concrete symmetric automata network (CSAN)* associated to the graph G . A family of concrete symmetric automata networks (*CSAN family*) \mathcal{F} is given by an alphabet Q , a set of local labeling constraints $\mathcal{C} \subseteq \Lambda \times R$ where $\Lambda = \{\lambda : Q \times 2^Q \rightarrow Q\}$ is the set of possible vertex labels and $R = 2^{\Sigma(Q)}$ is the set of possible neighboring edge labels. We say a CSAN (G, λ, ρ) belongs to \mathcal{F} if for any vertex v of G with incident edges E_v it holds $(\lambda(v), \rho(E_v)) \in \mathcal{C}$.

Note that the labeling constraints defining a CSAN family are local. In particular, the communication graph structure is a priori free. This aspect will play an important role later when building arbitrarily complex objects by composition of simple building blocks inside a CSAN family.

Let us now define the abstract automata network associated to a CSAN, by describing the semantics of labels defined above. Intuitively, labels on edges are state modifiers, and labels on nodes give a map that describes how the node changes depending on the set of states appearing in the neighborhood, after application of state modifiers. We use the following notation: given $k \geq 1$, $\sigma = (\sigma_1, \dots, \sigma_k) \in \Sigma(Q)^k$ and $x \in Q^k$ we note $x_\sigma = \{\sigma_1(x_1), \dots, \sigma_k(x_k)\}$. In addition, given $x \in Q^n$ we define the restriction of x to some subset $U \subseteq V$ as the partial configuration $x|_U \in Q^{|U|}$ such that $(x|_U)_u = x_u$ for all $u \in U$.

Definition 3. Given a CSAN (G, λ, ρ) , its associated global map $F : Q^V \rightarrow Q^V$ is defined as follows. For all node $v \in V$ and for all $x \in Q^n$:

$$F(x)_v = \lambda_v(x_v, (x|_{N(v)})_{\rho^v}),$$

where $N(i) = \{u_1, \dots, u_{\delta_u}\}$ is the neighborhood of v and $\rho^v = (\rho(v, u_1), \dots, \rho(v, u_{\delta_u}))$.

Note that if (G, λ, ρ) is a concrete automata network and F its global rule then, F is an abstract automata network with interaction graph included in G .

Our core set of CSAN families. Now we present some examples of families of automata networks that we study in this paper. They differ in the set of allowed labels and their degree of local symmetry.

Definition 4. Let $Q = \{0, 1\}$. The family of signed conjunctive automata networks (SCN) is the set of CSAN (G, λ, ρ) where for each node v we have $\lambda_v(q, X) = \min X = \bigwedge X$ and, for each edge e , ρ_e is either the identity map or the map $x \mapsto 1 - x$.

The family of locally positive conjunctive networks (LPCN) is the set of signed conjunctive automata networks (G, λ, ρ) where we require, in addition, that for each node v there is at least on edge e incident to v such that ρ_e is the identity.

Finally, the family of globally positive conjunctive networks (GPCN) is the set of signed conjunctive automata networks where, for all edge e , ρ_e is the identity.

Definition 5. Let Q be a totally ordered set. The family of min-max automata networks over Q is the set of CSAN (G, λ, ρ) such that for each edge e , ρ_e is the identity map and, for each node v , $\lambda_v(q, X) = \max X$ or $\lambda_v(q, X) = \min X$.

Remark 1. If $Q = \{0, 1\}$ then, \mathcal{F} is the class of AND-OR networks, i.e., $F(x)_i = \bigwedge_{j \in N(i)} x_j$ or $F(x)_i = \bigvee_{j \in N(i)} x_j$

3 Update schemes

Through its global rule F , an automata network \mathcal{A} defines a dynamics over Q^V by the subsequent iterations of F . This is the most natural way to define a dynamics from an automata network and its usually said that in this case the dynamics follows a *parallel* update scheme. The name comes from the fact that, at each time step, each node in the network updates its state according to its local function *at the same time*. Nevertheless, starting from the network structure of \mathcal{A} , one can also induce a dynamical system over Q^V by considering other ways of updating in which not all the nodes are updated at the same time. Observe that, generally speaking, this latter notion demands some sort of temporal information in the nodes that determines which nodes have to be updated at a particular time step. We use the following general definition of *update scheme*.

Definition 6. Consider an abstract automata network $F : Q^V \rightarrow Q^V$. An update scheme is a sequence $\mu : \mathbb{N} \rightarrow 2^V$. Given an update scheme μ and a natural number $k \in \mathbb{N}$ we call an *intermediate step* of the dynamics given by μ to the function $F^{\mu(k)}$ defined by $F^{\mu(k)}(x)_i = \begin{cases} F(x)_i & \text{If } i \in \mu(k), \\ x_i & \text{otherwise.} \end{cases}$ We define an *orbit* given by μ starting from some $x \in Q^V$ as the sequence $\mathcal{O}_{\mu, F}(x) = (x, F^{\mu(0)}(x), F^{\mu(1)}(F^{\mu(0)}(x)), F^{\mu(2)}(F^{\mu(1)}(F^{\mu(0)}(x))), \dots)$

3.1 Periodic update schemes

One of the most studied types of update schemes are the *periodic update schemes*, i.e. modes where map μ is periodic. This class contains well-studied particular cases, for instance: parallel update scheme, in which all the nodes of the networks are updated at the same time (see Figure 1) and also the block sequential update schemes in which each node is updated once every p steps (but not necessarily all at the same time). In addition, we explore a new class of update schemes which contains all the rest that it is called *local clocks*. In this class, each node v is updated once every p_v steps but the frequency of update p_v might depend on the node. This scheme can be seen intuitively as follows which justify the name: each node possesses an internal clock that ticks periodically and triggers an update of the node.

Definition 7. We say that an update scheme μ is a periodic update scheme if there exists $p \in \mathbb{N}$ such that $\mu(n + p) = \mu(n)$ for all $n \in \mathbb{N}$. Moreover, we say that μ is

- a block sequential scheme if there are subsets (called blocks) $B_0, \dots, B_{p-1} \subseteq V$ forming a partition of V such that $\mu(n) = B_{n \bmod p}$
- a local clocks scheme if for each $v \in V$ there is a local period $\tau_v \in \mathbb{N}$ and a shift $0 \leq \delta_v < \tau_v$ such that $v \in \mu(n) \iff \delta_v = n \bmod \tau_v$.

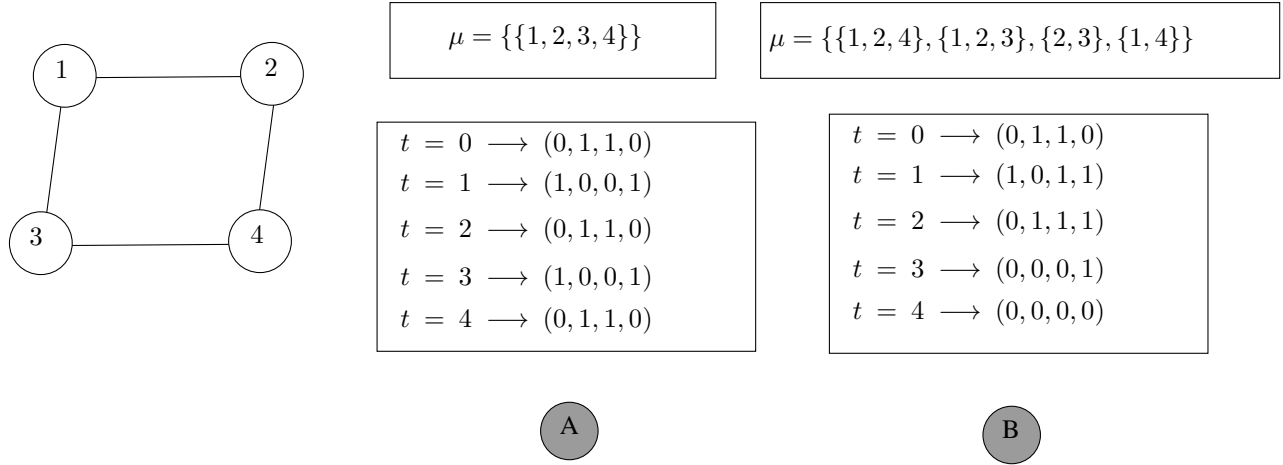


Figure 1: Synchronous update scheme and general periodic update scheme for the same conjunctive automata network. Local function is given by the minimum (AND function) over the set of states of neighbors for each node. A) Synchronous or parallel update scheme. In this case μ has period 1 and all nodes are updates simultaneously. Observe that dynamics exhibits an attractor of period 2 B) General periodic update scheme over a conjunctive network. In this case μ has period 4 and dynamics reach a fixed point after 4 time steps. Observe that there is no restriction on how many times a node is updated. For example, 1 is updated 3 times every 4 time steps but 4 is updated only 2 times every 4 time steps.

For a concrete example on how these update schemes work, see Figure 2 in which different dynamics for a simple conjunctive network under block sequential and local clocks update schemes are shown.

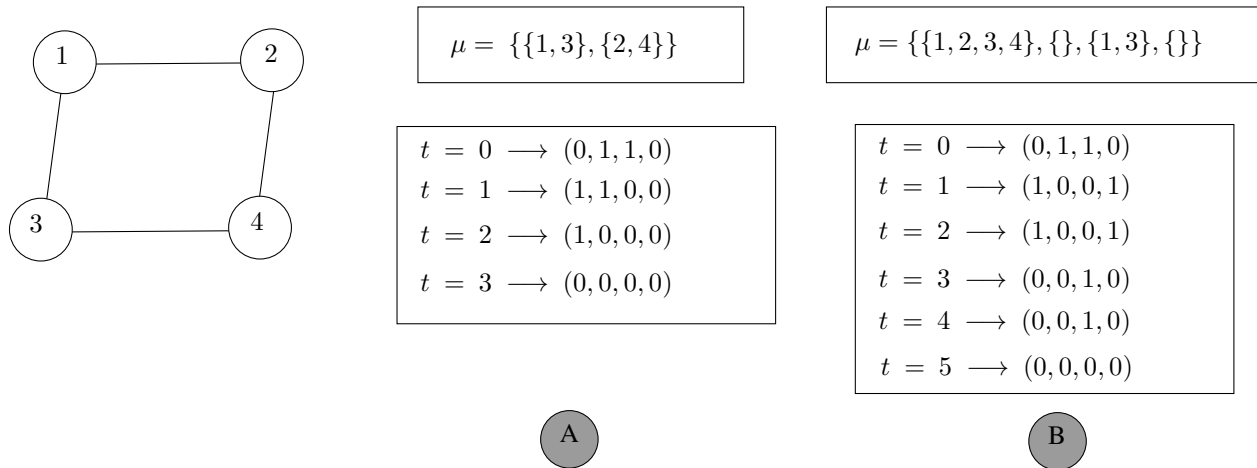


Figure 2: Block sequential and local clocks update schemes over a simple conjunctive network. Local functions are given by the minimum (AND) over the states of the neighbors of each node. A) Block sequential update scheme. In this case function μ is defined by two blocks: $\{1, 3\}$ and $\{2, 4\}$. Dynamics reach a fixed point after 3 time steps. B) Local clocks update scheme. In this case each node has an internal clock with different period. Nodes 1 and 3 are updated every two steps ($\tau_1 = \tau_3 = 2$) and nodes 2 and 4 are updated every 4 time steps (i.e. $\tau_2 = \tau_4 = 4$). Shift parameter is 0 for all nodes $\delta_1 = \delta_2 = \delta_3 = \delta_4 = 0$. Dynamics reach a fixed point after 3 time steps.

Block sequential and local clocks schemes are clearly periodic schemes. Moreover, any block sequential scheme given by $B_0, \dots, B_{p-1} \subseteq V$ is a local clocks scheme given by $\tau_v = p$ and $\delta_v = i \iff v \in B_i$ for all $v \in V$. As already said, block sequential schemes can thus be seen as local clocks schemes where all nodes share the same update frequency. General periodic update schemes allows different time intervals between two consecutive updates of a node, which local clocks schemes obviously can't do (see Figure 1, B). We will see later the tremendous consequences that such subtle differences in time intervals between updates at each node can have. For now let us just make the formal

observation that the inclusions between these families of update schedules are strict when focusing on the sets of maps μ .

Remark 2. A so-called block-parallel scheme has also been considered more recently [8] which is defined by a set of list of nodes $L_i = (v_{i,j})_{0 \leq j < p_i}$ (for $1 \leq i \leq k$) forming a partition (i.e. such that $v_{i,j}$ are all distinct and $\cup_{i,j} v_{i,j} = V$) to which is associated the map μ such that $v_{i,j} \in \mu(n) \iff j = n \bmod p_i$. It is a particular case of our definition of local clocks scheme above with the additional constraints that the size of the set $\mu(n)$ of updated nodes is constant with n (see Figure 3 for an example). We note that, conversely, any local clocks scheme on a given networked can be simulated by a block-parallel scheme by artificially adding disconnected nodes that do nothing but satisfy the constraint of $\mu(n)$ being of constant size.

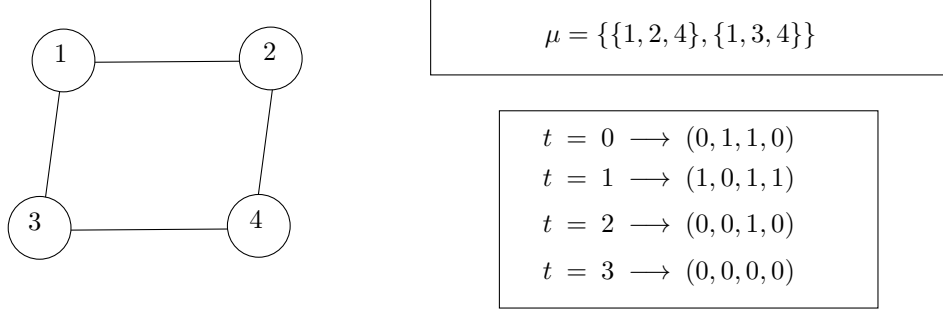


Figure 3: Block parallel update scheme defined over a conjunctive network. Updating list is given by $L = \{(1), (2, 3), (4)\}$. Observe that a constant amount of nodes (equal to the length of L , i.e., 3) is updated at each time step. Dynamics reach a fixed point after 3 time-steps.

3.2 Projections and asynchronous extensions

Now we present a dynamical formalism that allow us to include all the update schemes presented above and possibly other ones into one formalism. We remark that in all periodic schemes, a given node can take the decision to update or not by simply keeping track of the current value of time modulo the period. The key observation is that, when we add the knowledge of time modulo the period at each node as a new component of states, the whole system becomes deterministic. In fact, we are recovering the original dynamics of some automata network with alphabet Q under a periodic update scheme by projecting a specific deterministic automata network with alphabet $Q' \times Q$ onto Q .

Let $F : Q^V \rightarrow Q^V$ be an abstract automata network. We define its *asynchronous* version as an automata network that in every time-step (non-deterministically) choose if a node i should be updated or if it will be stay in the same state. More precisely a *asynchronous* version of F is a non-deterministic function $F^* : Q^V \rightarrow (\mathcal{P}(Q))^V$ such that $F^*(x)_i = \{x_i, F(x)_i\}$. Note that, analogously to the deterministic case one can define an orbit starting from x of F^* as a sequence of states $\mathcal{O}_{F^*}(x) = x^0 = x, x^1, x^2, \dots, x^t, \dots \in Q^n$ such that $x_i^s \in F(x^{s-1})_i$ for $i \in V$ and $s \geq 1$. Note also that, given $x \in Q^V$ and an orbit $\mathcal{O}_{F^*}(x)$ we can see $\mathcal{O}_{F^*}(x)$ as a particular realization of certain update scheme μ . More precisely, there exist an update scheme μ (which is defined in the obvious way i.e. by updating the corresponding nodes in every time step according to points in $\mathcal{O}_{F^*}(x)$) such that for every $x^s \in \mathcal{O}_{F^*}(x)$ we have $x^s = (\mathcal{O}_{F, \mu}(x))^s$. In addition, we have that for each update scheme μ there exist an orbit of F^* which coincides with its dynamics in every time step. Thus, we could work with F^* in order to globally study all possible update schemes. However, we are interested in specific update schemes and we would like to continue working in a deterministic framework in order to keep things simple (in particular the notion of simulation that we define later).

In order to achieve this task, we introduce the following notion of asynchronous extension which is a way to produce the dynamics of different update schemes through projection.

Definition 8. Let Q be a finite alphabet and $Q' = Q \times R$ where R is finite. Let $F : Q^V \rightarrow Q^V$ and $F' : Q'^V \rightarrow Q'^V$ two abstract automata networks. We say that F is a projection system of F' if for $x \in Q'^V$ and F' is an asynchronous extension of F

$$\bar{\pi}(F'(x)) \in F^*(\bar{\pi}(x))$$

where $\bar{\pi}$ is the node-wise extension of the projection $\pi : Q' \rightarrow Q$ such that $\pi(q, r) = q$ for all $q' = (q, r) \in Q'$.

We show hereunder that the dynamics associated to any of the previously presented periodic update schemes can be described as an asynchronous extension over a product alphabet. To simplify notations we sometimes identify $(A \times B \times \dots)^V$ with $A^V \times B^V \times \dots$.

Definition 9 (block sequential extension). *Let $F : Q^V \rightarrow Q^V$ be an abstract automata network. Let $b \leq n$ and let $Q' = Q \times \{0, \dots, b-1\}$. We define the block sequential extension of F with b blocs as the automata network $F' : (Q')^V \rightarrow (Q')^V$ such that for all $x = (x_Q, x_b) \in Q'^V$ and all $v \in V$:*

$$F'(x)_v = \begin{cases} (F(x_Q)_v, (x_b)_v - 1 \bmod b) & \text{if } (x_b)_v = 0, \\ ((x_Q)_v, (x_b)_v - 1 \bmod b) & \text{else.} \end{cases}$$

Definition 10 (local clocks extension). *Let $F : Q^V \rightarrow Q^V$ be an abstract automata network. Let $c \in \mathbb{N}$ and let $Q' = Q \times \{0, \dots, c-1\} \times \{1, \dots, c\}$. We define the local clocks extension of F with clock length c as the automata network $F' : (Q')^V \rightarrow (Q')^V$ such that for all $x = (x_Q, x_c, x_m) \in Q'^V$ and all $v \in V$:*

$$F'(x)_v = \begin{cases} (F(x_Q)_v, (\psi_{(x_m)_v}[(x_c)_v]) + 1 \bmod (x_m)_v, (x_m)_v) & \text{if } (x_c)_v = 0, \\ ((x_Q)_v, (\psi_{(x_m)_v}[(x_c)_v]) + 1 \bmod (x_m)_v, (x_m)_v) & \text{else.} \end{cases}$$

$\psi_m(r) : \{0, \dots, c-1\} \rightarrow \{0, \dots, c-1\}$ is such that $\psi_m(r) = \begin{cases} r & \text{if } r \leq m-1, \\ m-1 & \text{else.} \end{cases}$

Definition 11 (periodic extension). *Let $F : Q^V \rightarrow Q^V$ be an abstract automata network. Let $p \in \mathbb{N}$ and let $Q' = Q \times \{0, \dots, p-1\} \times 2^{\{0, \dots, p-1\}}$. We define the periodic extension of F with period length p as the automata network $F' : (Q')^V \rightarrow (Q')^V$ such that for all $x = (x_Q, x_p, x_s) \in Q'^V$ and all $v \in V$:*

$$F'(x)_v = \begin{cases} (F(x_Q)_v, (x_p)_v + 1 \bmod p, (x_s)_v) & \text{if } (x_p)_v \in (x_s)_v, \\ ((x_Q)_v, (x_p)_v + 1 \bmod p, (x_s)_v) & \text{else.} \end{cases}$$

Remark 3. *Observe that, given an abstract automata network $F : Q^V \rightarrow Q^V$ and an asynchronous extension $F' : (Q \times R)^V \mapsto (Q \times R)^V$ of the type previously defined i.e. a block sequential extension, a local clocks extension or a periodic extension, both F' and F^μ (where μ is some of the latter update schemes) describe the same dynamics. In fact, let us illustrate this fact by analyzing the case of the block sequential extension (the other cases are analogous). Let $b \geq 1$ and $\mu_b = (I_0, \dots, I_{b-1})$. Note that (I_0, \dots, I_{b-1}) is an ordered partition of V . On one hand, we consider an arbitrary initial condition $x \in Q^V$ and the correspondent block sequential orbit $\mathcal{O}_{\mu, F}(x)$. On the other hand, we consider a block extension $F' : (Q \times \{0, \dots, b-1\})^V \mapsto (Q \times \{0, \dots, b-1\})^V$ and an initial condition $z \in (Q \times \{0, \dots, b-1\})^V$ given by $z_v = (x_v, y_v)$ where $y_v = k$ if and only if $v \in I_k$ for $1 \leq k \leq b$. By the definition of F' , we have that $\bar{\pi}(F'(z)) = (\mathcal{O}_{\mu, F}(x))^1$ since the only nodes v in which F is applied (in the first coordinate) are the ones such that $v \in I_0$. In addition, we have that for each v in V , $\bar{\pi}_2(F'(x))_v = y_v - 1 \bmod b$, where $\bar{\pi}_2$ is the node-wise extension of the projection $\pi_2 : Q \times \{0, \dots, b-1\} \mapsto \{0, \dots, b-1\}$. Thus, we have $\bar{\pi}(F'(F'(x))) = (\mathcal{O}_{\mu, F}(x))^2$. Iteratively, we deduce $\bar{\pi}(F'^t(x)) = (\mathcal{O}_{\mu, F}(x))^t$ for each $t \geq 1$.*

Conversely, let us choose an arbitrary initial condition $x = (x_Q, x_b) \in (Q \times \{0, \dots, b-1\})^V$. We define the ordered partition I_0, \dots, I_{b-1} given by $v \in I_k$ if and only if $(x_b)_v = k$ for $0 \leq k \leq b-1$. Then, the second coordinate of the initial condition x_b induces a block sequential update scheme μ_{x_b} which is defined by the latter ordered partition.

The previous definitions are formalized for every abstract automata network. We now focus on CSAN families where the extensions are also CSAN as show in the following lemma.

Lemma 12. *Let F be a CSAN, then any block sequential extension (resp. local clocks extension, resp. periodic extension) of F is a CSAN. Moreover, for any CSAN family \mathcal{F} and any fixed b , the set of block sequential extensions with b blocs of networks of \mathcal{F} is again a CSAN family. The same holds for local clocks and periodic extensions.*

Proof. In each case, the definition of the extension F' with alphabet $Q' = Q \times R$ is such that the action of F' on the R component is purely local (the new value of the R component of a node evolves as a function of the old value of this R component) and the value of the R component at a node determines alone if the Q component should be updated according to F or left unchanged. Therefore clearly F' is a CSAN if F is.

In the context of a CSAN family \mathcal{F} , the CSAN definition of F' involves only local constraints coming from $F \in \mathcal{F}$ and the action on the R -component is the same at each node. So the second part of the lemma is clear. \square

Remark 4. *This approach by asynchronous extensions can also capture non-periodic update schemes. For instance [14] studies an update scheme for Boolean networks called firing memory which uses local delays at each node and, in addition, makes the delay mechanism depend on the state of the current configuration at the node. Firing memory schemes can be captured as an asynchronous extension in such a way that the above lemma for the CSAN case still works.*

To sum up, our formalism allows to treat variations in the update scheme as a change in the CSAN family considered. Given a CSAN family \mathcal{F} and integers b, c, p , we introduce the following notations:

- $\mathcal{F}^{\text{BLOCK},b}$ is the CSAN family of all block sequential extensions of networks from \mathcal{F} with b blocks,
- $\mathcal{F}^{\text{CLOCK},c}$ is the CSAN family of all local clocks sequential extensions of networks from \mathcal{F} with clock length c ,
- $\mathcal{F}^{\text{PER},p}$ is the CSAN family of all periodic extensions of networks from \mathcal{F} with period p .

4 Simulation and universality

In this section we introduce a key tool used in this paper: simulations. The goal is to easily prove computational or dynamical complexity of some family of automata networks by showing it can simulate some well-known reference family where the complexity analysis is already established. It can be thought as a complexity or dynamical reduction. Simulations of various kinds are often implicitly used in proofs of dynamical or computational hardness. We are going instead to explicitly define a notion of simulation and establish hardness results as corollaries of simulation results later in the paper. To be more precise, we will first define a notion of simulation between individual automata networks, and then extend it to a notion of simulation between families. This latter notion, which is the one we are really interested in requires more care if we want to use it as a notion of reduction for computational complexity. We introduce all the useful concepts progressively in the next subsections.

4.1 Simulation between individual automata networks

At the core of our formalism is the following definition of simulation where an automata network F is simulated by an automata network G with a constant time slowdown and using blocs of nodes in G to represent nodes in F . Our definition is rather strict and requires in particular an injective encoding of configurations of F into configurations of G . We are not aware of a published work with this exact same formal definition, but close variants certainly exist and it is a direct adaptation to finite automata networks of a classical definition of simulation for cellular automata [7].

Definition 13. *Let $F : Q_F^{V_F} \rightarrow Q_F^{V_F}$ and $G : Q_G^{V_G} \rightarrow Q_G^{V_G}$ be abstract automata networks. A block embedding of $Q_F^{V_F}$ into $Q_G^{V_G}$ is a collection of blocs $D_i \subseteq V_G$ for each $i \in V_F$ which forms a partition of V_G together with a collection of patterns $p_{i,q} \in Q_G^{D_i}$ for each $i \in V_F$ and each $q \in Q_F$ such that $p_{i,q} = p_{i,q'}$ implies $q = q'$. This defines an injective map $\phi : Q_F^{V_F} \rightarrow Q_G^{V_G}$ by $\phi(x)_{D_i} = p_{i,x_i}$ for each $i \in V_F$. We say that G simulates F via block embedding ϕ if there is a time constant T such that the following holds on $Q_F^{V_F}$:*

$$\phi \circ F = G^T \circ \phi.$$

See Figure 4 for a scheme of block simulation. In the following, when useful we represent a block embedding as the list of blocs together with the list of patterns. The size of this representation is linear in the number of nodes (for fixed alphabet).

Remark 5. *It is convenient in many concrete cases to define a block embedding through blocs D_i that are disjoint but do not cover V_G and add a context block C disjoint from the D_i that completes the covering of V_G . In this variant a block embedding of $Q_F^{V_F}$ into $Q_G^{V_G}$ is given by patterns $p_{i,q}$ and a constant context pattern $p_C \in Q_G^C$ which define an injective map $\phi : Q_F^{V_F} \rightarrow Q_G^{V_G}$ by $\phi(x)_{D_i} = p_{i,x_i}$ for each $i \in V_F$ and $\phi(x)_C = p_C$. This variant is actually just a particular instance of Definition 13 because we can include C in an arbitrary block ($D_i \leftarrow D_i \cup C$) and define the block embedding as in Definition 13.*

In our proofs of simulations in section 6, the variant blocs/context will be particularly relevant because the size of blocs will be bounded while the context will grow with the size of the considered automata. Said differently, the information about an encoded state will be very localized.

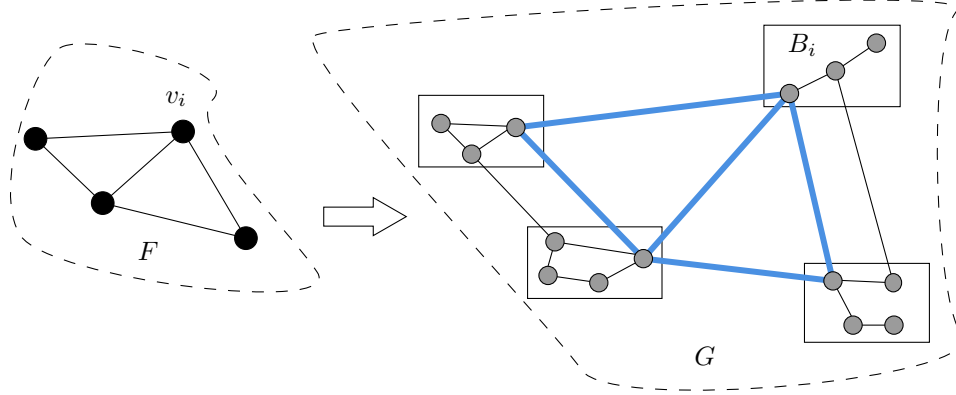


Figure 4: Scheme of one-to-one block simulation. In this case, network F is simulated by G . Each node in F is assigned to a block in G and state coding is injective. Observe that blocks are connected (one edge in the original graph may be represented by a path in the communication graph of G) according to connections between nodes in the original network F . This connections are represented by blue lines

Another natural particular case of Definition 13 corresponding to localized information is when in each block D_i , there is a special node $v_i \in D_i$ such that the map $q \mapsto p_{i,q}(v_i)$ is injective. It is only possible when Q_G is larger than Q_F , but it will be the case in several examples of Boolean automata networks below. Interestingly, this local coding phenomena is forced when some automata network G simulates some Boolean automata network F : indeed, in any block D_i of G at least one node v_i must change between patterns $p_{i,0}$ and $p_{i,1}$, but the map $x \in \{0, 1\} \mapsto p_{i,x}(v_i)$ being injective, it means that $x \mapsto p_{i,x}(v_i)$ is injective too.

Remark 6. The simulation relation of Definition 13 is a pre-order on automata networks.

The orbit graph G_F associated to network F with nodes V and alphabet Q is the digraph with vertices Q^V and an edge from x to $F(x)$ for each $x \in Q^V$. We also denote $G_F^t = G_{F^t}$.

Lemma 14. If G simulates F via block embedding with time constant T then the orbit graph G_F of F is a subgraph of G_G^T . In particular if F has an orbit with transient of length t and period of length p , then G has an orbit with transient of length Tt and period Tp .

Proof. The embedding of G_F inside G_G^T is realized by definition by the block embedding of the simulation. The consequence on the length of periods and transients comes from the fact that the embedding ϕ verifies: x is in a periodic orbit if and only if $\phi(x)$ is in a periodic orbit. \square

4.2 Representing Automata Networks

As we are interested in measuring computational complexity of decision problems related to the dynamics of automata networks belonging to a particular family, we introduce hereunder a general notion for the representation of a family of automata networks. We can always fix a canonical representation of automata networks as Boolean circuits. However, as we show in the first part of this section, families can have different natural representation which are closely related to their particular properties. Considering this fact, we introduce the notion of *standard representation* in order to denote some representation from which we can efficiently obtain a circuit family computing original automata network family. Finally, we resume previous discussion on different representations for some particular families, showing how difficult it is to transform one particular representation into another one.

4.2.1 Standard representations

We fix for any alphabet Q an injective map $m_Q : Q \rightarrow \{0, 1\}^{k_Q}$ which we extend cell-wise for each n to $m_Q : Q^n \rightarrow \{0, 1\}^{k_Q n}$. Given an abstract automata network $F : Q^n \rightarrow Q^n$, a circuit encoding of F is a Boolean circuit $C : \{0, 1\}^{k_Q n} \rightarrow \{0, 1\}^{k_Q n}$ such that $m_Q \circ F = C \circ m_Q$ on Q^n . We also fix a canonical way to represent

circuits as words of $\{0, 1\}^*$ (for instance given by a number of vertices, the list of gate type positioned at each vertex and the adjacency matrix of the graph of the circuit).

Let \mathcal{F} be a set of abstract automata network over alphabet Q . A standard representation \mathcal{F}^* for \mathcal{F} is a language $L_{\mathcal{F}} \subseteq \{0, 1\}^*$ together with a **DLOGSPACE** algorithm such that:

- the algorithm transforms any $w \in L_{\mathcal{F}}$ into the canonical representation of a circuit encoding $C(w)$ that code an abstract automata network $F_w \in \mathcal{F}$;
- for any $F \in \mathcal{F}$ there is $w \in L_{\mathcal{F}}$ with $F = F_w$.

The default general representations we will use are circuit representations, *i.e.* representations where $w \in L_{\mathcal{F}}$ is just a canonical representation of a circuit. In this case the **DLOGSPACE** algorithm is trivial (the identity map). However, we sometimes want to work with more concrete and natural representations for some families of networks: in such a case, the above definition allows any kind of coding as soon as it is easy to deduce the canonical circuit representation from it.

4.2.2 Example of standard representations of some particular families

Observe that communication graph is often an essential piece of information for describing an automata network, however, this information is usually not enough. Thus, in this section three examples of canonical types of families are discussed: bounded degree networks, CSAN and algebraic families. The first one is characterized by a communication graph which has bounded degree, *i.e.* there exist a constant $\Delta \in \mathbb{N}$ (not depending in the size of the network) such that maximum degree is at most Δ . The second one was widely presented in previous sections and its roughly the family of all set-valued functions described by labelled communication graphs. Finally, an algebraic family is essentially a family of linear maps in the case in which Q is seen as a finite field and Q^n as a linear space. We show in latter cases that there exists a natural representation that is based on its properties. Roughly, it is observed that bounded degree condition provides a way to store information in an efficient way, set-valued functions in the case of CSAN does not depend on graph structure and linear maps can be represented as matrices

The CSAN case A CSAN family is a collection of labeled graphs and thus is naturally represented as a graph G together with some circuit family which represents local functions (*i.e.* λ and ρ). Each local function will depend only in the local configuration composed by a node and its neighbors. In addition, for a fixed CSAN family, a collection of set-valued functions and edge-labels is finite and provided independently of the structure of each graph as they depend on the possible sets of elements in the alphabet Q (note that what makes different two networks in one family is the position of labels but local functions are chosen from the a same fixed set). Thus, we can represent a CSAN family \mathcal{F} by a collection of labeled graphs \mathcal{G} and a circuit family Λ . We call this representation a succinct representation for CSAN \mathcal{F} . As circuit family Λ depends only in the size of the alphabet and we are considering that alphabet is fixed, we will usually omit Λ and just write \mathcal{G} as a succinct representation for \mathcal{F} and we will denote a CSAN $(G, \lambda, \rho) \in \mathcal{F}$ by simply G (where of course G is a labeled graph in \mathcal{G}). Note that as alphabet size is constant, a succinct representation is, in particular, a standard representation for CSAN family as an abstract automata network family. Formally, the language $L_{\mathcal{G}}$ containing the encoding of each labeled graph in \mathcal{G} together with the standard encoding of constant circuit family Λ is a standard representation for CSAN family \mathcal{F} .

The case of bounded degree communication graphs Let us fix some positive constant Δ . It is natural to consider the family of automata networks whose interaction graph has a maximum degree bounded by Δ (see Remark 11 below). We associate to this family the following representation: an automata network F is given as pair $(G, (\tau_v)_{v \in V(G)})$ where G is a communication graph of F of maximum degree at most Δ and $(\tau_v)_{v \in V(G)}$ is the list for all nodes of G of its local transition map F_v of the form $Q^d \rightarrow Q$ for $d \leq \Delta$ and represented as a plain transition table of size $|Q|^d \log(|Q|)$.

Remark 7. *Given any CSAN family, there is a **DLOGSPACE** algorithm that transforms a bounded degree representation of an automata network of the family into a CSAN representation: since all local maps are bounded objects, it is just a matter of making a bounded computation for each node.*

The algebraic case When endowing the alphabet Q with a finite field structure, the set of configurations Q^n is a vector space and one can consider automata networks that are actually linear maps. In this case the natural representation is a $n \times n$ matrix. It is clearly a standard representation in the above sense since circuit encodings can be easily

computed from the matrix. Moreover, as in the CSAN case, when a linear automata network is given as a bounded degree representation, it is easy to recover a matrix in **DLOGSPACE**.

More generally, we can consider matrix representations without field structure on the alphabet. An interesting case is that of Boolean matrices: $Q = \{0, 1\}$ is endowed with the standard Boolean algebra structure with operations \vee, \wedge and matrix multiplication is defined by:

$$(AB)_{i,j} = \bigvee_k A_{i,k} \wedge B_{k,j}.$$

They are a standard representation of disjunctive networks (and by switching the role of 0 and 1 conjunctive networks), *i.e.* networks F over alphabet $\{0, 1\}$ whose local maps are of the form $F_i(x) = \bigvee_{k \in N(i)} x_k$ (respectively $F_i(x) = \bigwedge_{k \in N(i)} x_k$). When their dependency graph is symmetric, disjunctive networks (resp. conjunctive networks) are a particular case of CSAN networks for which ρ_v maps are the identity and λ_v are just max (resp. min) maps. For disjunctive networks (resp. conjunctive networks) the CSAN representation and the matrix representation are **DLOGSPACE** equivalent.

4.2.3 Computing interaction graphs from representations

One of the key differences between all the representations presented so far is in the information they give about the interaction graph of an automata network. For instance, it is straightforward to deduce the interaction graph of a linear network from its matrix representation in **DLOGSPACE**. At the other extreme, one can see that it is NP-hard to decide whether a given edge belongs to the interaction graph of an automata network given by a circuit representation: indeed, one can build in **DLOGSPACE** from any SAT formula ϕ with n variables a circuit representation of an automata network $F : \{0, 1\}^{n+1} \rightarrow \{0, 1\}^{n+1}$ with

$$F(x)_1 = \begin{cases} x_{n+1} & \text{if } \phi(x_1, \dots, x_n) \text{ is true,} \\ 0 & \text{else.} \end{cases}$$

This F is such that node 1 depends on node $n + 1$ if and only if ϕ is satisfiable.

For automata networks with communication graphs of degree at most Δ , there is a polynomial time algorithm to compute the interaction graph from a circuit representation: for each node v , try all the possible subsets S of nodes of size at most Δ and find the largest one such that the following map

$$x \in Q^S \mapsto F_v(\phi(x))$$

effectively depends on each node of S , where $\phi(x)_w$ is x_w if $w \in S$ and some arbitrary fixed state $q \in Q$ else. Note also, that we can compute a bounded degree representation in polynomial time with the same idea.

In the CSAN case, the situation is ambivalent. On one hand, the interaction graph can be computed in polynomial time from a CSAN representation because for any given node v the following holds: either λ_v is a constant map and then v has no dependence, or $\lambda_v(q, X)$ depends only on q and in this case node v depends on itself but no other node, or $\lambda_v(q, X)$ depends only on X and in this case node v depends exactly on its neighbors in the CSAN graph, or, finally, $\lambda_v(q, X)$ depends both on q and on X and in this case node v depends on itself and its neighbors in the CSAN graph. On the other hand, a polynomial time algorithm to compute the interaction graph from a circuit representation would give a polynomial algorithm solving Unambiguous-SAT (which is very unlikely following Valiant-Vazirani theorem [30]). Indeed, any “dirac” map $\delta : \{0, 1\}^n \rightarrow \{0, 1\}$ with $\delta(x) = 1$ if and only if $x_1 \cdots x_n = b_1 \cdots b_n$ can be seen as the local map of a CSAN network because it can be written as $\lambda(\{\rho_i(x_i) : 1 \leq i \leq n\})$ where $\rho_i(x_i) = x_i$ if $b_i = 1$ and $\neg x_i$ else, and λ is the map

$$S \subseteq Q \mapsto \begin{cases} 1 & \text{if } S = \{1\} \\ 0 & \text{else.} \end{cases}$$

A constant map can also be seen as the local map of some CSAN network. Therefore, given a Boolean formula ϕ with the promise that it has at most one satisfying assignment, one can easily compute the circuit representation of some CSAN network which has some edge in its interaction graph if and only if ϕ is satisfiable: indeed, the construction of F above from a SAT formula always produce a CSAN given the promise on ϕ .

It follows from the discussion above that a polynomial time algorithm to compute a CSAN representation of a CSAN represented by circuit would give a polynomial time algorithm to solve Unambiguous-SAT.

The following table synthesizes the computational hardness of representation conversions. It shall be read as follows: given a family $(\mathcal{F}, \mathcal{F}^*)$ listed horizontally and a family $(\mathcal{H}, \mathcal{H}^*)$ listed vertically, the corresponding entry in the table

indicates the complexity of the problem of transforming $w \in L_F$ with the promise that $F_w \in \mathcal{F} \cap \mathcal{H}$ into $w' \in L_H$ such that $F_w = H_{w'}$.

output \ input	circuit	CSAN	Δ -bounded degree	matrix
circuit	trivial	DLOGSPACE	DLOGSPACE	DLOGSPACE
CSAN	USAT-hard	trivial	DLOGSPACE	DLOGSPACE
Δ -bounded degree	PTIME	PTIME	trivial	DLOGSPACE

USAT-hard means that any PTIME algorithm would imply a PTIME algorithm for Unambiguous-SAT.

4.3 Simulation between automata network families

From now on, a family of automata networks will be given as a pair $(\mathcal{F}, \mathcal{F}^*)$ where \mathcal{F} is the set of abstract automata networks and \mathcal{F}^* a standard representation. We can now present our notion of simulation between families: a family \mathcal{A} can simulate another family \mathcal{B} if we are able to *effectively* construct for any $B \in \mathcal{B}$ some automata network $A \in \mathcal{A}$ that is able to simulate B in the sense of Definition 13. More precisely, we ask on one hand that the automata network which performs the simulation do this task in reasonable time and reasonable space in the *size of the simulated automata network*, and, on the other hand, that the construction of the simulator is efficient in the *size of the representation of the simulated one*.

Definition 15. Let $(\mathcal{F}, \mathcal{F}^*)$ and $(\mathcal{H}, \mathcal{H}^*)$ be two families with standard representations on alphabets Q_F and Q_H respectively. Let $T, S : \mathbb{N} \rightarrow \mathbb{N}$ be two functions. We say that \mathcal{F}^* simulates \mathcal{H}^* in time T and space S if there exists a **DLOGSPACE** Turing machine M such that for each $w \in L_{\mathcal{H}}$ representing some automata network $H_w \in \mathcal{H} : Q_H^n \rightarrow Q_H^n$, the machine produces a pair $M(w)$ which consists in:

- $w' \in L_{\mathcal{F}}$ with $F_{w'} : Q_F^{n_F} \rightarrow Q_F^{n_F}$,
- $T(n)$ and a representation of a block embedding $\phi : Q^{n_F} \rightarrow Q^n$,

such that $n_F = S(n)$ and $F_{w'}$ simulates H_w in time $T = T(n)$ under block embedding ϕ .

From now on, whenever \mathcal{F}^* simulates \mathcal{H}^* in time T and space S we write $\mathcal{H}^* \preceq_S^T \mathcal{F}^*$.

Remark 8. Note that both T and S maps must be **DLOGSPACE** computable from this definition. Moreover, the simulation relation between families is transitive because the class **DLOGSPACE** is closed under composition and simulation between individual automata networks is also transitive. When composing simulations time and space maps S and T get multiplied.

4.4 Decision problems and automata network dynamics

Studying the complexity of decision problems related to the dynamics of some discrete dynamical system is a very well known and interesting approach for measuring the complexity of the dynamics. In this section we introduce three variants of a classical decision problem that is closely related to the dynamical behavior of automata networks: the prediction problem. This problem consists in predicting the state of one node of the network at a given time. We study short term and long term versions of the problem depending on the way the time step is given in input. In addition, we explore a variant in which we ask if some node has eventually changed without specifying any time step, but only a constant observation time rate τ . In other words, we check the system for any change on the state of a particular node every multiple of τ time steps. The main point of this subsection is to show that these problems are coherent with our simulation definition in the sense that if some family of automata networks $(\mathcal{F}_2, \mathcal{F}_2^*)$ simulates $(\mathcal{F}_1, \mathcal{F}_1^*)$ then, if some of the latter problem is hard for \mathcal{F}_1 it will also be hard for \mathcal{F}_2 . We will precise this result in the following lines.

Let $(\mathcal{F}, \mathcal{F}^*)$ an automata network family and let $L \in \{0, 1\}^* \times \{0, 1\}^*$ be a parametrized language. We say that L is parametrized by \mathcal{F} if L has \mathcal{F}^* encoded as parameter. We note $L_{\mathcal{F}} \in \{0, 1\}^*$ as the language resulting on fixing \mathcal{F}^* as a constant.

In particular, we are interested in studying prediction problems. We start by defining two variants of this well-known decision problem:

Problem (Unary Prediction (U-PRED)).

Parameters: alphabet Q , a standard representation \mathcal{F}^* of an automata network family \mathcal{F}

Input:

1. a word $w_F \in \mathcal{F}^*$ representing an automata network $F : Q^n \rightarrow Q^n$ on alphabet Q , with $F \in \mathcal{F}$.
2. a node $v \in V(F) = [n]$
3. an initial condition $x \in Q^V$.
4. a natural number t represented in unary $t \in \mathbf{1}$.

Question: $F^t(x)_v \neq x_v$?

Problem (Binary Prediction (B-PRED)).

Parameters: alphabet Q , a standard representation \mathcal{F}^* of an automata network family \mathcal{F}

Input:

1. a word $w_F \in \mathcal{F}^*$ representing an automata network $F : Q^n \rightarrow Q^n$ on alphabet Q , with $F \in \mathcal{F}$.
2. a node $v \in V(F) = [n]$
3. an initial condition $x \in Q^V$.
4. a natural number t represented in binary $t \in \{0, 1\}^*$.

Question: $F^t(x)_v \neq x_v$?

Note that two problems are essentially the same, the only difference is the representation of time t that we call the *observation time*. We will also call node v the *objective node*. Roughly, as it happens with other decision problems, such as integer factorization, the representation of observation time will have an impact on the computation complexity of prediction problem. When the context is clear we will refer to both problems simply as PRED. In order to precise the latter observation we present now some general complexity results concerning PRED.

Proposition 1. *Let \mathcal{F} be a concrete automata network family. The following statements hold:*

1. $U\text{-PRED}_{\mathcal{F}} \in \mathbf{P}$
2. $B\text{-PRED}_{\mathcal{F}} \in \mathbf{PSPACE}$

Finally, we show that latter problem is coherent with our definition of simulation, in the sense that we can preserve the complexity of PRED. Note that this give us a powerful tool in order to classify concrete automata rules according to the complexity of latter decision problem.

Lemma 16. *Let $(\mathcal{F}, \mathcal{F}^*)$ and $(\mathcal{H}, \mathcal{H}^*)$ be two automata network families. Let $T, S : \mathbb{N} \rightarrow \mathbb{N}$ be two polynomial functions such that $\mathcal{H}^* \preceq_S^T \mathcal{F}^*$ then, $\text{PRED}_{\mathcal{H}^*} \leq_L^T \text{PRED}_{\mathcal{F}^*}$ ² where PRED denotes either U-PRED or B-PRED*

Proof. Let (w_H, v, x, t) be an instance of $\text{PRED}_{\mathcal{H}^*}$. By definition of simulation, there exists a **DLOGSPACE** algorithm which takes w_H and produces a word $w_F \in L_{\mathcal{F}}$ with $F : Q^{n_F} \rightarrow Q^{n_F}$ and a block representation $\phi : Q^{n_F} \rightarrow Q^n$ such that $n_F = S(n)$ and F simulates H in time $T(n)$ under block embedding ϕ . Particularly, there exists a partition of blocks $D_v \subseteq V(F) = [n_F]$ for each $v \in V(H) = [n]$ and a collection of injective patterns, i.e. patterns $p_{i,q} \in Q_F^{D_i}$ such that $p_{i,q} = p_{i,q'} \implies q = q'$. In addition, we have $\phi \circ H = F^T \circ \phi$. Let us define the configuration $y \in Q_F^{n_F}$ as $y_{D_i} = p_{i,x_i}$, i.e., $\phi(x)_{D_i} = y_{D_i}$. Note that y is well-defined as the block map is injective. In addition, let us choose an arbitrary vertex $v' \in D_v$ and let us consider now the instance of $\text{PRED}_{\mathcal{F}^*}$ given by $(w_F, v', y, t \times T)$. Note that for each $v' \in D_v$ the transformation $(w_H, v, x, t) \rightarrow (w_F, v', y, t \times T)$ can be done in **DLOGSPACE** $(|w_H|)$ because we can read the representation of ϕ for each block p_{i,x_i} and then output the configuration y . We claim that there exists a **DLOGSPACE** $(|w_H|)$ algorithm that decides if $(w_H, v, x, t) \in L_{\text{PRED}_{\mathcal{H}^*}}$

²Here we denote \leq_L^T as a **DLOGSPACE** Turing reduction. The capital letter ‘‘T’’ stands for *Turing reduction* and it is not related to the simulation time function which is also denoted by T.

with oracle calls to $\text{PRED}_{\mathcal{F}^*}$. More precisely, as a consequence of the injectivity of block embedding, we have that $(w_H, v, x, t) \in L_{\text{PRED}_{\mathcal{H}^*}}$ if and only if $(w_F, v', y, t \times T) \in L_{\text{PRED}_{\mathcal{F}^*}}$ for some $v' \in D_v$. In fact, latter algorithm just runs oracle calls of $\text{PRED}_{\mathcal{F}^*}$ for $(w_F, v', y, t \times T)$ for each $v' \in D_v$ and decides if some of these instances is a YES instance and thus, if some node in the simulation block have changed its state. As block-embedding is injective, this necessary means that node v have changed its state in t time steps. Finally, all of this can be done in **DLOGSPACE** as $n_F = S(n) = n^{\mathcal{O}(1)}$ and $T = n^{\mathcal{O}(1)}$ and thus, a polynomial amount of calls to each oracle is needed. \square

Finally, we would like to study the case in which the observation time is not unique and ask whether the state of some node eventually changes. However, in order to preserve complexity properties under simulation, we still need to have some sort of restriction on observation times. This will allow us to avoid giving misleading answers when the simulating network is performing one step of simulation: indeed, it could take several time steps for the simulating network in order to represent one step of the dynamics of the simulated network, so some state change could happen in the intermediate steps while the simulated dynamics involve no state change. In order to manage this sort of time dilation phenomenon between simulating and simulated systems, we introduce the following decision problem.

Problem (Prediction change $\text{PRED-CHG}_{\mathcal{F}^*}$).

Parameters: *alphabet Q , a standard representation \mathcal{F}^* of an automata network family \mathcal{F}*

Input:

1. a word $w_F \in \mathcal{F}^*$ representing an automata network $F : Q^n \rightarrow Q^n$ on alphabet Q , with $F \in \mathcal{F}$,
2. a node $v \in V(G)$,
3. an initial condition $x \in Q^V$,
4. a time gap $k \in \mathbb{N}$ in unary.

Question: $\exists t \in \mathbb{N} : F^{kt}(x)_v \neq x_v$

As we did with previous versions of prediction problem, we introduce a general complexity result and then, we show computation complexity is consistent under simulation.

Proposition 2. *Let $(\mathcal{F}, \mathcal{F}^*)$ be a automata network family. $\text{PRED-CHG}_{\mathcal{F}} \in \text{PSPACE}$.*

The injectivity of block encodings in our definition of simulation is essential for the following lemma as it guaranties that a state change in the simulating network always represent a state change in the simulated network at the corresponding time steps.

Lemma 17. *Let $(\mathcal{F}, \mathcal{F}^*)$ and $(\mathcal{H}, \mathcal{H}^*)$ be two automata network families and $T, S : \mathbb{N} \rightarrow \mathbb{N}$ two polynomial functions such that $\mathcal{H}^* \preceq_S^T \mathcal{F}^*$ then, $\text{PRED-CHG}_{\mathcal{H}^*} \leq_L^T \text{PRED-CHG}_{\mathcal{F}^*}$.*

Proof. Proof is analogous to short term prediction case. Let (w_H, v, x, k) be an instance of $\text{PRED-CHG}_{\mathcal{H}^*}$. Again, by the definition of simulation, there exists a **DLOGSPACE** algorithm which takes w_H and produces a word $w_F \in L_{\mathcal{F}}$ with $F : Q^{n_F} \rightarrow Q^{n_F}$ and a block representation $\phi : Q^{n_F} \rightarrow Q^n$ such that $n_F = S(n)$ and F simulates H in time $T(n)$ under block embedding ϕ . The latter statements means, particularly, that there exists a partition of blocks $D_v \subseteq V(F) = [n_F]$ for each $v \in V(H) = [n]$ and a collection of injective patterns, i.e. patterns $p_{i,q} \in Q_F^{D_i}$ such that $p_{i,q} = p_{i,q'} \implies q = q'$ and also that $\phi \circ H = F^{T(n)} \circ \phi$. Let us define the configuration $y \in Q_F^{n_F}$ as $y_{D_i} = p_{i,x_i}$, i.e., $\phi(x)_{D_i} = y_{D_i}$. Note, again, that y is well-defined as the block map is injective. Now we proceed in using the same approach than before: for each $v' \in D_v$ we can produce an instance $(w_F, v', y, kT(n))$ of $\text{PRED-CHG}_{\mathcal{F}^*}$. There exists a **DLOGSPACE** machine which produces $(w_F, v', y, kT(n))$ for each $v' \in D_v$ and calls for an oracle solving $\text{PRED-CHG}_{\mathcal{F}^*}(w_F, v', y, kT(n))$ and outputs 1 if there is at least one YES-instance for some v' . By definition of simulation and injectivity of block embedding function we have that this algorithm outputs 1 if and only if $(w_H, v, x, k) \in \text{PRED-CHG}_{\mathcal{H}^*}$. \square

To end this subsection, let us show that problems PRED-CHG and B-PRED are actually orthogonal: depending of the family of automata networks considered, one can be harder than the other and reciprocally.

Theorem 18. *The exists a family with circuit representation $(\mathcal{F}, \mathcal{F}^*)$ such that $\text{B-PRED}_{\mathcal{F}}$ is solvable in polynomial time while $\text{PRED-CHG}_{\mathcal{F}}$ is NP-hard. The exists a family with circuit representation $(\mathcal{G}, \mathcal{G}^*)$ such that $\text{B-PRED}_{\mathcal{G}}$ is PSPACE-complete while $\text{PRED-CHG}_{\mathcal{G}}$ is solvable in polynomial time.*

Proof. Given a SAT formula ϕ with n variables, let us define the automata network F_ϕ on $\{0, 1\}^{n+1}$ which interprets any configuration as a pair $(b, v) \in \{0, 1\} \times \{0, 1\}^n$ where b is the state of node 1 and v is both a number represented in base 2 and a valuation for ϕ and does the following:

$$F(b, i) = \begin{cases} (1, i + 1 \bmod 2^n) & \text{if } \phi \text{ is true on valuation } v, \\ (0, i + 1 \bmod 2^n) & \text{else.} \end{cases}$$

A circuit representation of size polynomial in n can be computed in **DLOGSPACE** from ϕ and we define $(\mathcal{F}, \mathcal{F}^*)$ as the family obtained by considering all F_ϕ for all SAT formulas ϕ . First, **B-PRED** $_{\mathcal{F}}$ can be solved in polynomial time: given F_ϕ , an initial configuration (b, v) and a time t , it is sufficient to compute $v' = v + t - 1 \bmod 2^n$ and verify the truth b' of ϕ on valuation v' and we have $(b', v' + 1 \bmod 2^n) = F^t(b, v)$. To see that **PRED-CHG** $_{\mathcal{F}}$ is NP-hard, it suffices to note that, on input $(0, 0 \dots 0)$, F_ϕ will test successively each possible valuation for ϕ and the state of node 1 will change to 1 at some time if and only if formula ϕ is satisfiable.

For the second part of the proposition, the key is the construction for any n of an automata network H_n on Q^n that completely trivializes problem **PRED-CHG** in the following sense: for any configuration $c \in Q^n$ and any $k \leq 2^n$ and any node v , there is some t such that $c_v \neq F^{kt}(c)_v$. Taking any automata network F with n nodes, the product automata network $F \times H_n$ (working on the product of alphabets in such a way that each component evolves independently) has the same property, namely that all instances of **PRED-CHG** with $k \leq 2^n$ have a positive answer. From this, taking any family with a PSPACE-hard **PRED-CHG** problem (they are known to exist, see Corollary 1 for details), and replacing each automata network F with n nodes by the product $F \times H_n$ (the circuit representation of the product is easily deduced from the representations of each component), we get a family $(\mathcal{G}, \mathcal{G}^*)$ such that **B-PRED** $_{\mathcal{G}}$ is PSPACE-complete while **PRED-CHG** $_{\mathcal{G}}$ is easy: on one hand, taking products does not simplify **B-PRED** problem; on the other hand, **PRED-CHG** becomes trivial (always true) on inputs where the observation interval k is less than 2^n , and if $k \geq 2^n$ then the size of the whole orbit graph of the input network is polynomial in k (since k is given in unary), so the entire orbit of the input configuration can be computed explicitly in polynomial time and the **PRED-CHG** can be answered in polynomial time.

Let us complete the proof by giving an explicit construction of the automata networks H_n over Q^n with the desired property. $Q = \{0, 1\} \times \{0, 1\} \times \{0, 1\}$ and H_n interprets any configuration as a triplet of Boolean configurations (c, i, k) with the following meaning: k is a global counter that will take all possible values between 0 and $2^n - 1$ and loop, i is a local counter that will run from 0 to $2k$ and c is the component where state changes will be realized at precise time steps to ensure the desired property of H_n . The goal is to produce in any orbit and for any k and at any node the sequence of states $O^k 1^k$ on the c -component: such a behavior is sufficient to ensure the desired property on H_n . This is obtained by defining $H_n(c, i, k) = (c', i', k')$ as follows:

- for any node v , $c'_v = 0$ if $i < k$ and $c'_v = 1$ else,
- $i' = 0$ if $i \geq 2k$ and $i' + 1$ else,
- $k' = k + 1 \bmod 2^n$ if $i \geq 2k$ and $k' = k$ else.

It is clear that such an H_n admits a polynomial circuit representation **DLOGSPACE** computable from n . □

4.5 Universal automata network families

Building upon our definition of simulation, we can now define a precise notion of universality. In simple words, an universal family is one that is able to simulate every other automata network under any circuit encoding. Our definition of simulation ensures that the amount of resources needed in order to simulate is controlled so that we can deduce precise complexity results.

Consider some alphabet Q and some polynomial map $P : \mathbb{N} \rightarrow \mathbb{N}$. We denote by $\mathcal{U}_{Q,P}$ the class of all possible functions $F : Q^n \rightarrow Q^n$ for any $n \in \mathbb{N}$ that admits a circuit representation of size at most $P(n)$. We also denote $\mathcal{U}_{Q,P}^*$ the language of all possible circuit representations of size bounded by P of all functions from $\mathcal{U}_{Q,P}$. Finally for any $\Delta \geq 1$, denote by $\mathcal{B}_{Q,\Delta}$ the set of automata networks on alphabet Q with a communication graph of degree bounded by Δ and by $\mathcal{B}_{Q,\Delta}^*$ their associated bounded degree representations made of a pair (graph, local maps) as discussed above.

Definition 19. A family of automata networks $(\mathcal{F}, \mathcal{F}^*)$ is :

- universal if for any alphabet Q and any polynomial map P it can simulate $(\mathcal{U}_{Q,P}, \mathcal{U}_{Q,P}^*)$ in time T and space S where T and S are polynomial functions;
- strongly universal if for any alphabet Q and any degree $\Delta \geq 1$ it can simulate $(\mathcal{B}_{Q,\Delta}, \mathcal{B}_{Q,\Delta}^*)$ in time T and space S where T and S are linear maps.

Remark 9. The link between the size of automata networks and the size of their representation is the key in the above definitions: a universal family must simulate any individual automata network F (just take P large enough so that $F \in \mathcal{U}_{Q,P}$), however it is not required to simulate in polynomial space and time the family of all possible networks without restriction. Actually no family admitting polynomial circuit representation could simulate the family of all networks in polynomial time and space by the Shannon effect (most n -ary Boolean function have super-polynomial circuit complexity). In particular the family $\mathcal{B}_{Q,\Delta}$ can't.

At this point it is clear, by transitivity of simulations, that if some $\mathcal{B}_{Q,\Delta}$ happens to be universal then, any strongly universal family is also universal. It turns out that $\mathcal{B}_{\{0,1\},3}$ is universal. We will however delay the proof until section 5.4 below where we prove a more precise result which happens to be very useful to get universality result in concrete families.

Finally, observe that the fact that S and T are polynomial or linear maps implies that they are computable in **DLOGSPACE** which is coherent with the reductions presented in Lemma 16 and Lemma 17.

Now, we introduce an important corollary of universality regarding complexity. Roughly speaking, a universal family exhibits all the complexity in terms of dynamical behaviour and computational complexity of prediction problems. Concerning computational complexity, let us introduce the following definition.

Definition 20. We say that an automata network family \mathcal{F} is computationally complex if the following conditions hold:

1. $U\text{-PRED}_{\mathcal{F}}$ is **P-hard**.
2. $B\text{-PRED}_{\mathcal{F}}$ is **PSPACE-hard**.
3. $PRED\text{-CHG}_{\mathcal{F}}$ is **PSPACE-hard**.

As a direct consequence of universality, we have the following result where there is no difference between strong or standard universality.

Corollary 1. Let \mathcal{F} a (strongly) universal automata network family then \mathcal{F} computationally complex.

Proof. We observe that $\mathcal{B}_{Q,\Delta}$ is computationally complex for large enough Q and Δ , which concludes the proof by definition of (strong) universality and Lemmas 16 and 17. First, any Turing machine working in bounded space can be directly embedded into a cellular automaton on a periodic configuration which is a particular case of automata network on a bounded degree communication graph (for the **PRED-CHG** variant we can always add a witness node that changes only when the Turing machine accepts for instance). This direct embedding is such that one step of the automata network correspond to one step of the Turing machine and one node of the network corresponds to one cell of the Turing tape. However, the alphabet of the automata network depends on the tape alphabet and the state set of the Turing machine. To obtain the desired result we need to fix the target alphabet, while allowing more time and/or more space. Such simulations of any Turing machine by fixed alphabet cellular automata with linear space/time distortion are known since a long time [23], but a modern formulation would be as follows: if there exists an intrinsically universal cellular automaton [7] with states set Q and neighborhood size Δ (whatever the dimension), then $\mathcal{B}_{Q,\Delta}$ is computationally complex. The 2D cellular automaton of Banks [3] is intrinsically universal [25] and has two states and 5 neighbors, which shows that $\mathcal{B}_{Q,\Delta}$ is computationally complex when $\Delta \geq 5$ and Q is not a singleton. The 1D intrinsically universal cellular automaton of Ollinger-Richard [26] has 4 states and 3 neighbors so $\mathcal{B}_{Q,\Delta}$ is computationally complex when $\Delta \geq 3$ and $|Q| \geq 4$. \square

In addition, reader interested in simulation results which does not involve cellular automata but automata networks having a more general graph structure, we provide following references in which authors have presented alternative simulation schemes by using very well-known boolean network families as threshold networks:

1. The family Θ of threshold networks over the binary alphabet $Q = \{0, 1\}$ satisfies that $U\text{-PRED}_{\Theta}$ is **P-hard** [15].

2. The family Θ of threshold networks over an alphabet $Q = \{0, 1\}$ (equipped with block sequential update scheme) satisfies that PRED-CHG_Θ is **PSPACE-hard** [18]

We now turn to the dynamical consequences of universality. By definition simulations are particular embeddings of orbit graphs into larger ones, but the parameters of the simulation can involve some distortion.

Definition 21. Fix a map $\rho : \mathbb{N} \rightarrow \mathbb{N}$, we say that the orbit graph G_F of F with n nodes is ρ -succinct if F can be represented by circuits of size at most $\rho(n)$. We say that the orbit graph G_H of H with m nodes embeds G_F with distortion $\delta : \mathbb{N} \rightarrow \mathbb{N}$ if $m \leq \delta(n)$ and there is $T \leq \delta(n)$ such that G_F is a subgraph of G_{H^T} .

Remark 10. The embedding of orbit graphs with distortion obviously modify the relation between the number of nodes of the automata networks and the length of paths or cycles in the orbit graph. In particular, with polynomial distortion δ , if F has n nodes and a cyclic orbit of length 2^n (hence exponential in the number of nodes) then in H it gives a cyclic orbit of size $O(\delta(n)2^n)$ for up to $\delta(n)$ nodes, which does not guarantee an exponential length in the number of nodes in general, but just a super-polynomial one ($n \mapsto 2^{n^\alpha}$ for some $0 < \alpha < 1$).

To fix ideas, we give examples of orbit graphs of bounded degree automata networks with large components corresponding to periodic orbits or transient.

Proposition 3. There is an alphabet Q such that for any $n \geq 1$ there is an automata network $F_n \in \mathcal{B}_{Q,2}$ whose orbit graph G_{F_n} has the following properties:

- it contains a cycle C of length at least 2^n ;
- there is a complete binary tree T with 2^n leaves connected to some $v_1 \in C$, i.e. for all $v \in T$ there is a path from v to v_1 ;
- there is a node $v_2 \in C$ with a directed path of length 2^n pointing towards v_2 ;
- it possesses at least 2^n fixed points.

Proof. First on a component of states $\{0, 1, 2\} \subseteq Q$ the large cycle C is obtained by the following 'odometer' behavior of F_n : if $x_n \in \{0, 1, 2\}$ then $F_n(x)_n = x_n + 1 \pmod 3$, and if both $x_i, x_{i+1} \in \{0, 1, 2\}$ for $1 \leq i < n$ then

$$F_n(x)_i = \begin{cases} 0 & \text{if } x_i = 2 \\ x_i + 1 & \text{else if } x_{i+1} = 2, \\ x_i & \text{else.} \end{cases}$$

C is realized on $\{0, 1, 2\}^n$ as follows. For $x \in \{0, 1, 2\}^n$ denote by S_i the sequence $(F^t(x)_i)_{t \geq 0}$ for any $1 \leq i \leq n$. Clearly S_n is periodic of period 012. S_{n-1} is ultimately periodic of period 200111 (of length 6) and by a straightforward induction we get that S_1 is ultimately periodic of period $20^{3 \cdot 2^{n-2} - 1} 1^{3 \cdot 2^{n-2}}$ which is of length $3 \cdot 2^{n-1}$.

For the tree T , just add states $\{a, b\} \subseteq Q$ with the following behavior: if $x_1 \in \{a, b\}$ then $F_n(x)_1 = 0$ and if $x_i \in \{a, b\}$ and $x_{i-1} = 0$ then $F_n(x)_i = 0$ for $1 < i \leq n$. In any other case, we set $F(x)_i = x_i$ for $x \in \{0, 1, 2, a, b\}^n$ and $1 \leq i \leq n$.

Using similar mechanisms as above on additional states $c_0, c_1, c_2 \in Q$, F_n runs another odometer whose behavior is isomorphic to the behavior of F_n on $\{0, 1, 2\}^n$ through $i \mapsto c_i$, but with the following exception: when $x_1 = c_2$ we set $F_n(x)_1 = 0$ and then state 0 propagates from node 1 to node n as in the construction of tree T . We thus get a transient behavior of length more than $3 \cdot 2^{n-1}$ which yields to configuration 0^n , which itself (belongs or) yields to cycle C .

Finally, the fixed points are obtained by adding two more states to the alphabet on which the automata network just acts like the identity map. \square

We can now state that any universal family must be dynamically rich in a precise sense.

Theorem 22. Let \mathcal{F} be an automata networks family.

- If \mathcal{F} is universal then, for any polynomial map ρ , there is a polynomial distortion δ such that, any ρ -succinct orbit graph can be embedded into some $F \in \mathcal{F}$ with distortion δ . In particular \mathcal{F} contains networks with

super-polynomial periods and transients, and a super-polynomial number of disjoint periodic orbits of period at most polynomial.

- *If \mathcal{F} is strongly universal then it embeds the orbit graph of any bounded-degree automata network with linear distortion. In particular it contains networks with exponential periods and transients, and an exponential number of disjoint periodic orbits of period at most linear.*

Proof. This is a direct consequence of Lemma 14, Definition 15 and Proposition 3 above. □

Of course, we do not claim that computational complexity and dynamical richness as stated above are the only meaningful consequences of universality. To conclude this subsection about universality, let us show that it allows to prove finer results linking the global dynamics with the interaction graph.

In a directed graph, we say a node v belongs to a strongly connected component if there is a directed path from v to v .

Corollary 2. *Any universal family \mathcal{F} satisfies the following: there is a constant α with $0 < \alpha \leq 1$ such that for any $m > 0$ there is a network $F \in \mathcal{F}$ with $n \geq m$ nodes such that some node v belonging to a strongly connected component of the interaction graph of F and a periodic configuration x such that the trace at v of the orbit of x is of period at least 2^{n^α} .*

Proof. Consider a Boolean network F with nodes $V = \{1, \dots, m\}$ that do the following on configuration $x \in \{0, 1\}^V$: it interprets x_1, \dots, x_m as an number k written in base 2 where x_1 is the most significant bit and produces $F(x)$ which represents number $k + 1 \bmod 2^m$.

F is such that node 1 has a trace of exponential period and belongs to a strongly connected component of the interaction graph of F (because it depends on itself). Note that F has a circuit representation which is polynomial in m , and take $F' \in \mathcal{F}$ of size polynomial in m that simulates F in polynomial time (by universality of family \mathcal{F}). Taking the notations of Definition 13, we have that each node $v \in D_i$ for each block D_i is such that the map $q \in \{0, 1\} \mapsto p_{i,q}(v)$ is either constant or bijective (because F has a Boolean alphabet, see Remark 5). In the last case, the value of the node $v \in D_i$ completely codes the value of the corresponding node i in F . Take any $v \in B_1$ that has this coding property. Since node 1 depends on itself in F , there must be a path from v to some node $v' \in D_1$ that is also coding in the interaction graph of F' . Then we can also find a path from v' to some coding node in D_1 . Iterating this reasoning we must find a cycle, and in particular we have a coding node in D_1 which belongs to some strongly connected component of the interaction graph of F' . Since this node is coding the values taken by node 1 of F and since the simulation is in polynomial time and space, we deduce the super-polynomial lower bound on the period of its trace for a well-chosen periodic configuration. □

5 Gadgets and glueing

In the same way as Boolean circuits are defined from Boolean gates, many automata network families can be defined by fixing a finite set of local maps \mathcal{G} that we can freely connect together to form a global network, called a \mathcal{G} -network.

Such families can be strongly universal as we will see, even for very simple choices of \mathcal{G} , which is an obvious motivation to consider them. In this section, we introduce a general framework to prove simulation results of a \mathcal{G} -network family by some arbitrary family that amounts to a finite set of conditions to check. From this we will derive a framework to certify strong universality of an arbitrary family just by exhibiting a finite set of networks from the family that verify a finite set of conditions. As already said above, our goal is to analyze automata networks with symmetric communication graph (CSAN families). Our framework is targeted towards such families.

The idea behind is that of building large automata networks from small automata networks in order to mimic the way a \mathcal{G} -network is built from local maps in \mathcal{G} . The difficulty, and the main contribution of this section, is to formalize how small building blocks are glued together and what conditions on them guaranty that the large network correctly simulates the corresponding \mathcal{G} -networks.

We will now introduce all the concepts used in this framework progressively.

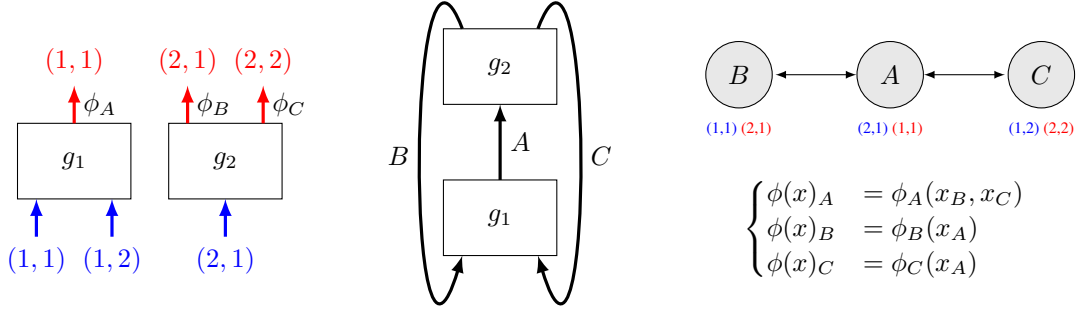


Figure 5: On the left a set of maps \mathcal{G} over alphabet Q , in the middle an intuitive representation of input/output connections to make a \mathcal{G} -network, on the right the corresponding formal \mathcal{G} -network $\phi : Q^3 \rightarrow Q^3$ together with the global map associated to it. The bijections α and β from Definition 23 are represented in blue and red (respectively).

5.1 \mathcal{G} -networks

Let Q be a fixed alphabet and \mathcal{G} be any set of maps of type $g : Q^{i(g)} \rightarrow Q^{o(g)}$ for some $i(g), o(g) \in \mathbb{N}$. We say g is *reducible* if it can be written as a disjoint union of two gates, and *irreducible* otherwise. Said differently, if G is the (bipartite) dependency graph of g describing on which inputs effectively depends each output, then g is irreducible if G is weakly connected.

From \mathcal{G} we can define a natural family of networks: a \mathcal{G} -network is an automata network obtained by wiring outputs to inputs of a number of gates from \mathcal{G} . To simplify some later results, we add the technical condition that no output of a gate can be wired to one of its inputs (no self-loop condition).

Definition 23. A \mathcal{G} -network is an automata network $F : Q^V \rightarrow Q^V$ with set of nodes V associated to a collection of gates $g_1, \dots, g_n \in \mathcal{G}$ with the following properties. Let

$$I = \{(j, k) : 1 \leq j \leq n \text{ and } 1 \leq k \leq i(g_j)\} \text{ and}$$

$$O = \{(j, k) : 1 \leq j \leq n \text{ and } 1 \leq k \leq o(g_j)\}$$

be respectively the sets of inputs and outputs of the collection of gates $(g_j)_{1 \leq j \leq n}$. We require $|V| = |I| = |O|$ and the existence of two bijective maps $\alpha : I \rightarrow V$ and $\beta : V \rightarrow O$ with the condition that there is no $(j, k) \in I$ such that $\beta(\alpha(j, k)) = (j, k')$ for some k' (no self-loop condition). For $v \in V$ with $\beta(v) = (j, k)$, let $I_v = \{\alpha(j, 1), \dots, \alpha(j, i(g_j))\}$ and denote by g_v the map: $x \in Q^{I_v} \mapsto g_j(\tilde{x})_k$ where $\tilde{x} \in Q^{i(g_j)}$ is defined by $\tilde{x}_k = x_{\alpha(j, k)}$. Then F is defined as follows:

$$F(x)_v = g_v(x_{I_v}).$$

Remark 11. Once \mathcal{G} is fixed, there is a bound on the degree of dependency graphs of all \mathcal{G} -networks. Thus, it is convenient to represent \mathcal{G} -networks by the standard representation of bounded degree automata networks (as a pair of a graph and a list of local update maps). Another representation choice following strictly Definition 23 consists in giving a list of gates $g_1, \dots, g_k \in \mathcal{G}$, fixing $V = \{1, \dots, n\}$ and give the two bijective maps $\alpha : I \rightarrow V$ and $\beta : V \rightarrow O$ describing the connections between gates (maps are given as a simple list of pairs source/image). One can check that these two representations are **DLOGSPACE** equivalent when the gates of \mathcal{G} are irreducible: we can construct the interaction graph and the local maps from the list of gates and maps α and β in **DLOGSPACE** (the incoming neighborhood of a node v , I_v , and its local map g_v are easy to compute as detailed in Definition 23); reciprocally, given the interaction graph G and the list of local maps (g_v) , one can recover in **DLOGSPACE** the list of gates and their connections as follows:

- for v from 1 to n do:

- gather the (finite) incoming neighborhood $N^-(v)$ of v then the (finite) outgoing neighborhood $N^+(N^-(v))$ and iterate this process until it converges (in finite time) to a set I_v of inputs and O_v of outputs with $v \in O_v$;
- check that all $v' \in I_v \cup O_v$ are such that $v' \geq v$ otherwise jump to next v in the loop (this guaranties that each gate is generated only once);
- since the considered gates are irreducible, I_v and O_v actually correspond to input and output sets of a gate $g \in \mathcal{G}$ that we can recover by finite checks from the local maps of nodes in O_v ;
- output gate g and the pairs source/image to describe α and β for nodes in I_v and O_v respectively.

In the sequel we denote $\Gamma(\mathcal{G})$ the family of all possible \mathcal{G} -networks associated to their bounded degree representation.

5.2 Glueing of automata networks

In this section we define an operation that allows us to 'glue' two different abstract automata networks on a common part in order to create another one which, roughly, preserve some dynamical properties in the sense that it allows to glue pseudo-orbits of each network to obtain a pseudo-orbit of the glued network. One might find useful to think about the common part of the two networks as a dowel attaching two pieces of wood: each individual network is a piece of wood with the dowel inserted in it, and the result of the glueing is the attachment of the two pieces with a single dowel (see Figure 5.2).

Definition 24. Consider $F_1 : Q^{V_1} \rightarrow Q^{V_1}$ and $F_2 : Q^{V_2} \rightarrow Q^{V_2}$ two automata networks with V_1 disjoint from V_2 , C a set disjoint from $V_1 \cup V_2$, $\varphi_1 : C \rightarrow V_1$ and $\varphi_2 : C \rightarrow V_2$ two injective maps with $\varphi_1(C) \cap \varphi_2(C) = \emptyset$ and C_1, C_2 a partition of C in two sets. We define

$$V' = C \cup (V_1 \setminus \varphi_1(C)) \cup (V_2 \setminus \varphi_2(C))$$

and the map $\alpha : V' \rightarrow V_1 \cup V_2$ by

$$\alpha(v) = \begin{cases} v & \text{if } v \notin C \\ \varphi_i(v) & \text{if } v \in C_i, \text{ for } i = 1, 2. \end{cases}$$

We then define the glueing of F_1 and F_2 over C as the automata network $F' : Q^{V'} \rightarrow Q^{V'}$ where

$$F'_v = \begin{cases} (F_1)_{\alpha(v)} \circ \rho_1 & \text{if } \alpha(v) \in V_1, \\ (F_2)_{\alpha(v)} \circ \rho_2 & \text{if } \alpha(v) \in V_2, \end{cases}$$

where $\rho_i : Q^{V'} \rightarrow Q^{V_i}$ is defined by

$$\rho_i(x)_v = \begin{cases} x_{\varphi_i^{-1}(v)} & \text{if } v \in \varphi_i(C), \\ x_v & \text{else.} \end{cases}$$

When necessary, we will use the notation $F' = F_1 \overset{\phi_1}{\oplus}_{C_1} \overset{\phi_2}{\oplus}_{C_2} F_2$ to underline the dependency of the glueing operation on its parameters.

Given an automata network $F : Q^V \rightarrow Q^V$ and a set $X \subseteq V$, we say that a sequence $(x^t)_{0 \leq t \leq T}$ of configurations from Q^V is a X -pseudo-orbit if it respects F as in a normal orbit, except on X where it can be arbitrary, formally: $x_v^{t+1} = F(x^t)_v$ for all $v \in V \setminus X$ and all $0 \leq t < T$. The motivation for Definition 24 comes from the following lemma.

Lemma 25 (Pseudo-orbits glueing). Taking the notations of Definition 24, let $X \subseteq V_1 \setminus \varphi_1(C)$ and $Y \subseteq V_2 \setminus \varphi_2(C)$ be two (possibly empty) sets. If $(x^t)_{0 \leq t \leq T}$ is a $X \cup \varphi_1(C_2)$ -pseudo-orbit for F_1 and if $(y^t)_{0 \leq t \leq T}$ is a $Y \cup \varphi_2(C_1)$ -pseudo-orbit for F_2 and if they verify for all $0 \leq t \leq T$

$$\forall v \in C, x_{\varphi_1(v)}^t = y_{\varphi_2(v)}^t, \quad (1)$$

then the sequence $(z^t)_{0 \leq t \leq T}$ of configurations of $Q^{V'}$ is a $X \cup Y$ -pseudo-orbit of F' , where

$$z_v^t = \begin{cases} x_{\alpha(v)}^t & \text{if } \alpha(v) \in V_1, \\ y_{\alpha(v)}^t & \text{if } \alpha(v) \in V_2. \end{cases}$$

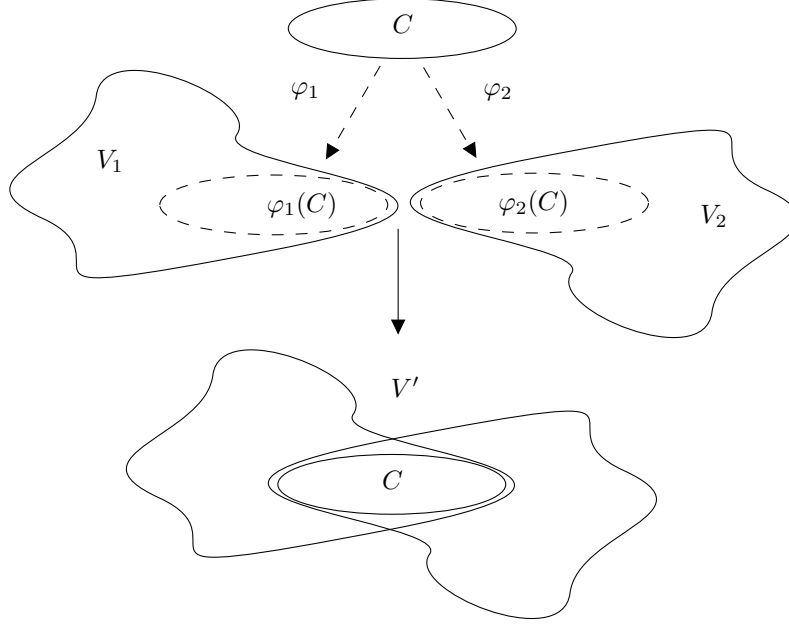


Figure 6: Scheme of a glueing

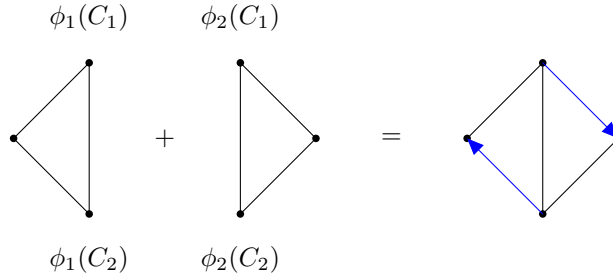


Figure 7: Symmetry breaking in interaction graph after a glueing operation. Arrows indicate influence of a node (source) on another (target), edges without arrow indicates bi-directional influence. Here C consists in two nodes only.

Proof. Take any $v \in V' \setminus (X \cup Y)$. Suppose first that $\alpha(v) \in V_1$. By definition of F' , we have $F'(z^t)_v = (F_1)_{\alpha(v)} \circ \rho_1(z^t)$ but $\rho_1(z^t) = x^t$ (using the Equation 1 in the hypothesis) so $F'(z^t)_v = F_1(x^t)_{\alpha(v)}$. Since (x^t) is a $X \cup \varphi_1(C_2)$ -pseudo-orbit and since $\alpha(v) \notin X \cup \varphi_1(C_2)$, we have

$$F_1(x^t)_{\alpha(v)} = x_{\alpha(v)}^{t+1} = z_v^{t+1}.$$

We conclude that $z_v^{t+1} = F'(z^t)_v$. By a similar reasoning, we obtain the same conclusion if $\alpha(v) \in V_2$. We deduce that (z^t) is a $X \cup Y$ -pseudo-orbit of F' . \square

In the case of a CSAN family where the transition rules are determined by a labeled non-directed graph, the result of a glueing operation has no reason to belong to the family because the symmetry of the interaction graph might be broken (see Figure 7). The following lemma gives a sufficient condition in graph theoretical terms for glueing within a concrete family of automata networks. Intuitively, it consists in asking that, in each graph, all the connections of one half of the dowel to the rest of the graph goes through the other half of the dowel. Here the wooden dowel metaphor is particularly relevant: when considering a single piece of wood with the dowel inserted inside, one half of the dowel is 'inside' (touches the piece of wood), the other half is 'outside' (not touching the piece of wood); then, when the

two pieces are attached, each position in the wood assembly is locally either like in one piece of wood with the dowel inserted or like in the other one with the dowel inserted.

Lemma 26 (Glueing for CSAN). *Let (G_1, λ_1, ρ_1) and (G_2, λ_2, ρ_2) be two CSAN from the same CSAN family \mathcal{F} where G_1 and G_2 are disjoint and F_1 and F_2 are the associated global maps. Taking again the notations of Definition 24, if the following conditions hold*

- *the labeled graphs induced by $\varphi_1(C)$ and $\varphi_2(C)$ in G_1 and G_2 are the same (using the identification $\varphi_1(v) = \varphi_2(v)$)*
- $N_{G_1}(\varphi_1(C_2)) \subseteq \varphi_1(C)$
- $N_{G_2}(\varphi_2(C_1)) \subseteq \varphi_2(C)$

then the glueing $F' = F_1 \overset{\phi_1}{\underset{C_1}{\oplus}} \overset{\phi_2}{\underset{C_2}{\oplus}} F_2$ can be defined as the CSAN on graph $G' = (V', E')$ where V' is as in Definition 24 and each node $v \in V'$ has the same label and same labeled neighborhood as either a node of (G_1, λ_1, ρ_1) or a node of (G_2, λ_2, ρ_2) . In particular F' belongs to \mathcal{F} .

Proof. Let us define $\beta_i : V_i \rightarrow V'$ by

$$\beta_i(v) = \begin{cases} \phi_i^{-1}(v) & \text{if } v \in \phi_i(C), \\ v & \text{else.} \end{cases}$$

Fix $i = 1$ or 2 . According to Definition 24, if $v \in V'$ is such that $\alpha(v) \in V_i$ then $F'_v = (F_i)_{\alpha(v)} \circ \rho_i$. By definition of CSAN, this means that for any $x \in Q^{V'}$ we have $F'_v(x) = \psi_{i,\alpha(v)}(x|_{\beta(N_{G_i}(\alpha(v)))})$ where $\psi_{i,\alpha(v)}$ is a map depending only on the labeled neighborhood of $\alpha(v)$ in G_i as in Definition 2. So the dependencies of v in F' are in one-to-one correspondence through β with the neighborhood of $\alpha(v)$ in G_i . The key observation is that the symmetry of dependencies is preserved, formally for any $v' \in \beta(N_{G_i}(\alpha(v)))$:

- either $\alpha(v') \in V_i$ in which case the dependency of v' on v (in map $\psi_{i,\alpha(v')}$) is the same as the dependency of v on v' (in map $\psi_{i,\alpha(v)}$), and both are determined by the undirected labeled edge $\{\alpha(v), \alpha(v')\}$ of G_i ;
- or $\alpha(v') \notin V_i$ and in this case necessarily $v \in C_i$ and $v' \in C_{3-i}$ (because $N_{G_i}(V_i \setminus \varphi_i(C)) \cap \varphi_i(C) \subseteq \varphi_i(C_i)$ from the hypothesis), so the dependency of v' on v is the same as the dependency of v on v' because the labeled graphs induced by $\phi_1(C)$ and $\phi_2(C)$ in G_1 and G_2 are the same.

Concretely, F' is a CSAN that can be defined on graph $G' = (V', E'_1 \cup E'_2 \cup E(C))$ with

$$E'_i = E(V_i \setminus \varphi_i(C)) \cup \{(u, v_i) : u \in V(C_i), v_i \in (V_i \setminus \varphi_i(C)), (\varphi_i(u), v_i) \in E_i\},$$

and labels as follows:

- on $E(C)$ as in both G_1 and G_2 (which agree through maps ϕ_1 and ϕ_2 on C),
- on $E(V_i \setminus \varphi_i(C))$ as in G_i ,
- for each $u \in V(C_i), v_i \in (V_i \setminus \varphi_i(C))$ such that $(\varphi_i(u), v_i) \in E_i$, edge (u, v_i) has same label as $(\varphi_i(u), v_i)$.

Since any CSAN families (Definition 2) is entirely based on local constraints on labels (vertex label plus set of labels of the incident edges), we deduce that F' is in \mathcal{F} . \square

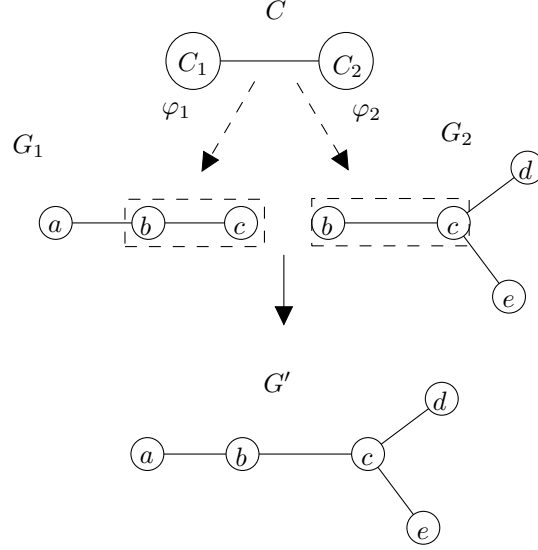


Figure 8: Example of a gluing of two compatibles CSAN. The labeling in nodes of G_1 , G_2 and G' shows equalities between local λ maps of these three CSAN.

5.3 \mathcal{G} -gadgets, gadget glueing and simulation of \mathcal{G} -networks

We now give a precise meaning to the intuitively simple fact that, if a family of automata networks can coherently simulate a set of small building blocks (gates from \mathcal{G}), it should be able to simulate any automata network that can be built out of them (\mathcal{G} -networks).

The key idea here is that gates from \mathcal{G} will be represented by networks of the family called \mathcal{G} -gadgets, and the wiring between gates to obtain a \mathcal{G} -network will translate into glueing between \mathcal{G} -gadgets. Following this idea there are two main conditions for the family to simulate any \mathcal{G} -network:

- the glueing of gadgets should be freely composable inside the family to allow the building of any \mathcal{G} -network;
- the gadgets corresponding to gates from \mathcal{G} should correctly and coherently simulate the functional relation between inputs and outputs given by their corresponding gate.

For clarity, we separate these conditions in two definitions.

We start by developing a definition for gadget glueing. Recall first that Definition 24 relies on the identification of a common dowel in the two networks to be glued. Here, as we want to mimic the wiring of gates which connects inputs to outputs, several copies of a fixed network called *glueing interface* will be identified in each gadget, some of them corresponding to input, and the other ones to outputs. In this context, the only glueing operations we will use are those where some output copies of the interface in a gadget A are glued on input copies of the interface in a gadget B and some input copies of the interface in A are glued on output copies of the interface in B . Then, the global dowel used to formally apply Definition 24 is a disjoint union of the selected input/output copies of the interface. Figure 5.3 illustrates with the notations of the following Definition.

Definition 27 (Glueing interface and gadgets). *Let $C = C_i \cup C_o$ be a fixed set partitioned into two sets. A gadget with glueing interface $C = C_i \cup C_o$ is an automata network $F : Q^{V_F} \rightarrow Q^{V_F}$ together with two collections of injective maps $\phi_{F,k}^i : C \rightarrow V_F$ for $k \in I(F)$ and $\phi_{F,k}^o : C \rightarrow V_F$ for $k \in O(F)$ whose images in V_F are pairwise disjoint and where $I(F)$ and $O(F)$ are disjoint sets which might be empty.*

Given two disjoint gadgets $(F, (\phi_{F,k}^i), (\phi_{F,k}^o))$ and $(G, (\phi_{G,k}^i), (\phi_{G,k}^o))$ with same alphabet and interface $C = C_i \cup C_o$, a gadget glueing is a glueing of the form $H = F \overset{\phi_F}{\underset{\phi_G}{\oplus}} G$ defined as follows:

- a choice of a set A of inputs from F and outputs from G given by injective maps $\sigma_F : A \rightarrow I(F)$ and $\sigma_G : A \rightarrow O(G)$,

- a choice of a set B of outputs from F and inputs from G given by injective maps $\tau_F : B \rightarrow O(F)$ and $\tau_G : B \rightarrow I(G)$ (the set B is disjoint from A),
- C_F is a disjoint union of $|A|$ copies of C_i , and $|B|$ copies of C_o : $C_F = A \times C_i \cup B \times C_o$,
- C_G is a disjoint union of $|A|$ copies of C_o , and $|B|$ copies of C_i : $C_G = A \times C_o \cup B \times C_i$,
- $\phi_F : C_F \cup C_G \rightarrow V_F$ is such that $\phi_F(a, c) = \phi_{F, \sigma_F(a)}^i(c)$ for $a \in A$ and $c \in C$, and $\phi_F(b, c) = \phi_{F, \tau_F(b)}^o(c)$ for $b \in B$ and $c \in C$,
- $\phi_G : C_F \cup C_G \rightarrow V_G$ is such that $\phi_G(a, c) = \phi_{G, \sigma_G(a)}^o(c)$ for $a \in A$ and $c \in C$, and $\phi_G(b, c) = \phi_{G, \tau_G(b)}^i(c)$ for $b \in B$ and $c \in C$.

The resulting network H is a gadget with same alphabet and same interface with $I(H) = I(F) \setminus \sigma_F(A) \cup I(G) \setminus \tau_G(B)$ and $O(H) = O(F) \setminus \tau_F(B) \cup O(G) \setminus \sigma_G(A)$ and $\phi_{H,k}^i$ is $\phi_{F,k}^i$ when $k \in I(F)$ and $\phi_{G,k}^i$ when $k \in I(G)$, and $\phi_{H,k}^o$ is $\phi_{F,k}^o$ when $k \in O(F)$ and $\phi_{G,k}^o$ when $k \in O(G)$.

Given a set of gadgets X with same alphabet and interface, its closure by gadget glueing is the closure of X by the following operations:

- add a disjoint copy of some gadget from the current set,
- add the disjoint union of two gadgets from the current set,
- add a gadget glueing of two gadgets from the current set.

Remark 12. The representation of the result of a gadget glueing can be easily computed from the two gadgets F and G and the choices of inputs/outputs given by maps σ_F , σ_G , τ_F and τ_G : precisely, by definition of glueing (Definition 24) the local map of each node of the result automata network is either a local map of F (when in $V_F \setminus \phi_F(C_G)$ or in C_F) or a local map of G (when in $V_G \setminus \phi_G(C_F)$ or in C_G). Note also that the closure by gadget glueing of a finite set of gadgets X is always a set of automata networks of bounded degree.

Lemma 26 gives sufficient conditions on a set of gadgets to have its closure by gadget glueing contained in a CSAN family.

Lemma 28. Fix some alphabet Q and some glueing interface $C = C_i \cup C_o$ and some CSAN family \mathcal{F} . Let (G_n, λ_n, ρ_n) for $n \in S$ be a set of CSAN belonging to \mathcal{F} with associated global maps F_n . Let $\phi_{F_n,k}^i$ for $k \in I(F_n)$ and $\phi_{F_n,k}^o$ for $k \in O(F_n)$ be maps as in Definition 27 so that $(F_n, (\phi_{F_n,k}^i), (\phi_{F_n,k}^o))$ is a gadget with interface $C = C_i \cup C_o$. Denote by X the set of such gadgets. If the following conditions hold:

- the labeled graphs induced by $\phi_{F_n,k}^i(C)$ and by $\phi_{F_n,k}^o(C)$ in G_n are all the same for all n and k with the identification of vertices given by the $\phi_{*,*}^*$ maps,
- $N_{G_n}(\phi_{F_n,k}^i(C_o)) \subseteq \phi_{F_n,k}^i(C)$ for all $n \in S$ and all $k \in I(F_n)$,
- $N_{G_n}(\phi_{F_n,k}^o(C_i)) \subseteq \phi_{F_n,k}^o(C)$ for all $n \in S$ and all $k \in O(F_n)$,

then the closure by gadget glueing of X is included in \mathcal{F} .

Proof. Consider first the gadget glueing H of two gadgets F_n and $F_{n'}$ from X . Following Definition 27, the global dowel $C_{F_n} \cup C_{F_{n'}}$ used in such a glueing is a disjoint union of copies of C , and its embedding ϕ_{F_n} in G_n (resp. $\phi_{F_{n'}}$ in $G_{n'}$) is a disjoint union of maps $\phi_{F_n,*}^*$ (resp. $\phi_{F_{n'},*}^*$). Therefore the three conditions of Lemma 26 follow from the three conditions of the hypothesis on gadgets from X and we deduce that H belongs to family \mathcal{F} . Moreover, it is clear that gadget H then also verifies the three conditions from the hypothesis, and adding a copy of any gadget to the set also verifies the conditions. We deduce that the closure by gadget glueing of X is included in \mathcal{F} .

□

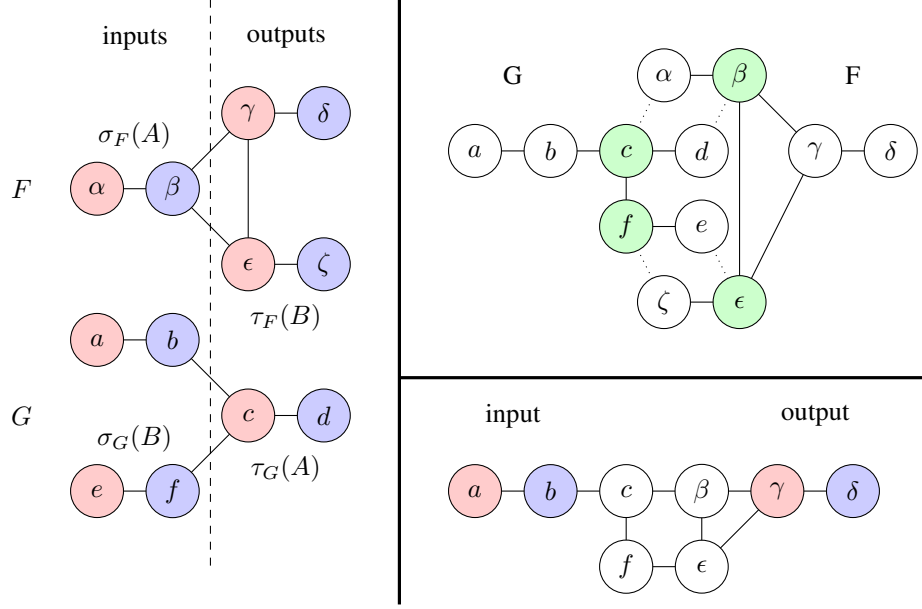


Figure 9: Gadget glueing as in Definition 27. On the left, two gadgets with interface $C = C_i \cup C_o$ where C_i part in each copy of the interface dowel is in red and C_o part in blue. The gadget glueing is done with input $\sigma_F(A)$ on output $\sigma_G(A)$ (here A is a singleton) and output $\tau_F(B)$ on input $\tau_G(B)$ (B is also a singleton). On the upper right, a representation of the global glueing process where nodes in green are those in the copy of C_F in F or in the copy C_G in G ; dotted links show the bijection between the embeddings of $C = C_F \cup C_G$ into V_F and V_G via maps ϕ_F and ϕ_G . On the lower right the resulting gadget with the same interface $C = C_i \cup C_o$ as the two initial gadgets.

The second key aspect to have a coherent set X of \mathcal{G} -gadgets is of dynamical nature: there must exist a collection of pseudo-orbits on each gadget satisfying suitable conditions to permit application of Lemma 25 for any gadget glueing in the closure of X ; moreover, these pseudo-orbits must simulate via an appropriate coding the input/output relations of each gate $g \in \mathcal{G}$ in the corresponding gadget. To obtain this, we rely on a standard set of traces on the glueing interface that must be respected on any copy of it in any gadget.

Definition 29 (Coherent \mathcal{G} -gadgets). *Let \mathcal{G} be any set of finite maps over alphabet Q and let \mathcal{F} be any set of abstract automata networks over alphabet $Q_{\mathcal{F}}$. We say \mathcal{F} has coherent \mathcal{G} -gadgets if there exists:*

- a unique glueing interface $C = C_i \cup C_o$,
- a set X of gadgets $(F_g, (\phi_{g,k}^i)_{1 \leq k \leq i(g)}, (\phi_{g,k}^o)_{1 \leq k \leq o(g)})$ for each $g \in \mathcal{G}$ where $F_g : Q_{\mathcal{F}}^{V_g} \rightarrow Q_{\mathcal{F}}^{V_g} \in \mathcal{F}$ and sets V_g and C are pairwise disjoint, and the closure of X by gadget glueing is contained in \mathcal{F} ,
- a state configuration $s_q \in Q_{\mathcal{F}}^C$ for each $q \in Q$ such that $q \mapsto s_q$ is an injective map,
- a context configuration $c_g \in Q_{\mathcal{F}}^{\hat{V}_g}$ for each $g \in \mathcal{G}$ where $\hat{V}_g = V_g \setminus (\cup_k \phi_{g,k}^i(C) \cup_k \phi_{g,k}^o(C))$,
- a time constant T ,
- a standard trace $\tau_{q,q'} \in (Q_{\mathcal{F}}^C)^{\{0, \dots, T\}}$ for each pair $q, q' \in Q$ such that $\tau_{q,q'}(0) = s_q$ and $\tau_{q,q'}(T) = s_{q'}$,
- for each $g \in \mathcal{G}$ and for any uples of states $q_{i,1}, \dots, q_{i,i(g)} \in Q$ and $q_{o,1}, \dots, q_{o,o(g)} \in Q$ and $q'_{i,1}, \dots, q'_{i,i(g)} \in Q$ and $q'_{o,1}, \dots, q'_{o,o(g)} \in Q$ such that $g(q_{i,1}, \dots, q_{i,i(g)}) = (q'_{o,1}, \dots, q'_{o,o(g)})$, a P_g -pseudo-orbit $(x^t)_{0 \leq t \leq T}$ of F_g with $P_g = \cup_{1 \leq k \leq i(g)} \phi_{g,k}^i(C_o) \cup \cup_{1 \leq k \leq o(g)} \phi_{g,k}^o(C_i)$ and with
 - for each $1 \leq k \leq i(g)$, the trace $t \mapsto x_{\phi_{g,k}^i(C)}^t$ is exactly $\tau_{q_{i,k}, q'_{i,k}}$,

- for each $1 \leq k \leq o(g)$, the trace $t \mapsto x_{\hat{\phi}_{g,k}^o(C)}^t$ is exactly $\tau_{q_{o,k}, q'_{o,k}}$,
- $x_{\hat{V}_g}^0 = x_{\hat{V}_g}^T = c_g$.

We can now state the key lemma of our framework: having coherent \mathcal{G} -gadgets is sufficient to simulate the whole family of \mathcal{G} -networks.

Lemma 30. *Let \mathcal{G} be a set of irreducible gates. If an abstract automata network family \mathcal{F} has coherent \mathcal{G} -gadgets then it contains a subfamily of bounded degree networks with the canonical bounded degree representation $(\mathcal{F}_0, \mathcal{F}_0^*)$ that simulates $\Gamma(\mathcal{G})$ in time T and space S where T is a constant map and S is bounded by a linear map.*

Proof. We take the notations of Definition 29. To any \mathcal{G} -network F with set of nodes V given as in Definition 23 by a list of gates $g_1, \dots, g_k \in \mathcal{G}$ and maps α and β (see Remark 11) we associate an automata network from \mathcal{F} as follows. First, let $(F_{g_i})_{1 \leq i \leq k}$ be the gadgets corresponding to gates g_i and suppose they are all disjoint (by taking disjoint copies when necessary). Then, start from the gadget $F_1 = F_{g_1}$ and for any $1 \leq i < k$ we define F_{i+1} as the gadget glueing of F_i and $F_{g_{i+1}}$ on the input/outputs as prescribed by maps α and β . More precisely, the gadget glueing select the set of inputs (j, k) with $1 \leq j \leq i$ and $1 \leq k \leq i(g_j)$ such that $\beta(\alpha(j, k)) = (i+1, k')$ for some $1 \leq k' \leq o(g_{i+1})$ and glue them on their corresponding output $(i+1, k')$ of g_{i+1} (precisely, through maps σ_{F_i} and $\sigma_{F_{g_{i+1}}}$ of domain A_{i+1} playing the role of maps σ_F and σ_G of Definition 27), and, symmetrically, selects the inputs $(i+1, k)$ with $1 \leq k \leq i(g_{i+1})$ such that $\beta(\alpha(i+1, k)) = (j, k')$ for some $1 \leq j \leq i$ and $1 \leq k' \leq o(g_j)$ and glue their corresponding output (j, k') (precisely, through maps $\tau_{F_{g_{i+1}}}$ and τ_{F_i} of domain B_{i+1} playing the role of maps τ_G and τ_F from Definition 27). If both of these sets of inputs/outputs are empty, the gadget glueing is replaced by a simple disjoint union.

The final gadget F_k has no input and no output, and a representation of it as a pair graph and local maps can be constructed in **DLOGSPACE**, because the local map of each of its nodes is independent of the glueing sequence above and completely determined by the gadget F_{g_j} it belongs to and whether the node is inside some input or some output dowel or not (see Remark 12).

It now remains to show that the automata network F_k simulates F . To fix notations, let V_k be the set of nodes of F_k . For each $v \in V$, define $D_v \subseteq V_k$ as the copy of the dowel that correspond to node v of F , i.e. that was produced in the gadget glueing of F_i with $F_{g_{i+1}}$ for i such that $\beta(v) = (i+1, k')$ for some $1 \leq k' \leq o(g_{i+1})$ (or symmetrically $\alpha(i+1, k) = v$ for some $1 \leq k \leq i(g_j)$). More precisely, if $a \in A_{i+1}$ is such that $\sigma_{F_{g_{i+1}}}(a) = (i+1, k')$ then $D_v = \{a\} \times C$ (symmetrically if $b \in B_{i+1}$ is such that $\tau_{F_{g_{i+1}}}(b) = (i+1, k)$ then $D_v = \{b\} \times C$). Also denote by $\rho_v : D_v \rightarrow C$ the map such that $\rho_v(a, c) = c$ for all $c \in C$ (symmetrically, $\rho_v(b, c) = c$). With these notations, we have

$$V_k = \bigcup_{v \in V} D_v \cup \bigcup_{1 \leq i \leq k} \hat{V}_{g_i}$$

Let us define the block embedding $\phi : Q^V \rightarrow Q_{\mathcal{F}}^{V_k}$ as follows

$$\phi(x)(v') = \begin{cases} s_{x_v}(\rho_v(v')) & \text{if } v' \in D_v, \\ c_{g_i}(v') & \text{if } v' \in \hat{V}_{g_i}. \end{cases}$$

for any $x \in Q^V$ and any $v' \in V_k$, where s_q for $q \in Q$ are the state configurations and c_g for $g \in \mathcal{G}$ are the context configurations granted by Definition 29. Note that ϕ is injective because the map $q \mapsto s_q$ is injective. By inductive applications of Lemma 25, the P_{g_i} -pseudo-orbits of each F_{g_i} from Definition 29 can be glued together to form valid orbits of F_k that start from any configuration $\phi(x)$ with $x \in Q^V$ and ends after T steps in a configuration $\phi(y)$ for some $y \in Q^V$ which verifies $y = F(x)$. Said differently, we have the following equality on Q^V :

$$\phi \circ F = F_k^T \circ \phi.$$

Note that T is a constant and that the size of V_k is at most linear in the size of V . The lemma follows. \square

Remark 13. *Note that in Lemma 30 above, the block embedding that is constructed can be viewed as a collection of blocs of bounded size that encode all the information plus a context (see Remark 5).*

In the case of CSAN families and using Remark 7 we have a simpler formulation of the Lemma.

Corollary 3. *If \mathcal{G} in a set of irreducible gates and \mathcal{F} a CSAN family which has coherent \mathcal{G} -gadgets then \mathcal{F} simulates $\Gamma(\mathcal{G})$ in time T and space S where T is a constant map and S is bounded by a linear map.*

5.4 \mathcal{G}_m -networks and $\mathcal{G}_{m,2}$ -networks as standard universal families

Let $i, o \in \{1, 2\}$ be two numbers. We define the functions $\text{OR}, \text{AND} : \{0, 1\}^i \rightarrow \{0, 1\}^o$ where $\text{OR}(x) = \max(x)$ and $\text{AND}(x) = \min(x)$. Note that in the case in which $i = o = 1$ we have $\text{AND}(x) = \text{OR}(x) = \text{Id}(x) = x$ and also in the case $i = 1$ and $o = 2$ we have that $\text{AND}(x) = \text{OR}(x) = (x, x)$. We define the set $\mathcal{G}_m = \{\text{AND}, \text{OR}\}$. Observe that in this case o and i may take different values. In addition, we define the set $\mathcal{G}_{m,2}$ in which we fix $i = o = 2$.

It is folklore knowledge that monotone Boolean networks (with AND/OR local maps) can simulate any other network. Here we make this statement precise within our formalism: \mathcal{G}_m -networks are strongly universal. Note that there is more work than the classical circuit transformations involving monotone gates because we need to obtain a simulation of any automata network via block embedding. In particular we need to build monotone circuitry that is synchronized and reusable (*i.e.* that can be reinitialized to a standard state before starting a computation on a new input). Moreover, our definitions requires a production of \mathcal{G}_m -networks in **DLOGSPACE**. The main ingredient for establishing universality of \mathcal{G}_m -networks is an efficient circuit transformation due to Greenlaw, Hoover and Ruzzo in [20, Theorems 6.2.3 to 6.2.5]. Let us start by proving that this family is strongly universal, which is slightly simpler to prove.

Theorem 31. *The family $\Gamma(\mathcal{G}_m)$ of all \mathcal{G}_m -networks is strongly universal.*

Proof. Let Q an arbitrary alphabet and $F : Q^n \rightarrow Q^n$ an arbitrary automata network on alphabet Q such that the communication graph of F has maximum degree Δ . Let $C : \{0, 1\}^n \rightarrow \{0, 1\}^n$ be a constant depth circuit representing F . Let us assume that C has only OR, AND and NOT gates. We can also assume that C is synchronous because, as its depth does not depend on the size of the circuit, one can always add fanin one and fanout one OR gates in order to modify layer structure. We are going to use a very similar transformation to the one proposed in [20, Theorem 6.2.3] in order to efficiently construct an automata network in $\Gamma(\mathcal{G}_m)$. In fact, we are going to duplicate the original circuit by considering the coding $x \in \{0, 1\} \rightarrow (x, 1 - x) \in \{0, 1\}^2$. Roughly, each gate will have a positive part (which is essentially a copy) and a negative part which produces the negation of the original output by using De Morgan's laws. More precisely, we are going to replace each gate in the network by the gadgets shown in Figure 10. The main idea is that one can represent the function $x \wedge y$ by the coding: $(x \wedge y, \bar{x} \vee \bar{y})$ and $x \vee y$ by the coding: $(x \vee y, \bar{x} \wedge \bar{y})$. In addition, each time there is a NOT gate, we replace it by a fan in 1 fan out 1 OR gadget and we connect positive outputs to negative inputs in the next layer and negative outputs to positive inputs as it is shown in Figure 11. We are going to call C^* to the circuit constructed by latter transformations. Observe that C^* is such that it holds on $\{0, 1\}^i$:

$$\phi \circ C = C^* \circ \phi$$

where $\phi : \{0, 1\}^n \rightarrow \{0, 1\}^{2n}$ is defined for any n by $\phi(x)_{2j} = x_j$ and $\phi(x)_{2j+1} = \neg x_j$.

Now consider the coding map $m_Q : Q \rightarrow \{0, 1\}^k$ and let $n = k|V|$. Build from C^* the $\mathcal{G}_{m,2}$ -network $F^* : \{0, 1\}^{V^+} \rightarrow \{0, 1\}^{V^+}$ that correspond to it (gate by gate) and where the output j is wired to input j for all $1 \leq j \leq 2n$. Define a block embedding of Q^V into $\{0, 1\}^{V^+}$ as follows (see Remark 5):

- for each $v \in V$ let D_v be the set of input nodes in F^* that code v (via m_Q and then double railed logic),
- let $C = V^+ \setminus \bigcup_v D_v$ be the remaining context block,
- let $p_{v,q} \in \{0, 1\}^{D_v}$ be the pattern coding node v in state q ,
- let $p_C = 0^C$ be the context pattern,
- let $\phi : Q^V \rightarrow \{0, 1\}^{V^+}$ be the associated block embedding map.

We claim that F^* simulates F via block embedding ϕ with time constant equal to the depth of C^* plus 1. Indeed, F^* can be seen as a directed cycle of N layers where layer $L_{i+1 \bmod N}$ only depends on layer i . The block embedding is such that for any configuration $x \in Q^V$, $\phi(x)$ is 0 on each layer except the layer containing the inputs. On configurations where a single layer L_i is non-zero, F^* will produce a configuration where the only non-zero layer is $L_{i+1 \bmod N}$. From there, it follows by construction of F^* that $\phi \circ F(x) = (F^*)^N \circ \phi(x)$ for all $x \in Q^V$.

The fact that construction is obtainable in **DLOGSPACE** follows from the same reasoning used to show in [20, Theorem 6.2.3]. In fact, authors show that reduction is actually better as they show it is **NC¹**. \square

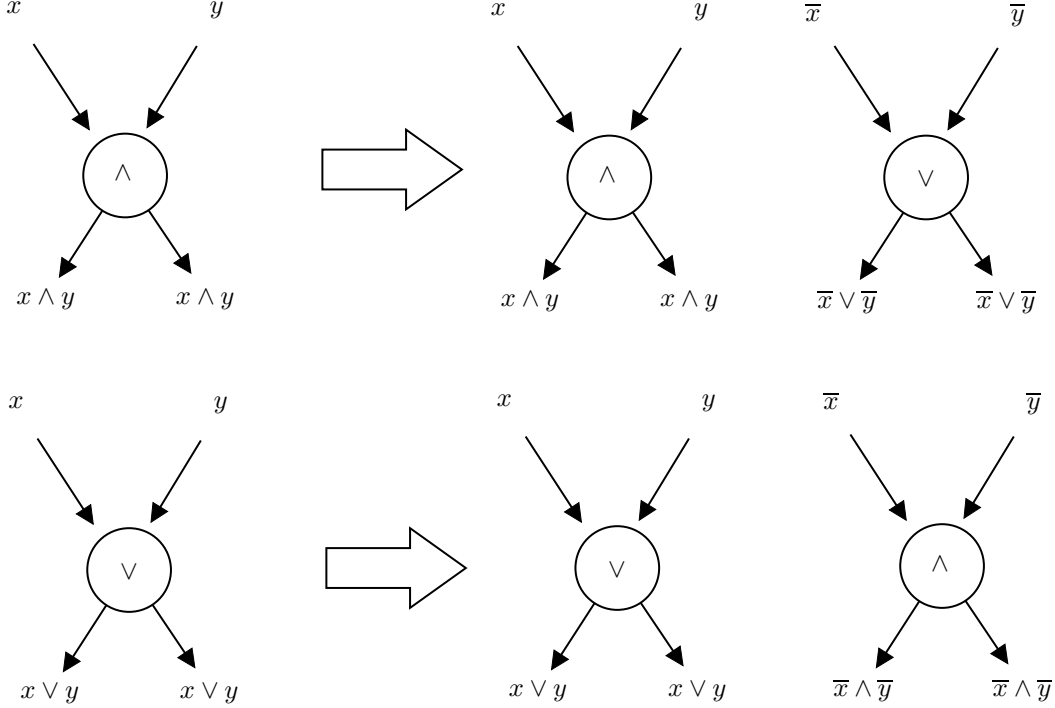


Figure 10: AND and OR gadgets for simulating AND/OR gates with fanin and fanout 2. For other values of fanin and fanout gadgets are the same but considering different number of inputs/outputs

Theorem 32. *The family $\Gamma(\mathcal{G}_{m,2})$ simulates in constant time and linear space the family $\Gamma(\mathcal{G}_m)$, i.e. there exists a constant function $T : \mathbb{N} \rightarrow \mathbb{N}$ and a linear function $S : \mathbb{N} \rightarrow \mathbb{N}$ such that $\Gamma(\mathcal{G}_m) \preceq_S^T \Gamma(\mathcal{G}_{m,2})$*

Proof. Let $F : Q^V \rightarrow Q^V$ be an arbitrary \mathcal{G}_m -network coded by its standard representation defined by a list of gates g_1, \dots, g_n and two functions α and β mapping inputs to nodes in F and nodes in F to outputs respectively. We are going to construct in **DLOGSPACE** a $\mathcal{G}_{m,2}$ -network G that simulates F in time $T = \mathcal{O}(1)$ and space $S = \mathcal{O}(|V|)$ where H is the communication graph of F . In order to do that, we are going to replace each gate g_k by a small gadget. More precisely, we are going to introduce the following coding function: $x \in \{0, 1\} \rightarrow (x, x, 0) \in \{0, 1\}^3$. We are going to define gadgets for each gate. Let us take $k \in \{1, \dots, n\}$ and call g_k^* the corresponding gadget associated to g_k . Let us suppose g_k is an OR gate and that it has fanin 2 and fanout 1 then, we define $g_k^* : \{0, 1\}^6 \rightarrow \{0, 1\}^6$ as a function that for each input of the form $(x, x, y, y, 0, 0)$ produces the output $g_k^*((x, x, y, y, 0, 0)) = (x \vee y, x \vee y, 0, 0, 0, 0)$. The case fanin 1 and fanout 1 is given by $g_k^*((x, x, 0, 0, 0, 0)) = (x, x, 0, 0, 0, 0)$, the case fanin 2 and fanout 2 is given by $g_k^*((x, x, y, y, 0, 0)) = (x \vee y, x \vee y, x \vee y, x \vee y, 0, 0)$ and finally case fanin 1 and fanout 2 is given by the same latter function but on input $(x, x, 0, 0, 0, 0)$. The AND case is completely analogous. We are going to implement the previous functions as small (constant depth) synchronized circuits that we call block gadgets. More precisely, we are going to identify functions g_k^* with its correspondent block gadget. The detail on the construction of these circuits that define latter functions are provided in Figures 12, 13 and 14.

Once we have defined the structure of block gadgets, we have to manage connections between them and also manage the fixed 0 inputs that we have added in addition to the zeros that are produced by the blocks as outputs. In order to do that, let us assume that gates g_i and g_j are connected. Note from the discussion on coding above that AND/OR gadgets have between 2 and 4 inputs and outputs fixed to 0. In particular, as it is shown in Figures 12, 13 and 14, all the block gadgets have the same amount of zeros in the input and in the output with the exception of the fanin 1 fanout 2 gates and the fanin 2 fanout 1 gates. However, as \mathcal{G} -networks are closed systems (the amount of inputs must be the same that the amount of outputs) we have that, for each fanin 1 fanout 2 gate, it must be a fanin 2 fanout 1 gate and vice versa (otherwise there would be more input than outputs or more outputs than inputs). In other words, there is a bijection between the set of fanin 1 fanout 2 gates and the set of fanin 2 fanout 1. Observe that fanin 2 fanout 1 gates consume 2 zeros in input but produce 4 zeros in output while fanin 1 fanout 2 gates consume 4 in input and produce 2 zeros in output (see Figures 13 and 14). So, between g_i^* and g_j^* we have to distinct two cases: a) if both gates have

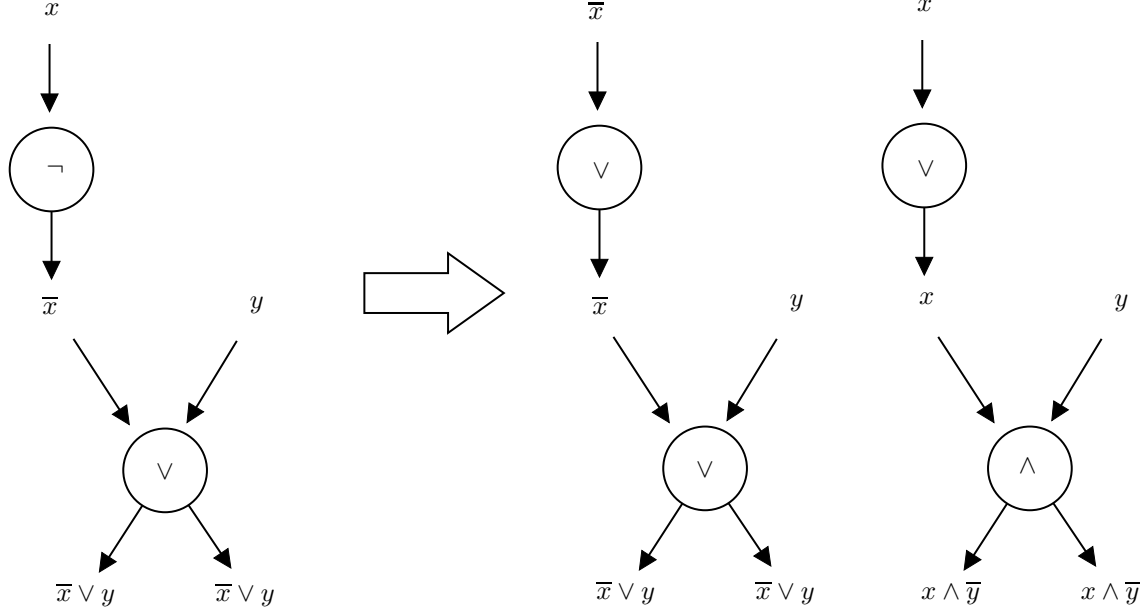


Figure 11: NOT gadget wiring for circuit simulation using gates from \mathcal{G}_m . In this case a NOT gate is connected to an OR gate in the original circuit. Copies of the NOT gate in the circuit performing simulation are connected to the copies of the OR gate switched: positive part is connected to negative part of the OR gate and viceversa.

the same number of inputs and outputs, connections are managed in the obvious way i.e., outputs corresponding to the computation performed by original gate are assigned between g_i^* and g_j^* and each gate uses the same zeros they produce to feed its inputs. b) if g_i^* or g_j^* have more inputs than outputs or vice versa, we have to manage the extra zeros (needed or produced). Without loss of generality, we assume that g_i^* is fanin 2 fanout 1. Then, by latter observation it must exist another gate g_k and thus, a gadget block g_k^* with fanin 1 and fanout 2. We simply connect extra zeros produced by g_i^* to block g_k^* and we do the same we did in previous case in order to manage connections.

Note that F^* is constructible in **DLOGSPACE** as it suffices to read the standard representation of F and produce the associated block gadgets which have constant size. In addition we have that previous encoding $g \rightarrow g^*$ induce a block map $\phi : \{0, 1\}^V \rightarrow \{0, 1\}^{V^+}$ where $|V^+| = \mathcal{O}(|V|)$ and that $\phi \circ F = F^{*T} \circ \phi$ where $T = 6$ is the size of each gadget block in F^* . We conclude that $F^* \in \Gamma(\mathcal{G}_{m,2})$ simulates F in space $|V^+| = \mathcal{O}(|V|)$ and time $T = 6$ and thus, $\Gamma(\mathcal{G}_m) \preceq_S^T \Gamma(\mathcal{G}_{m,2})$ where T is constant and S is a linear function. .

□

Corollary 4. *The family $\Gamma(\mathcal{G}_{m,2})$ is strongly universal.*

Proof. Result is direct from Theorem 31 ($\Gamma(\mathcal{G}_m)$ is strongly universal) and Theorem 32 ($\Gamma(\mathcal{G}_m) \preceq_S^T \Gamma(\mathcal{G}_{m,2})$ where T is constant and S is a linear function). □

Now we show the universality of $\Gamma(\mathcal{G}_m)$. The proof is essentially a consequence of [20, Theorem 6.2.5]. Roughly, latter result starts with alternated monotone circuit which has only fanin 2 and fanout 2 gates (previous results in the same reference show that one can always reduce to this case starting from an arbitrary circuit) and gives an \mathbf{NC}^1 construction of a synchronous circuit preserving latter properties. We need additional care here because we want a reusable circuit whose output is fed back to its input. Note also that the construction uses quadratic space in the number of gates of the circuit given in input, so we cannot show strong universality this way but only universality.

Theorem 33. *The family $\Gamma(\mathcal{G}_m)$ of all \mathcal{G}_m -networks is universal*

Proof. Let $F : Q^k \rightarrow Q^k$ be some arbitrary network with a circuit representation $C : \{0, 1\}^n \rightarrow \{0, 1\}^n$ such that $n = k^{\mathcal{O}(1)}$. By [20, Theorem 6.2.5] we can assume that there exists a circuit $C' : \{0, 1\}^{n'} \rightarrow \{0, 1\}^{n'}$ where

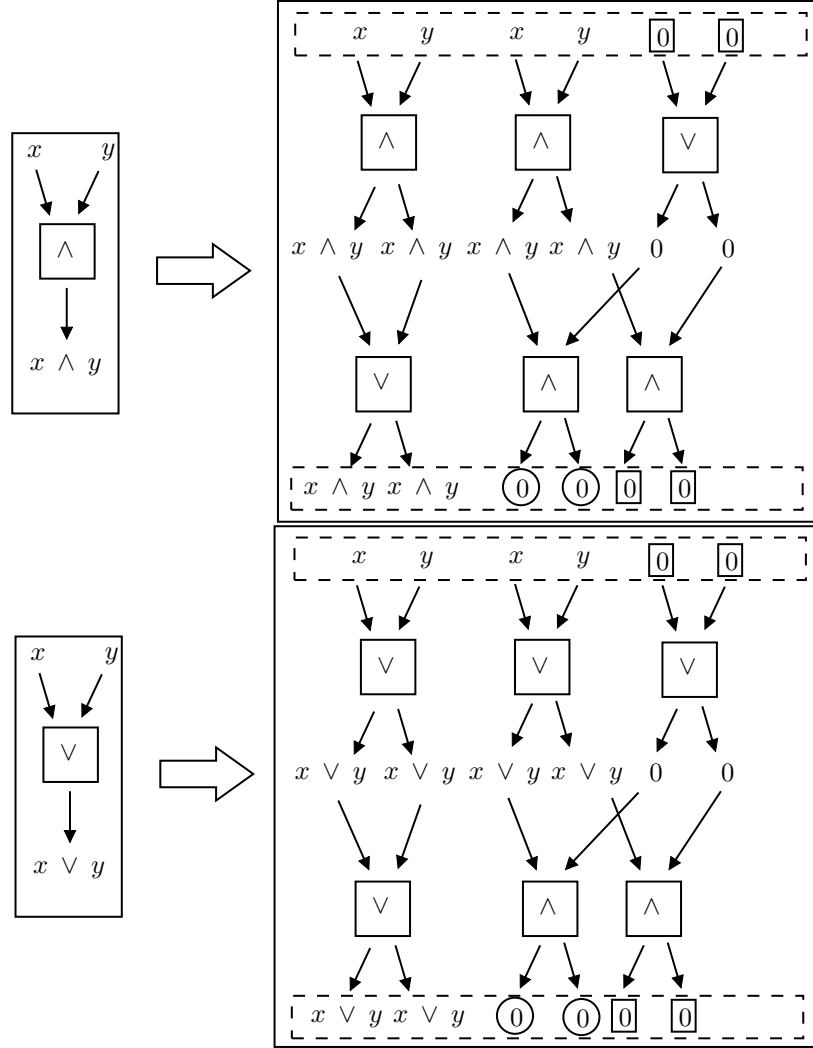


Figure 12: Block gadgets for simulating Fanin 2 Fanout 1 AND/OR gates using only gates in $\mathcal{G}_{m,2}$. Squared zeros represent the amount of zeros that can be used as inputs for the same block. Circled zeros correspond to extra zeros that need to be assigned to a Fanin 1 Fanout 2 gate.

$n' = \mathcal{O}(n^2)$ such that C' is synchronous alternated and monotone. In addition, every gate in C' has fanin and fanout 2. We remark that latter reference do not only provides the standard encoding of C' but also give us a **DLOGSPACE** algorithm (it is actually \mathbf{NC}^1) which takes the standard representation of $C : \{0, 1\}^n \rightarrow \{0, 1\}^n$ and produces C' . We are going to slightly modify latter algorithm in order to construct not only a circuit but a \mathcal{G}_m -network. In fact, the only critical point is to manage the identification between outputs and inputs. This is not direct from the result by Ruzzo et al. as their algorithm involves duplication of inputs and also adding constant inputs. In order to manage this, it suffices to simply modify their construction in order to mark original, copies and constant inputs. Then, as \mathcal{G}_m includes COPY gates and also AND/OR gates with fanout 1, one can always produce copies of certain input if we need more, or erase extra copies by adding and small tree of $\mathcal{O}(\log(n))$ depth. Same goes for constant inputs. Formally, at the end of the algorithm, the **DLOGSPACE** can read extra information regarding copies and constant inputs, and then can construct $\mathcal{O}(\log(n))$ depth circuit that produces a coherent encoding for inputs and outputs. This latter construction defines a \mathcal{G}_m -network $G : \{0, 1\}^{n''} \rightarrow \{0, 1\}^{n''}$ and an encoding $\phi : Q^n \rightarrow \{0, 1\}^{n''}$ where $n'' = \mathcal{O}(n^2)$ such that $\phi \circ F = G^T \circ \phi$ where $T = \mathcal{O}(\text{depth}(C') + \log(n))$. Thus, \mathcal{G}_m is universal. \square

We can now state the following direct corollary.

Corollary 5. *Let \mathcal{F} be a strongly universal automata network family. Then, \mathcal{F} is universal.*

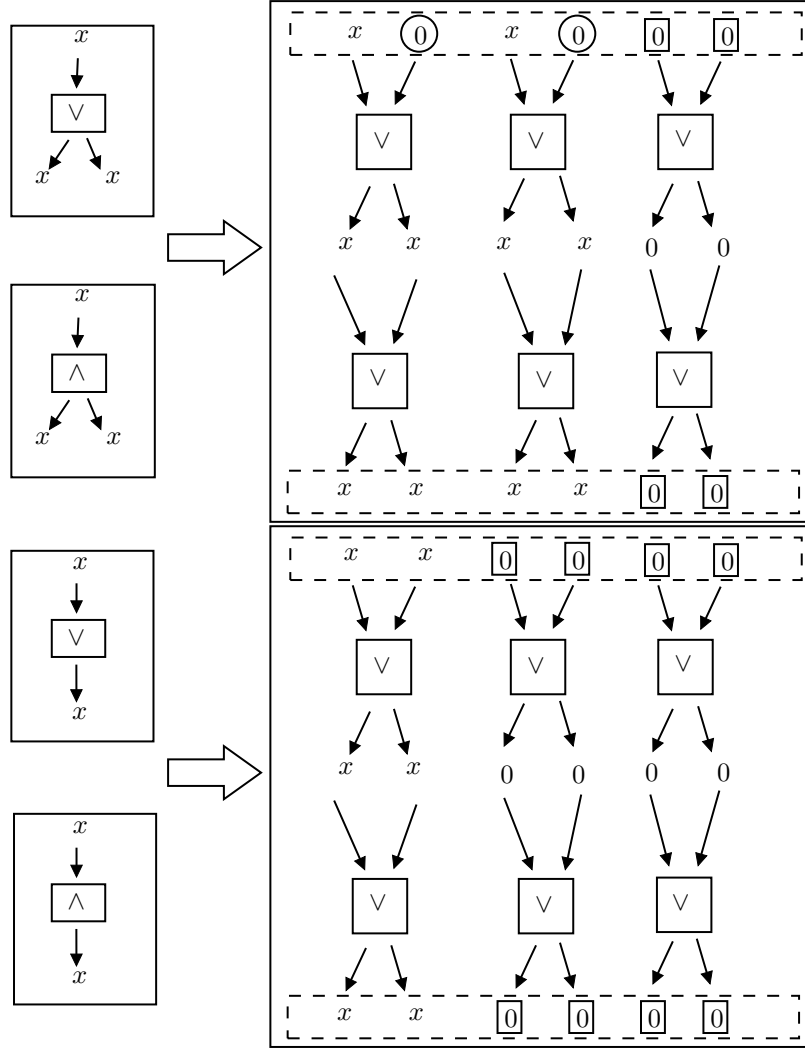


Figure 13: Block gadgets for simulating Fanin 1 Fanout 2 and Fanin 1 Fanout 1 AND/OR gates using only gates in $\mathcal{G}_{m,2}$. Squared zeros represent the amount of zeros that can be used as inputs for the same block. Circled zeros correspond to extra zeros that need to be received from a Fanin 2 Fanout 1 gate.

Proof. In order to show the result, it suffices to exhibit a \mathcal{G} -network family (\mathcal{G} -networks are bounded degree networks) which is strongly universal and universal at the same time. By Theorem 33 we take $\mathcal{G} = \mathcal{G}_m$ and thus, corollary holds. \square

Corollary 6. *Let \mathcal{G} be either \mathcal{G}_m or $\mathcal{G}_{m,2}$. Any family \mathcal{F} that has coherent \mathcal{G} -gadgets contains a subfamily of bounded degree networks with bounded degree representation which is (strongly) universal. Any CSAN family with coherent \mathcal{G} -gadgets is (strongly) universal.*

5.5 Closure and synchronous closure

Although monotone gates are sometimes easier to realize in concrete dynamical system which make the above results useful, there is nothing special about them to achieve universality: any set of gates that are expressive enough for Boolean functions yields the same universality result. Given a set of maps \mathcal{G} over alphabet Q , we define its *closure* $\overline{\mathcal{G}}$ as the set of maps that are computed by circuits that can be built using only gates from \mathcal{G} . More precisely, $\overline{\mathcal{G}}$ is the closure of \mathcal{G} by composition, i.e. forming from maps $g_1 : Q^{I_1} \rightarrow Q^{O_1}$ and $g_2 : Q^{I_2} \rightarrow Q^{O_2}$ (with I_1, I_2, O_1, O_2 disjoint) a composition g by plugging a subset of outputs $O \subseteq O_2$ of g_1 into a subset of inputs $I \subseteq I_2$ of g_2 , thus

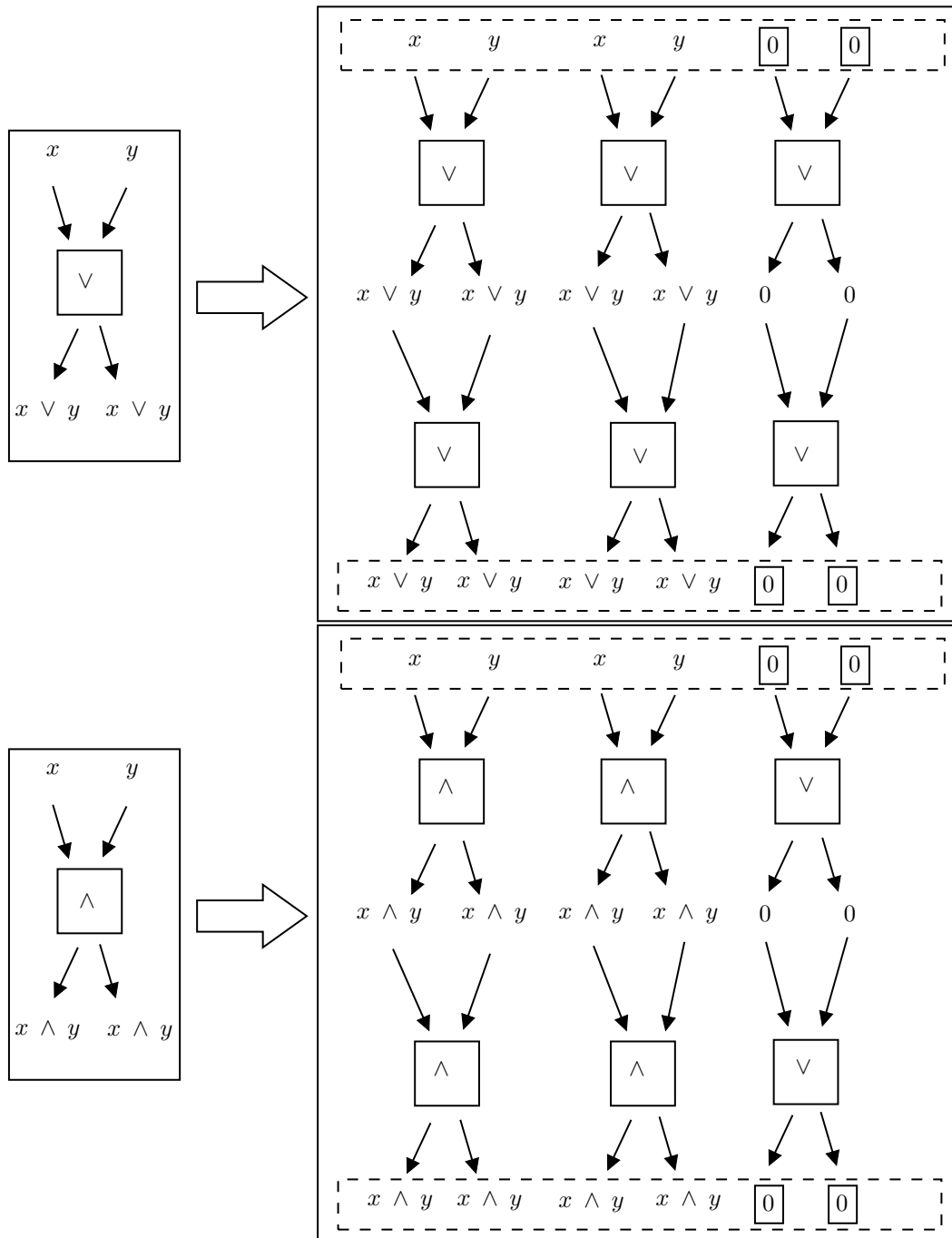


Figure 14: Block gadgets for simulating fanin 2 fanout 2 AND/OR gates using only gates in $\mathcal{G}_{m,2}$. Squared zeros represent the amount of zeros that can be used as inputs for the same block.

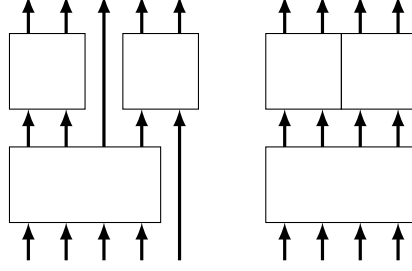


Figure 15: Non-synchronous composition (on the left) and synchronous composition (on the right).

obtaining $g : Q^{I_1 \cup I_2 \setminus I} \rightarrow Q^{O_1 \setminus O \cup O_2}$ with

$$g(x)_o = \begin{cases} g_1(x_{I_1})_o & \text{if } o \in O_1 \setminus O \\ g_2(y)_o & \text{if } o \in O_2 \end{cases}$$

where $y_j = x_j$ for $j \in I_2 \setminus I$ and $y_j = g_1(x_{I_1})_{\pi(j)}$ where $\pi : I \rightarrow O$ is the chosen bijection between I and O (the wiring of outputs of g_1 to inputs of g_2). A composition is *synchronous* if either $I = \emptyset$ or $I = I_2$. We then define the synchronous closure $\overline{\mathcal{G}}^2$ as the closure by synchronous composition. The synchronous composition correspond to synchronous circuits with gates in \mathcal{G} . A \mathcal{G} -circuit is a sequence of compositions starting from elements of \mathcal{G} . It is synchronous if the compositions are synchronous. The depth of a \mathcal{G} -circuit is the maximal length of a path from an input to an output. In the case of a synchronous circuits, all such path are of equal length.

Remark 14. *The above definitions are very close to the classical notion of clones [31]. However, we stress that, in our case, projections maps $Q^k \rightarrow Q$ are generally not available. This is important because in a given dynamical systems, erasing information might be impossible (think about reversible systems) and hiding it into some non-coding part might be complicated.*

Proposition 4. *Fix some alphabet Q and consider two finite sets of maps \mathcal{G} and \mathcal{G}' over alphabet Q such that:*

- *either contains the identity map $Q \rightarrow Q$ and is such that $\overline{\mathcal{G}}$ contains \mathcal{G}' ,*
- *or there is an integer k such that $\overline{\mathcal{G}}_k^2$, the set of elements of $\overline{\mathcal{G}}^2$ that can be realized by a circuit of depth k , contains \mathcal{G}' .*

Then, any family \mathcal{F} that has coherent \mathcal{G} -gadgets has coherent \mathcal{G}' -gadgets.

Proof. Suppose first that the first item holds. Since $\overline{\mathcal{G}}$ contains \mathcal{G}' there must exist a circuit made of gates from \mathcal{G} that produces any given element $g \in \mathcal{G}'$. One then wants to apply gadget glueing on gadgets from \mathcal{G} to mimic the composition and thus obtain a gadget corresponding to g . However this doesn't work as simply because propagation delay is a priori not respected at each gate in the circuit composition yielding g and there is a risk that information arrives distinct delays at different outputs. However, since \mathcal{G} contains the identity map, there is a corresponding gadget in the family that actually implements a delay line. This additional gadget solves the problem: it is straightforward to transform by padding with identity gates all circuit with gates in \mathcal{G} into synchronous ones. Moreover, by padding again, we can assume that the finite set of such circuits computing elements of \mathcal{G}_m are all of same depth. It is then straightforward to translate this set of circuits into coherent \mathcal{G}_m -gadgets by iterating gadget glueing and using Lemma 25.

If the second item holds the situation is actually simpler because the synchronous closure contains only synchronous circuits of gates from $\mathcal{G}_{m,2}$ so we can directly translate the circuits producing the maps of $\mathcal{G}_{m,2}$ into gadgets via gadget glueing by Lemma 25 as in the previous case. Moreover, the hypothesis is that all elements of \mathcal{G}' are realized by circuit of same depth so we get gadgets that share the same time constant.

□

5.6 Super-polynomial periods without universality

A universal family must exhibit super-polynomial periods, however universality is far from necessary to have this dynamical feature. In this subsection we define the family of wire networks to illustrate this.

In order to do that, we need the following classical result about the growth of Chebyshev function and prime number theorem.

Lemma 34. [21] *Let $m \geq 2$ and $\mathcal{P}(m) = \{p \leq m \mid p \text{ prime}\}$. If we define $\pi(m) = |\mathcal{P}(m)|$ and $\theta(m) = \sum_{p \in \mathcal{P}(m)} \log(p)$ then we have $\pi(m) \sim \frac{m}{\log(m)}$ and $\theta(m) \sim m$.*

By using the Lemma 34 we can construct automata networks with non-polynomial cycles simply by making disjoint union of rotations (*i.e.* network whose interaction graph is a cycle that just rotate the configuration at each step). Indeed, it is sufficient to consider rotations on cycle whose length are successive prime numbers. It turns out that these automata networks are exactly \mathcal{G}_w -networks where \mathcal{G}_w is a single 'wire gate': $\mathcal{G}_w = \{id_B\}$ where id_B is the identity map over $\{0, 1\}$.

Formally, according to Definition 23, for any \mathcal{G}_w -network $F : Q^V \rightarrow Q^V$ there exist a partition $V = C_1 \cup C_2 \dots \cup C_k$ where $C_i = \{u_1^i, \dots, u_{l_i}^i\}$ with $l_i \geq 2$ for each $i = 1, \dots, k$ and $F(x)_{u_{s+1}^i \bmod l_i} = x_{u_s^i}$ for any $x \in Q^V$ and $0 \leq s \leq l_i$.

Theorem 35. *Any family \mathcal{F} that has coherent \mathcal{G}_w -gadgets has superpolynomial cycles, more precisely: there is some $\alpha > 0$ such that for infinitely many $n \in \mathbb{N}$, there exists a network $F_n \in \mathcal{F}$ with $O(n)$ nodes and a periodic orbit of size $\Omega(\exp(n^\alpha))$.*

Proof. Taking the notations of Lemma 34, define for any n the \mathcal{G}_w -network G_n made of disjoint union of circuits of each prime length less than n . G_n has size at most $n\pi(n)$ and if we consider a configuration x which is in state 1 at exactly one node in each of the $\pi(n)$ disjoint circuit, it is clear that the orbit of x is periodic of period $\exp(\theta(n))$. Therefore, from Lemma 34, for any n , G_n is a circuit of size $m \leq n\pi(n)$ with a periodic orbit of size $\theta(n) \in \Omega(\exp(\sqrt{m} \log m))$. By hypothesis there are linear maps T and S such that for any n , there is F_n that simulates G_n (by Lemma 30), therefore F_n also has a super-polynomial cycle by Lemma 14. \square

5.7 Conjunctive networks and \mathcal{G}_{conj} -networks

Let $G = (V, E)$ be any directed graph. The conjunctive network associated to G is the automata network $F_G : \{0, 1\}^V \rightarrow \{0, 1\}^V$ given by $F(x)_i = \bigwedge_{j \in N^-(i)} x_j$ where $N^-(i)$ denotes the incoming neighborhood of i . Conjunctive networks are thus completely determined by the interaction graph and a circuit representation can be deduced from this graph in **DLOGSPACE**. We define the family \mathcal{F}_{conj} as the set of conjunctive networks together with the standard representation \mathcal{F}_{conj}^* which are just directed graphs encoded as finite words in a canonical way.

Remark 15. *We can of course do the same with disjunctive networks. Any conjunctive network F_G on graph G is conjugated to the disjunctive network F'_G on the same graph by the negation map $\rho : \{0, 1\}^V \rightarrow \{0, 1\}^V$ defined by $\rho(x)_i = 1 - x_i$, formally $\rho \circ F_G = F'_G \circ \rho$. In particular, this means that the families of conjunctive and disjunctive networks simulate each other. In the sequel we will only state results for conjunctive networks while they hold for disjunctive networks as well.*

Let us now consider the set $\mathcal{G}_{conj} = \{\text{AND}, \text{COPY}\}$. \mathcal{G}_{conj} -networks are nothing else than conjunctive networks with the following degree constraints: each node has either in-degree 1 and out-degree 2, or in-degree 2 and out-degree 1. The following theorem shows that, up to simulation, these constraints are harmless.

Theorem 36. *The family of \mathcal{G}_{conj} -networks simulates the family $(\mathcal{F}_{conj}, \mathcal{F}_{conj}^*)$ of conjunctive networks in linear time and polynomial space.*

Proof. Let F be an arbitrary conjunctive network on graph $G = (V, E)$ with n nodes. Its maximal in/out degree is at most n . For each node of indegree $i \leq n$ we can make a tree-like \mathcal{G}_{conj} -gadget with i inputs and 1 output that computes the conjunction of its i inputs in exactly n steps: more precisely, we can build a sub-network of size $O(n)$ with i identified 'input' nodes of fanin 1 and one identified output node of fanout 1 such that for any $t \in \mathbb{N}$ the state of the output node at time $t + n$ is the conjunction of the states of the input nodes at time t (the only sensible aspect is to maintain synchronization in the gadget, see Figure 16).

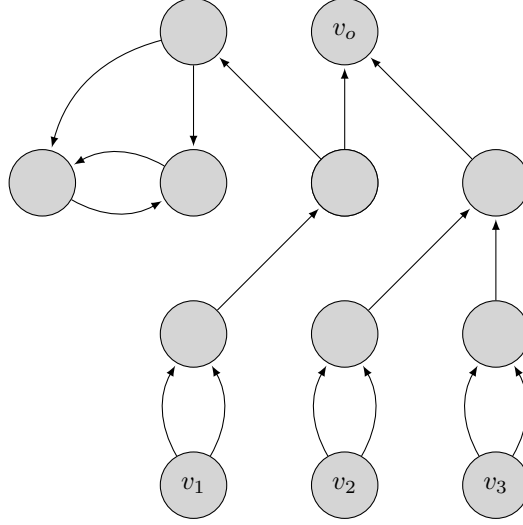


Figure 16: Fanin gadget of degree 3. For any configuration x , $F^3(x)_{v_0} = x_{v_1} \wedge x_{v_2} \wedge x_{v_3}$.

We do the same for copying the output of a gate i times and dealing with arbitrary fanout. Then we replace each node of F by a meta node made of the two gadgets to deal with fanin/fanout and connect everything together according to graph G (note that fanin/fanout is granted to be 1 in the gadgets so connections respect the degree constraints). We obtain in **DLOGSPACE** a \mathcal{G}_{conj} -network of size polynomial in n that simulates F in linear time. \square

Remark 16. *The family of conjunctive networks can produce super-polynomial periods but is not universal. There are several ways to show this. It is for instance impossible to produce super-polynomial transients within the family [28, Theorem 3.20] so Corollary 1 conclude. One could also use Corollary 2 since a node in a strongly connected component of a conjunctive network must have a trace period of at most the size of the component (actually much more is known about periods in conjunctive networks through the concept of loop number or cyclicity, see [28]).*

5.8 Super-polynomial transients and periods without universality

Let us consider in this section alphabet $Q = \{0, 1, 2\}$. We are going to define a set \mathcal{G}_t such that \mathcal{G}_t -networks exhibit super-polynomial transients but are not universal. To help intuition, \mathcal{G}_t -networks can be thought as standard conjunctive networks on $\{0, 1\}$ that can in some circumstances produce state 2 which is a spreading state (a node switches to state 2 if one of its incoming neighbors is in state 2). The extra state 2 will serve to mark super-polynomial transients, but it cannot escape a strongly connected component once it appears in and, as we will see, \mathcal{G}_t -networks are therefore too limited in their ability to produce large periodic behavior inside strongly connected components.

\mathcal{G}_t is made of the following maps:

$$\begin{aligned} \text{AND}_{\{0,1\}} : (x, y) &\mapsto \begin{cases} 2 & \text{if } 2 \in \{x, y\} \\ x \wedge y & \text{else.} \end{cases} \\ \text{AND}_2 : (x, y) &\mapsto \begin{cases} 2 & \text{if } 2 \in \{x, y\} \text{ or } x = y = 1 \\ 0 & \text{else.} \end{cases} \\ \Lambda : (x, y) &\mapsto \begin{cases} 2 & \text{if } 2 \in \{x, y\} \\ x & \text{else.} \end{cases} \\ \text{Id} : x &\mapsto x. \\ \Upsilon : x &\mapsto (x, x). \end{aligned}$$

\mathcal{G}_t -networks can produce non-polynomial periods by disjoint union of rotations of prime lengths as in Theorem 35, but they can also wait for a global synchronization of all rotations and freeze the result of the test for this synchronization condition inside a small feedback loop attached to a “controlled AND map”.

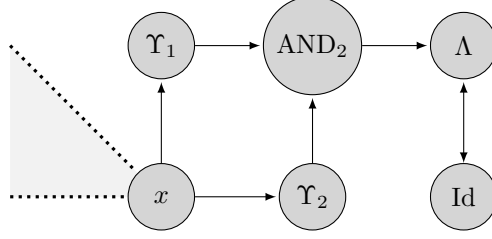


Figure 17: Freezing the result of a test in a \mathcal{G}_t -network. The module $T(x)$ is made of the nodes marked Υ , AND_2 , Λ and Id . Observe that each node represents some output of its corresponding label (for more details on \mathcal{G} -networks see Definition 23). Each gate has one output with the exception of the gate Υ which is represented by two nodes. The module $T(x)$ reads the value of node x belonging to an arbitrary \mathcal{G}_t -network (represented in light gray inside dotted lines). The output Λ is fed back to its control input via the Id node (self-loops are forbidden in \mathcal{G}_t -networks). Note that x as well as the rest of the network is not influenced by the behavior of the gates of the module $T(x)$.

More precisely, as shown in Figure 17 we can use in the context of any \mathcal{G}_t -network a small module $T(x)$ of made of five nodes with the following property: if the Λ node of the module is in state 0 in some initial configuration, then it stays in state 0 as long as nodes x is not in state 1, and when $x = 1$ at some time step t then from step $t + 2$ on the Λ node is in state 2 at least one step every two steps. This module is the key to control transient behavior.

Besides, the map $\text{AND}_{\{0,1\}}$ behaves like standard Boolean AND map when its inputs are in $\{0, 1\}$. More generally, by combining such maps in a tree-like fashion, one can build modules $A(x_1, \dots, x_k)$ for any number k of inputs with a special output node which has the following property for some time delay $\Delta \in O(\log(k))$: the output node at time $t + \Delta$ is in state 1 if and only if all nodes x_i (with $1 \leq i \leq k$) are in state 1 at time t .

Combining these two ingredients, we can build upon the construction of Theorem 35 to obtain non-polynomial transients in any family having coherent \mathcal{G}_t -gadgets.

Theorem 37. *Any family \mathcal{F} that has coherent \mathcal{G}_t -gadgets has superpolynomial transients, more precisely: there is some $\alpha > 0$ such that for any $n \in \mathbb{N}$, there exists a network $F_n \in \mathcal{F}$ with $O(n)$ nodes and a configuration x such that $F_n^t(x)$ is not in an attractor of F_n with $t \in \Omega(\exp(n^\alpha))$.*

Proof. Like in Theorem 35, the key of the proof is to show that there is a \mathcal{G}_t -network with transient length as in the theorem statement, then the property immediately holds for networks of the family \mathcal{F} by Lemma 30 and Lemma 14.

For any $n > 0$ we construct a \mathcal{G}_t -network G_n made of two parts:

- the 'bottom' part of G_n uses a polynomial set of nodes B_n and consists in a disjoint union of circuits for each prime length less than n as in Theorem 35, but where for each prime p , the circuit of length p has a node v_p which implements a copy gate COPY, thus not only sending its value to the next node in the circuit, but also outputting it to the second part of G_n ;
- the 'top' part of G_n is made of a module $A(x_1, \dots, x_k)$ connected to all nodes v_p as inputs and whose output is connected to a test module $T(x)$ as in Figure 17.

Note that the size of G_n is polynomial in n . With this construction we have the following property as soon as the modules $A(x_1, \dots, x_k)$ and $T(x)$ are initialized to state 0 everywhere: as long as nodes v_p are not simultaneously in state 1 then the output of the test module $T(x)$ stays in state 0; moreover, if at some time t nodes v_p are simultaneously in state 1, then after time $t + O(\log(t))$ the output node of module $T(x)$ is in state 1 one step every two steps. This means that $t + O(\log(t))$ is a lower bound on the transient of the considered orbit. To conclude the theorem it is sufficient to consider the initial configuration where all nodes are in state 0 except the successor of node v_p in each circuit of prime length p , which are in state 1. In this case it is clear that the first time t at which all nodes v_p are in state 1 is the product of prime numbers less than n . As in theorem 35, we conclude thanks to Lemma 34. \square

As said above, \mathcal{G}_t -networks are limited in their ability to produce large periods. More precisely, as shown by the following lemma, their behavior is close enough to conjunctive networks so that it can be analyzed as the superposition

of the propagation/creation of state 2 above the behavior of a classical Boolean conjunctive network. To any \mathcal{G}_t -network F we associate the Boolean conjunctive network F^* with alphabet $\{0, 1\}$ as follows: nodes with local map $\text{AND}_{\{0,1\}}$ or AND_2 are simply transformed into nodes with Boolean conjunctive local maps on the same neighbors, nodes with local maps Υ or Id are left unchanged (only their alphabet changes), and nodes with map $\Lambda(x, y)$ are transformed into a node with only x as incoming neighborhood.

Lemma 38. *Let F be a \mathcal{G}_t -network with node set V and F^* its associated Boolean conjunctive network. Consider any $x \in \{0, 1, 2\}^V$ and any $x^* \in \{0, 1\}^V$ such that the following holds:*

$$\forall v \in V : x_v \in \{0, 1\} \Rightarrow x_v^* = x_v,$$

then the same holds after one step of each network:

$$\forall v \in V : F(x)_v \in \{0, 1\} \Rightarrow F^*(x^*)_v = F(x)_v.$$

Proof. It is sufficient to check that if $F(x)_v \neq 2$, it means that all its incoming neighbors are in $\{0, 1\}$ so x and x^* are equal on these incoming neighbors, and that it only depend on neighbor a in the case of a local map $\Lambda(a, b)$. In any case, we deduce $F^*(x^*)_v = F(x)_v$ by definition of F^* . \square

\mathcal{G}_t -networks are close to Boolean conjunctive networks as shown by the previous lemma. The following result shows that this translates into strong limitations in their ability to produce large periods and prevents them to be universal.

Theorem 39. *The family of \mathcal{G}_t -networks is not universal.*

Proof. Consider a Boolean conjunctive automata network F , a configuration x with periodic orbit under F and some node v such that there is a walk of length L from v to v . We claim that $x_v = F^L(x)_v$ so the trace at node v in x is periodic of period less than L . Indeed, in a conjunctive network state 0 is spreading so clearly if $x_v = 0$ then $F^L(x)_v = 0$ and, more generally, $F^{kL}(x)_v = 0$ for any $k \geq 1$. On the contrary, if $x_v = 1$ then we can't have $F^L(x)_v = 0$ because then $F^{P^k}(x)_v = 0$ with P the period of x which would imply $x_v = 0$.

With the same reasoning, if we consider any \mathcal{G}_t -network F , any configuration x with periodic orbit and some node v such that there is a walk of length L from v to v , then it holds:

$$x_v = 2 \Leftrightarrow F^L(x)_v = 2.$$

We deduce thanks to Lemma 38 that for any configuration x with periodic orbit of some \mathcal{G}_t -network F with n nodes, and for any node v belonging to some strongly connected component, the period of the trace at v starting from x is less than n^2 : it is a periodic pattern of presence of state 2 of length less than n superposed on a periodic trace on $\{0, 1\}$ of length less than n . We conclude that the family of \mathcal{G}_t -networks cannot be universal thanks to Theorem 2. \square

5.9 Simulations and update mode extensions

In the the next sections, we establish various strong universality results concerning families which are block sequential, local clocks or periodic extensions, *i.e.* of the form $\mathcal{F}^{\text{BLOCK},b}$ or $\mathcal{F}^{\text{CLOCK},c}$ or $\mathcal{F}^{\text{PER},p}$ for some CSAN family \mathcal{F} . Automata networks from such families have a composite alphabet $Q \times Q'$ where the extension component Q' is used to code the update mode. If such an automata network F simulates some automata network G , it could potentially use distinct values on both components of states in the block embedding in order to represent different initial configurations for G . Since the Q' component determines the update mode, this means that F could potentially “be in distinct update modes” in the same simulation of G , depending on the initial configuration of G to simulate. It would then be illegitimate to say that some automata network under some update mode is simulating G . On the contrary, if the block embedding is such that the Q' component is constant whatever the configuration of G to be represented, then all the orbits of F used to simulate G correspond to the exact same update mode. In this case, we say that the simulation is *update mode coherent*, and it is legitimate to speak about an automata network under a specific update mode being simulating another one.

First, we would like to make it clear that all results of Section 6 are obtained by update mode coherent simulations: it can be checked that all the pseudo orbits of all the gadgets used are indeed update mode coherent (the extension component of state is always the same, whatever the configuration being encoded).

Second, we would like point out that in fact if some family $\mathcal{F}^{\text{BLOCK},b}$ or $\mathcal{F}^{\text{CLOCK},c}$ or $\mathcal{F}^{\text{PER},p}$ is (strongly) universal then it is also (strongly) universal using only update mode coherent simulations. The reason for this is as follows:

<i>Family/Update scheme</i>	Block sequential	Local clocks	General periodic
Conjunctive networks	BPA	BPA	SPA
Locally positive	BPA	SU	SU
Signed conjunctive networks	SU	SU	SU
Min-max networks	SU	SU	SU

Table 1: Summary of the main results on complexity of the dynamics of the network families studied in the current chapter, depending on different update schemes. BPA = Bounded period attractors. SPA = Superpolynomial attractors. SU = Strong universality. Black fonts indicate the emergence of complex behavior such as long period attractors or universality.

inside the extension component Q' coding the update mode at some node v , there are (at most) two components, one being constant (for local clocks or periodic extensions) and one having a deterministic cyclic behavior of the type $i \mapsto i + 1 \pmod p$ where p is the period of the update mode at the considered node v . Since the block embedding of any simulation must be injective, if node v is part of some block to encode states q and q' of some simulated automata network G , and if G allows both transitions from q to q' and from q' to q , then:

- the constant part of the extension component Q' must be the same in the coding of q and q' (to make possible the transition from q to q');
- the time constant of the simulation must be a multiple of the period of the update mode at v (to allow transition from q to q'), and therefore the cyclic part of the extension component must hold the same value in the coding of q and q' (to allow transition from q' to q).

We deduce that if G can transition from any state q to any state q' at each node, then simulation of G is forced to be update mode coherent. Finally, observe that any automata network in the family of \mathcal{G}_m -networks has the property of allowing a transition from any q to any q' at any node, therefore a (strongly) universal family $\mathcal{F}^{\text{BLOCK},b}$ or $\mathcal{F}^{\text{CLOCK},c}$ or $\mathcal{F}^{\text{PER},p}$ must simulate the family of \mathcal{G}_m -networks using update mode coherent simulations only. By using the fact that \mathcal{G}_m -networks are strongly universal, we deduce that the considered family can actually simulate any network using update mode coherent simulations.

6 Effect of asynchronism: a case study of symmetric networks

In this section, we focus on studying concrete symmetric automata network (CSAN) families. We use previous theoretical framework on complexity of automata networks families in order to classify different CSAN families according to their dynamical behavior under different update schemes. More precisely, we focus on two different families of CSAN: signed conjunctive networks and min-max networks. We distinguish three main subfamilies inside signed conjunctive networks (for detailed definitions see Definition 4 and Definition 5) :

- symmetric conjunctive networks, which are regular conjunctive networks (in which all edge labels are the identity function);
- locally positive signed conjunctive networks, in which we allow negative edges (labeled by the switch function: $\text{Switch}(x) = 1 - x$) but with a local constraint forcing the existence of at least one positive edge in each neighborhood (one edge labeled by the identity function); and
- general signed conjunctive networks, in which there could be negative edges without any constraint (possibly all edges can be negative).

In addition, we consider the latter presented three update schemes: block sequential, local clocks and general periodic update scheme. We classify previous families according to their dynamical behavior and simulation capabilities by using the framework presented in previous sections. Before we enter into the detail, we present in Table 1 a summary of the main results obtained. Observe that in each row of Table 1, we show how the dynamical behavior of some CSAN families changes as we change the update scheme. In particular, the most simple ones, such as conjunctive and locally positive networks exhibit a relatively simple dynamical behavior (they have bounded period attractors). Contrarily, the last two families have strong universality even for block sequential update schemes. In addition, we would like to remark that there is not only a hierarchy for update schemes (block sequential update schemes are a particular case of local clocks and both are a particular cases of periodic update schemes) but that network families are also somehow

related as conjunctive networks are a sub-family of all the other ones. Additionally, one can also observe that there is some sort of "diagonal emergence" of strong universality in Table 1 consisting in the fact that it seems to exist a trade-off between the complexity in the definition of some network families and the complexity of the corresponding update schemes. In other words, simple families seem to need more complex update schemes in order to be universal and as we pass to more complex rules one can observe this property for simpler update schemes.

6.1 Symmetric Conjunctive networks

As said previously, symmetric conjunctive networks form a particular CSAN family on alphabet $\{0, 1\}$ where all edges are labeled by the identity map and all nodes have the same 'conjunctive' local map

$$\lambda(q, X) = \begin{cases} 0 & \text{if } 0 \in X, \\ 1 & \text{else.} \end{cases}$$

First, symmetric conjunctive networks being a particular case of symmetric threshold networks it follows from the classical results of [19, 13], that they always converge to some fixed point or cycle of length two. Therefore they are not dynamically complex with parallel update schedule. It turns out that with a periodic update schedule of period 3 they can break this limitation and produce super-polynomial cycles.

Particularly, we show that symmetric conjunctive networks with a periodic update schedule of period 3 can break latter limitation on attractor period and produce superpolynomial cycles. Observe that all the graphs over we are defining networks in this family (and also in the other concrete examples we will be exploring in next sections) are non-directed (symmetric). If we observe carefully the effect of periodic update schemes, we note that we are actually changing the interaction graph of the network by considering different orders for updating nodes and thus, breaking the symmetry in the different connections that nodes have in the network. Moreover, we corroborate this remark by showing that actually, we can simulate arbitrary conjunctive networks by using a periodic update scheme. We accomplish this by applying our formalism on simulation and gadget glueing. In fact, we show that the family of symmetric conjunctive networks admits coherent $\mathcal{G}_{\text{conj}}$ -gadgets and thus it is capable of simulating the family $(\mathcal{F}_{\text{conj}}, \mathcal{F}_{\text{conj}}^*)$. Particularly, this implies that this family admits attractors of superpolynomial period.

Proposition 5. *Let $p \geq 1$ and denote by $\mathcal{F}_{\text{sym-conj}}^{\text{PER}, p}$ be the family of symmetric conjunctive networks under periodic update schedules of period p . Then, $\mathcal{F}_{\text{sym-conj}}^{\text{PER}, p}$ is not universal and therefore it does not admit coherent \mathcal{G}_m -gadgets.*

Proof. We actually show that the longest transient of any $F \in \mathcal{F}$ with n nodes is $O(n^2)$, then the conclusions follow by Corollary 2 and Theorem 33. Recall that the alphabet is $\{0, 1\} \times \{0, \dots, p\} \times 2^{0, \dots, p}$ and, by definition, on any given configuration x the component $\{0, \dots, p\} \times 2^{0, \dots, p}$ is periodic of period p independently of the behavior on the first $\{0, 1\}$ component. Moreover, the behavior on the $\{0, 1\}$ component is that of a fixed (non-symmetric) conjunctive network F' in the following sense:

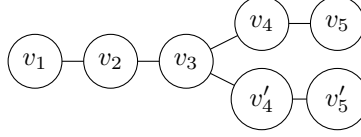
$$F^{t+p}(x) = F'(F^t(x)), \forall t \geq 0.$$

By [28, Theorem 3.20], the transient of any orbit of F' is $O(n^2)$. We deduce that the transient of the orbit of x under F is also $O(n^2)$. \square

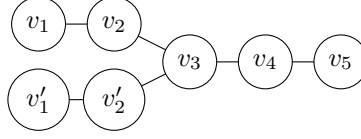
Theorem 40. *Let $\mathcal{F}_{\text{sym-conj}}^{\text{PER}, 3}$ be the family of symmetric conjunctive networks under periodic update schedule of period 3. $\mathcal{F}_{\text{sym-conj}}^{\text{PER}, 3}$ has coherent $\mathcal{G}_{\text{conj}}$ -gadgets and therefore simulates the family of conjunctive networks $(\mathcal{F}_{\text{conj}}, \mathcal{F}_{\text{conj}}^*)$ and for instance can produce superpolynomial cycles.*

Proof. The conclusion about simulations of conjunctive networks and superpolynomial cycles follows from previous results as soon as we prove that the family has coherent $\mathcal{G}_{\text{conj}}$ -gadgets: Theorem 36 for simulation of conjunctive networks and Proposition 4 to show that the family has coherent \mathcal{G}_w -gadgets (because the identity map is obtained by composition of COPY and AND) and then Theorem 35 for super-polynomial cycles.

We now describe the coherent $\mathcal{G}_{\text{conj}}$ -gadgets within family $\mathcal{F}_{\text{sym-conj}}^{\text{PER}, 3}$ using notations from Definition 29. Let $F_{\text{COPY}} \in \mathcal{F}_{\text{sym-conj}}^{\text{PER}, 3}$ be defined on the following graph with nodes $V_{\text{COPY}} = \{v_1, v_2, v_3, v_4, v_5, v'_4, v'_5\}$:



Let also $F_{\text{AND}} \in \mathcal{F}$ be defined on the following graph with nodes $V_{\text{AND}} = \{v_1, v_2, v'_1, v'_2, v_3, v_4, v_5\}$:



Recall that both F_{COPY} and F_{AND} have alphabet $Q = \{0, 1\} \times \{0, 1, 2\} \times 2^{\{0,1,2\}}$. Now let $C = C_i \cup C_o$ be the glueing interface with $C_i = \{i\}$ and $C_o = \{o\}$. F_{COPY} is seen as a gadget with one input and two outputs for the gate $\text{COPY} \in \mathcal{G}_{\text{conj}}$ as follows:

- $\phi_{\text{COPY},1}^i(i) = v_2$ and $\phi_{\text{COPY},1}^i(o) = v_1$;
- $\phi_{\text{COPY},1}^o(i) = v_5$ and $\phi_{\text{COPY},1}^o(o) = v_4$.
- $\phi_{\text{COPY},2}^o(i) = v'_5$ and $\phi_{\text{COPY},2}^o(o) = v'_4$.

F_{AND} is seen as a gadget with two inputs and one output for the gate $\text{AND} \in \mathcal{G}_{\text{conj}}$ as follows:

- $\phi_{\text{AND},1}^i(i) = v_2$ and $\phi_{\text{AND},1}^i(o) = v_1$;
- $\phi_{\text{AND},2}^i(i) = v'_2$ and $\phi_{\text{AND},2}^i(o) = v'_1$.
- $\phi_{\text{AND},1}^o(i) = v_5$ and $\phi_{\text{AND},1}^o(o) = v_4$.

Note in particular that the conditions of Lemma 28 are satisfied so the closure by gadget glueing of this gadget stays in our family \mathcal{F} . Indeed F_{COPY} has the same induced label graphs on all images of C under ϕ maps, and the neighborhood of $\phi_{\text{COPY},1}^i(C_o) = v_1$ is v_2 which belongs to $\phi_{\text{COPY},1}^i(C)$, and similarly for other ϕ maps. Corresponding properties hold also for F_{AND} . We now define the following elements required by Definition 29:

- the two state configurations s_q for $q \in \{0, 1\}$ are defined by $s_q(i) = (q, 0, \{0, 2\})$ and $s_q(o) = (1, 0, \{0, 1\})$;
- the context configuration is defined by $c(v_3) = (1, 0, \{1, 2\})$;
- the time constant is $T = 3$
- the standard trace $\tau_{q,q'}$ over the glueing interface from $q \in \{0, 1\}$ to $q' \in \{0, 1\}$ is given by:

time	i	o
0	$(q, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$
1	$(1, 1, \{0, 2\})$	$(q, 1, \{0, 1\})$
2	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$
3	$(q', 0, \{0, 2\})$	$(1, 0, \{0, 1\})$

- for any $q_i, q_i^T, q_o, q_o' \in \{0, 1\}$ we have the following $\{v_1, v_5, v'_5\}$ -pseudo orbit for F_{COPY} :

time	v_1	v_2	v_3	v_4	v_5	v'_4	v'_5
0	$(q_i, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(1, 0, \{1, 2\})$	$(q_o, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(q'_o, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$
1	$(1, 1, \{0, 2\})$	$(q_i, 1, \{0, 1\})$	$(1, 1, \{1, 2\})$	$(1, 1, \{0, 2\})$	$(q_o, 1, \{0, 1\})$	$(1, 1, \{0, 2\})$	$(q'_o, 1, \{0, 1\})$
2	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$	$(q_i, 2, \{1, 2\})$	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$
3	$(q'_i, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(1, 0, \{1, 2\})$	$(q_i, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(q_i, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$

- for any $q_i, q_i^T, q_{i'}, q_{i'}^T, q_o \in \{0, 1\}$ we have the following $\{v_1, v_1', v_5\}$ -pseudo orbit for F_{AND} :

time	v_1	v_2	v_1'	v_5'	v_3	v_4	v_5
0	$(q_i, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(q_{i'}, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(1, 0, \{1, 2\})$	$(q_o, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$
1	$(1, 1, \{0, 2\})$	$(q_i, 1, \{0, 1\})$	$(1, 1, \{0, 2\})$	$(q_{i'}, 1, \{0, 1\})$	$(1, 1, \{1, 2\})$	$(1, 1, \{0, 2\})$	$(q_o, 1, \{0, 1\})$
2	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$	$(q_i \wedge q_{i'}, 2, \{1, 2\})$	$(1, 2, \{0, 2\})$	$(1, 2, \{0, 1\})$
3	$(q_i^T, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(q_{i'}^T, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$	$(1, 0, \{1, 2\})$	$(q_i \wedge q_{i'}, 0, \{0, 2\})$	$(1, 0, \{0, 1\})$

□

Interestingly, local clock update schedules on conjunctive networks are not able to produce superpolynomial cycles. To avoid too much conflicts in indices in notations, we denote by x^Q, x^c and x^m the three components of a configuration x in some local clocks extension network.

Lemma 41. *Let us fix any $c > 0$ and consider the family $\mathcal{F}_{\text{sym-conj}}^{\text{CLOCK}, c}$ of all symmetric conjunctive networks under local clocks update scheme with clock period c . Fix $n > 0$ and let $F : Q_c^n \rightarrow Q_c^n \in \mathcal{F}_{\text{sym-conj}}^{\text{CLOCK}, c}$. For any configuration $(x^Q, x^c, x^m) \in Q_c^n$ we have that the period of the attractor reached from (x^Q, x^c, x^m) is at most $2 \text{lcm}\{x_v^m : v \in \{1, \dots, n\}\}$. Moreover, for each attractor $\bar{x} \in \text{Att}(F)$, the set of nodes whose Q component is not constant in \bar{x} induces a bipartite subgraph.*

Proof. Let $(x^Q, x^c, x^m) \in Q_c^n$ be a configuration and $(\bar{x}^Q, \bar{x}^c, \bar{x}^m) \in \text{Att}(F)$ be an attractor that is reachable from (x^Q, x^c, x^m) and that is not a fixed point, i.e. $p(\bar{x}) \geq 2$. In order to simplify the notation, we are going to denote \bar{x}^Q by \bar{x} . Let $i \in \{0, \dots, n-1\}$ be a coordinate such that \bar{x}_i changes its state, i.e there exists some $t \in \mathbb{N}$ such that $\bar{x}(0)_i \neq \bar{x}(t)_i$. Without loss of generality we assume that $\bar{x}(0)_i = 1$ and $\bar{x}^c(0)_i = 0$ and $\bar{x}(1)_i = 0$. Consider t_1 as the first time step such that the coordinate i changes its state from 0 to 1, which means that, t_1 is the first time step such that $\bar{x}(t_1) = 0$ and $\bar{x}(t_1 + 1) = 1$. Observe that $t_1 = sx_i^m$ for some $s \geq 1$. Note that, for all $j \in N(i)$, $\bar{x}(t_1)_j = 1$. Moreover, we have that for all $j \in N(i)$: and that $\bar{x}(s)_j = 1$ for all $s \in [1, t_1]$ (see Figure 6.1). This is because, since the interaction graph is symmetric, j cannot be in state 0 during interval $[1, t_1]$ otherwise both i and j would stay in state 0 forever, thus contradicting the hypothesis on i . We deduce that $x_j^m \geq t_1 = sx_i^m$ (otherwise j would update its state and become 0 on interval $[1, t_1]$). Now consider t_0 to be the first time step in which the node i changes its state from 1 to 0, i.e. $\bar{x}(t_0) = 1$ and $\bar{x}(t_0 + 1) = 0$. Observe that, $t_1 < t_0 = t_1 + s^*x_i^m$ for some $s^* \geq 1$. In addition, there must exist some neighbor $k \in N(i)$ satisfying that $\bar{x}(t_0)_k = 0$, otherwise i cannot change to 0 (it requires at least one neighbor in 0 in order to change its state from 1 to 0). Observe that node k satisfies $x_k^m \leq x_i^m$ because it needs to update to 0 before node i . More precisely, by the definition of t_0 we have that i is fixed in state between t_1 and t_0 (see Figure 6.1). Additionally, we have that $\bar{x}_k(t_1) = 1, \bar{x}_k(t_0) = 0$ and also we have that $\bar{x}(t_0 - x_i^m)_k = 1$ (otherwise it contradicts the minimality of t_0). Finally, since i is remains in state 0 on the interval $[t_0, t_0 + x_i^m]$ then, k cannot be updated in the same interval. Thus, $x_k^m \leq x_i^m$. Moreover, the latter observations imply that i and k are synchronized, i.e. $(\bar{x}^c(0))_k = (\bar{x}^c(0))_i, x_k^m = x_i^m, t_1 = x_i^m$ and $t_0 = t_1 + x_i^m$. Note that also, we have that for all $t, \bar{x}(t)_i = 1 - \bar{x}(t)_k$. We have shown that the period of any node v is at most $2x_v^m$, so we deduce $p(\bar{x}) \leq 2 \text{lcm}\{x_v^m : v \in \{1, \dots, n\}\}$.

At this point, we know that i must have at least one neighbor that is not constant in \bar{x} and that it is synchronized. Let us assume that there is a non constant neighbor ℓ of i that satisfies $x_\ell^m > x_i^m$. On the other hand, we have that ℓ is in state 1 on the interval $[0, t_1]$ (see Figure 6.1) because otherwise i cannot switch to state 1 at the time step t_1 . Observe that, by hypothesis, ℓ cannot change its state on intervals of the form $[rx_i^m, (r+1)x_i^m]$ for $r \in \mathbb{N}$ even since i is in state 0 on those intervals (otherwise i cannot switch back to 1 because it would have a neighbor in 0). However, for r even, i is in state 1 on intervals of the form $[rx_i^m, (r+1)x_i^m]$. Suppose that ℓ changes its value for the first time on an interval of the form $[r^*x_i^m, (r^*+1)x_i^m]$ for some $r^* \in \mathbb{N}$ odd, i.e. $\bar{x}(s)_\ell = 0$ for some $s \in [r^*x_i^m, (r^*+1)x_i^m]$. Observe now that $\bar{x}((r^*+2)x_i^m) = 1$ since i must return to state 1 but ℓ cannot change its state in $[(r^*+1)x_i^m, (r^*+2)x_i^m]$ because i is in state 0. Then, we must have $x_\ell^m \leq x_i^m$, which contradicts the hypothesis. We conclude that every non constant neighbor of i is synchronized. Repeating the same argument now for any non constant neighbor of i we have that all the nodes in the connected component containing i have local delay x_i^m . Iterating this same technique now for each i in the network, we deduce that \bar{x} is such that $p(\bar{x}) \leq 2 \text{lcm}\{x_v^m : v \in \{1, \dots, n\}\}$ since locally, each connected component containing some node i is synchronized and thus, each non-constant node is switching its state every $2x_i^m$ time steps. In addition, for each node i every non-constant neighbor is in the state 0 whenever i is in the state 1. Thus, the set of nodes which are not constant for $(\bar{x}^Q, \bar{x}^c, \bar{x}^m)$ i.e. $S(\bar{x}^Q, \bar{x}^c, \bar{x}^m)$, induces a two colorable subgraph. The result holds. □

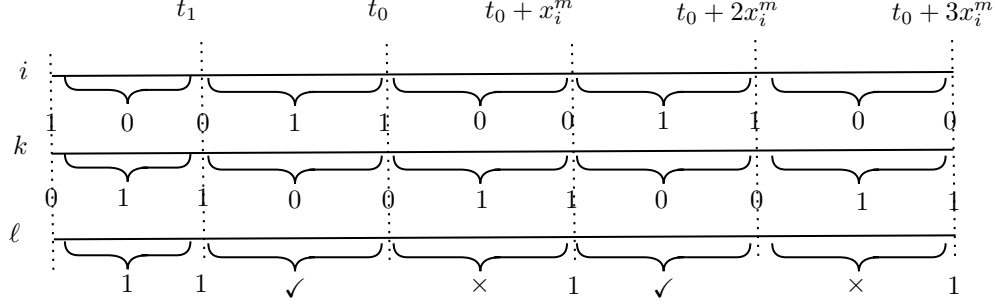


Figure 18: Scheme of the dynamics of nodes i , k and ℓ defined in the proof of Lemma 41. The checkmarks indicate where it is feasible for ℓ to be updated and the crosses mark the intervals on which ℓ can change its state.

As seen above, there is a qualitative jump between local clocks and periodic update schedules for conjunctive networks in the size of dynamical cycles. However, for transient and prediction problems, even general periodic update schedules fail to produce maximal complexity (under standard complexity classes separations assumptions).

Theorem 42. *Let $p \in \mathbb{N}$ and consider the family $\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}$ of symmetric conjunctive networks under periodic update schedules of period p . $\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}$ is neither dynamically nor computationally complex: more precisely, the transients of any network in $\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}$ with n nodes are of length at most $O(n^2)$, the problem $\text{PREDU}_{\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}}$ can be solved by a NC^2 algorithm and $\text{PREDB}_{\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}}$ can be solved in polynomial time.*

Proof. Let $F \in \mathcal{F}_{\text{sym-conj}}^{\text{PER},p}$ with n nodes and consider any initial configuration x . By definition, the orbit of x under F^p is constant on the second and third component of states, and the action of F^p on the first component when starting from x is a particular non-symmetric conjunctive network F_x that can be seen as an arbitrary Boolean matrix M_x . First, by [28, Theorem 3.20], the transient of the orbit of x under F_x is of length at most $2n^2 - 3n + 2$. We deduce that the transient of x under F is in $O(n^2)$.

Second, it is easy to compute M_x from F and x in NC^1 . Moreover, matrix multiplication can be done in NC^1 and by fast exponentiation circuits we can compute M_x^t with polynomial circuits of depth $O(\log(t) \log(n))$. With a constant computational overhead, we can therefore efficiently compute $F^t(x)_v$ and the complexity upper bounds on $\text{PREDU}_{\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}}$ and $\text{PREDB}_{\mathcal{F}_{\text{sym-conj}}^{\text{PER},p}}$ follow. \square

Remark 17. *We remark that even when symmetric conjunctive networks under periodic update schemes are not dynamically nor computationally complex, it has been shown that, with a different type of update schemes, it is possible to construct coherent $\mathcal{G}_{m,2}$ -gadgets and thus, show strong universality for this latter family. Particularly, we allude to the case of firing memory update schemes, which can also be studied in the context of our asynchronous extensions framework, as same as periodic update schemes. Just as we have mentioned before, this type of update scheme introduces internal clocks in each node, which will actually depend on the dynamics of the network (contrarily to the case of local clocks, where clocks are fixed and independent from the dynamics of the network). This key aspect gives the network strong dynamical and computational capabilities which can be used to simulate monotone boolean networks. Interested reader is referred to in order [16] to see details.*

6.2 Locally positive symmetric signed conjunctive networks

In this section, we study a generalization of conjunctive networks that we call *locally positive* symmetric conjunctive networks. Using latter notation for CSAN families we denote this family by $\mathcal{F}_{\text{locally-pos}}$. In this particular case, we allow edges to have negative signs (which will switch the state of the corresponding neighbor) but with a local constraint: no neighborhood in which all the connections (remember that all edges are undirected) are negative is allowed. More precisely, a locally positive symmetric conjunctive network is a CSAN (G, λ, ρ) in for any $v \in V(G)$ we have $\lambda_v(q, S) = \bigwedge_{q \in S} q$ and there exists $w \in N(v) : \rho(vw) = \text{Id}$.

We will show for this family that the *threshold of universality* when changing update modes is between block sequential update schemes and local clocks update schemes. More precisely, we show, on one hand, that the family remains

dynamically constrained under block sequential schedule, and, on the other hand, we show that a local clocks version of this family is strongly universal as a consequence of its capability of simulating coherent $\mathcal{G}_{m,2}$ -gadgets. A similar construction will be used also for the general case in which we allow any kind of sign label over the edges of the network as we will show in next sections.

Theorem 43. *Fix any $b \geq 1$ and consider $\mathcal{F}_{\text{locally-pos}}^{\text{BLOCK},b}$ the family of all locally positive symmetric signed conjunctive networks under blocksequential schedule with at most b blocks. Any periodic orbit of any $F \in \mathcal{F}_{\text{locally-pos}}^{\text{BLOCK},b}$ has length 1 or $2b$.*

Proof. Take some configuration x in a periodic orbit. If no node changes its state in the orbit then x is actually a fixed-point. Otherwise take some node i that changes its state and consider a maximal time interval $I = [t_1; t_2]$ with $t_1 > 0$ during which i is in state 0: $\forall t \in I, F^t(x)_i = 0$ but $F^{t_1-1}(x)_i = F^{t_2+1}(x)_i = 1$. Let j be any positive neighborhood (i.e. such that $\rho(i, j)$ is the identity). First, we must have $\forall t \in I, F^t(x)_j = 1$ because supposing $F^t(x)_i = F^t(x)_j = 0$ implies $F^{t'}(x)_i = F^{t'}(x)_j = 0$ for all $t' \geq t$ which would contradict the hypothesis that i changes its state in the orbit. Thus $t_2 - t_1 + 1 = b$ because it is by definition a multiple of b and if it were strictly larger than b then node j would be updated in the interval $[t_1; t_2 - 1]$ and therefore would turn into state 0 in the interval I which is impossible. The same argument actually shows that i and j must be updated synchronously. Therefore it is updated at time t_2 and we must have $F^{t_2+1}(x)_j = 0$. This implies that $F^{t_2+b+1}(x)_i = 0$ and shows that the maximal time interval starting from $t_2 + 1$ during which i is in state 1 is of length exactly b . We can then iterate this reasoning starting at time $t_2 + b + 1$ and we deduce that the orbit of x at node i alternates b steps in state 0 and b steps in state 1 forever. The same holds for any node that changes its state and finally we have shown that the orbit of x is of period 1 or $2b$. \square

Now, we want to show that coherent AND/OR gadgets can be implemented in $\mathcal{F}_{\text{locally-pos}}^{\text{CLOCK},c}$, i.e. we want to show that this family has coherent $\mathcal{G}_{m,2}$ gadgets where $\mathcal{G}_{m,2} = \{\text{AND}_2, \text{OR}_2\}$ where $\text{OR}_2 : \{0, 1\}^2 \rightarrow \{0, 1\}^2$ is such that $\text{OR}_2(x, y) = (x \vee y, x \vee y)$ and function $\text{AND}_2 : \{0, 1\}^2 \rightarrow \{0, 1\}^2$ is such that $\text{AND}_2(x, y) = (x \wedge y, x \wedge y)$. However, in order to accomplish this task we need a construction that we will be using for the next subsection. Particularly, we need to implement the gadgets that are shown in Figures 25 and 26. Then, we will adapt latter gadgets in order to make it work for locally positive symmetric conjunctive networks. Thus, as a consequence, we will have that $\mathcal{F}_{\text{locally-pos}}^{\text{CLOCK},c}$ is strongly universal.

As results on general signed symmetric conjunctive networks are required first in order to show the proof of the main theorem of this section, we will just state the main result and then, we will show the proof of next theorem in the next section, in order to simplify things. By doing this we will respect the order given by hierarchy between different families and preserve coherence of results at the same time.

Theorem 44. *There exist $c > 0$ such that the family $\mathcal{F}_{\text{locally-pos}}^{\text{CLOCK},c}$ of all locally positive symmetric conjunctive networks under local clocks update scheme with clock parameter c has coherent $\mathcal{G}_{m,2}$ -gadgets.*

Finally, as a direct consequence of latter theorem we have the following corollary:

Corollary 7. *There exist $c > 0$ such that the family $\mathcal{F}_{\text{locally-pos}}^{\text{CLOCK},c}$ of all locally positive symmetric conjunctive networks under local clocks update scheme with clock parameter c is strongly universal. In particular, $\mathcal{F}_{\text{locally-pos}}^{\text{CLOCK},c}$ is both dynamically and computationally complex.*

Proof. Proof is a direct consequence of Theorem 22, Corollary 1 and Corollary 6. \square

6.3 Symmetric signed conjunctive networks

In this section we study conjunctive networks with negative edges without any local constraint in the number of positive edges. Formally the symmetric signed conjunctive networks family is a CSAN family in $\{0, 1\}$ in which $\lambda_v : Q \times 2^Q \rightarrow Q$ is given by $\lambda(q, S) = 0$ if $0 \in S$ and $\lambda(q, S) = 1$ if $0 \notin S$ and for any $e \in E$ we have $\rho_e \in \{\text{Id}, \text{Switch}\}$. where $\text{Switch}(x) = 1 - x$. We denote previous family as $\mathcal{F}_{\text{sign-sym-conj}}$ and for a different update schemes we consider the notation $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$, $\mathcal{F}_{\text{sign-sym-conj}}^{\text{CLOCK},c}$ and $\mathcal{F}_{\text{sign-sym-conj}}^{\text{PER},p}$ for block sequential, local clocks and periodic versions of this family respectively.

We start by remarking that for the parallel update scheme, $\mathcal{F}_{\text{sign-sym-conj}}$ family is not universal as it is a type of threshold family and thus it have bounded period transient and attractors (see [19, 13]). Then, a natural question is whether this

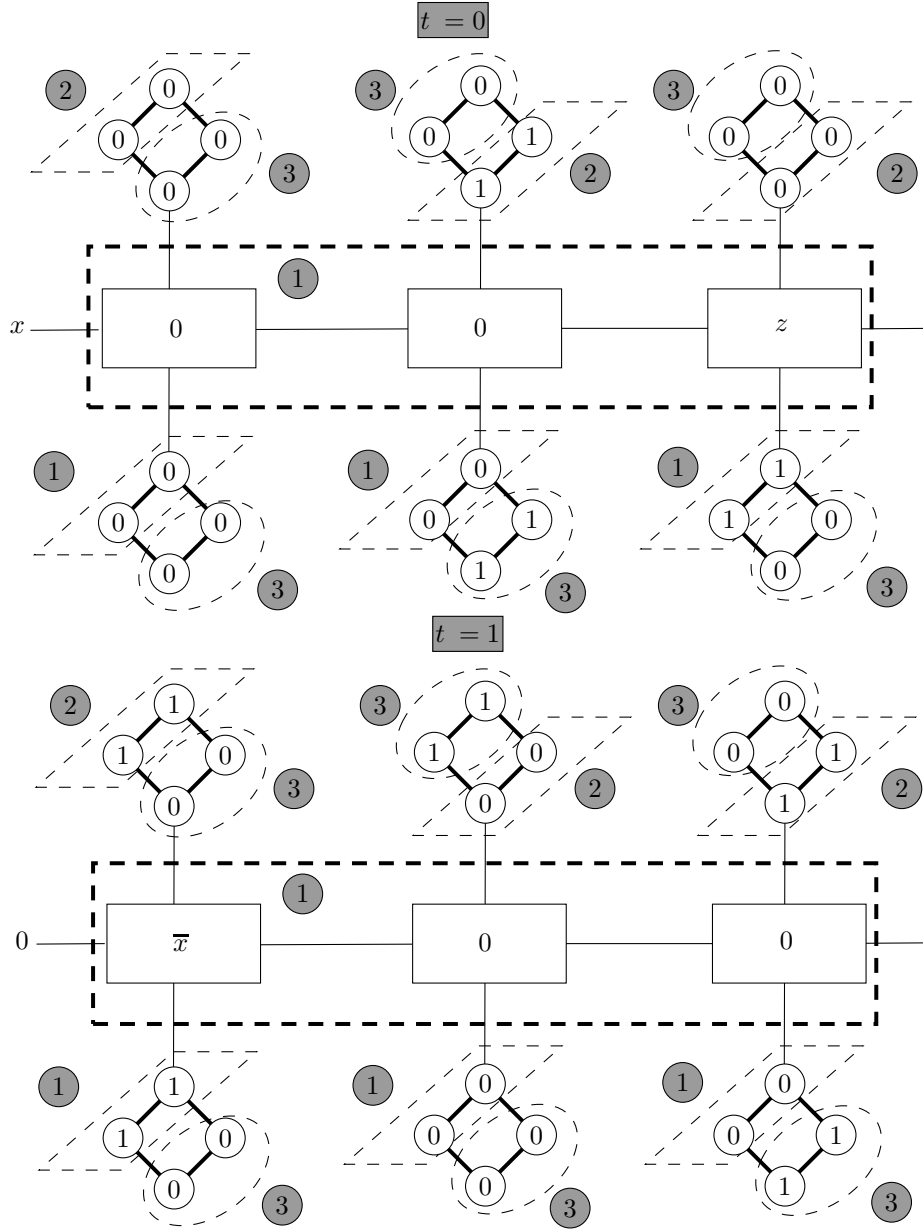


Figure 19: One step of the dynamics of the NOT part of NOR gadget implemented by a signed symmetric conjunctive network. Dotted circles and triangles represent blocks. Numbers in gray represent the updating order of each block. Each time step t is taken after three time steps (one for each block). Total simulation time is $T = 9$.

remains true for other update schemes. In this sense, we are going to show that, when we consider the next update scheme in our hierarchy, the block sequential update scheme then, $\mathcal{F}_{\text{sign-sym-conj}}$ family is strongly universal.

6.3.1 Block sequential case

In this section, we will show that $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$ is strongly universal as a consequence of its capability to implement coherent $\mathcal{G}_{m,2}$ -gadgets. In addition, we conclude that, as a direct consequence of the latter property, that previous family is both dynamically complex and computationally complex. This means that for this family, complex behavior is exhibited under block sequential update schemes.

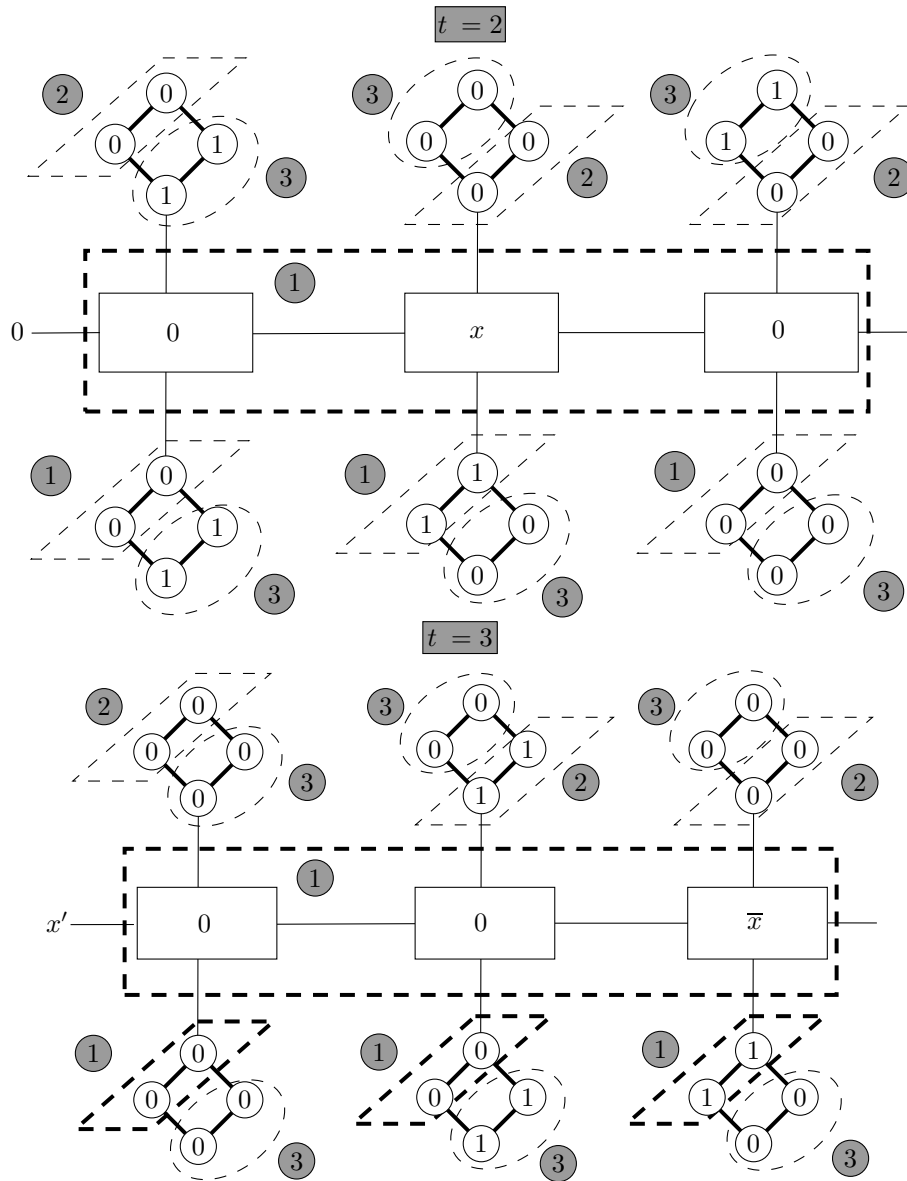


Figure 20: Two last steps of the dynamics described by the NOT part of NOR gadget implemented by a symmetric signed conjunctive network. Dotted circles and triangles represent blocks. Numbers in gray represent the updating order of each block. Each time step t is taken after three time steps (one for each block). Total simulation time is $T = 9$.

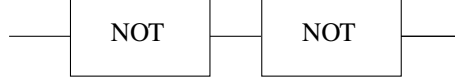


Figure 21: Wire gadget implemented on a signed symmetric conjunctive network. 2 copies of NOT gadget are combined in order to form a wire. Simulation time is $T = 6$.

As we will be using the same structures to show the main result, we start by showing a less powerful result. We show that $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$ is able to simulate \mathcal{G}_w -networks. This is only a way to motivate the main result by showing how key structures work in a particular simple context. As we will see when we present the main result, we do not use exactly this result in the actual proof of main theorem but we apply the same type of ideas as related structures are part of the gadgets we construct. We remark that all involved gadgets have only negative labels, i.e. each edge is labeled by the Switch function which takes a bit x and produces $1 - x$.

Lemma 45. *The family $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},3}$ of all signed symmetric conjunctive networks under block sequential update schemes of at most 3 blocks has coherent \mathcal{G}_w -gadgets.*

Proof. We define a gadget simulating Id by considering two copies of the NOT gadget presented in Figures 19 and 20. Observe that this gadget has 3 central nodes (marked inside a thick dotted rectangle in Figures 19 and 20) together with 2 copies of a 4 nodes cycle graph. Generally speaking, the dynamics on this cycle graphs works as a clock which allows information to flow through the central part in only one direction (from left to right). In addition, they allow the gadget to erase information once it has been transmitted. This latter property allows the gadget to clean itself in order to receive new information. The wire gadget is composed by two copies of the NOT gadget as is presented in Figure 21. Since this gadget is composed by two copies of the NOT gadget, this gadget is defined by a path graph with 6 central nodes together with $2 \times 2 \times 3 = 12$ cycle graphs (two copies for each node). We enumerate nodes in the central part from left to right by the following ordering: $\{0, 1, 2, 3, 4, 5\}$. Additionally, since the functioning of each copy of the NOT gadget is based in an ordered partition of 3 blocks, we take the union of corresponding blocks in each partition in order to define 3 larger blocks for the wire gadget. For one of this larger blocks we use the notation $\{0, 1, 2\}$. Thus, observe that each wire takes $T = 6 \times 3 = 18$ time steps in order transport the information. This is because for each round of 3 time steps in which we update each block, we make the signal pass through exactly one node. We define the glueing interface as $C_i = \{i\}$ and $C_o = \{o\}$. We map input and output in the following way: $\phi^i(i) = 1$, $\phi^i(o) = 0$, $\phi^o(i) = \{5\}$, $\phi^o(o) = \{4\}$. Note that in each case the neighborhood of output and input part is completely contained in C_i and C_o respectively and also the image of C by functions ϕ is always the same (two nodes path graph). Thus, the glueing interface satisfies conditions of Lemma 28. Now we enumerate the remaining elements required by Definition 29:

- State configurations $s_q(i) = (q, 0)$ and $s_q(o) = (q, 1)$ for any $q \in \{0, 1\}$.
- Context configurations are given in Figure 19 as well as the iteration order which defines the blocks.
- Standard trace and pseudo orbit for nodes $\{0, 1, 4, 5\}$ are defined in Figure 19 and Figure 20.

□

We are now in conditions to introduce the main result:

Lemma 46. *The family $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},3}$ of all signed symmetric conjunctive networks under block sequential update schemes of at most 3 blocks has coherent $\mathcal{G}_{m,2}$ -gadgets.*

Proof. We will show that the gadgets in the Figures 25 and 26 are coherent $\mathcal{G}_{m,2}$ -gadgets. Note that both these gadgets are made of several wires made of NOT gadgets (we call the two in the left hand side of the figure input wires and the other two at the right hand side output wires) and a computation gadget. Observe that, each of these structures (wires and computation gadget) needs exactly 3 blocks. In addition, note that in Table 4 the dynamics of the 4-cycles that are attached to each node is shown. We recall that the function of these clocks is to allow information to flow in one direction only (all the interactions are symmetric so this is not straightforward) and to erase information once it has been copied or processed by the nodes in the gadget. We use the following notation in order to represent nodes in these structures: $c_{s,i,j,p}$ where s is the number of the NOT gadget (a 3-node-path together with two 4-cycles for each

node, see Figure 22) to which the cycle is attached, so $s \in \{1, 2, 3, 4, 5, 6, 7, 8, 9\}$ where the order is taken from left to right (for example in Figure 25 the copy associated to (v_1, v_2, v_3) comes first then, (v'_1, v'_2, v'_3) , then (v_4, v_5, v_6) and so on). Additionally, i is in the position of the cycle in the NOT block, so $i \in \{1, 2, 3\}$, j is the position of the node relative to the 4-cycle graph considered in counter clockwise order (see Figure 22), so $j \in \{1, 2, 3, 4\}$ and p is the position of the cycle in the central structure of the gadget. Observe that, since there are two copies for each node, we denote them by *upper* and *lower* so $p \in \{u, l\}$. Since input wires and output wires are needed to be updated at the same time and are independent (we have two copies for each one) we combine their blocks in the obvious way (we take the union of pairs of blocks that have the same update order). More precisely, we combine 3×9 blocks of each particular part (there are 8 copies of NOT and one computation gadget having 3 blocks each one) in order to define again only 3 blocks. Precise definition, using notation shown in Figures 25 and 26, is the following:

- $B_0 = \bigcup_{i=1}^{12} \{v_i\} \cup \{v'_i\} \cup \bigcup_{s,i} \{c_{s,i,1,l}, c_{s,i,2,l}\};$
- $B_1 = \bigcup_s \{c_{s,1,3,u}, c_{s,1,4,u}, c_{s,2,1,u}, c_{s,2,2,u}, c_{s,3,1,u}, c_{s,3,2,u}\};$ and
- $B_2 = \bigcup_{s,i} \{c_{s,i,3,l}, c_{s,i,4,l}\}.$

Now we are going to use information in Tables 2, 3 and 4 in order to show that these gadgets satisfy the conditions of Definition 29. Observe that, on the one hand, for the OR gadget (see Figure 25), input wires compute the result in 3×3 (it needs to carry the signal through the three nodes in the wire and each of this intermediate steps takes three steps, one for each block) time steps and computation gadget takes 3×3 as well. On the other hand, for the AND gadget (see Figure 26) input wires compute desired in 3×6 time steps. We define the associated network as $F_{\text{AND}} : (\{0, 1\} \times \{0, 1, 2\})^{15 \times 2 \times 4} \rightarrow (\{0, 1\} \times \{0, 1, 2\})^{15 \times 2 \times 4}$ and $F_{\text{OR}} : (\{0, 1\} \times \{0, 1, 2\})^{15 \times 2 \times 4} \rightarrow (\{0, 1\} \times \{0, 1, 2\})^{15 \times 2 \times 4}$. In fact we have that:

1. There is a unique glueing interface given by $C = C_i \cup C_o$ where:

- $C_i = \{i\} \cup \{a(i, 1), a(i, 2), a(i, 3), a(i, 4)\} \cup \{a'(i, 1), a'(i, 2), a'(i, 3), a'(i, 4)\};$ and
- $C_o = \{o', o\} \cup \{a(o', 1), a(o', 2), a(o', 3), a(o', 4)\} \cup \{a(o, 1), a(o, 2), a(o, 3), a(o, 4)\} \cup \{a'(o', 1), a'(o', 2), a'(o', 3), a'(o', 4)\} \cup \{a'(o, 1), a'(o, 2), a'(o, 3), a'(o, 4)\}$

We define labelling functions $\phi_{\text{AND},k}^i, \phi_{\text{AND},k}^o, \phi_{\text{OR},k}^i, \phi_{\text{OR},k}^o$ (for the sake of simplicity we show the definition for the AND gadget since the one for the OR gadget is completely analogous) for $k = 1, 2$ as:

- $\phi_{\text{AND},1}^i(i) = v_1, \phi_{\text{AND},1}^i(o') = v_2$ and $\phi_{\text{AND},1}^i(o) = v_3;$
- $\phi_{\text{AND},1}^i(a(i, r)) = c_{1,1,r,u}, \phi_{\text{AND},1}^i(a'(i, r)) = c_{1,1,r,l}$ for $r = 1, 2, 3, 4$, where 1 corresponds to the NOT gadget which starts with v_1 in Figure 26;
- $\phi_{\text{AND},1}^i(a(o', r)) = c_{1,2,r,u}, \phi_{\text{AND},1}^i(a'(o', r)) = c_{1,2,r,l}$ for $r = 1, 2, 3, 4$, where 1 corresponds to the NOT gadget which starts with v_1 in Figure 26;
- $\phi_{\text{AND},1}^i(a(o, r)) = c_{1,3,r,u}, \phi_{\text{AND},1}^i(a'(o, r)) = c_{1,3,r,l}$ for $r = 1, 2, 3, 4$, where 1 corresponds to the NOT gadget which starts with v'_1 in Figure 26;
- $\phi_{\text{AND},2}^i(i) = v'_1, \phi_{\text{AND},2}^i(o') = v'_2$ and $\phi_{\text{AND},2}^i(o) = v'_3;$
- $\phi_{\text{AND},2}^i(a(i, r)) = c_{1',1,r,u}, \phi_{\text{AND},2}^i(a'(i, r)) = c_{1',1,r,l}$ for $r = 1, 2, 3, 4$, where 2 correspond to the NOT gadget which starts with v'_1 in Figure 26;
- $\phi_{\text{AND},2}^i(a(o', r)) = c_{1',2,r,u}, \phi_{\text{AND},2}^i(a'(o', r)) = c_{1',2,r,l}$ for $r = 1, 2, 3, 4$, where 2 corresponds to the NOT gadget which starts with v'_1 in Figure 26;
- $\phi_{\text{AND},2}^i(a(o, r)) = c_{1',3,r,u}, \phi_{\text{AND},2}^i(a'(o, r)) = c_{1',3,r,l}$ for $r = 1, 2, 3, 4$, where 2 corresponds to the NOT gadget which starts with v'_1 in Figure 26.
- $\phi_{\text{AND},1}^o(i) = v_{10}, \phi_{\text{AND},1}^o(o') = v_{11}$ and $\phi_{\text{AND},1}^o(o) = v_{12};$
- $\phi_{\text{AND},1}^o(a(i, r)) = c_{5,1,r,u}, \phi_{\text{AND},1}^o(a'(i, r)) = c_{5,1,r,l}$ for $r = 1, 2, 3, 4$, where 8 corresponds to the NOT gadget which starts with v_{10} in Figure 26.

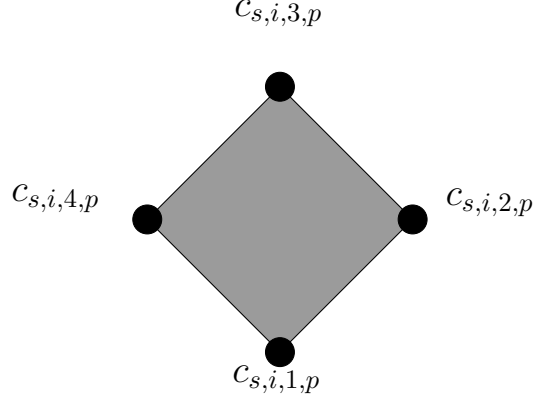


Figure 22: Scheme of labelling for 4-cycles in AND/OR gadgets. Notation is given by the following guidelines: s represent the associated group of three nodes, second two coordinates indicate its position relative to the original gadget (there are 3 clocks) and its position in the 4-cycle graph (considering counter clock-wise order), and u, l stands for upper or lower according to its position in the gadget.

- $\phi_{\text{AND},1}^o(a(o', r)) = c_{5,2,r,u}$, $\phi_{\text{AND},1}^o(a'(o', r)) = c_{5,2,r,l}$ for $r = 1, 2, 3, 4$, where 8 corresponds to the NOT gadget which starts with v_{10} in Figure 26;
 - $\phi_{\text{AND},1}^o(a(o, r)) = c_{5,3,r,u}$, $\phi_{\text{AND},1}^o(a'(o, r)) = c_{5,3,r,l}$ for $r = 1, 2, 3, 4$, where 8 corresponds to the NOT gadget which starts with v_{10} in Figure 26;
 - $\phi_{\text{AND},2}^o(i) = v'_{10}$, $\phi_{\text{AND},2}^o(o') = v'_{11}$ and $\phi_{\text{AND},2}^o(o) = v'_{12}$;
 - $\phi_{\text{AND},2}^i(a(i, r)) = c_{5',1,r,u}$, $\phi_{\text{AND},2}^i(a'(i, r)) = c_{5',1,r,l}$ for $r = 1, 2, 3, 4$, where 9 correspond to the NOT gadget which starts with v'_{10} in Figure 26;
 - $\phi_{\text{AND},2}^o(a(o', r)) = c_{5',2,r,u}$, $\phi_{\text{AND},2}^o(a'(o', r)) = c_{5',2,r,l}$ for $r = 1, 2, 3, 4$, where 9 corresponds to the NOT gadget which starts with v_{10} in Figure 26;
 - $\phi_{\text{AND},2}^o(a(o, r)) = c_{5',3,r,u}$, $\phi_{\text{AND},2}^o(a'(o, r)) = c_{5',3,r,l}$ for $r = 1, 2, 3, 4$, where 9 corresponds to the NOT gadget which starts with v'_{10} in Figure 26.
2. State configurations are defined for each $q \in \{0, 1\}$ as $s_q(i) = (q, 1)$ and $s_q(o') = s_q(o) = (0, 1)$ and the state configuration of the nodes in the clocks i.e. the ones labeled by a are constant and shown in Table 4. The block number for each of these nodes can be the same as the original NOT gadget 19.
 3. Context configurations are described in Tables 2, 3 and 4 as the ones related to the cycles of length 4 connected to central path of the gadgets and nodes in the path which are not part of the glueing interface.
 4. Standard trace is defined in Tables 2 and 3 (which contain the information related to the dynamics of nodes $v_1, v'_1, v_2, v'_2, v_3, v'_3, v_{10}, v'_{10}, v_{11}, v'_{11}, v_{12}, v'_{12}$) and in Table 4 (which contains the dynamics of the nodes in the 4-cycles).
 5. Simulation constant is $T = 3 \times 12$ as it is shown in Tables 2,3 and
 6. Pseudo-orbit is given by the dynamics shown in in Tables 2, 3, and 4 where x, y, x', y', z are variables.

□

Corollary 8. *The family $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},2}$ of all signed symmetric conjunctive networks under block sequential update schemes with at most 3 blocks is strongly universal. In particular, it is complex both dynamically and computationally complex.*

Proof. Strong universality holds from the fact that family is capable of simulating $\mathcal{G}_{m,2}$ in linear space and constant time (See Corollary 6). Family is dynamically and computationally complex as a direct consequence of strong universality (see Theorem 22 and Corollary 1). □

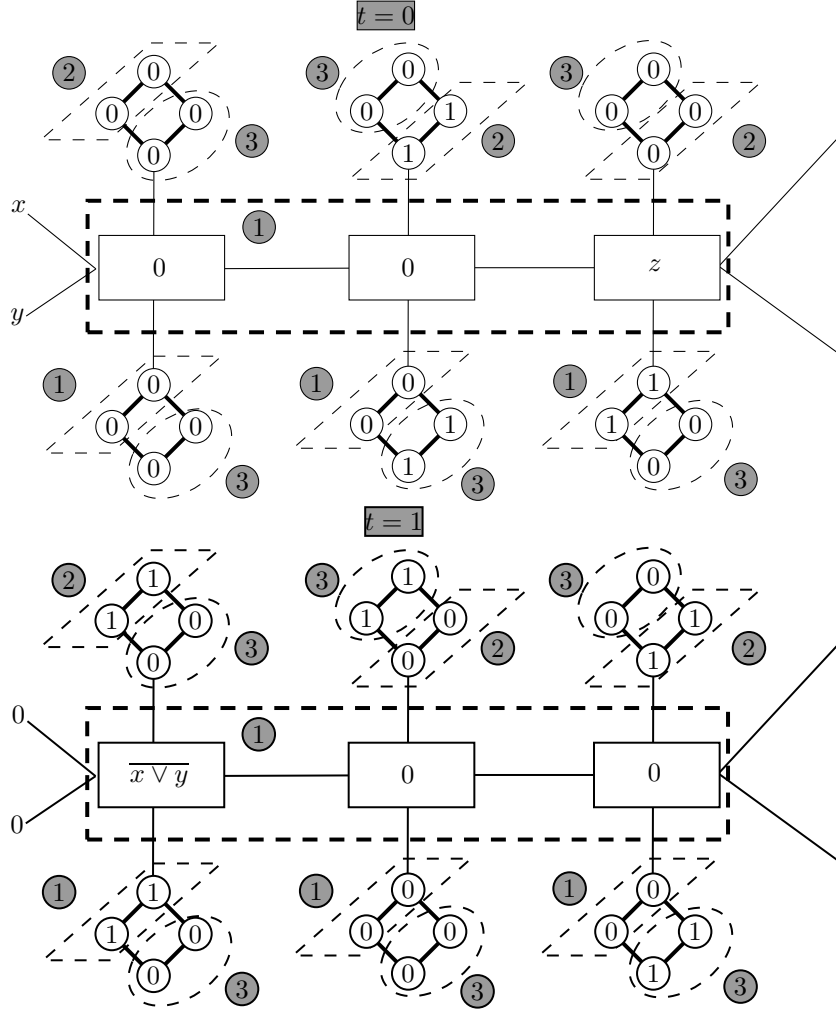


Figure 23: One step of the dynamics of the computation gadget inside NOR gadget implemented by a signed symmetric conjunctive network. Dotted circles and triangles represent blocks. Numbers in gray represent the updating order of each block. Each time step t is taken after three time steps (one for each block). Total simulation time is $T = 9$.

We are now in conditions to resume the proof of Theorem 44, we give again the statement of the theorem and we provide the complete proof.

There exist $c > 0$ such that the family $\mathcal{F}^{\text{CLOCK},c}_{\text{locally-pos}}$ of all locally positive symmetric conjunctive networks under local clocks update scheme with clock parameter c has coherent $\mathcal{G}_{m,2}$ -gadgets.

Proof. We start by observing that the gadgets in Figures 25 and 26 can be implemented in $\mathcal{F}^{\text{CLOCK},c}_{\text{locally-pos}}$ for some c . This can be easily done by adding a positive node to each node in the gadget. The main idea here is that each of these artificial positive nodes will play no role in calculations and will stay in state 1 most of the time. In fact, it suffices that these positive neighbors reach state 1 before critical steps of computation are performed inside the gadget.

In order to illustrate this idea, let us consider two different cases and analyze why computation gadget still works in this case:

1. **Nodes in 4-cycles:** observe that these nodes have a fixed trajectory that is independent on the input that computation part is handling. Thus, it suffices to note that each node in the context effectively changes its state (they are in an attractor of period 3×3 as it is shown in Figure 4). As a consequence of this latter

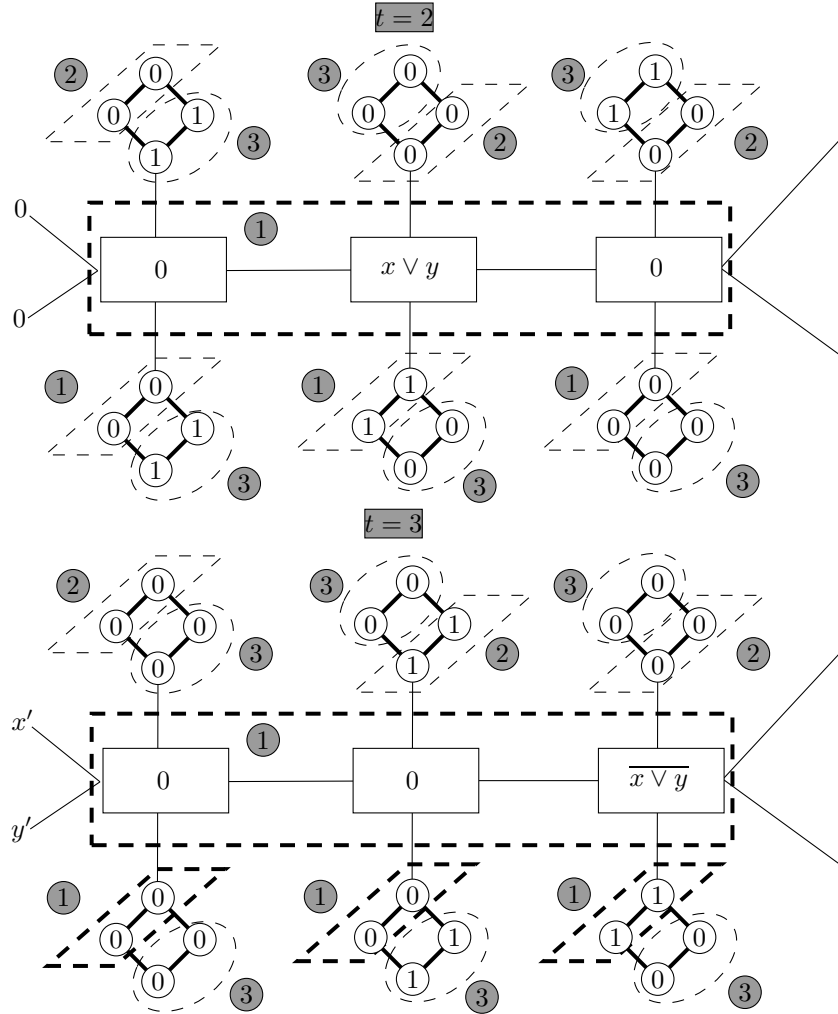


Figure 24: Last two steps of the dynamics of the computation gadget inside NOR gadget implemented by an AND-not network. Dotted circles and triangles represent blocks. Numbers in gray represent the updating order of each block. Each time step t is taken after three time steps (one for each block). Total simulation time is $T = 12$.

observation, we can set the local period of each positive neighbor so it is updated when its neighbor in the clock is in state 1. More precisely, we fix the corresponding local period value to 9 and correctly initialize them so each positive neighbor is updated exactly when their correspondent node is in state 1.

2. **Central nodes:** Observe that, in this case, we have that in the pseudo-orbit given in Table 2 and Table 3 each node eventually reaches the state 1 independently from the value of x, y, z, x' and y' . Thus, as same as the nodes that are in the 4-cycles, it suffices to set up the local clock of each positive neighbor in order to be updated while its neighbor in the gadget is in state 1. More precisely, it suffices to set up clocks following values in Table 2 and Table 3 and set clocks to be updated every 18 time-steps. Note that this work since nodes in the central part are in the first block so positive neighbors are updated at the same time as its neighbors but only when nodes in the gadget are in state 1.

Thus, gadgets in Figures 25 and 26 can be implemented as same as we did for general symmetric signed conjunctive networks and desired result holds.

□

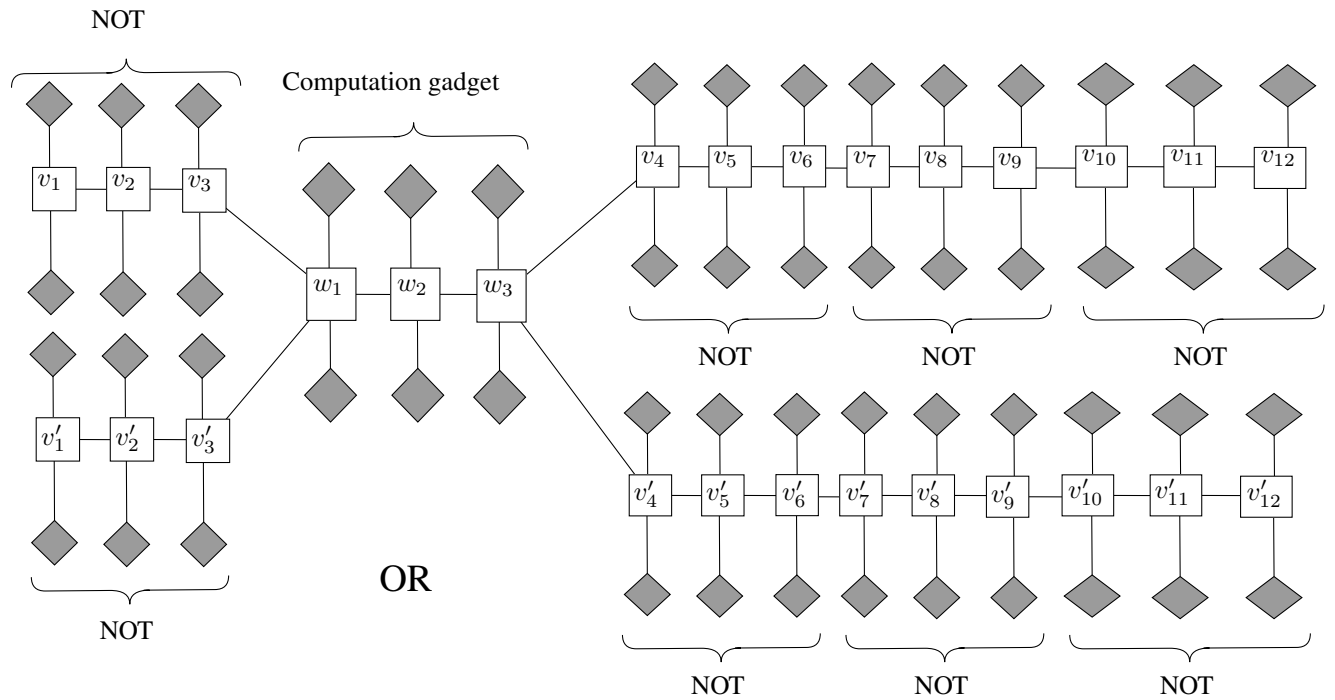


Figure 25: OR gadget structure. In order to produce a OR gadget, wire gadget and NOT gadget are combined with computation part showed in Figures 23 and 24.

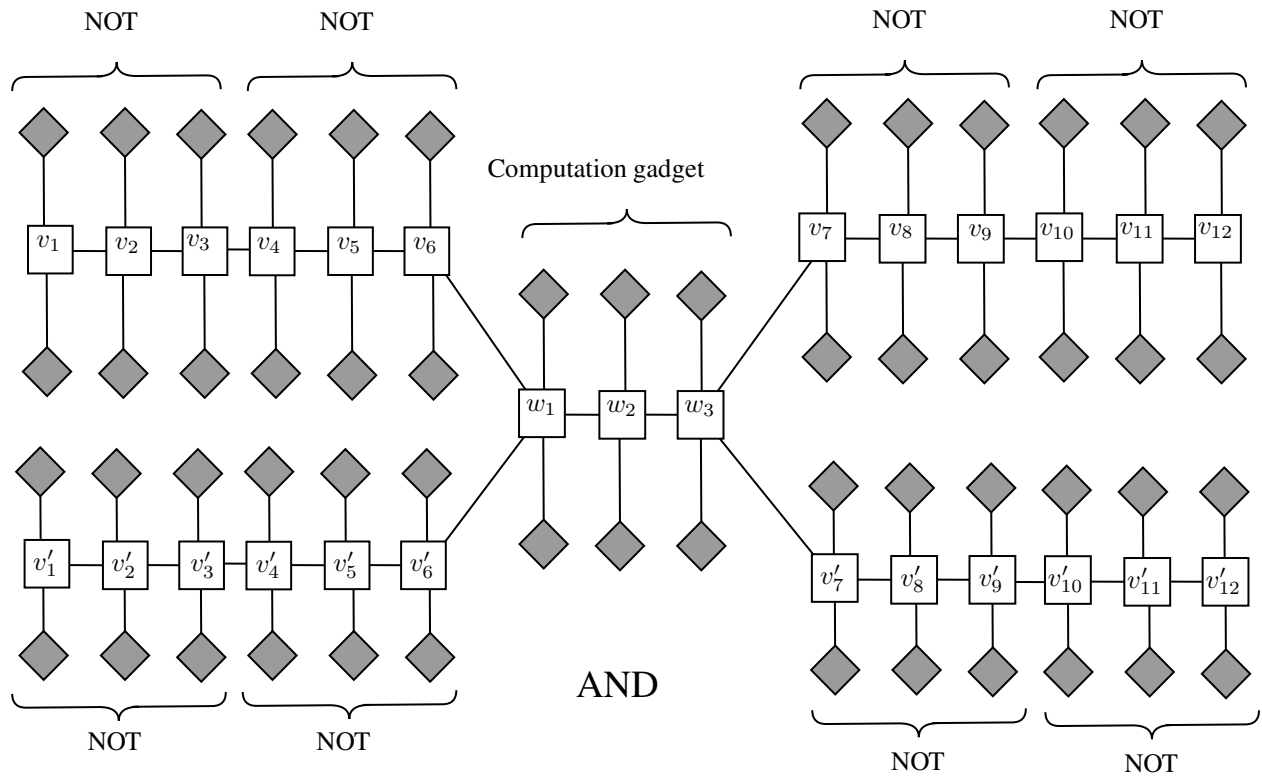


Figure 26: AND gadget structure. In order to implement an AND gadget, wire gadget and NOT gadget are combined with computation part showed in Figures 23 and 24.

Node/Time	v_1	v_2	v_3	v_4	v_5	v_6	v_7	v_8	v_9	v_{10}	v_{11}	v_{12}	v_{13}	v_{14}	v_{15}	v_{16}	v_{17}	v_{18}	v_{19}	v_{20}	v_{21}	v_{22}		
0	x	0	0	0	0	0	y	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
3	0	\bar{x}	0	0	1	0	\bar{y}	0	0	1	0	0	1	0	0	0	0	1	0	0	0	0	\bar{z}	0
6	0	0	x	0	0	0	0	y	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	z
9	1	0	0	\bar{x}	0	0	1	0	\bar{y}	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0
12	0	0	0	0	x	0	0	0	0	y	0	0	0	0	0	0	0	0	0	0	0	0	0	0
15	0	0	1	0	0	\bar{x}	0	0	1	0	0	\bar{y}	0	0	1	0	0	1	0	0	1	0	0	1
18	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	$x \wedge y$	0	0	0	0	0	0	0	0
21	0	1	0	0	1	0	0	1	0	0	1	0	0	0	0	$x \wedge y$	0	0	1	0	0	1	0	0
24	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	$x \wedge y$	0	0	0	0	0	0	0
27	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	0	$x \wedge y$	0	0	1	0	0	0	0
30	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	$x \wedge y$	0	0	0	0	0	0
33	0	0	1	0	0	1	0	0	1	0	0	1	0	0	0	0	0	0	$x \wedge y$	0	0	1	0	0
36	x	0	0	0	0	0	y	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	$x \wedge y$	0

Table 2: Dynamics for central gadgets in AND gadget implemented over a symmetric signed conjunctive network. Notation is the same of the one shown in Figure 26

Node/Time	v_1	v_2	v_3	v_4	v_5	w_1	w_2	w_3	v_4	v_5	v_6	v_7	v_8	v_9	v_{10}	v_{11}	v_{12}	v_{13}	v_{14}	v_{15}	v_{16}	v_{17}	v_{18}	v_{19}	v_{20}	v_{21}	v_{22}	
0	x	0	0	y	0	0	0	0	0	0	0	0	0	0	z	0	0	0	0	0	0	0	0	0	0	0	0	0
3	0	\bar{x}	0	\bar{y}	0	0	1	0	0	1	0	0	1	0	0	0	0	1	0	0	1	0	0	1	0	0	0	\bar{z}
6	0	0	x	0	0	y	0	0	0	0	0	0	0	0	0	0	0	z	0	0	0	0	0	0	0	0	0	z
9	1	0	0	1	0	$x \vee y$	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0
12	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
15	0	0	1	0	0	1	0	0	$x \vee y$	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0
18	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0
21	0	1	0	0	1	0	0	1	0	0	$x \vee y$	0	0	1	0	0	1	0	0	$x \vee y$	0	0	0	1	0	0	1	0
24	0	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0
27	1	0	0	1	0	0	1	0	0	1	0	0	$x \vee y$	0	0	1	0	0	1	0	0	$x \vee y$	0	0	0	1	0	0
30	0	0	0	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0
33	0	0	1	0	0	1	0	0	1	0	0	1	0	0	0	0	1	0	0	0	0	$x \vee y$	0	0	$x \vee y$	0	0	1
36	x	0	0	y	0	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0	0	0	0	0	0	0	0	0	$x \vee y$	0	0

Table 3: Dynamics for central gadgets in OR gadget implemented over a symmetric signed conjunctive network. Notation is the same of the one shown in Figure 25

6.4 Symmetric min-max networks

In this section, we study min-max networks. This is also a particular CSAN family in which local functions take the maximum or minimum value of some set of states. More precisely, a min-max network with ordered alphabet Q is a CSAN (G, λ, ρ) characterized by local functions $\lambda_v(q, S) = \min S$ or $\lambda_v(S) = \max S$ for every $v \in V(G)$ and trivial edge labels $\rho_e \in \{\text{Id}\}$ for each $e \in E(G)$. Note that in the particular case in which $Q = \{0, 1\}$ we have $\lambda_v(S) = \bigvee_{x \in S} x$ or $\lambda_v(q, S) = \bigwedge_{x \in S} s$ for every $v \in V(G)$. We call these particular CSAN family given by alphabet

$Q = \{0, 1\}$ the family of AND-OR networks and we write $\mathcal{F}_{\text{AND-OR}}$, $\mathcal{F}_{\text{AND-OR}}^{\text{BLOCK}, b}$, $\mathcal{F}_{\text{AND-OR}}^{\text{CLOCK}, c}$ and $\mathcal{F}_{\text{AND-OR}}^{\text{PER}, p}$ to denote different update schemes as we did before. Analogously, we use the notation $\mathcal{F}_{\text{MIN-MAX}}$, $\mathcal{F}_{\text{MIN-MAX}}^{\text{BLOCK}, b}$, $\mathcal{F}_{\text{MIN-MAX}}^{\text{CLOCK}, c}$ and $\mathcal{F}_{\text{MIN-MAX}}^{\text{PER}, p}$.

In the Boolean case, max and min functions are threshold functions because $\min(x_1, \dots, x_k) = 1 \Leftrightarrow \sum x_i = k$ and $\max(x_1, \dots, x_k) = 0 \Leftrightarrow \sum x_i = 0$. Therefore their periodic orbits are of length at most 2 by the results in [19]. In the non-Boolean case, they are not threshold functions. However, as shown by the following lemma the general alphabet case can be understood through multiple factorings onto the Boolean case.

Lemma 47. *Let $n \geq 2$ and let $\mathcal{A} = (G, \lambda, \rho)$ be a min-max automata network with alphabet Q such that $|V(G)| = n$. Let F be a global rule for \mathcal{A} . There exists an AND-OR automata network $\mathcal{A}^* = (G, \lambda, \rho)$ and a global rule $F^* : \{0, 1\}^n \rightarrow \{0, 1\}^n$ such that for every $\alpha \in Q$ the function $\pi^\alpha : Q^n \rightarrow \{0, 1\}^n$ is such that $\pi^\alpha \circ F = F^* \circ \pi^\alpha$ where*

$$\pi^\alpha(x)_i = \begin{cases} 1 & \text{if } x_i \geq \alpha \\ 0 & \text{otherwise.} \end{cases}$$

Proof. Let us take F^* as global function given by the same min/max labels than F but considering the fact that in the alphabet $\{0, 1\}$ we have $\min(x, y) = x \wedge y$ and $\max(x, y) = x \vee y$. Fix $\alpha \in Q$ and let $x \in Q^n$. Additionally, let us fix $i \in V$. Suppose that $F(x)_i = \max(x_{i_1}, \dots, x_{i_k})$ then $F^*(\pi^\alpha(x))_i = \bigvee_{s=1}^k \pi^\alpha(x)_{i_s}$ where $N(i) = \{i_1, \dots, i_k\}$.

Note that

$$\pi^\alpha(F(x))_i = \begin{cases} 1 & \text{if } \max(x_{i_1}, \dots, x_{i_k}) \geq \alpha, \\ 0 & \text{otherwise.} \end{cases}$$

Node/Time	$c_{s,1,1,u}$	$c_{s,1,2,u}$	$c_{s,1,3,u}$	$c_{s,1,4,u}$	$c_{s,2,1,u}$	$c_{s,2,2,u}$	$c_{s,2,3,u}$	$c_{s,2,4,u}$	$c_{s,3,1,u}$	$c_{s,3,2,u}$	$c_{s,3,3,u}$	$c_{s,3,4,u}$
0	0	0	1	1	0	0	1	1	1	1	0	0
3	1	1	0	0	0	0	0	0	0	0	1	1
6	0	0	0	0	1	1	0	0	0	0	0	0
9	0	0	1	1	0	0	1	1	1	1	0	0
Node/Time	$c_{s,1,1,l}$	$c_{s,1,2,l}$	$c_{s,1,3,l}$	$c_{s,1,4,l}$	$c_{s,2,1,l}$	$c_{s,2,2,l}$	$c_{s,2,3,l}$	$c_{s,2,4,l}$	$c_{s,3,1,l}$	$c_{s,3,2,l}$	$c_{s,3,3,b}$	$c_{s,3,4,b}$
0	1	1	0	0	0	0	0	0	0	0	1	1
3	0	0	1	1	1	1	0	0	0	0	0	0
6	0	0	0	0	0	0	1	1	1	1	0	0
9	1	1	0	0	0	0	0	0	0	0	1	1

Table 4: Dynamics for context in AND/OR gadgets implemented on symmetric signed conjunctive networks. Notation is given by the following guidelines: s represent the associated group of three nodes, second two coordinates indicate its position relative to the original gadget (there are 3 clocks) and its position in the 4-cycle graph (considering counter clock-wise order), and u, l stands for upper or lower according to its position in the gadget.

Then, it is clear that we have $\pi^\alpha(F(x))_i = 1$ if and only if $\pi^\alpha(x)_{i_s} = 1$ for some $s \in \{1, \dots, k\}$ and thus if and only if $\bigvee_{s=1}^k \pi^\alpha(x)_{i_s} = 1$ which is equivalent to $F^* \circ \pi(x)_i = 1$. The case in which $F(x)_i = \min(x_{i_1}, \dots, x_{i_k})$ is analogous

since we have that $F^*(\pi^\alpha(x))_i = \bigwedge_{s=1}^k \pi^\alpha(x)_{i_s}$. \square

We deduce that periodic orbits in MIN-MAX networks are of length at most 2 and therefore the family cannot be universal.

Corollary 9. *Let F be any MIN-MAX network over alphabet Q and x any limit configuration (i.e. such that $F^t(x) = x$ for some t). Then, $F^2(x) = x$. Therefore the family of MIN-MAX networks cannot be universal (considered under the parallel update scheme).*

Proof. We show that $F^2(x)_i = x_i$ for any node i . Let q be the maximum state appearing in the sequence $(F^t(x)_i)_{t \in \mathbb{N}}$. Using Lemma 47 with projection π^q and the fact that periodic orbits of AND-OR networks have period at most 2 we deduce that if $F^t(x)_i = q$ then $F^{t+2}(x)_i = q$ for some t . Suppose without loss of generality that $x_i = q$. If $F(x)_i = q$ we are done because then $(F^t(x)_i)_{t \in \mathbb{N}}$ is constant equal to q . Otherwise let q' be the minimum state appearing in the sequence $(F^t(x)_i)_{t \in \mathbb{N}}$. Necessarily $q' < q$, and using again Lemma 47 with projection $\pi^{q'}$ we deduce that if $F^t(x)_i = q'$ then $F^{t+2}(x)_i = q'$. With our assumption that $x_i = q$ it must be the case that $F^{2k+1}(x)_i = q'$ for some $k \geq 0$. From the previous facts and periodicity of the orbit of x we deduce that $F^t(x)_i$ is q when t is even and q' when t is odd. The claim about non-universality follows from Theorem 22. \square

6.5 Block sequential update schemes

We have shown that MIN-MAX networks are very limited under the parallel update mode. We now consider them under block sequential update schedules. We actually show that AND-OR networks under such update modes can simulate AND-NOT networks and therefore inherit the universality property. Since MIN-MAX networks on any alphabet Q with $|Q| > 1$ simulates Boolean AND-OR networks (by just restricting their alphabet to size 2), we only focus on AND-OR networks.

6.5.1 Strong universality

As we did for $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$ we will show that $\mathcal{F}_{\text{AND-OR}}^{\text{BLOCK},b}$ is also strongly universal as a direct consequence of the fact that we can simulate $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$ in linear space and constant time. We accomplish this by using again the coding trick of “double railed logic”. In fact, given the interaction graph of a signed symmetric conjunctive network having n nodes, we simply double each node and thus, our simulator has $2n$ nodes. Simulation is made in real time so $T = 1$. We precise these ideas in the following lemma:

Lemma 48. *Let $\mathcal{F}_{\text{AND-OR}}^{\text{PER},p}$ be the family of AND-OR networks updated according to some arbitrary periodic update scheme of period p . Let T be the constant function equal to 1 and $S : \mathbb{N} \rightarrow \mathbb{N}$ be defined as $S(n) = 2n$. Then, we have that $\mathcal{F}_{\text{sign-sym-conj}}^{\text{PER},p} \preceq_{T,S}^{\text{AND-OR}} \mathcal{F}_{\text{AND-OR}}^{\text{PER},p}$.*

Proof. Let us fix a periodic update scheme of period $p \in \mathbb{N}$. Let us take the graph representation of some arbitrary network (G, λ, ρ) in $\mathcal{F}_{\text{sign-sym-conj}}^{\text{PER},p}$. We construct a network in $\mathcal{F}_{\text{AND-OR}}^{\text{PER},p}$ capable to simulate the latter network in the following way: first, we represent each state $q \in \{0, 1\}$ by (q, \bar{q}) where $\bar{q} = 1 - q$; then, for each $v \in V(G)$ we consider two nodes v', \bar{v} . By doing this, we store the original state of v in v' and \bar{v} store its complement. More precisely, we replace each node in the network by the gadget in Figure 27. As it is shown in the same figure, we define $\lambda'(v') \equiv \text{AND}$ and $\lambda'(\bar{v}) \equiv \text{OR}$. Let us define the set $V' = \{(v', \bar{v}) : v \in V\}$. We define a set of edges in V' denoted E' as follows: for each edge $(u, v) \in E(G)$ we add the following edges depending on $\rho((u, v))$:

- if $\rho((u, v)) = \text{Id}$, we add the edges (u', v') and (\bar{u}, \bar{v}) .
- if $\rho((u, v)) = \text{Switch}$, we add the edges (u', \bar{v}) and (\bar{u}, v') .

Note that this immediately defines a local rule for the simulator network that we call f'_w for each $w \in V'$. We show that this local rule effectively simulates (G, λ, ρ) . More precisely, let us call f_u the local rule of (G, λ, ρ) for each $u \in V(G)$. In addition, let us call $N(u)_+$ to the neighborhood of u in G such that any $v \in N(u)_+$ satisfies $\rho((u, v)) = \text{Id}$ and also let us define $N(u)_-$ such that $v \in N(u)_-$ if and only if $\rho((u, v)) = \text{Switch}$. Finally, for each $x \in \{0, 1\}^n$ let us call $x' \in \{0, 1\}^{2n}$ the configuration defined by $x'_{u'} = x_u$ and $x'_{\bar{u}} = \bar{x}_u$, for each $u \in V(G)$. Fix $u \in V$, we have for (u', \bar{u}) the following result:

$$\begin{aligned} \bullet f'_{u'}(x' |_{N(u')}) &= \left(\bigwedge_{v' \in N_{G'}(u') : v \in N(u)_+} x'_{v'} \right) \wedge \left(\bigwedge_{\bar{v} \in N_{G'}(u') : v \in N(u)_-} x'_{\bar{v}} \right) = \left(\bigwedge_{v \in N(u)_+} x_v \right) \wedge \left(\bigwedge_{v \in N(u)_-} \bar{x}_v \right) = f_u(x) \\ \bullet f'_{\bar{u}}(x' |_{N(\bar{u})}) &= \left(\bigvee_{\bar{v} \in N_{G'}(\bar{u}) : v \in N(u)_+} x'_{\bar{v}} \right) \vee \left(\bigvee_{v' \in N_{G'}(\bar{u}) : v \in N(u)_-} x'_{v'} \right) = \left(\bigvee_{v \in N(u)_+} \bar{x}_v \right) \vee \left(\bigvee_{v \in N(u)_-} x_v \right) = \\ &= \overline{\left(\bigwedge_{v \in N(u)_+} x_v \right) \wedge \left(\bigwedge_{v \in N(u)_-} \bar{x}_v \right)} = \overline{f_u(x)} \end{aligned}$$

And thus, we have that $x'_{u'} \rightarrow f_u(x)$ and $x'_{\bar{u}} \rightarrow \overline{f_u(x)}$.

Finally, in order to be coherent with the given periodic update scheme, it suffices to define an update scheme with period p such that the nodes (u', \bar{u}) are updated at each time step at which $u \in V(G)$ is. We conclude that the network $(G', \lambda', \rho') \in \mathcal{F}_{\text{AND-OR}}^{\text{PER},p}$ (where $\rho(u, v) = \text{Id}$ for each $(u, v) \in E$), simulates (G, λ, ρ) in constant time $T = 1$ and linear space $S(n) = 2n$. \square

Theorem 49. *There exists some $b > 0$ such that the family $\mathcal{F}_{\text{AND-OR}}^{\text{BLOCK},b}$ of all AND-OR networks under block sequential update scheme of block parameter b is strongly universal. In particular, $\mathcal{F}_{\text{AND-OR}}^{\text{BLOCK},b}$ is dynamically and computationally complex.*

Proof. The result is a direct consequence of Lemma 48 and strong universality of $\mathcal{F}_{\text{sign-sym-conj}}^{\text{BLOCK},b}$ given by Corollary 8. \square

7 Perspectives

The main results of this paper extend previous results [16, 18, 15] on the strong interplay between local interactions and update modes, but are still limited to finite hierarchies of local interactions and update modes. A natural research direction is to try to further extend this analysis to other families of networks and update modes. For instance, we have no example of a CSAN family which is (strongly) universal under periodic update schemes, but not universal under local clocks update schemes.

Besides, our results were obtained as an application of various concepts and tools, and we believe that several research directions around them are worth being considered. We detail some of them below.

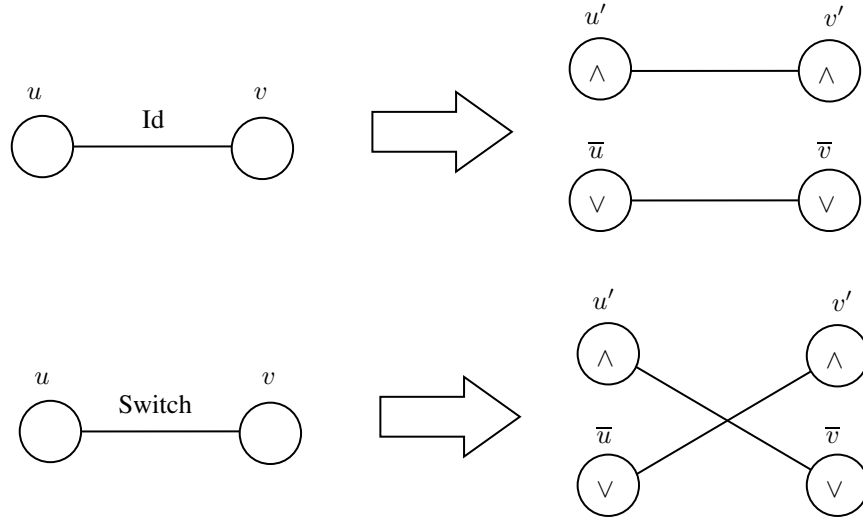


Figure 27: Gadget used for simulation of an AND-NOT network with arbitrary periodic update scheme implemented over an AND-OR network with periodic update scheme.

Glueing We think it would be interesting to understand the properties of the glueing process itself and see what information on the result of the glueing process can be deduced from the knowledge of each network to be glued. We are particularly interested in dynamical properties. In addition, it would be very interesting to explore if latter process can be seen in the opposite way, i.e., given an automata network, determine if it is possible to decompose the network into glued blocks satisfying some particular properties as gadgets do.

Simulations and universality Two notions of universality are introduced in this paper. We see how to build families which are universal but not strongly universal by adding a somewhat artificial mechanism that slows down polynomially any useful computation made by networks in the family. However, we don't have any natural example so far. In the same spirit, we can ask how a strongly universal family can fail to have coherent \mathcal{G}_m -gadgets (recall that Corollary 6 only gives a sufficient condition to be strongly universal). We don't think that strongly universality implies coherent \mathcal{G}_m -gadgets in all generality, but it might be true under some additional hypothesis, and possibly in natural families like \mathcal{G} -networks.

Families, \mathcal{G} -networks and gadgets Proposition 4 together with theorems 35 and 39 provide an interesting starting point to explore the link between different gate sets and the richness of their synchronous closure and the associated family of \mathcal{G} -networks. It is natural to further study the hierarchy between sets of gates and we believe that a promising direction would be to study reversible gate sets such as Toffoli or Fredkin gates.

Update schedules As we have pointed out in the update schemes section (see Remark 2), it would be interesting to study other types of update schemes which are not captured by general periodic update schemes such as firing memory schemes [16, 14], or other modes where the decision to apply an update at a node depends on its state (for instance we can interpret reaction-diffusion systems [11] as threshold networks with an update mode with memory). We can consider even more general ones inspired by the most permissive semantics studied in [5]. In some cases, Definition 8 of asynchronous extension should be adapted in order to capture latter update schemes.

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