Region Array SSA
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To cite this version:
Silvius Rus, Guobin He, Christophe Alias, Lawrence Rauchwerger. Region Array SSA. ACM/IEEE International Conference on Parallel Architectures and Compilation Techniques (PACT’06), Sep 2006, Seattle, United States. hal-03106226

HAL Id: hal-03106226
https://hal.science/hal-03106226
Submitted on 11 Jan 2021

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ABSTRACT

Static Single Assignment (SSA) has become the intermediate program representation of choice in most modern compilers because it enables efficient data flow analysis of scalars and thus leads to better scalar optimizations. Unfortunately not much progress has been achieved in applying the same techniques to array data flow analysis, a very important and potentially powerful technology. In this paper we propose to improve the applicability of previous efforts in array SSA through the use of a symbolic memory access descriptor that can aggregate the accesses to the elements of an array over large, interprocedural program contexts. We then show the power of our new representation by using it to implement a basic data flow algorithm, reaching definitions. Finally we apply this analysis to array constant propagation and array privatization and show performance improvement (speedups) for benchmark codes.

1. INTRODUCTION

Important compiler optimization or enabling transformations such as constant propagation, loop invariant motion, expansion/privatization depend on the power of data flow analysis. The Static Single Assignment (SSA) [10] program representation has been widely used to explicitly represent the flow between definitions and uses in a program.

SSA relies on assigning each definition a unique name and ensuring that any use may be reached by a single definition. The corresponding unique name appears at the use site and offers a direct link from the use to its corresponding and unique definition. When multiple control flow edges carrying different definitions meet before a use, a special \( \phi \) node

\[
\begin{align*}
&x = 5 \\
&x = 7 \\
&\ldots = x \\
&x_1 = 5 \\
&x_2 = 7 \\
&\ldots = x_2 \\
&A(3) = 5 \\
&A(4) = 7 \\
&\ldots = A(3) \\
&A_1(3) = 5 \\
&A_2(4) = 7 \\
&\ldots = A_2(3)
\end{align*}
\]

(a) (b) (c) (d)

Figure 1: (a) Scalar code, (b) scalar SSA form, (c) array code and (d) improper use of scalar SSA form for arrays.

is inserted at the merge point. Merge nodes are the only statements allowed to be reached directly by multiple definitions.

Classic SSA is limited to scalar variables and ignores control dependence relations. Gated SSA [1] introduced control dependence information in the \( \phi \) nodes. This helps selecting, for a conditional use, its precise definition point when the condition of the definition is implied by that of the use [27]. The first extensions to array variables ignored array indices and treated each array definition as possibly killing all previous definitions. This approach was very limited in functionality. Array SSA was proposed by [16, 24] to match definitions and uses of partial array regions. However, their approach of representing data flow relations between individual array elements makes it difficult to complete the data flow analysis at compile time and requires potentially high overhead run-time evaluation. Section 6 presents a detailed comparison of our approach against previous related work.

We propose an Array SSA representation of the program that accurately represents the use-def relations between array regions and accounts for control dependence relations. We use the USR symbolic representation of array regions [23] which can represent uniformly memory location sets in a compact way for both static and dynamic analysis techniques. We present a reaching definition algorithm based on Array SSA that distinguishes between array subregions and is control accurate. The algorithm is used to implement array constant propagation and array privatization for automatic parallelization, for which we present whole application improvement results. Although the Array SSA form that we present in this paper only applies to structured programs that contain no recursive calls, it can be generalized to any programs with an acyclic Control Dependence Graph (except for self-loops).

2. REGION ARRAY SSA FORM

Static Single Assignment (SSA) is a program representation that presents the flow of values explicitly. In Fig. 1(a), the compiler must analyze the control flow graph in order to
We use the USR [23] representation for array regions, which can represent uniformly arbitrarily complex regions. Moreover, when an analysis based on USRs cannot reach a static decision, the analysis can be continued at run time with minimal necessary overhead. For instance, in the example in Fig. 2, let us assume that the loop bounds were not known at compile time. In that case the MAX operation could not be performed statically. Its run time as proposed by [24] would require $O(n)$ time, where $n$ is the dimension of the array. Using Region Array SSA, the region corresponding to $A_3 = A_1$ can be computed at run time in $O(1)$ time, thus independent of the array size. Our resulting Region Array SSA representation has two main advantages over [16]:

- We can analyze many complex patterns at compile time using symbolic array region analysis (essentially symbolic set operations), whereas the previous Array SSA representation often fails to compute element-wise MAX operations symbolically (for the complex cases).
- When a static optimization decision cannot be reached, we can extract significantly less expensive run time tests based on partial aggregation of array regions.

We will now present the USR representation for array regions, describe the structure of Region Array SSA, and then illustrate its use in an algorithm that computes reaching definitions for arrays.

### 2.1 Array Region Representation: the USR

In the example in Fig. 3(a), we can safely propagate constant value 0 from the definition at site 2 to the use at site 5 because the array region used, $[1:5]$, is included in the array region defined above, $[1:10]$. In the example in Fig. 3(b), we cannot represent the array regions as intervals because the memory references are guarded by an array of conditionals. However, we can represent the array regions as expressions on intervals, in which the operators represent predication ($\#$) and expansion across an iteration space ($\otimes$). This symbolic representation allows us to compare the defined and used regions even though their shapes are not linear. In the example in Fig. 3(c), a static decision cannot be made. The needed values of the predicate array $C(\cdot)$ may only be known at run time. We can still perform constant propagation on array $A$ optimistically and validate the transformation dynamically, in the presence of the actual values of the predicate array. Although the profitability of such a transformation in this particular example is debatable due to the possibly high cost of checking the values of $C(\cdot)$ at run time, in many cases such costs can be reduced by partial aggregation and amortized through hoisting and memoization.

The Uniform Set of References (USR) previously introduced in [23] formalizes the expressions on intervals shown in Fig. 3. It is a general, symbolic and analytical representation for memory reference sets in a program. It can represent the aggregation of scalar and array memory references at any hierarchical level (on the loop and subprogram call graph) in a program. It can represent the control flow (predicates), inter-procedural issues (call sites, array reshaping, type overlaps) and recurrences. The simplest form of a USR is the Linear Memory Access Descriptor (LMAD) [20], a symbolic representation of memory reference sets accessed through linear index functions. It may have multiple dimen-
In Fig. 3(a), the array subregion denoted by the first loop can be represented as an LMAD, [1:10], and the array subregion used in the second loop can also be represented as another LMAD, [1:5].

The USR is stored as an abstract syntax tree with respect to the language presented in Fig. 4 and can be thought of as symbolic expressions on sets of memory locations. When memory references are expressed as linear functions, USRs consist of a single leaf, i.e., a list of LMADs. When the analysis process encounters a nonlinear reference pattern or when it performs an operation (such as set difference) whose result cannot be represented as a list of LMADs, we add internal nodes that record accurately the operations that could not be performed.

In the examples in Fig. 3(b,c), memory references are mediated by an array of conditions. This nonlinear reference pattern cannot be represented as an LMAD, so it is expressed as a nontrivial USR. Although nothing is known about the predicates, the USR representation allows us to compare the definition and use sets symbolically in case (b). In case (c) a static decision cannot be made. However, using USRs we can formulate efficient run time tests that will guarantee the legality of the constant propagation transformation at run time. [23] showed how to extract efficient run time tests from identities of type $S = \emptyset$, where $S$ is a USR. By setting $S = \text{used} - \text{defined}$, we can extract conditions that guarantee the safety of the optimistic constant propagation (Fig. 3(d)). Additionally, constant propagation may enable other more profitable transformations such as automatic parallelization by simplifying the control flow and the memory reference pattern.

Most examples in this paper only present single-indexed arrays accessed using a unit stride solely for the simplicity of the presentation. The USRs can represent any memory reference pattern produced by multidimensional arrays accessed over arbitrary large loop nests spanning multiple subroutines. Strides can be arbitrary symbolic expressions.

### Figure 3: Constant propagation scenarios

- **(a)** symbolically comparable linear reference pattern
- **(b)** symbolically comparable nonlinear reference pattern
- **(c)** nonlinear reference pattern that require a run time test
- **(d)** dynamic constant propagation code for case (c)

### Figure 4: USR formal definition

- $\Sigma = \{\cap, \cup, \neg, \#, \\otimes, \otimes\}^c$,
- $\text{LMADs, Gate, Recurrence, CallSite}$
- $N = \{\text{USR}\}$, $S = \text{USR}$
- $P = \{\text{USR} \rightarrow \text{LMADs}(\text{USR}) \}$
- $\text{USR} \rightarrow \text{USR} \cap \text{USR}$
- $\text{USR} \rightarrow \text{USR} \cup \text{USR}$
- $\text{USR} \rightarrow \text{USR} - \text{USR}$
- $\text{USR} \rightarrow \text{Gate} \#\text{USR}$
- $\text{USR} \rightarrow 0\text{US}r \text{US}r$\text{Recurr}\text{ence}\text{USR}$
- $\text{USR} \rightarrow \text{USR} \text{US}r \text{US}r$\text{US}r \text{CallSite}$

USRs also contain control dependence information. The only restriction is that the program must be structured.

### 2.2 Array SSA Definition and Construction

#### 2.2.1 Region Array SSA Nodes

In scalar SSA, pseudo statements $\phi$ are inserted at control flow merge points. These pseudo statements show which scalar definitions are combined. [1] refines the SSA pseudo statements in three categories, depending on the type of merge point: $\gamma$ for merging two forward control flow edges, $\mu$ for merging a loop-back arc with the incoming edge at the loop header, and $\eta$ to account for the possibility of zero-trip loops. The array SSA form proposed in [24] presents the need for additional $\phi$ nodes after each assignment that does not kill the whole array. These extensions, while necessary, are not sufficient to represent array data flow efficiently because they do not represent array indices.

In order to provide a useful form of Array SSA, it is nec-
essary to incorporate array region information into the representation. Region Array SSA gates differ from those in scalar SSA in that they represent, at each merge point, the used to report locations undefined within the program block. These USR comparisons can to USR comparisons.

\[ \Re(A_0, \emptyset) = \text{Undefined} \]

\[ A_0 : [A_0, \emptyset] = \Re(A_0, A_1, \{i\}) \]

\[ A_2 : [A_2, \{1\}] = \Re(A_0, A_2, \{1\}) \]

\[ A_3 : [A_3, \emptyset] = \Re(A_2, x > 0) \]

\[ A_4 : A_4(2) = 3 \]

\[ A_5 : [A_5, \{2\}] = \Re(A_3, A_4, \{2\}) \]

\[ A_6 : [A_6, \{1\} \cup (x > 0)\#2] = \Re(A_0, A_2, \{1\}, [A_5, (x > 0)\#2]) \]

\[ A_7 : [A_7, 3 : [i + 2] \cup [11 : i + 8]] = \mu(A_0, (i = 3, 10), [A_3, 3 : [i - 1], [A_4, 11 : i + 7]) \]

\[ A_8 : A_8(i) = 3 \]

\[ A_9 : [A_9, \{1\}] = \Re(A_7, A_8, \{i\}) \]

\[ \ldots = A_9(\ldots) \]

\[ A_{10} : A_{10}(i + 8) = 4 \]

\[ A_{11} : [A_{11}, (i, i + 8)] = \Re(A_7, A_9, \{i\}, [A_{10}, \{i + 8\}] \]

\[ \text{EndDo} \]

\[ A_{12} : [A_{12}, \{1\} \cup (x > 0)\#2 \cup [3 : 18]] = \Re(A_0, A_2, \{1\} \cup (x > 0)\#2) \]

\[ \ldots = A_{12}(2) \]

\[ \text{EndIf} \]

\[ A_{13} : [A_{13}, \emptyset] = \Re(A_{12}, (x > 0)) \]

\[ \ldots = A_{13}(2) \]

---

**Figure 5:** (a) Sample code and (b) Array SSA form

- be solved at run time with minimal necessary overhead, based on USR partial aggregation capabilities.

Our node placement scheme is essentially the same as in [24]. In addition to \(\phi\) nodes at control flow merge points, we add a \(\phi\) node after each array definition. These new nodes are named \(\delta\). They merge the effect of the immediately previous definition with that of all other previous definitions.

Each node corresponds to a structured block of code. In the example in Fig. 5, \(A_2\) corresponds to statement 1, \(A_6\) to statements 1 to 4, \(A_{11}\) to statements 6 to 8, and \(A_{12}\) to statements 1 to 9. In general, a \(\delta\) node corresponds to the maximal structured block that ends with the previous statement.

### 2.2.2 Abstraction of Partial Kills: \(\delta\) Nodes

In the example in Fig. 5, the array use \(A(1)\) at statement 10 only could have been defined at statement 1. Between statement 1 and statement 10 there are two blocks, an \(if\) and a \(do\). We would like to have a mechanism that could quickly tell us not to search for a reaching definition in any of those blocks. We need SSA nodes that can summarize the array definitions in these two blocks. Such summary nodes could tell us that the range of locations defined from statement 2 to statement 9 does not include \(A(1)\).

The function of a \(\delta\) node is to aggregate the effect of disjoint structured blocks of code. Fig. 6(a) shows the way we build \(\delta\) gates for straight line code. Since the USR representation contains built-in predication, expansion by a recurrence space and translation across subprogram boundaries, the \(\delta\) functions become a powerful mechanism for computing accurate use-def relations.

Returning to our example, the exact reaching definition of the use at line 10 can be found by following the use-def chain \(\{A_{12}, A_6, A_2, A_1\}\). A use of \(A_{12}(20)\) can be classified as undefined using a single USR intersection, \(\{A_{12}, A_0\}\).

### 2.2.3 Abstraction of Loops: \(\mu\) Nodes

The semantics of \(\mu\) for Array SSA is different than those for scalar SSA. Any scalar assignment kills all previous ones (from a different statement or previous iteration). In Array SSA, different locations in an array may be defined by various statements in various iterations, and still be visible at the end of the loop.

In the code in Fig. 5(a), Array A is used at statement 7 in a loop. In case we are only interested in its reaching definitions from within the same iteration of the loop (as is the case in array privatization), we can apply the same reasoning as above, and use the \(\delta\) gates in the loop body. However, if we are interested in all the reaching definitions from previous iterations as well as from before the loop, we need additional information. The \(\mu\) node serves this purpose.

\[ [A_n, \Re_n] = \mu(A_0, (i = 1, p), [A_1, \Re_1], \ldots, [A_m, \Re_m]) \]

The arguments in the \(\mu\) state at each loop header are all the \(\delta\) definitions within the loop that are at the immediately inner block nesting level (Fig. 6(c)), and in the order in which they appear in the loop body. Sets \(\Re_n\) are functions of the loop index \(i\). They represent the sets of memory

\[ \text{EndIf} \]
locations defined in some iteration \( j < i \) by definition \( A_k \) and not killed before reaching the beginning of iteration \( i \). For any array element defined by \( A_k \) in some iteration \( j < i \), in order to reach iteration \( i \), it must not be killed by other definitions to the same element, which occur from that point on until the beginning of iteration \( i \). We must thus subtract the regions defined in iteration \( j \) after definition \( A_k \): \( \text{Kill}_a(j) \), as well as all the regions defined in the subsequent iterations \( j + 1, ..., i - 1 \): \( \text{Kill}_a(i) \).

\[
\mathcal{R}_k^a(i) = \bigcup_{j=1,i-1} \left( \mathcal{R}_k(j) \setminus \left( \text{Kill}_a(j) \cup \bigcup_{l=j+1,i-1} \text{Kill}_a(l) \right) \right)
\]

where \( \text{Kill}_a = \bigcup_{h=0} m \mathcal{R}_h \), and \( \text{Kill}_a = \bigcup_{h=0} m \mathcal{R}_h \)

This representation gives us powerful closed forms for array region definitions across the loop. We avoid fixed point iteration methods by hiding the complexity of computing closed forms in USR operations. The USR simplification process will attempt to reduce these expressions to LMADs. However, even when that is not possible, the USR can be used for run-time optimization and speculative execution.

The reaching definition for the array use \( A_{12}(5) \) at statement 11 (Fig. 5(b)) is found inside the loop using \( \delta \) gates. We use the \( \mu \) gate to narrow down the block that defined \( A(5) \). We intersect the use region \( \{5\} \) with \( \mathcal{R}_5^a(i = 11) = \{3 : 10\} \), and \( \mathcal{R}_{11}(i = 11) = \{11 : 18\} \). We substituted \( i \) because the \( \text{use} \) happens after the last iteration. The use-def chain is \( \{A_{12}, A_7, A_9\} \).

### 2.2.4 Abstraction of Control: \( \pi \) Nodes

The control dependence predicates corresponding to array definitions are embedded in USRs as seen in the \( \gamma \) gate at the definition site of \( A_6 \) in Fig. 5. The remainder of this section presents an extension to classic SSA which represents explicitly the control predicates of array uses.

Array element \( A_{13}(2) \) is used conditionally at statement 13. Based on its range, it could have been defined only by statement 3. In order to prove that it was defined at statement 3, we need to have a way to associate the predicate of the use with the predicate of the definition. We create fake definition nodes \( \pi \) to guard the entry to control dependence regions associated with \( \text{Then} \) and \( \text{Else} \) branches: \( \{A_6, \emptyset\} = \pi(A_6, \text{cond}) \). This type of gate does not have a correspondent in classic scalar SSA, but in the Program Dependence Web [1]. Their advantage is that they lead to more accurate use-def chains. Their disadvantage is that they create a new SSA name in a context that may contain no array definitions. Such a fake definition \( A_{13} \) placed between statement 12 and 13 will force the reaching definition search to collect the conditional \( x > 1 \) on its way to the possible reaching definition at line 2. This conditional is crucial when the search reaches the \( \gamma \) statement that defines \( A_9 \), which contains the same condition. The use-def chain is \( \{A_{13}, A_{12}, A_6, A_5, A_4\} \).

### 2.2.5 Array SSA Construction

Fig. 6 presents the way we create \( \delta, \eta, \gamma, \mu, \) and \( \pi \) gates for various program constructs. The associated array regions are built in a bottom-up traversal of the Control Dependence Graph intraprocedurally, and the Call Graph interprocedurally. At each block level (loop body, \( \text{then} \) branch, \( \text{else} \) branch, subprogram body), we process sub-blocks in program order.

### 2.2.6 Complexity

The number of \( \phi \) nodes is \( O(|E(CFG)| \cdot \text{Variable Count}) \) because every statement (CFG node) could modify all the variables. [23] showed that the number of USR nodes added by each operation (union, intersection, etc) is \( O(1) \), and that each USR node consumes \( O(1) \) memory. Since the number of USR operations to build any \( \phi \) node is also \( O(1) \), the space complexity is thus \( O(|E(CFG)| \cdot \text{Variable count}) \).

USR construction optimizations such as symbolic aggregation push the time of each operation to \( O(|V(CFG)|) \) in the worst case, though we have observed in practice an amortized \( O(1) \). The total compilation time ranges from seconds on a 200 lines code to minutes on a 5000 lines code, using a Pentium IV 2.8GHz PC.
2.3 Reaching Definitions

Algorithm Search ($A_u$, $R_{use}$, GivenBlock)
If $A_u \notin \text{GivenBlock}$ or $R_{use} = \emptyset$ Then Return
Switch definition site($A_u$)
Case original statement:
$R^{RD} = R_u \cap R_{use}$
Case $\delta$, $\gamma$, $\eta$, $\theta$:
$[A_u, R_u] = \phi(A_0, [A_1, R_1^e], \ldots)$
ForEach $[A_k, R_k^e]$
  Call Search ($A_k$, $R_{use} \cap R_k^e$, GivenBlock)
EndIf

Figure 7: Recursive algorithm to find reaching definitions. $A_u$ is an SSA name and $R_{use}$ is an array region. Array regions $R$ are represented as USRs. They are built using USR operations such as $\cap$, $\setminus$, $\otimes$.

Finding the reaching definitions for a given use is required to implement a number of optimizations: constant propagation, array privatization etc. We present here a general algorithm based on Array SSA that finds, for a given SSA name and array region, all the reaching definitions and the corresponding subregions. These subregions can then be used to implement particular optimizations such as constant propagation. Any such optimization can be performed either at compile time, when associated USR comparison can be solved symbolically, or at run-time, when USR comparisons depend on input values.

For each array use $R_{use}(A_u)$ of an SSA name $A_u$, and for a given block, we want to compute its reaching definition set, $\{[A_1, R_{1}^{RD}], [A_2, R_{2}^{RD}], \ldots [A_n, R_{n}^{RD}], [A_0, R_0^{RD}]\}$, in which $R_i^{RD}$ specifies the region of this use defined by $A_i$ and not killed by any other definition before it reaches $A_u$. $R_0^{RD}$ is the region undefined within the given block. Restricting the search to different blocks produces different reaching definition sets. For instance, for a use within a loop, we may be interested in reaching definitions from the same iteration of the loop (block = loop body) as is the case in array privatization. We can also be interested in definitions from all previous iterations of the loop (block = whole loop) or for a whole subroutine (block = routine body). Fig. 7 presents the algorithm for computing reaching definitions. The algorithm is invoked as Search($A_u$, $R_{use}(A_u)$, GivenBlock). $R_{use}$ is the region whose definition sites we are searching for. $A_u$ is the SSA name of array $A$ at the point at which it is used, and GivenBlock is the block that the search is restricted to.

The set of memory locations containing undefined data is computed as: $R_{use} = \bigcup_{i=0}^{n} R_i^{RD}$.

In case the SSA name given as input corresponds to an original statement, the reaching definition set is computed directly by intersecting the region of the definition with the region of the use. If the definition is a $\delta$, $\gamma$, $\eta$, $\theta$, we perform two operations. First, we find the reaching definitions corresponding to each argument of the $\phi$ function. Second, we continue the search outside the current block for the region containing undefined values. As shown, the algorithm would make repeated calls with the same arguments to search for undefined memory locations. The actual implementation avoids repetitions work, but we omitted the details here for clarity.

When $A_u$ is inside a loop within the given block, the search will eventually reach the $\mu$ node at the loop header. At this point, we first compare $R_{use}$ to the arguments of the $\mu$ function to find reaching definition from previous iterations of the loop. Second, we continue the search before the loop for the region undefined within the loop.

When the definition site of $A_u$ is a $\gamma$ node, we simply predicate $R_{use}$ and continue the search.

The search paths presented in Section 2.2 were obtained using this algorithm.

3. APPLICATION: ARRAY CONSTANT PROPAGATION

We present an Array Constant Propagation optimization technique based on our Region Array SSA form. Often programmers encode constants in array variables to set invariant or initial arguments to an algorithm. Analogous to scalar constant propagation, if these constants get propagated, the code may be simplified which may result in (1) speedup or (2) simplification of control and data flow which enable other optimizing transformations, such as automatic parallelization.

We define a constant region as the array subregion that contains constant values at a particular use point. We define array constants are either (1) integer constants, (2) literal floating point constants, or (3) an expression $f(v)$ which is assigned to an array variable in a loop nest. We name this last class of constants expression constants. They are parameterized by the iteration vector of their definition loop nest. Presently, our framework can only propagate expression constants when (1) their definition indexing formula is a linear combination of the iteration vector described by a nonsingular matrix with constant terms and (2) they are used in another loop nest based on linear subscripts (similar to [29]).

3.1 Array Constant Collection

3.1.1 Intraprocedural Collection

The constant regions for SSA name $A_u$ in a subprogram are computed (Fig. 8) by invoking algorithm Search($A_u$, $T$, WholeSubprogram), where $T$ is a symbolic name for the whole subscript space of array $A$. This call results in a set of tuples $\{[A_1, R_{1}], [A_2, R_{2}], \ldots [A_n, R_{n}], [A_0, R_0]\}$, such that region $R_i$ contains all the subscripts at which the value defined by $A_i$ is available to any use of $A_u$. The constant regions are those descriptors $R_i$ for which the definition site of $A_i$ is an assignment of a constant value. All $R_i$ are guaranteed to be mutually disjoint by the logic of the Search algorithm and the fact that all the array regions on the right hand side of a $\delta$ gate are mutually disjoint. When propagating expression constants, special care is taken at $\mu$ nodes to update the iteration vector corresponding to the constant.

Region $R_0$ contains all the locations that hold data undefined in the subprogram. The conservative approach is to consider them nonconstant. However, if an interprocedural analysis can infer that certain regions are constant upon entry to the subprogram, the algorithm will also report the corresponding subregions that reach $A_u$ (last loop in Fig. 8).
Algorithm \text{CollectInterProcedural} \\
\textbf{Input}: \text{Program} \\
\textbf{Output}: \text{AvailableConstants} \\
\text{change} = \text{true} \\
\text{while} \ \text{change} \ \text{do} \\
\text{change} = \text{false} \\
\text{for each} \ \text{subprogram} \ \text{caller} \\
\text{for each} \ \text{SSA} \ \text{name} \ A_i \ \text{in} \ \text{caller} \\
\text{call} \ \text{CollectInterProcedural}(A_i, \text{Incoming}(\text{caller})) \\
\text{end for each} \\
\text{for each} \ \text{call site} \ \text{call callee(...)} \\
\text{OldIncoming(callee)} = \text{Incoming}(\text{callee}) \\
\text{call} \ \text{Adjust}(\text{Incoming}(\text{callee})) \\
\text{if} \ \{(\text{Incoming}(\text{callee}) \neq \text{OldIncoming}(\text{callee}))\} \\
\text{change} = \text{true} \\
\text{end if} \\
\text{end for each} \\
\text{end while} \\

\text{Figure 8: Algorithm to collect array constant regions available to any use of SSA name } A_u \text{ in a given subprogram.}

Algorithm \text{CollectIntraProcedural} \\
\textbf{Input}: A_u \text{ as SSA name,} \\
\text{IncomingConstants as } [\mathcal{R}_0, c_1], \ldots, [\mathcal{R}_0, c_m] \\
\text{Output}: \text{AvailableConstants as } [\mathcal{R}, c_i] \\
\text{Call} \ \text{Search}(A_u, \mathcal{T}, \text{WholeSubprogram}) \\
\text{if} \ \text{i} = 1, n \ \text{//Constants from this subprogram} \\
\text{if} \ (\text{IsArrayConstant(} \text{RightHandSide(Definition}(A_i)))\} \\
\text{Report}([\mathcal{R}_0, \text{RightHandSide(Definition}(A_i)])\} \\
\text{end if} \\
\text{end for} \\
\text{end if} \\
\text{end for} \\

\text{Figure 9: Algorithm to collect array constant regions across the whole program.}

In the example in Fig. 5, the constant regions for SSA name \( A_{12} \) are \{(1), 0\}, \{(x > 0)\#(2), 1\}, \{(3 : 10), 3\} and \{(11 : 18), 4\}. It is interesting to note that the algorithm is control sensitive since the USRs embed control dependence information. We can thus know that location \( A_{12}(2) \) holds value 1 when \( x > 0 \) holds true.

3.1.2 Interprocedural Collection

When collecting constants in a single subprogram, we have assumed conservatively that the set of undefined elements \( \mathcal{R}_0 \) is not constant. However, arrays containing constant regions may be passed as actual arguments or as global names from a calling context into another subprogram. Even though their corresponding formal names may appear undefined locally, they will contain constant values upon entry to the subprogram. For example, in Fig. 10, during the first traversal of the program, the outgoing set of subroutine \text{jacld} is collected and translated into subroutine \text{ssor} at call site \text{call jacld}. In the next traversal, the value set of \( A \) at call site \text{call blts} is computed and translated into the incoming value set of subroutine \text{blts}.

Our interprocedural constant propagation algorithm (Fig. 9) starts by invoking its intraprocedural counterpart on each subprogram. The incoming constant regions are considered empty. This phase computes array constants available at each subprogram call site, which are used to compute actual incoming constant regions for the corresponding callees. Conversely, this may lead to the callee producing more constant values which will be taken into account when re-analyzing the call site. The procedure is repeated until the incoming constants do not change globally.

When a subprogram is called at a single site, its incoming constants are simply a copy of the constants available at the call site. The USRs that describe the associated regions are translated symbolically, as are the expression constants. In general, subprograms are called at several call sites, from different contexts and with different arguments, which could result in conflicting incoming constants. The conservative solution is to consider only the constants that are available at all call sites to the given subprogram. Alternatively, we can create several versions of the subprogram (by cloning). In the worst case this could lead to the same code increase as if inlining every subprogram. There are also situations where, although the number of call sites is large, the available constants may be grouped in a much smaller number of equivalence (identity) classes.

3.2 Propagating and Substituting Constants

After the available value sets for array uses are computed, we substitute the uses with constants. Since an array use is often enclosed in a nested loop and it may take different constant values at different iterations, loop unrolling may become necessary in order to substitute the use with constants. For an array use, if its value set only has one array constant and its access region is a subset of the constant region, then this use can be substituted with the constant. Otherwise, loop unrolling is applied to expose this array use when the iteration count is a small constant. Constant propagation is followed by aggressive dead code elimination based on simplified control and data dependences.

4. APPLICATION: ARRAY PRIVATIZATION

Privatization is a crucial transformation which removes memory related dependences (anti- and output dependences) and thus allows the parallelization of loops (among other optimizations). This is achieved by allocating private storage for each iteration instead of reusing it across the iterations. In practice, private storage is allocated per thread, and not per iteration.
Figure 11: Loop parallelization example. Array \( W \) must be privatized. Privatization can be proved at compile-time in (a) and only at run-time in (b).

```
Do j = 1, 1000
  Do i = 1, 10
    \( W(i) = \ldots \)
  EndDo
  \( W_2[1:10] = \eta(...) \)
EndDo
```

(b)

```
Do j = 1, 1000
  Do i = 1, 10
    If (c(i,j))
      \( W_1(i) = \ldots \)
    EndIf
    \( W_2 \cup \cup_{i=1}^{10}(i,j)\#i) = \eta(...) \)
  EndDo
  \( W_2(i) = \ldots \)
EndDo
```

(a)

Figure 12: Algorithm to decide whether a read is covered by a previous write within every iteration of a given loop.

```
Algorithm IsCovered
Input: \([A_0, R_0], \text{ Loop} \)
Output: truth value
Call Search\((A_0, R_0, \text{ LoopBody})\)
\[\rightarrow \{[A_1, R_1], \ldots, [A_n, R_n], [A_0, R_0]\}\]
Return isEmpty\((R_0)\)
```

5. IMPLEMENTATION AND EXPERIMENTAL RESULTS

Our goal is not to prove the profitability of constant propagation and array privatization, but to show that they can be implemented easily using Region Array SSA. The purpose of this section is to show that, in the cases where constant propagation and array privatization can improve the performance of an application, the accurate array dataflow information produced by Region Array SSA makes a significant difference.

We have showed how to use the generic Search algorithm to implement both constant propagation and array privatization. However, in some cases it is not necessary to perform a full search. When performing array privatization, we only need to compute \( R_0 \), which may be much cheaper than computing the whole list of reaching definitions. When propagating constants, it is not necessary to compute the reaching definition sets that cannot originate from a constant assignment. Our implementation takes into account these optimizations.

We implemented (1) Region Array SSA construction, (2) the reaching definition algorithm and (3) array constant collection in the Polaris research compiler [2], while constant substitution was done by hand because our compiler framework lacked some of the necessary infrastructure. We applied constant propagation to four benchmark codes 173 applu, 048 ora, 107.mgrid (from SPEC) and QCD2 (from PERFECT). The speedups were measured on four different machines (Table 1). The codes were compiled using native compilers at O3 optimization level (O4 on the Regatta). 107.mgrid and QCD2 were compiled with O2 on SGI because the codes compiled with O3 did not validate).

In subroutine OBSERV in QCD2, which takes around 22% execution time, the whole array epsilon is initialized with 0 and then six of its elements are reassigned with 1 and -1. The array is used in loop nest OBSERV do2, where much of the loop body is executed only when epsilon takes value 1 or -1. Moreover, the values of epsilon are used in the innermost loop body for real computation. From the value set, we discover that the use is all defined with constant 0, 1 and -1. We manually unrolled the loop OBSERV do2, substituted the array elements with their corresponding values, eliminated If branches and dead assignments and succeeded in removing more than 30% of the floating-point multiplications. Additionally, array ptr is used in loops HIT do1 and HIT do2 after it is initialized with constants in a DATA statement. In subroutine SYSLOP, called from within these two loops, the iteration count of a While loop is determined by the values in ptr. After propagation, we can fully unroll the loop and eliminate several If branches.

In 173 applu, a portion of arrays a, b, c, d is assigned with constant 0.0 in loop JACLD do1 and JACU do1. These arrays are only used in BLTS do1 and BUTS do1 (Fig. 10), which account for 40% of the execution time. We find that the uses in BLTS do1 and BUTS do1 are defined as constant 0.0 in JACLD do1 and JACU do1. Loops BLTS do111 to BLTS do1111 and BUTS do1111 to BUTS do1111 are unrolled. After unrolling and substitution, 35% of the multiplications are eliminated.

In 048 ora, array if is initialized with value 6 and then
### Table 1: Constant propagation results. (a) Experimental setup and (b) Speedup.

<table>
<thead>
<tr>
<th>Machine</th>
<th>Processor</th>
<th>Speed</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel PC</td>
<td>Pentium 4</td>
<td>2.8 GHz</td>
</tr>
<tr>
<td>HP9000/R390</td>
<td>PA-8200</td>
<td>200 MHz</td>
</tr>
<tr>
<td>SGI Origin 3800</td>
<td>MIPS R14000</td>
<td>500 MHz</td>
</tr>
<tr>
<td>IBM Regatta P690</td>
<td>PowerR4</td>
<td>1.3 GHz</td>
</tr>
</tbody>
</table>

(a)

<table>
<thead>
<tr>
<th>Program</th>
<th>Intel</th>
<th>HP</th>
<th>IBM</th>
<th>SGI</th>
</tr>
</thead>
<tbody>
<tr>
<td>QCD2</td>
<td>14.0%</td>
<td>17.4%</td>
<td>12.8%</td>
<td>15.5%</td>
</tr>
<tr>
<td>173.applu</td>
<td>20.0%</td>
<td>4.6%</td>
<td>16.4%</td>
<td>10.5%</td>
</tr>
<tr>
<td>048.ora</td>
<td>1.5%</td>
<td>22.8%</td>
<td>11.9%</td>
<td>20.6%</td>
</tr>
<tr>
<td>107.mgrid</td>
<td>12.5%</td>
<td>8.9%</td>
<td>6.4%</td>
<td>12.8%</td>
</tr>
</tbody>
</table>

(b)

Table 2: Array privatization results. Speedup after automatic parallelization on 1, 2 and 4 processors on an SGI Altix machine. The coverage column shows the percentage of the execution time that could be parallelized only after array privatization. S = at compile time using static analysis, D = at run time using dynamic analysis.

<table>
<thead>
<tr>
<th>Program</th>
<th>Coverage</th>
<th>1 proc</th>
<th>2 procs</th>
<th>4 procs</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADM</td>
<td>46%</td>
<td>44%</td>
<td>-3.5%</td>
<td>78%</td>
</tr>
<tr>
<td>BDNA</td>
<td>32%</td>
<td>0%</td>
<td>-11.9%</td>
<td>71%</td>
</tr>
<tr>
<td>DYFESM</td>
<td>0%</td>
<td>9%</td>
<td>-4.5%</td>
<td>75%</td>
</tr>
<tr>
<td>MDG</td>
<td>4%</td>
<td>91%</td>
<td>-2.0%</td>
<td>91%</td>
</tr>
</tbody>
</table>

Some of its elements are reassigned with constants -2 and -4 before being used in subroutine ABC, which takes 95% of the execution time. The subroutine body is a While loop. The iteration count of the While loop is determined by i1 (there are premature exits). Array a1 is used in ABC after a portion of it is assigned with floating-point constant values. After array constant propagation, the While loop is unrolled and many If branches are eliminated.

107.mgrid was used as a motivating example by previous papers on array constant propagation [30, 24]. Array elements A(1) and C(3) are assigned with constant 0.0 at the beginning of the program. They are used in subroutines RESID and PSINV, which account for 80% of the execution time. After constant propagation, the uses of A(1) and C(3) in multiplications are eliminated.

Table 2 shows the impact of array privatization on the automatic parallelization of major loops in all the applications. In ADM, DYFESM and MDG the privatization problems could be solved only at run time. However, the cost of the run time tests was greatly reduced through partial aggregation of USRs, which led to significant speedups on 2 and 4 processors. The slowdowns on 1 processor are due to the overhead of parallelization and that of run time tests.

### 6. RELATED WORK

Array Data Flow. There has been extensive research on array dataflow, most of it based on reference set summaries: regular sections (rows, columns or points) [4] linear constraint sets [26, 12, 11, 3, 28, 18, 21, 17, 22, 15, 14, 9, 19, 7, 30, 23], and triplet based [13]. Most of these approaches approximate nonlinear references with linear ones [17, 9].

Nonlinear references are handled as uninterpreted function symbols in [22], using symbolic aggregation operators in [23] and based on nonlinear recurrence analysis in [14]. [8] presents a generic way to find approximative solutions to dataflow problems involving unknowns such as the iteration count of a while statement, but limited to intraprocedural contexts. Conditionals are handled only by some approaches (most relevant are [28, 17, 13, 19, 23]). Extraction of run-time tests for dynamic optimization based on data flow analysis is presented in [19, 23].

Array SSA and its use in constant propagation and parallelization. In the Array SSA form introduced by [16, 24], each array assignment is associated a reference descriptor that stores, for each array element, the iteration in which the reaching definition was executed. Since an array definition may not kill all its old values, a merge function φ is inserted after each array definition to distinguish between newly defined and old values. This Array SSA form extends data flow analysis to array element level and treats each array element as a scalar. However, their representation lacks an aggregated descriptor for memory location sets. This makes it in generally impossible to do array data flow analysis when arrays are defined and used collectively in loops. Constant propagation based on this Array SSA can only propagate constants from array definitions to uses when their subscript are all constant. [7, 6] independently introduced Array SSA forms for explicitly parallel programs. Their focus is on concurrent execution semantics, e.g. they introduce π gates to account for the out-of-order execution of parallel sections in the same parallel block. Although [6] mentions the benefits of using reference aggregation they do not implement it.

Array constant propagation can be done without using Array SSA [30, 25]. However, we believe that our Array SSA form makes it easier to formulate and solve data flow problems in a uniform way.

Table 3 presents a comparison of some of the most relevant related work to Region Array SSA. The table shows that RA SSA is the only representation of data flow that is explicit (uses SSA numbering), is aggregated, and can be computed efficiently at both compile-time and run-time even in the presence of nonlinear memory reference patterns. The precision of RA SSA is not as good as that of the other two SSA representations because we lack iteration vector information. However, iteration vectors would become very complex in interprocedural contexts (they must include call stack information), whereas USRs represent arbitrarily large interprocedural program contexts in a scalable way.

### 7. CONCLUSIONS AND FUTURE WORK

We introduced a region based Array SSA providing accurate, interprocedural, control-sensitive use-def information at array region level. Furthermore, when the data flow problems cannot be completely solved statically we can continue the process dynamically with minimal overhead. We used RA SSA to write a compact Reaching Definitions algorithm that breaks up an array use region into subregions corresponding to the actual definitions that reach it. The implementation of array constant propagation and array privatization shows that our representation is powerful and easy to use.
Table 3: Related work on array dataflow and array SSA. CT/RT Nonlinear = able to solve problems involving nonlinear reference patterns at compile time / run time.

8. REFERENCES


