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Full band quantum transport of heterojunction electron devices with empirical pseudopotentials

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Abstract—This paper presents the methodology, implementation and application of a full-band quantum transport model based on the non-equilibrium Green's function formalism and the empirical pseudopotentials. In particular the paper reports the treatment of heterojunctions between lattice matched semiconductors, comprising a gradual transition region described according to a virtual crystal approximation. Our approach entails several numerical techniques to make the full-band quantum transport method computationally affordable, and thus enable robust and efficient self-consistent device simulations.

Then we employ our simulation scheme for the analysis of some exemplary devices based on quantum tunnelling, such as an Esaki tunnelling diode, as well as n- and p-type heterojunction Tunnel FETs. In particular we investigate the influence on the current-voltage characteristics of the width of the heterojunction transition region. We observe that a gradual transition region mainly affects the device characteristics by lengthening the tunnelling path at the heterojunction, which has a different impact on device current depending on the external bias conditions.

Index Terms—Empirical pseudopotential, quantum transport, NEGF, heterojunctions, electronic devices.

I. INTRODUCTION

THE aggressive shrinkage of FETs has effectively turned them into tiny quantum constrictions where quantum transport effects have become important, such as the sourcedrain tunnelling in MOSFETs [1], [2], or the band-to-bandtunnelling (BTBT) in Tunnel FETs [3], [4]. The interface to volume ratio has also steadily increased in 3D device architectures, culminating in CMOS stacked nanowire transistors [5], [6], [7], [8].

The use of heterojunctions is also an important technology option used, among other possible examples, in bipolar transistors [9][10], as well as in Tunnel FETs [11], [12]. Moreover there is clear experimental evidence that fabricated heterojunctions are not abrupt, but instead feature a transition region extending for a few atomic layers [13][14][15], which solicits an atomistic description of the interfaces.

Actually an atomistic approach is nowadays perceived as the frontier of transport modelling, and different efforts have been reported for models based directly on first-principle calculations, which in turn have become available in several *ab-initio* packages [16], [17].

An empirical pseudopotentials method (EPM) provides an interesting compromise between accuracy and computational burden, in that it can accurately and effectively describe both

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bulk and nanostructured semiconductors by using only few fitting parameters [18], [19], [20], [21], [22]. Some recent papers have explored the use of the EPM for transport modelling by using either a quantum transmitting boundary method [23], [24], [25], [26], or a Non Equilibrium Green's Function (NEGF) method [27], [28], [29]. However, no attempts have been reported to our knowledge for the use of such transport methods for electron devices whose operation relies on the heterojunction of different semiconductors.

In this work we report a substantial extension of our recent work related to EPM based quantum transport, and we describe the treatment of lattice matched heterojunctions in nanoscale devices. We here propose a methodology for the description of either abrupt or gradual heterojunctions, where the EPM parameters are changed with the position so as to ensure a correct alignment of conduction and valence band edges along the heterojunction. The NEGF based formalism for quantum transport is also extended in order to duly account for a non homogeneus system. Two exemplary and yet technologically relevant case studies are then addressed, namely a III-V semiconductor based Esaki diode and both n- and p-type heterojunction Tunnel FETs.

The paper is organized as follow. Section II illustrates the self-consistent transport model by focussing on the treatment of heterojunction within the EPM method. In Sec. III we report some simulation case studies for devices employing heterojunctions, and in Sec. IV we finally offer our concluding remarks.

II. MODEL DESCRIPTION

Here we briefly describe the full-band model employed in this work for band-structure and quantum transport calculations, which is based on the empirical pseudopotential method (EPM) [30], [31]. The general methodology has been already described in previous publications [27], [28], so that we here focus on the aspects related to the treatment of heterojunctions that have been explicitly developed for this work.

We used a local formulation of the EPM where, for a diamond semiconductor (e.g. Si or Ge) or a zinc-blende compound (e.g. GaAs, InAs), the spectral components of the pseudopotential can be written as [30], [31]

$$V_L(\mathbf{G}) = U_S(|\mathbf{G}|) \cos[\mathbf{G} \cdot \boldsymbol{\tau}] + iU_A(|\mathbf{G}|) \sin[\mathbf{G} \cdot \boldsymbol{\tau}] \quad (1)$$

where **G** is a reciprocal lattice vector and $\tau = (1/8)a_0(1, 1, 1)$ is the atomic basis vector. The material dependent $U_S(|\mathbf{G}|)$, $U_A(|\mathbf{G}|)$ denote respectively the symmetric and antisymmetric form factors, with $U_A(|\mathbf{G}|)$ being null for diamond materials. It is well known that the full band relation of most technologically relevant semiconductors can be described well by

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using only three non null $U_S(|\mathbf{G}|)$ and $U_A(|\mathbf{G}|)$ form factors, corresponding to $|\mathbf{G}|^2=3, 8, 11 [2\pi/a_0]^2$ for $U_S(|\mathbf{G}|)$ and to $|\mathbf{G}|^2=3, 4, 11 [2\pi/a_0]^2$ for $U_A(|\mathbf{G}|)$ (with a_0 being the lattice constant) [30].

The quantum transport model used in this work relies on the Non Equilibrium Green's Function (NEGF) method formulated in a hybrid basis, $x\mathbf{K}_{yz}$, consisting of a discretized real space in the transport direction x and of plane waves in the (y,z) directions [28]. In the hybrid basis the Hamiltonian matrix inside a given semiconductor takes the block tridiagonal form

$$\begin{bmatrix} \mathbf{H}(x_1, x_1) & \mathbf{H}_{0,1} & 0 & 0 & \cdots \\ \mathbf{H}_{0,1}^{\dagger} & \mathbf{H}(x_2, x_2) & \mathbf{H}_{0,1} & 0 & \cdots \\ \vdots & \vdots & \vdots & \vdots \\ 0 & 0 & \mathbf{H}_{0,1}^{\dagger} & \mathbf{H}(x_j, x_j) & \mathbf{H}_{0,1} & 0 & \cdots \\ \vdots & \vdots & \vdots & \vdots & \vdots \\ \end{bmatrix}$$

where x_j is a discretization point in the transport direction. For a 3D electron gas (i.e. no quantum confinement in directions y, z) the size of each blocks in Eq. (2) is $M_{3D} = 2N_G/N_d$, where N_G is the number of **G** vectors in EPM calculations (which is in turn set by the EPM cutoff energy), and N_d is the number of discretization points inside a unit cell. For a 2D or a 1D electron gas the Hamiltonian matrix still has the tridiagonal form in Eq. (2), but the size of the blocks is increased [28]. In all the calculations of this work we used N_d =32, and an EPM cutoff energy E_{cut} = 4.5 Ry corresponding to $N_G \approx 800$.



Fig. 1. Pictorial illustration of the blocks of the Hamiltonian matrix across a gradual heterojunction where, in order to simplify the notation, one end of the heterojunction has been placed at x=0. W_{HJ} is the width of the heterojunction transition region and $d=a_0/N_d$ is the discretization step along x. The notation is consistent with Eq.2. In the heterojunction region the EPM parameters change with the position and thus contribute to the position dependence of the diagonal Hamiltonian blocks, $H^{HJ}(x_i, x_i)$.

A. Modelling of heterojunctions with the EPM method

For a homogeneous semiconductor the diagonal blocks $\mathbf{H}(x_j, x_j)$ in Eq. (2) depend on x_j only through the local pseudopotential $V_L(x_j, \mathbf{G}_{yz} - \mathbf{G}'_{yz})$, which is obtained via a discrete Fourier transform from the G_x to the x_j and it is periodic of a_0 [28]. In this work we studied lattice matched heterojunctions by using a virtual crystal approximation [32], whose soundness has been proved also for quaternary alloys [33]. In this approach the EPM parameters $U_S(|\mathbf{G}|), U_A(|\mathbf{G}|)$ change along the transport direction so as to describe either

an abrupt heterojunction, or a gradual heterojunction having a transition width, W_{HJ} , long up to a few lattice constants [34]. More precisely, the Hamiltonian blocks of the two materials are first computed in the hybrid space $x\mathbf{K}_{yz}$, then they are patched at adjacent positions to form the heterojunction. This approach implies that at the heterojunction interface a discontinuity exists in the empirical pseudopotential. For the case at study in this paper, however, such a discontinuity is small because GaSb and InAs have similar EPM spectral components (see Tab. I) The coupling Hamiltonian matrix between adjacent blocks is simply given by the spatial discretization of the kinetic energy operator [28], hence it is material independent.

TABLE IEPM parameters in Ry for GaSb and InAs employed in thiswork. $U_S(0)$ is set to 0 for InAs and to 0.84 eV for GaSb.

	$U_S(3)$	$U_S(8)$	$U_{S}(11)$	$U_A(3)$	$U_A(4)$	$U_A(11)$
GaSb	-0.22	0	0.05	0.06	0.05	0.01
InAs	-0.22	0	0.05	0.08	0.05	0.03



Fig. 2. (Left) Highest valance bands and lowest conduction bands for GaSb and InAs along the main symmetry points inside the first Brillouin zone and (right) the corresponding density of states.

In the description of heterojunctions via the EPM method the correct alignment of conduction and valence band edges between the different materials must be ensured. At this regard, Eq. (1) shows that a non zero $U_S(0)=U_S(|\mathbf{G}|=0)$ simply results in a rigid shift of V_L and thus of the bandstructure of a given material. Consequently, in the analysis of a homogeneous system one typically sets $U_S(0)=0$, because $U_S(0)$ can be absorbed in the choice of the energy reference.

In our modelling of heterojunctions, however, $U_S(0)$ has been changed in order to ensure the correct alignment of the conduction and valence band edges of the constituent materials, which is an approach already discussed and validated in [35]. For devices including heterojunctions the position dependence of the EPM parameters contributes to the x_j dependence of the diagonal Hamiltonian blocks $\mathbf{H}(x_j, x_j)$ in Eq. (2), as it is illustrated in the sketch of Fig. 1.

Tab. I reports the EPM parameters for GaSb and InAs used in all the calculations of this work, and Fig. 2 illustrates



Fig. 3. (a) Electron effective mass m^* at the Γ minimum point of the conduction band versus the molar fraction x of the alloy $GaSb_{1-x}InAs_x$. (b) Top of valence band, bottom of conduction band and band-gap versus the molar fraction. The energy reference is the top of the InAs valence band.

the bandstructure of the two materials, as well as the corresponding density of states. As it can be seen, the GaSb-InAs heterojunction provides a broken bandgap system, in the sense that the bottom of the InAs conduction band is slightly below the top of the GaSb valence band. By varying the molar fraction x in GaSb_{1-x}InAs_x the corresponding effective mass at the Γ minimum of the conduction band change as reported in Fig. 3

B. Quantum transport

Our transport model is based on the NEGF method, where the transport equations are self-consistently solved with the Poisson equation for the overall device electrostatics. For ballistic transport the retarded Green's function can be formally defined as $\mathbf{G}_r(E) = [E\mathbf{I} - \mathbf{H} - \boldsymbol{\Sigma}_L - \boldsymbol{\Sigma}_R]^{-1}$, where **H** is the Hamiltonian matrix in Eq. (2), and $\boldsymbol{\Sigma}_{R/L}$ are the left and right contact self-energies. Because only a small subset of the blocks of $\mathbf{G}_r(E)$ are needed to calculate the carrier density and the current, we here employed the recursive Green's function algorithms based on Dysons's equations [36], which make the size of the Hamiltonian blocks the crucial parameter for the computational burden.

In this latter respect, we could obtain a substantial reduction of the size of such blocks by employing a mode-space transformation [37]. Differently from Ref.[28], the modes along the heterojunction direction change with the position x_j , which we duly accounted for in the transformation of the Hamiltonian to the mode space.

In the case studies of this work we found that, by selecting only the lowest energy transverse modes in each device section, the size of the Hamitonian blocks could be vastly reduced compared to the blocks in Eq. (2). Even in heterojunction devices, in fact, a few tens of modes were sufficient to achieve results that agree within a few percent with those obtained without introducing the mode space reduction.

III. Self-consistent device simulations

In this section we report exemplary simulation results obtained with the EPM based simulation methodology developed in this work for two band-to-band-tunnelling (BTBT) based devices employing a GaSb-InAs heterojunction. In all simulations the NEGF transport equations were solved selfconsistently with the Poisson equation describing the device electrostatics. All simulations were carried at room temperature and for the transport direction [100].



Fig. 4. (Top) Sketch of the Esaki diode where red is used for InAs and blue is used for GaSb, and (bottom) corresponding current density, J_D , versus the applied bias V_D , for different widths, W_{HJ} , of the heterojunction transition region. The inset shows a focus on the negative differential resistance region.



Fig. 5. (Left) Conduction and valence band profile along the Esaki diode for different widths W_{HJ} of heterojunction transition region. The source Fermi level, $E_{f,n}$ in the *n* region is taken as the energy reference. (Right) Corresponding current spectrum at $k_y = k_z = 0$, J_D , versus the energy and for different W_{HJ} . The bias is $V_D = 0.1V$

A. Esaki tunnelling diode

The Esaki diode is sketched in Fig. 4 and consists of a *p*-type GaSb region and an *n*-type InAs region. The doping of the *p* and *n*-type regions was set to respectively $N_A=10^{20}$ cm⁻³ and $N_D=3 \times 10^{19}$ cm⁻³, which result in a degenerate carrier gas in both regions. We assumed periodic boundary conditions along the transverse directions and sampled the wave vectors k_y and k_z with a constant step of $\Delta k_y = \Delta k_z = 0.05 \ 2\pi/a_0$.

The current per unit area, I_D , versus the applied bias V_D of the Esaki diode is shown Fig. 4 for different widths, W_{HJ} , of the heterojunction transition region. In the forward bias a negative differential resistance (NDR) is observed, which is more clearly illustrated in the inset of the figure. As it can be seen the peak current in the NDR region decreases for more gradual heterojunctions (i.e. for larger W_{HJ}), and it is maximum for the abrupt case. A similar dependence of I_D on W_{HJ} is observed also in the reverse bias region, where the current is still completely dominated by BTBT in the bias range explored in Fig. 4.

Fig. 5 illustrates the conduction and valence band profiles along the device for V_D =0.1 V (i.e. approximately at the peak current in the NDR region), as well as the energy resolved current density J_D . As expected the J_D is practically relevant only in the narrow Fermi energy window identified by the Fermi levels $E_{f,n}$ and $E_{f,p}$ of respectively the *n*- and *p*region. Moreover, the narrower the transition region W_{HJ} is the shorter is the BTBT path, which in turn leads to the corresponding I_D increase in Fig. 4.

TABLE II DEVICE PARAMETERS FOR THE HETEROJUNCTION *n*-TYPE TUNNEL FET SIMULATED IN FIG. 6. T_{ch} , L_G , $L_{S/D}$, N_A , N_D denote the channel THICKNESS, THE GATE LENGTH, THE SOURCE/DRAIN LENGTH AND THE DOPANT CONCENTRATIONS IN THE SOURCE AND DRAIN REGION, RESPECTIVELY.

	T_{ch}	L_G	$L_{S/D}$	N_A	N_D
	[nm]	[nm]	[nm]	[cm ⁻³]	$[cm^{-3}]$
GaSb/InAs	3.0	16	14	10^{20}	10^{19}

B. Ultra-thin body, heterojunction Tunnel FETs

Nanoscale FETs are often fabricated by using ultra-thin semiconductor films or nanowires, which can induce significant quantum confinement effects in the resulting low dimensional electron gas, as it is the case for the Tunnel FETs (TFETs) simulated in this section. The modelling of quantum confinement in our EPM based formalism has been described in Sec.II.A of reference [28], and it is based on a pseudo-oxide material. The EPM parameters of the pseudooxide are tailored so as to obtain the appropriate discontinuity at the semiconductor-oxide interface of the conduction band minimum and valence band maximum, which in turn confine the carriers in the semiconductor region. For both the n- and the *p*-type TFETs we used a physical oxide thickness of 1.5 nm and a relative dielectric constant $\kappa = 10$, corresponding to an equivalent oxide thickness of 0.6 nm. The transverse wave vector k_y was sampled with a constant step of $\Delta k_y = 0.05 \ 2\pi/a_0$.



Fig. 6. (Top) Sketch of the simulated *n*-type HJ TFET. (Middle) Conduction and valence band profile at different V_{GS} and Fermi level in the source region $E_{f,S}$ and drain region $E_{f,D}$ for a abrupt heterojunction GaSb-InAs *n*-type tunnel FET. (Bottom) Drain-current versus gate-voltage characteristics at V_{DS} =0.3 V for different widths, W_{HJ} , of the heterojunction transition region. The metal gate workfunction was set to 4.625 eV.

In this work we used the pseudo-oxide EPM parameters proposed in [28], which result in a conduction and valence band discontinuity between InAs and the pseudo-oxide of respectively 3.8 eV and 4.5 eV.

A sketch of the *n*-type heterojunction Tunnel FET is shown in Fig. 6 and the main device parameters are reported in Tab. II. Fig. 6 also reports the current versus gate voltage characteristics of the TFET for different widths, W_{HJ} , of the GaSb-InAs heterojunction and for $V_{DS}=0.3$ V. For both the



Fig. 7. Local density of states, LDOS, for the T-FET simulated in Fig.6. (a),(c) report the LDOS in the off state for $V_{GS}=0.0V$ and $V_{DS}=0.3V$; (b),(d) report the LDOS in the on state for $V_{GS}=0.4V$ and $V_{DS}=0.3V$. Plots in (a) and (b) correspond to an abrupt heterojunction (i.e. $W_{HJ}=0$), while plots in (c) and (d) correspond to $W_{HJ}=3a_0$.

n- and *p*-type TFET the metal gate workfunction was adjusted so that the off current (i.e. the I_D at $V_{GS} = 0$ V and $V_{DS} = 0.3$ V) is approximately 1 nA/ μ m. The transistor exhibits a very steep sub-threshold region with a sub-threshold swing below 60 mV/dec, which is an expected result for a device with no defects and with an semiconductor-oxide interface free of traps and microscopic roughness [38], [39].

Quite interestingly the width W_{HJ} of the GaSb-InAs heterojunction has no sizeable influence on the subthreshold current and swing but it does affect the on current, which is in fact degraded when the transition region is wider. Fig. 7 shows that the markedly different influence of W_{HJ} on the off and on current is again related to its modulation of the band-to-bandtunnelling path. In the off-state, in fact, the impact of W_{HJ} on the relatively long BTBT path is only modest, whereas in the on state the relative importance of W_{HJ} is larger because the tunnelling path is much shorter.

 TABLE III

 Device parameters for the heterojunction p-type Tunnel FET

 SIMULATED IN FIG. 8.

	T_{ch}	L_G	$L_{S/D}$	N _A	N_D
	[nm]	[nm]	[nm]	[cm ⁻³]	[cm ⁻³]
InAs/GaSb	3.7	16	14	5×10^{19}	2×10^{19}

By leveraging the inherently full-band nature of our EPM based modelling approach, we also investigate a *p*-type heterojunction Tunnel FET, whose sketch is shown in Fig. 8 and whose parameters are reported in Tab. III. Due to the fairly low conduction band density of states of InAs, the source doping $N_D=2 \times 10^{19}$ cm⁻³ is large enough to induce a strongly degenerate electron gas in the source. Such a doping concentration is fairly consistent with the value recommended in Ref. [40], which was the outcome of an optimization of the tunnelling junction operation. The current-voltage characteristics in Fig. 8 show that the *p*-type Tunnel FET can hardly achieve a sub-threshold swing below 60 mV/dec, with a degraded minimum swing compared to the *n*-type counterpart



Fig. 8. (Top) Sketch of the simulated *p*-type HJ TFET. (Middle) Conduction and valence band profile at different V_{GS} for a abrupt HJ GaSb-InAs *p*type tunnel FET. (Bottom) Drain-current versus gate-voltage characteristics for different widths, W_{HJ} , of the heterojunction transition region. The metal gate workfunction was set to 4.665 eV.

in Fig. 6. This result is not unexpected because the low density of states in the source region leads to a relatively large screening length of the electric field produced by the gate bias (compared to the p-type source of the n-type Tunnel FET) [41], which in turn degrades the sub-threshold swing [4].

As for the influence of the width W_{HJ} of the heterojunction transition region on the current-voltage characteristic of the device, the main features observed for the *n*-type Tunnel FET are confirmed. Namely, W_{HJ} hardly affects the subthreshold region, where the tunnelling distance is substantially larger than W_{HJ} , whereas the on current of the device is progressively reduced for increasing W_{HJ} . In the on state, in fact, a gradual transition region for the heterojunction results in a sizeable lengthening of the tunnelling path.

IV. CONCLUSION

We have presented the implementation and application of a full band quantum transport methodology for nanoscale electron devices with heterojunctions. The model was developed in the framework of the empirical pseudopotentials method, and the heterojunctions were described according to a virtual crystal approximation, which allowed us to investigate the influence on the device characteristics of a gradual interface between the constituent materials.

We then leveraged the inherently full-band nature of the model and reported results for an *n*- and a *p*-type, ultra-thin body heterojunction Tunnel FET, as well as for an Esaki diode.

The interface at the heterojunction was varied from the abrupt case to a more realistic transition width extending up to a few unit cells. The tunnelling current in the Esaki diode was found to be sensitive to the width of the heterojunction both in reverse bias and in the negative resistance region. In the GaSb-InAs Tunnel FETs the transition width affects the current only in the on-state. In the sub-threshold region, in fact, the effective tunnelling path is much larger than W_{HJ} and thus it is not appreciably modulated by heterojunction width.

The results of this work qualify our methodology based on the empirical pseudopotentials and the NEGF formalism as a viable approach for the modelling and design of homojunction and heterojunction nanoscale electron devices.

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