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### Conductive thermal diode based on two phase-change materials

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#### Abstract

The thermal rectification of a conductive thermal diode made up of two phase-change materials, whose thermal conductivities significantly change within a narrow interval of temperatures, is theoretically studied and optimized. This is done by deriving analytical expressions for the temperature profiles, heat fluxes and rectification factor. An optimal rectification factor of 60% is obtained for a thermal diode operating with terminals of VO<sub>2</sub> and Polyethylene with a temperature difference of 250 K spanning the metal-insulator transition of both materials. It is shown that this high rectification of a conductive thermal diode could be maximized even more by increasing the thermal conductivity variations of both diode terminals. The obtained results can thus be useful to guide the development of phase-change materials capable of optimizing the rectification of conductive heat fluxes.

Keywords: Thermal rectification, conductive thermal diode, phase-change materials.

#### 1. INTRODUCTION

Thermal diodes allowing the heat exchange between two terminals in a given direction and blocking it in the opposite one, have attracted significant attention over the past decade, due to their potential applications in heat control [1, 2, 3]. This thermal rectification is analogous to the electrical one driven by electronic diodes and has been studied through thermal diodes operating with photons [4, 5, 6, 7], phonons [8, 9, 10, 11?, 3], electrons [12] and quantum structures [13, 14]. This phenomenon was first experimentally observed by Starr [15] at the interface between copper/cuprous oxide and then observed in carbon nanotubes structures [16, 17], dielectric quantum dots [18], phase-change materials (PCMs) [19, 20, 21, 22] and bulk materials [23].

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Thermal rectification of conductive heat fluxes is generally generated by the temperature dependence of the thermal conductivities of the diode terminals [24] and therefore it can, in principle, be achieved with arbitrary solid materials subjected to a sufficiently large temperature gradient. Based on the Fourier's law of heat conduction, Yang et al. [25] modeled and optimized the rectification ratio of a conductive thermal diode made up of two terminals, whose thermal conductivities follow either a linear or a quadratic dependence on temperature. For the linear variation, they obtained a maximum rectification ratio of 0.5, which increases to 0.86, for the quadratic dependence. These relatively high rectification ratios required a temperature difference greater than 100 K between the diode terminals, which could be difficult and expensive to achieve in practice. This drawback can be overcome by means of PCMs, such as vanadium dioxide  $(VO_2)$ , nitinol, and polyethylene (PE), whose thermal conductivities strongly change in a relatively narrow interval of temperatures [26, 27, 28, 29, 30, 31, 32]. These PCMs with a strong phonon-electron correlation have recently attracted a lot of experimental and theoretical interest, due to their metal-insulator transition (MIT) at temperatures close to room temperature. For instance, the thermal conductivities of  $VO_2$  and PE typically change within a relatively small ranges of temperature 340 K < T < 345 K and 395 K < T < 400K, respectively [33, 34]. The fact that the MIT of VO<sub>2</sub> occurs in a temperature range of only 5 K has motivated its application in combination with a non-PCM, as the terminal of a conductive thermal diode, however, the maximal rectification factor obtained was lower ( $\sim 20\%$ ) [34] than that reported by Yang et al. [25] This indicates that the operation of a conductive thermal diode with both terminals made up of PCMs could enable not only to maximize its rectification factor, but also to reduce the temperature difference between its terminals, as was recently proposed [35]. Based on an alternative definition of the rectification factor, Hyungmook et al. [36] recently determined a rectification factor of 147% for a thermal diode with the terminals of  $VO_2$  and PE, whose thermal conductivities significantly change with temperatures. This relatively high rectification was obtained by describing the thermal conductivities of both PCMs as step functions, which is similar but not equal to their real behaviors, as the phase transitions of  $VO_2$  and PE occur in a temperature interval greater than 5 K. Therefore, a more realistic description of the thermal conductivities of the rectification of thermal diodes based on two PCMs is thus required.

In this work, we theoretically study the optimization of the rectification factor of a conductive thermal diode made up of a junction of  $VO_2$  and PE. This is done by taking into account the temperature dependence of the thermal conductivities of these PCMs within their phase transitions. Simple analytical expressions for the temperature profiles, heat fluxes and optimal rectification factor are derived and analyzed for the steady-state heat conduction. The present study is thus based on the combination of two PCMs, which are expected to yield higher rectification factors than a thermal diode based on a single PCM, which we addressed in a previous work *et al.* [34].



Figure 1: Scheme of a dual PCMs conductive thermal diode operating in the (a) forward and (b) backward configurations. Hot and cool thermal shells are set at the temperatures  $T_h$  and  $T_c$ , respectively.

#### 2. THEORETICAL MODELING

Let us consider a system of two phase change materials exchanging heat through steady-state conduction as a result of their temperature difference  $T_h - T_c > 0$ , as illustrated in Figs. 1(a) and 1(b), respectively. In the forward configuration [Fig 1(a)], the heat flux  $q_F$  flows from PCM1 to PCM2, while in the backward one [Fig 1(b)], the heat flux  $q_B$  occurs in the opposite direction, as a result of the interchange of the temperatures  $T_h$  and  $T_c$ . The values of  $q_F$  and  $q_B$  are driven by the thermal conductivities of  $\kappa_1(T)$  of PCM1 and  $\kappa_2(T)$  of PCM2, and are not expected to be equal  $(q_F \neq q_B)$ , due of the temperature dependence of  $\kappa_1(T)$  and  $\kappa_2(T)$ . For temperatures within the MIT of VO<sub>2</sub> and PE. Therefore, the asymmetry of  $\kappa_n(T)$  around its transition temperature  $T_{0n}$ , with n = 1, 2, is much larger. It allows optimizing the heat flux difference  $q_F - q_B$  by setting the terminals' temperatures as follows:  $T_c < T_{0n} < T_h$ . The ability of this temperature-controlled thermal diode to rectify heat fluxes can thus be defined by the following rectification factor R [34]

$$R = \frac{|q_F - q_B|}{\max(q_F, q_B)}.\tag{1}$$

Equation (1) establishes that a nonzero rectification is achieved when the reversal of the temperature gradient induces not only the inversion of the heat flux direction, but also a significant variation of the heat flux magnitude. Optimal rectification (R = 1) is reached when one of the heat fluxes is zero.



Figure 2: Thermal conductivities of  $VO_2$  and PE, as functions of the temperature within their corresponding MIT. Dots and triangles stand for experimental data reported in the literature [37, 33], while lines represent the theoretical predictions of Eq. (3).

#### 2.0.1. Forward configuration

According to the Fourier's law of heat conduction and the principle of energy conservation, the steadystate heat flux  $q_F$  in the forward configuration of the thermal diode shown in Fig 1(a), is given by

$$q_F = -\kappa_1(T_1)\frac{dT_1}{dx} = -\kappa_2(T_2)\frac{dT_2}{dx},$$
(2)

where  $T_1(x)$  and  $T_2(x)$  are the temperatures within the PCM1 ( $0 \le x \le L_1$ ) and PCM2 ( $L_1 \le x \le L_1 + L_2$ ), respectively. In this work, we consider that the thermal conductivities  $\kappa_1(T)$  and  $\kappa_2(T)$  belong to VO<sub>2</sub> and PE, respectively, and their temperature variations are shown in Fig. 2, within their MITs. Note that the thermal conductivity contrast of PE (25/5 = 5) is higher than that of VO<sub>2</sub> (6/3.6 = 1.7), such that they reach their maximum thermal conductivities for temperatures T < 395 K and T > 345 K, respectively. This fact along with Eq. (2), indicates that  $q_F$  can be maximized by properly setting the temperatures  $T_h$  and  $T_c$  to fulfill these temperature conditions.

The thermal conductivities of both  $VO_2$  and PE (Fig. 2) can be described by

$$\kappa_n(T) = \kappa_{in} + \frac{\kappa_{mn} - \kappa_{in}}{1 + e^{-\beta_n(T - T_{0n})}},\tag{3}$$

where  $\kappa_{mn}$  and  $\kappa_{in}$  are the thermal conductivities in their respective high- and low-temperature phases, and  $\beta_n$  is the phase-transition slope of  $\kappa_n(T)$  at the transition temperature  $T = T_{0n}$ , for n = 1 and 2. The

values of these four parameters were determined by fitting Eq. (3) to the experimental data shown in Fig. 2 for VO<sub>2</sub> and PE, and they are summarized in Table I.

Table 1: Material properties involved in Eqs. (3).				
Material	$\kappa_{in}(\mathrm{Wm}^{-1}\mathrm{K}^{-1})$	$\kappa_{mn}(\mathrm{Wm}^{-1}\mathrm{K}^{-1})$	$\beta_n(\mathbf{K}^{-1})$	$T_{0n}(\mathbf{K})$
VO <sub>2</sub>	3.6	6	1.7	342.3
PE	25	5	2.2	397.4

The temperatures  $T_1(x)$  and  $T_2(x)$  can now be determined by integrating Eq. (2) for the thermal conductivities in Eq. (3), which yields

$$T_n(x) - T_{0n} + \frac{1}{\beta_n \gamma_n} \ln\left[1 + M_n(T_n(x))\right] = \frac{C_n}{\kappa_{mn}} - \frac{q_F}{\kappa_{mn}} x,$$
(4)

where  $C_n$  is an integration constant,  $\gamma_n = \kappa_{mn}/(\kappa_{mn} - \kappa_{in})$ , and  $M_n(T) = \exp[-\beta_n(T - T_{0n})]$ . The values of the three parameters  $q_F$ ,  $C_1$  and  $C_2$  are determined by the boundary conditions  $T_1(0) = T_h$ ,  $T_2(L_1 + L_2) = T_c$ , and  $T_1(L_1) - T_2(L_1) = \rho q_F$ , with  $\rho$  being the interface thermal resistance between the PCM1 and PCM2. Equation (4) thus yields the following expressions for the heat flux and temperature profiles

$$q_F = \frac{1}{\alpha_1} \ln \left[ \frac{Z_1(T_h)}{Z_1(T_1(L_1))} \right],$$
(5a)

$$q_F = \frac{1}{\alpha_2} \ln \left[ \frac{Z_2(T_1(L_1) - \rho q_F)}{Z_2(T_c)} \right],$$
(5b)

$$Z_1(T_1(x)) = \left[Z_1(T_h)\right]^{1-x/L_1} \left[Z_1(T_1(L_1))\right]^{x/L_1},$$
(5c)

$$Z_2(T_2(x)) = \left[Z_2(T_1(L_1) - \rho q_F)\right]^{1 - (x - L_1)/L_2} \left[Z_2(T_c)\right]^{(x - L_1)/L_2},$$
(5d)

where  $\alpha_n = \beta_n \gamma_n \rho_{mn}$ ,  $\rho_{mn} = L_n / \kappa_{mn}$ , and  $Z_n(T) = [1 + M_n(T)] / [M_n(T)]^{\gamma_n}$ . The substitution of Eq. (5a) into Eq. (5b) results in a strong non-linear dependence of the temperature  $T_1(L_1) = \theta$  on  $T_h$  and  $T_c$ , as follows:

$$\left(\frac{Z_1(T_h)}{Z_1(\theta)}\right)^{\frac{\alpha_2}{\alpha_1}} = \frac{Z_2\left(\theta - \frac{\rho}{\alpha_1}\ln\left[\frac{Z_1(T_h)}{Z_1(\theta)}\right]\right)}{Z_2(T_c)}.$$
(6)

After numerically solving Eq. (6) for the interface temperature  $\theta$ , the forward heat flux and temperature profiles can be determined through Eqs. (5b)-(5d), for a given set of temperatures  $T_h$  and  $T_c$ .

#### 2.0.2. Backward configuration

The backward heat flux  $(q_B)$  and the temperatures  $[T_1(x) \text{ and } T_2(x)]$  for the backward configuration [Fig. 1(b)], can be determined by following a similar approach than the one developed for the forward

configuration. The appropriate boundary conditions are  $T_2(0) = T_h$ ,  $T_1(L_1+L_2) = T_c$ , and  $T_2(L_2)-T_1(L_2) = \rho q_B$ . The final results are

$$q_B = \frac{1}{\alpha_1} \ln \left[ \frac{Z_1(T_1(L_2))}{Z_1(T_c)} \right],$$
(7a)

$$q_B = \frac{1}{\alpha_2} \ln \left[ \frac{Z_2(T_h)}{Z_2(T_1(L_2) + \rho q_B)} \right],$$
(7b)

$$Z_1(T_1(x)) = \left[Z_1(T_1(L_2))\right]^{1-(x-L_2)/L_1} \left[Z_1(T_c)\right]^{(x-L_2)/L_1},$$
(7c)

$$Z_2(T_2(x)) = \left[Z_2(T_h)\right]^{1-x/L_2} \left[Z_2(T_1(L_2) + \rho q_B)\right]^{x/L_2}.$$
(7d)

The combination of Eqs. (7a) and (7b) yields the following transcendental equation for the interface temperature  $T_1(L_2) = \phi$ , as a function of  $T_h$  and  $T_c$ :

$$\left(\frac{Z_1(\phi)}{Z_1(T_c)}\right)^{\frac{\alpha_2}{\alpha_1}} = \frac{Z_2(T_h)}{Z_2\left(\phi + \frac{\rho}{\alpha_1}\ln\left[\frac{Z_1(\phi)}{Z_1(T_c)}\right]\right)}.$$
(8)

The numerical solution of Eq. (8) for  $\phi$ , fully describes the heat conduction in the thermal diode through Eqs. (7b)-(7d).

#### 2.0.3. Rectification factor

In order to maximize  $q_F$  and minimize  $q_B$  for optimizing the rectification factor R in Eq. (1), we have conveniently rewrite Eqs. (5a), (5b), (7a), and (7b), as follows:

$$q_F = \frac{T_h - \theta}{\rho_{m1}} + \frac{(\rho_{m1})^{-1}}{\beta_1 \gamma_1} \ln\left[\frac{1 + e^{-\beta_1(T_h - T_{01})}}{1 + e^{-\beta_1(\theta - T_{01})}}\right],\tag{9a}$$

$$q_F = \frac{\theta - T_c}{\rho + \rho_{m2}} + \frac{(\rho + \rho_{m2})^{-1}}{\beta_2 \gamma_2} \ln\left[\frac{1 + e^{\beta_2(T_{02} + \rho_q_F - \theta)}}{1 + e^{\beta_2(T_{02} - T_c)}}\right],\tag{9b}$$

$$q_B = \frac{\phi - T_c}{\rho_{m1}} - \frac{(\rho_{m1})^{-1}}{\beta_1 \gamma_1} \ln \left[ \frac{1 + e^{\beta_1 (T_{01} - T_c)}}{1 + e^{\beta_1 (T_{01} - \phi)}} \right],\tag{9c}$$

$$q_B = \frac{T_h - \phi}{\rho + \rho_{m2}} - \frac{(\rho + \rho_{m2})^{-1}}{\beta_2 \gamma_2} \ln \left[ \frac{1 + e^{-\beta_2(\phi - T_{02} + \rho q_B)}}{1 + e^{-\beta_2(T_h - T_{02})}} \right].$$
(9d)

Equations (9a) and (9b) establishes that when the temperature  $T_h$  ( $T_c$ ) is high (low) enough for fulfilling the conditions  $\beta_1(T_h - T_{01}) \gg 1$ ,  $\beta_1(\theta - T_{01}) \gg 1$ ,  $\beta_2(T_{02} - T_c) \gg 1$ , and  $\beta_2(\rho q_F + T_{02} - \theta) \gg 1$ , they reduce to

$$q_F = \frac{T_h - \theta}{\rho_{m1}} = \frac{\theta - T_c}{\rho + \rho_{i2}},\tag{10}$$

where  $\rho_{i2} = L_2/\kappa_{i2}$  and  $\rho_{m1} = L_1/\kappa_{m1}$ . Equation (10) explicitly indicates that, under these conditions, the whole terminal 1 (PCM1) is in its high-temperature (metallic) phase, while the entire terminal 2 (PCM2) is in its low-temperature one. According to Fig. 2, both of these phases are characterized by the highest thermal conductivities of PCM1 and PCM2, and therefore they maximize the heat flux  $q_F = q_{F,\text{max}}$  determined by

Eq. (10), as follows

$$q_{F,\max} = \frac{T_h - T_c}{\rho_{m1} + \rho + \rho_{i2}},$$
(11a)

$$T_{1,\max}(L_1) = \frac{(\rho + \rho_{i2})T_h + \rho_{m1}T_c}{\rho_{m1} + \rho + \rho_{i2}},$$
(11b)

$$T_{2,\max}(L_1) = \frac{\rho_{i2}T_h + (\rho_{m1} + \rho)T_c}{\rho_{m1} + \rho + \rho_{i2}},$$
(11c)

where  $T_1(L_1) = T_{1,\max}(L_1)$  and  $T_2(L_1) = T_{2,\max}(L_1)$  are the interface temperatures associated to  $q_{F,\max}$ . On the other hand, Eqs. (9c) and (9d) show that when the temperature  $T_h(T_c)$  is high (low) enough for satisfying the conditions  $\beta_1(T_{01} - T_c) \gg 1$ ,  $\beta_1(T_{01} - \phi) \gg 1$ ,  $\beta_2(T_h - T_{02}) \gg 1$ , and  $\beta_2(\rho q_B + \phi - T_{02}) \gg 1$ , they turn into

$$q_B = \frac{\phi - T_c}{\rho_{i1}} = \frac{T_h - \phi}{\rho + \rho_{m2}},$$
(12)

where  $\rho_{i1} = L_1/\kappa_{i1}$  and  $\rho_{m2} = L_2/\kappa_{m2}$ . Equation (12) explicitly points out that, under these conditions, the whole terminal 1 (PCM1) is in its low-temperature (insulating) phase, while the entire terminal 2 (PCM2) is in its high-temperature one. According to Fig. 2, both of these phases correspond to the lowest thermal conductivities of PCM1 and PCM2, and hence they minimize the heat flux  $q_B = q_{B,\min}$  given by Eq. (12), as follows

$$q_{B,\min} = \frac{T_h - T_c}{\rho_{i1} + \rho + \rho_{m2}},$$
(13a)

$$T_{1,\min}(L_2) = \frac{\rho_{i1}T_h + (\rho + \rho_{m2})T_c}{\rho_{i1} + \rho + \rho_{m2}},$$
(13b)

$$T_{2,\min}(L_2) = \frac{(\rho_{i1} + \rho)T_h + \rho_{m2}T_c}{\rho_{i1} + \rho + \rho_{m2}},$$
(13c)

where  $T_1(L_2) = T_{1,\min}(L_2)$  and  $T_2(L_2) = T_{2,\min}(L_2)$  are the interface temperatures related to  $q_B = q_{B,\min}$ . According to Eqs. (1), (11a) and (13a), the optimal rectification factor  $R_{\text{opt}} = 1 - q_{B,\min}/q_{F,\max}$  is then determined by

$$R_{\text{opt}} = 1 - \frac{\rho_{m1} + \rho + \rho_{i2}}{\rho_{i1} + \rho + \rho_{m2}}$$

$$= 1 - \frac{\kappa_{i1}}{\kappa_{m1}} \frac{(1 + \rho/\rho_{m1})r + \kappa_{m1}/\kappa_{i2}}{(1 + \rho/\rho_{i1})r + \kappa_{i1}/\kappa_{m2}}.$$
(14)

where  $r = L_1/L_2$ . Note that  $R_{opt}$  is symmetric on the thermal resistances of both PCMs and increases with the thermal conductivity contrasts  $\kappa_{m1}/\kappa_{i1}$  and  $\kappa_{i2}/\kappa_{m2}$  between the metallic and insulator phases of the PCMs. In contrast to the optimal rectification factor of a conductive thermal diode operating with a single PCM [34],  $R_{opt}$  in Eq. (14) does depend on the interface thermal resistance  $\rho$  between the terminals, such that lower values of  $\rho$  yield higher values of  $R_{opt}$ . The optimization of the rectification factor of heterojunction based on two PCMs thus requires high values of  $\kappa_{m1}/\kappa_{i1}$  and  $\kappa_{i2}/\kappa_{m2}$  and low ones of  $\rho$ .



Figure 3: Rectification factor, forward and backward heat fluxes of the conductive thermal diode of VO<sub>2</sub> and PE, as functions of the temperature  $T_h$ . Calculations were done for  $T_c = 300$  K and  $\rho = 0$  mm<sup>2</sup>KW<sup>-1</sup>.

#### 3. RESULTS AND DISCUSSION

Figure 3 shows the temperature evolution of the forward  $(q_F)$  and backward  $(q_B)$  heat fluxes of a conductive thermal diode operating with terminals of VO<sub>2</sub> and PE. Note that both  $q_F$  and  $q_B$  exhibit a nearly linear increase with  $T_h$ , such that their difference is positive  $(q_F - q_B > 0)$ , for most temperatures higher than the VO<sub>2</sub> transition one  $(T_{01} = 342.5 \text{ K})$ . The slope change of  $q_F$  is due to the MIT transition of VO<sub>2</sub> at  $T_h = T_{01}$ , such that the difference  $q_F - q_B$  increases for  $T_h \gg T_{01}$ . The heat fluxes  $q_F$  and  $q_B$  shown in Fig. 3 also increase with the interface temperatures  $T_1(L_1)$  and  $T_2(L_2)$ , as shown in Figs. 4(a) and 4(b), respectively. This fact is consistent with Fig. 3, Eqs. (11a) and (13a) as well as with theoretical predictions [34, 38] and experimental data [35]. The metallic phase of VO<sub>2</sub> is thus able to enhance the rectification factor of the proposed conductive thermal diode when the temperature difference of its terminals is high enough.

The interface temperatures for the forward  $[T_1(L_1)$  and  $T_2(L_1)]$  and backward  $[T_2(L_2)$  and  $T_1(L_2)]$ configurations of the conductive thermal diode are respectively shown in Figs. 5(a) and 5(b), as functions of the temperature  $T_h$ . The expected increase of these four temperatures with  $T_h$  is driven by the combined effect of the thermal conductivities of VO<sub>2</sub> and PE, while the temperature differences  $T_1(L_1) - T_2(L_1) = \rho q_F$ and  $T_2(L_2) - T_1(L_2) = \rho q_B$  are determined by the forward and backward heat fluxes shown in Fig. 3, respectively. Larger interface temperature jumps are obtained for higher temperatures  $T_h$ , such as they tend to constant values (> 5 K) for  $T_h > 380$  K. This fact along with Fig. 3, indicates that the optimization of the rectification factor of the conductive thermal diode requires relatively high temperature jumps at



Figure 4: Temperature evolution of the forward and backward heat fluxes, as functions of (a)  $T_1(L_1)$  and (b)  $T_2(L_2)$ . Calculations were done for  $T_c = 300$  K and  $\rho = 0$  mm<sup>2</sup>KW<sup>-1</sup>.



Figure 5: Interface temperatures for the (a) forward  $[T_1(L_1) \text{ and } T_2(L_1)]$  and (b) backward  $[T_2(L_2) \text{ and } T_1(L_2)]$  configurations, as functions of the temperature  $T_h$ . Calculations were done for  $T_c = 300$  K and  $\rho = 1$  mm<sup>2</sup>KW<sup>-1</sup>.

the interface of its terminals, for a given interface thermal resistance  $\rho > 0$ . These jumps, of course, also increase with  $\rho$  and vanish for  $\rho = 0$ , as shown in Fig. 6. For a typical  $\rho = 1 \text{ mm}^2 \text{KW}^{-1}$ , temperature jumps of  $T_1(L_1) - T_2(L_1) = 65$  K and  $T_2(L_2) - T_1(L_2) = 55$  K, are obtained for the forward and backward configurations, respectively. As the calculations in this latter figure were done for optimizing the rectification factor  $[(T_h, T_c) = (550, 300) \text{ K}]$ , the almost linear behavior of the temperature within the PCM terminals is due to the fact that the terminals are either in their pure metallic or pure insulating phases. The slope change of the temperature at the interface  $x = L_1$  is, therefore, as a result of the change of phase.

The rectification factor R of the proposed thermal diode is shown in Figs. 7, as a function of the



Figure 6: Temperature profiles within the terminals of the conductive thermal diode operating in the forward and backward configurations. Arrows stand for the direction of heat flux and calculations were done for  $T_c = 300$  K,  $T_h = 550$  K, and two values of the interface thermal resistance  $\rho$ .



Figure 7: Rectification factor of the conductive thermal diode, as a function of the interface thermal resistance  $\rho$ . Calculations were done using  $R = [1 - \min(q_F, q_B)/\max(q_F, q_B)]$  and  $T_c = 300$  K.

interface thermal resistance  $\rho$ , for three values of the temperature  $T_h$  of the hotter terminal. Note that R increases as  $\rho$  decreases and/or  $T_h$  increases, such that its value for  $T_h = 550$  K is well predicted by the analytical expression in Eq. (14). This verify the reliability of Eq. (14) to describe the optimal rectification factor, for different interface thermal resistance, and it indicates that the rectification factor reaches its maximum value for  $T_h = 550$  K, as confirmed by the density plot in Fig. 8. Given that  $\rho$  is the interface thermal resistance between VO<sub>2</sub> and PE, its reduction to optimize the rectification factor will require careful control of the deposition parameters of the VO<sub>2</sub>/PE heterojunction. The rectification factor



Figure 8: Density plot of the conductive thermal diode rectification factor as a function of the temperature  $T_h$  and interface thermal resistance  $\rho$ . Calculations were done using  $R = [1 - \min(q_F, q_B) / \max(q_F, q_B)]$  and  $T_c = 300$  K.



Figure 9: Rectification factor R of the conductive thermal diode as a function of the temperature  $T_h$ . Calculations were done by using the alternative definition  $R_a = [\max(q_F, q_B)/\min(q_F, q_B)] - 1$  for three representative interface thermal resistances and  $T_c = 300$  K.

at  $T_h = 550$  K differs from the optimal one by only 3%, which reduces for higher values of  $T_h$ . The optimal rectification factor  $R_{opt} = 60\%$  is reached for  $\rho = 0 \text{ mm}^2 \text{KW}^{-1}$  and a temperature difference of  $T_h - T_c = 550 - 300 = 250$  K between the diode terminals. Similar temperature and thermal rectification factors have been reported for heterojunction made up of graphene-hexagonal boron nitride [39], graphene-graphene

phononic crystals [40], graphene nanoribbons [?] and carbon nanotube-graphene [41], but by involving temperatures relatively higher than the ones determined here for a VO<sub>2</sub>/PE heterojunction. Furthermore, we note that the maximum rectification factor obtained for a conductive thermal diode operating with two (VO<sub>2</sub> and PE) PCMs is about 3 times the corresponding one obtained with a single PCM [34], which shows the advantage of properly combining the operation of two PCMs to enhance the thermal performance of conductive thermal diode. It is worth mentioning that in terms of the alternative definition of the rectification factor  $R_a = [\max(q_F, q_B)/\min(q_F, q_B)] - 1$ , the optimal rectification factor generated by the junction of VO<sub>2</sub> and PE is around 240%, which is greater than the 147% reported by Kang *et al.* [36]. This improvement is due to the difference in the thermal conductivity of tungsten-doped VO<sub>2</sub> used by Kang et al. [36] with respect to the one of pure VO<sub>2</sub> utilized in this work. This is reasonable, given that thermal diodes with higher rectification factors require larger thermal conductivity contrasts ( $\kappa_{high}/\kappa_{low}$ ) for both of its terminals, as established by Eq. (14).

#### 4. CONCLUSIONS

We have theoretically analyzed and optimized the rectification factor of a conductive thermal diode made up of a junction between two phase-change materials, whose thermal conductivities significantly change in a narrow interval of temperatures. This has been done by deriving analytical expressions for the temperature profiles, heat fluxes and rectification factor. A rectification factor up to 60% have been determined for a conductive thermal diode operating with terminals of VO<sub>2</sub> and polyethylene set with a temperature difference of 250 K from either side of the MIT temperatures. Higher rectification factors in a narrower temperature interval could be obtained with phase-change materials exhibiting higher thermal conductivity contrasts and faster phase transitions. The proposed model could thus be useful for guiding the development of phasechange materials capable of optimizing the rectification of conductive thermal diodes, which represent a fundamental building block for developing thermal circuits based on heat currents.

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