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# InAs-based quantum cascade lasers grown on on-axis (001) silicon substrate <sup>EP</sup>

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## ABSTRACT

We present InAs/AlSb quantum cascade lasers (QCLs) monolithically integrated on an on-axis (001) Si substrate. The lasers emit near 8  $\mu\text{m}$  with threshold current densities of 0.92–0.95  $\text{kA}/\text{cm}^2$  at 300 K for 3.6-mm-long devices and operate in pulsed mode up to 410 K. QCLs of the same design grown for comparison on a native InAs substrate demonstrated a threshold current density of 0.75  $\text{kA}/\text{cm}^2$  and the same maximum operating temperature. The low threshold current density of the QCLs grown on Si makes them suitable for photonic integrated sensor implementation.

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Integrating mid-infrared (IR) semiconductor lasers onto silicon platforms is highly desired for the development of compact, cost-effective, smart sensor systems.<sup>1</sup> The direct epitaxial growth of III–V emitters on silicon substrates is better suited for mass-scale production than their heterogeneous integration,<sup>2</sup> but this monolithic approach is complicated by the difference in the crystal structure and a large lattice mismatch between Si and the III–V materials usually employed in semiconductor lasers. Indeed, there are only a few reports on electrically pumped mid-IR lasers directly grown on Si. GaSb-based quantum well lasers operating at 2  $\mu\text{m}$  in the continuous wave (cw) regime above room temperature (RT) have been first demonstrated.<sup>3</sup> More recently, we have also reported the first quantum cascade lasers (QCLs) directly grown on Si.<sup>4</sup> These InAs-based devices emitting near 11  $\mu\text{m}$  exhibited RT threshold current densities  $J_{\text{th}}$  of 1.3  $\text{kA}/\text{cm}^2$  and operated in pulsed mode up to 380 K, which was close to the characteristics of QCLs grown on a native InAs substrate.<sup>4</sup> InP-based QCLs grown on a silicon substrate have also been subsequently reported, but their performance was much poorer than that of similar devices fabricated on InP substrates.<sup>5</sup> These lasers emitting at 4.35  $\mu\text{m}$  operated in pulsed mode only up to 170 K with a  $J_{\text{th}}$  of 1.85  $\text{kA}/\text{cm}^2$  at 80 K.

In order to suppress the formation of antiphase domains (APDs) caused by the different crystal structures of non-polar Si

substrates and polar III–V compound epitaxial layers,<sup>6</sup> these mid-IR lasers were all grown on silicon substrates exhibiting a large miscut angle ( $>6^\circ$ ) with respect to the (001) orientation. This makes them incompatible with the standard industrial process based on on-axis (001) Si wafers. In this letter, we report the first QCLs directly grown on an on-axis silicon substrate.

The active zone of the InAs/AlSb QCLs is based on vertical transitions in four coupled quantum wells and resonant phonon extraction. It was designed to emit around 7.5  $\mu\text{m}$ , a region rich in strong absorption features of different molecules.<sup>7</sup> It contains 40 repetitions of the following layer sequence in Å, starting from the injection barrier: **21/69/3/56/3.5/54/4.5/51/7/48/8/46/9/46/12/42/13/39/18/37**, where AlSb layers are in bold and the Si-doped layers ( $n = 6 \times 10^{16} \text{ cm}^{-3}$ ) are underlined. The total electron sheet density in the structure, taking into account residual doping of InAs ( $n \sim 8 \times 10^{15} \text{ cm}^{-3}$ , estimated), is considered to be  $1 \times 10^{11} \text{ cm}^{-2}$  per period. The plasmon enhanced dielectric waveguide of the laser was formed by 2- $\mu\text{m}$ -thick cladding layers made of *n*-InAs doped with Si to  $n = 4 \times 10^{18} \text{ cm}^{-3}$ , separated from the active zone by undoped InAs spacers of the same thickness in order to reduce the overlap of the guided mode with the absorbing doped material. The electromagnetic modeling of the guided modes, using a finite element solver, gives an overlap of the fundamental mode

with the active region  $\Gamma = 55\%$  and the waveguide loss  $\alpha = 3 \text{ cm}^{-1}$  for an empty waveguide.

The QCL structure (EQ609) was grown in a RIBER 412 solid-source molecular-beam epitaxy (MBE) reactor equipped with As and Sb valved cracker cells. InAs and AlSb layers were grown using deposition rates of  $3.03 \text{ \AA/s}$  and  $1 \text{ \AA/s}$ , respectively, and a V/III flux ratio of 2 on a GaSb-on-Si template prepared in a separate growth run as follows. Prior to growth, the (001) Si substrate exhibiting a  $\sim 0.5^\circ$  residual miscut was annealed at  $1000^\circ\text{C}$  for 10 min under ultra-high vacuum in a dedicated preparation chamber before being vacuum transferred into the III-V epitaxy chamber where a  $1.5 \text{ }\mu\text{m}$  GaSb buffer layer was grown underneath the QCL structure. This GaSb-on-Si template was inspected before being reloaded into the MBE reactor for growth of the QCL structure. After thermal oxide removal from the GaSb surface under  $\text{Sb}_2$  flux at  $560^\circ\text{C}$ , the growth of the full QCL structure was performed at a temperature of  $\sim 450^\circ\text{C}$ . In addition, a similar QCL structure (EQ746) was grown under the same conditions in a separate growth run on an InAs substrate for the sake of comparison.

The surface morphology of the QCL structure grown on Si was evaluated by atomic force microscopy (AFM) and scanning electron microscopy (SEM). The corresponding images are shown in Figs. 1 and 2(a), respectively. The surface morphology of the wafer was much better than that of the previous QCLs grown on a Si substrate with a  $6^\circ$  miscut [Fig. 2(b), sample from Ref. 4], but the surface was nevertheless quite rough. The rms roughness measured by AFM is  $9.9 \text{ nm}$ . On the other hand, no antiphase domains were visible in the AFM images, which is ascribed to the high-temperature preparation of the Si substrate. The grown wafers were processed into ridge lasers using wet etching and standard photolithography, the ridge width  $w$  being varied between  $9 \text{ }\mu\text{m}$  and  $17 \text{ }\mu\text{m}$ . Hard baked photoresist was employed for electrical insulation. The laser ridges were etched down to the bottom  $n^+$ -InAs cladding layer. Electrical contacts were formed on the top of the ridges and on the etched part of the wafer using non-alloyed Ti/Au metallization. The Si substrate was thinned down to  $50 \text{ }\mu\text{m}$  by mechanical polishing, and the wafer was cleaved to form  $3.6\text{-mm}$ -long Fabry-Perot lasers. The lasers were then soldered with indium on copper heatsinks, wire-bonded, and tested in pulsed mode ( $333 \text{ ns}$ ,  $12 \text{ kHz}$ ). Emission spectra of the devices were

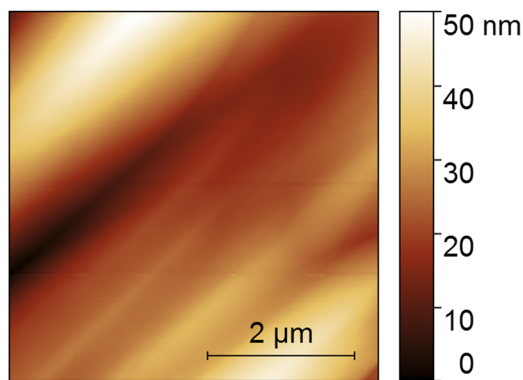


FIG. 1. AFM image of the EQ609 surface (rms roughness  $9.9 \text{ nm}$ ).

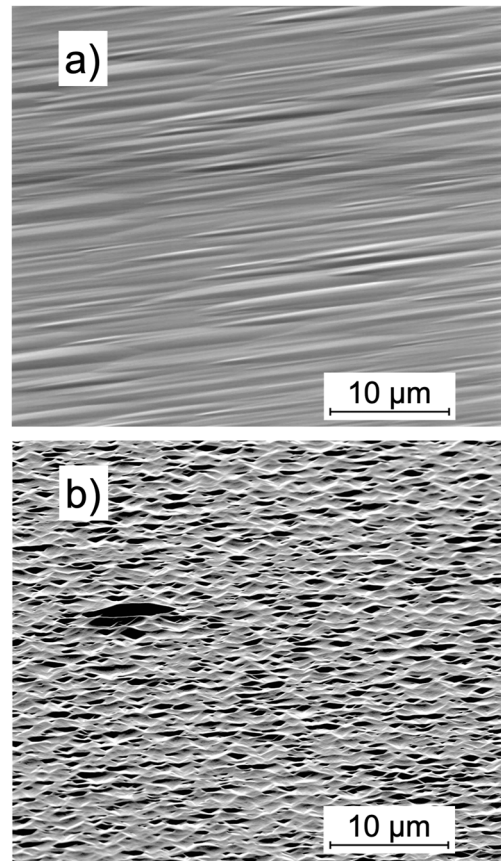
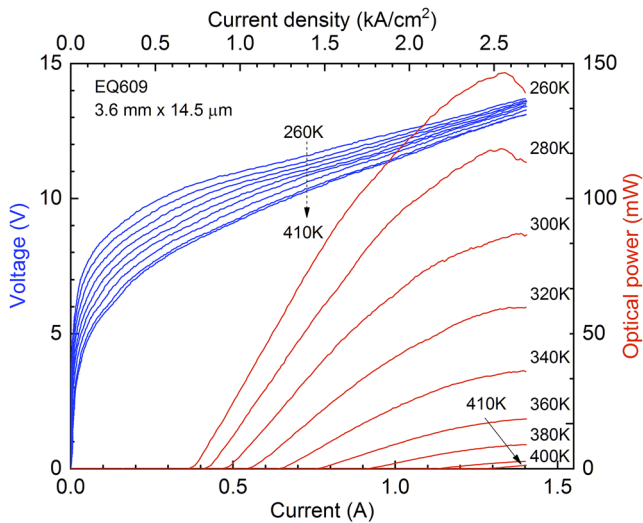


FIG. 2. SEM images of the QCL structures grown on Si. (a) This work (on-axis Si substrate). (b) Sample from Ref. 4 ( $6^\circ$  off Si substrate).

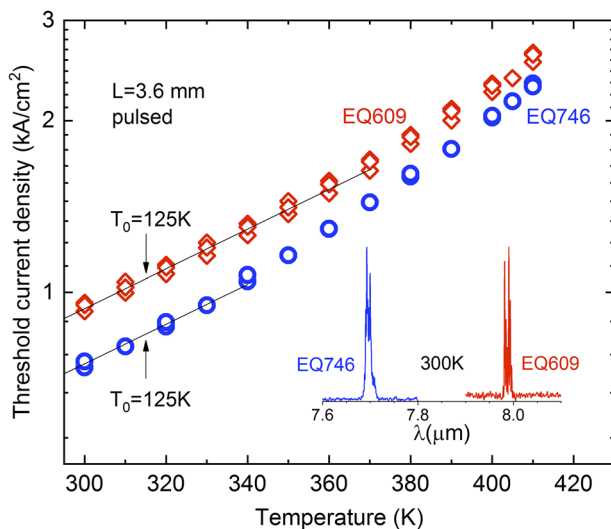
measured using a Bruker V70 infrared Fourier transform spectrometer (FTIR). No special selection was made to choose the lasers for measurements. Electrical contacts to the devices fabricated from the QCL wafer grown on InAs were taken on the top of the ridges and at the back of the substrate.

The QCLs grown on Si had RT pulsed threshold current densities in the  $0.92\text{--}0.95 \text{ kA/cm}^2$  range and operated up to a temperature of  $410 \text{ K}$  (Fig. 3). The reference devices grown on InAs exhibited threshold current densities around  $0.75 \text{ kA/cm}^2$  at  $300 \text{ K}$  and also operated up to  $410 \text{ K}$  (Fig. 4). The temperature dependence of the threshold current density of the tested QCLs is shown in Fig. 4. Straight lines in this figure indicate a slope corresponding to a characteristic temperature of the exponential dependence  $T_0 = 125 \text{ K}$ , identical around RT for lasers grown on both InAs and Si substrates. However, above  $340 \text{ K}$ ,  $J_{\text{th}}$  of the QCLs grown on Si increased slowly with temperature. Although dedicated aging studies will be needed to assess device robustness, we noticed that the threshold current density of the lasers measured at  $300 \text{ K}$  did not change after the high temperature characterizations around  $400 \text{ K}$ . Typical emission spectra are shown in the inset in Fig. 4. The lasers emitted wavelengths of  $7.7 \text{ }\mu\text{m}$  and  $8.0 \text{ }\mu\text{m}$  at  $300 \text{ K}$  for the lasers grown on InAs and Si, respectively.



**FIG. 3.** Voltage–current and light–current characteristics of a QCL grown on a Si substrate measured in pulsed mode at different temperatures.

In general, the operation of InAs/AlSb QCLs grown on silicon can be affected by antiphase boundaries (APBs), delimiting APDs, and threading dislocations originating from the large ( $\approx 11\%$ ) difference in the lattice parameters of InAs (and AlSb) and Si. A degradation of the performance is therefore expected in these devices compared with QCLs grown on native InAs substrates. The procedure used for preparing the GaSb-on-Si template allowed us to ensure full annihilation of APBs within the  $1.5\text{-}\mu\text{m}$  thick GaSb



**FIG. 4.** Threshold current density of QCLs grown on Si (EQ609,  $w = 13\text{ }\mu\text{m}$ ,  $14\text{ }\mu\text{m}$ , and  $14.5\text{ }\mu\text{m}$ ) and on InAs (EQ746,  $w = 13\text{ }\mu\text{m}$ , and  $17\text{ }\mu\text{m}$ ) as a function of temperature (semi-logarithmic scale). Emission spectra measured at a current of  $0.6\text{ A}$  at  $300\text{ K}$  are shown in the inset.

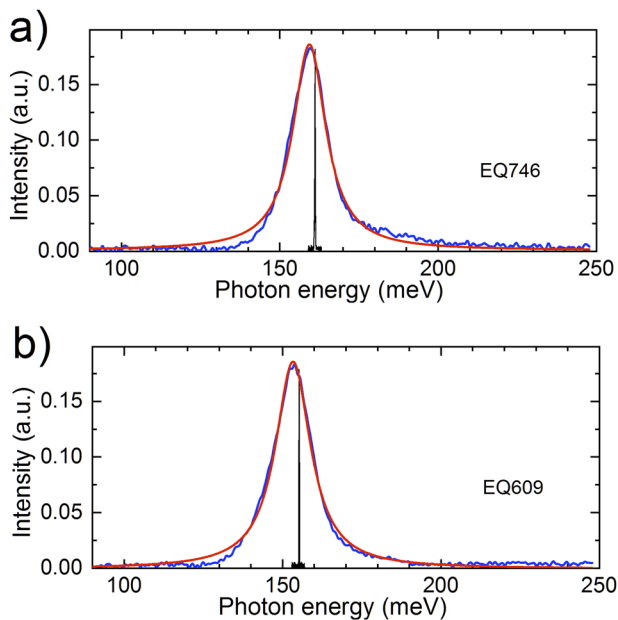
buffer layer sitting well below the QCL active zone, the absence of emerging APBs being confirmed by AFM during preliminary studies. However, the unavoidable high density of threading dislocations (estimated to be in the  $10^8\text{ cm}^{-2}$  range from preliminary transmission electron microscopy investigations) does penalize the device performance. Yet, the studied QCLs grown on silicon exhibited RT threshold current densities below  $1\text{ kA/cm}^2$  and only 22% higher than the reference lasers. Both  $J_{\text{th}}$  values and their deviation from the values observed on identical devices grown on InAs are smaller than those in the previous InAs/AlSb QCLs that were grown on an off-axis Si substrate, where a 30% increase in  $J_{\text{th}}$  was observed.<sup>4</sup> In lasers with short resonators, this difference was even smaller, which was explained by a higher optical gain due to smoother interfaces in the structure grown on an off-axis substrate that favored a step-flow regime of MBE growth. This argument is probably still valid for the lasers studied in this work since the on-axis Si substrate exhibited a residual  $0.5^\circ$  misorientation that can stimulate the step-flow growth mode, thus reducing interface scattering in the QCL structure. A smaller difference in  $J_{\text{th}}$  at high temperatures can be considered as an indication of a higher gain in the new lasers grown on Si compared with the devices grown on InAs. However, the most likely explanation of the high QCL performance achieved in this work is the much better crystalline quality of the wafer grown on the on-axis Si substrate (Fig. 2).

The  $J_{\text{th}}$  degradation in QCLs grown on Si can be due to the broadening of the gain curve caused by non-homogeneity of the layer thickness in the active zone of the device. As suggested in Ref. 4, in InAs/AlSb QCLs, this effect is quite weak because of the opposite influence of the fluctuations of the AlSb barriers and InAs wells on the transition energy in the case of their in-phase variations.<sup>4</sup> In order to verify this assumption, we measured spontaneous emission spectra of the devices studied in this work. The spectra were measured at  $300\text{ K}$  at a low current density of  $0.4\text{ kA/cm}^2$ , corresponding to about 50% of  $J_{\text{th}}$  for these devices, in order to avoid narrowing due to the optical amplification (Fig. 5). The full width at half maximum of the spectra extracted from the Lorentzian fits of the data was comparable for both devices,  $12.5\text{ meV}$  and  $13.5\text{ meV}$  for the lasers grown on InAs and Si, respectively. Measurements on the samples without resonators are necessary for more reliable analysis of the emission linewidth, but the observed trend confirms the conclusion made in Ref. 4. Another reason of the observed increase in  $J_{\text{th}}$  of the QCLs grown on silicon can be higher optical losses due to additional absorption on crystal imperfections. This mechanism can similarly explain a weaker performance degradation in short devices observed in Ref. 4.

The reference lasers EQ746 mounted epi-side down operated in the cw regime at temperatures up to  $30^\circ\text{C}$ . At high temperatures, the QCLs EQ609 grown on Si exhibit  $J_{\text{th}}$  close to the characteristics of the devices grown on InAs, and they should thus be able to operate under continuous wave operation near RT provided a suitable heat dissipation scheme, such as thick gold plating, is implemented.

In summary, we demonstrated InAs/AlSb quantum cascade lasers monolithically integrated on an on-axis (001) Si substrate. At room temperature, the lasers emitted near  $8\text{ }\mu\text{m}$  and exhibited threshold current densities below  $1\text{ kA/cm}^2$ , only 22% higher than reference QCLs of the same design grown on a native InAs substrate. The operating temperature of the QCLs grown on silicon reached





**FIG. 5.** Emission spectra of the lasers measured at 300 K at a current density of  $0.4 \text{ kA/cm}^2$  ( $\sim 0.5 \text{ J/th}$ ) and above the threshold (black lines). Red curves show Lorentzian fits of the spectra.

410 K, demonstrating the same performance as the reference devices. The low threshold current density of these devices makes them suitable for photonic integrated sensor implementation. Even though at this stage, the substrate preparation process is not fully CMOS

compatible, various remotely controlled sensor chips can already be envisioned based on the co-integration of QCLs and quantum cascade detectors on the same Si photonic circuit or III-V-on-Si photonic circuit.

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