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Nonlinear Fuzzy Control Based on Fuzzy Anti-Windup of VSC-HVDC Embedded into AC Grid

E. Kamal, B. Marinescu and G. Denis

Abstract—In this work, new robust fuzzy control based on model recovery anti-windup strategy based on robust fuzzy anti-windup is proposed for disturbances of multivariable High Voltage Direct Current (HVDC) link embedded with AC grid subject to a wide range of parameter uncertainties. Saturations on both control and the states are also taken into account. This work concentrates on the fault ride-through capability of the AC embedded HVDC system which may be exploited to improve the stability of the AC embedded HVDC system. The developed robust control scheme is demonstrated to be effective for temporary short-circuit conditions. The algorithm utilizes fuzzy systems based on "Takagi-Sugeno" fuzzy models to represent a nonlinear AC embedded HVDC system. Sufficient conditions are derived for robust stabilization in the sense of Lyapunov stability and are formulated with linear matrix inequalities to obtain controller gains and anti-windup gains. First, a nonlinear robust fuzzy controller is designed subject to a disturbance with parameter uncertainties. Next, an additional anti-windup is designed to mitigate the adverse effects due to state and the control constraints. The proposed control system is compared with alternative frameworks existing in the literature based on robust control via simulations performed with MATLAB software, to illustrate the effectiveness of the proposed method.

I. Introduction

N the 1950s, with the development of mercury switches, the systems High Voltage Direct Current (HVDC) transmission are become attractive for long-distance power transmissions. Their principle can be summarized as follows: the alternative power provided by one (or more) source (s) is first transformed into a continuous power. A converter (rectifier) ensures the operation. The power then passes through a transmission line and, finally, another converter (inverter) converts this continuous power into a power alternative delivered to one or more loads [1], [2]. Recently, rapid advancement has been achieved in the field of control of VSC-HVDC systems. Firstly, Fault Ride-Through (FRT) capability [3], is a high priority consideration for any power system. Presently, many studies and works have been done both in improving transient stability and voltage regulation of the power system. HVDC converter controls have an impact on the dynamics of neighboring AC zones of the grid in which the HVDC is inserted [2]. An

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HVDC embedded in an AC grid is referred to in what follows as an embedded HVDC system. In [1] control synthesis has been shown to improve critical clearing time at specific AC critical buses in the HVDC neighborhood. In addition, [3]-[8] neglect the effect of the parameter uncertainties which make the system unstable. Also, all the previous strategies [3]-[8] study the robust stability for HVDC without considering saturation on both control and states which cause the system unstable.

Secondly, robustness and stability control for VSC-HVDC system with uncertainties. Parameter uncertainties denote the phenomena that values of the parameters deviate from the nominal values and make the system unstable [9].

Takagi-Sugeno's fuzzy models [10]-[16] have the property of approximating any non-linear function, while dispensing with explicit models of the process. The control laws commonly used for Takagi- Sugeno fuzzy models are of the non-linear static state feedback type called PDC (Parallel Distributed Compensation) control [17]. This type of control law uses the same non-linear functions to interpolate the linear models of the TS models. The study of the stability and stabilization of these types of models uses, in the vast majority of cases, the second Lyapunov method (direct method). In almost all cases, a quadratic Lyapunov function is used. This is mainly due to the fact that it is possible to solve the conditions of stability/stabilisation. Indeed, one of the main difficulties is to be able to write these conditions in the form of LMI constraints (Linear Matrix Inequalities) [10]. LMIs, if it appears that they admit a solution, can be solved using tools from the field of convex optimization.

This work develops a new Nonlinear Robust Fuzzy Controller (NRFC) based on model recovery anti-windup strategy based on Robust Fuzzy Model Recovery Anti-windup (RFM-RAW). The results of this paper support that the proposed strategy is capable of:

- (i) Overcoming the problems linked with nonlinearities in [1], [2];
- (ii) Fault Ride-Through (FRT) capability of the HVDC-AC, for instance, short-circuits and the control saturation which are the main drawback of the studies in [18];
- (iii) Compensating for the effect of the parameter uncertainties which are neglected in [3];
- (iv) Stabilization uncertain HVDC systems having restricted states and controls which are the main drawback of the study in [1].

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We consider here the problems of stability analysis and synthesizing dynamic RFMRAW compensators for a TS fuzzy AC embedded HVDC system subject to control and state constraints, parameter uncertainties and disturbances. First, we propose a simple model of the AC embedded HVDC system. Then, the proposed strategy is designed based on RFMRAW for the AC embedded HVDC system with normbounded parametric uncertainties, disturbance, and saturation on both control and states. In addition, the proposed strategy is proved to be effective for temporary short-circuits in AC grid. The objective of paper is to establish the control laws based on PDC [17] and LMI [10] that allow stabilization HVDC systems of the VSC type. The control laws assume in this paper that the state of the system is known at any time. They have been validated in simulation and we will be able to see that the presence of the converters is a benefit for the stabilization of HVDC.

The paper is organized as follows. In Section II, the control problem is formulated. In Section III, the proposed robust fuzzy model recovery control strategy structure is developed. Stability and robustness analysis of proposed strategy are presented in Section IV. Overall VSC-HVDC link embedded with AC grid is given in Section V. Simulation results are presented in Section VI. Finally, the conclusions and future work are presented in section VII.

II. CONTROL PROBLEM FORMULATION

This section states the formulation of problem of input and/or state constraints with a wide range of parametric uncertainties and the effect of disturbances on the system. The global model takes following form:

$$\dot{x}(t) = \sum_{i=1}^{p} \mu_i(q(t))[(A_i + \Delta A_i)x(t) + (B_i + \Delta B_i)sat(u(t)) + B_{di}w(t)]$$

$$y(t) = \sum_{i=1}^{p} \mu_i(q(t))[C_ix(t) + D_isat(u(t))]$$
(1)

where $x(t) \in \Re^{n \times 1}$, $y(t) \in \Re^{g \times 1}$ and $u(t) \in \Re^{m \times 1}$ are the state, the output and the input vectors, respectively, $C_i \in \Re^{g \times n}$ is the system output matrix, $\Delta A_i \in \Re^{n \times n}$ and $\Delta B_i \in \Re^{n \times m}$ are the uncertainties of the constant system matrices $A_i \in \Re^{n \times n}$ and $B_i \in \Re^{n \times m}$, respectively, $B_{di} \in \Re^{n \times 1}$ is a known matrix, w(t) is the disturbance vector and sat is the saturation which is given by sat(u) is defined by $sat(u) = u_{min}$ if $u(t) \leq u_{min}$, sat(u) = u(t) if $u_{max} \leq u(t) \leq u_{min}$, $sat(u) = u_{max}$ if $u(t) \leq u_{max}$. In this study, the parameter uncertainties are assumed given by:

$$\Delta A_i = E_{1i}F_i(t)H_{1i}$$
 $\Delta B_i = E_{2i}F_i(t)H_{2i}$ (2)

where E_{1i} , E_{2i} , H_{1i} and H_{2i} are known real constant matrices and $F_i(t)$ are unknown real matrices with Lebesgue measurable elements, satisfying $F_i(t)^T F_i(t) \leq I$, i=1,...,p, where I is the identity matrix. The states are also constrained as follows

$$\gamma(M, N) = \{x \in \Re^{n \times 1} / Mx \le N\}$$
 (3)

where $M \in \Re^{h \times n}$ and $N \in \Re^{h \times 1}$ are known. The control problem formulated has both local (L) and grid (G) objectives:

- L: Minimize the steady state error (so that the controlled output tracks its reference value). In order to force to zero the steady-state tracking errors, we add new states which corresponds to the integral action.
- G: The regulator should provide good FRT capability. This will improve the steady-state and transient stabilities of the AC embedded HVDC system.

III. PROPOSED ROBUST FUZZY CONTROL BASED ON ${\bf RFMRAW}$

In this section, the proposed NRFC based on RFMRAW is designed.

A. Proposed NRFC

First, the dynamic controller fuzzy rules are constructed based on the PDC [17] for the fuzzy model (1),

$$u(t) = \sum_{i=1}^{p} \mu_i K_i x(t) \tag{4}$$

where $K_i \in \Re^{m \times n}$ are feedback gain vectors for rule.

B. Proposed RFMRAW

After controller gains are obtained using (4), the antiwindup adapted to the fuzzy model is designed to limit the effect of actuators constraints. According to the PDC [17], dynamic equations of the modified RFMRAW are given by:

$$\dot{x}_d = \sum_{j=1}^p \mu_j (A_i x_d + B_i \varphi), \quad \sigma = \sum_{j=1}^p \mu_j (G_{di} x_d + L_{di} \varphi)$$
(5)

where $x_d(t)$ is the RFMRAWstate vector, G_{di} and L_{di} are the compensation gains and $\varphi = sat(u) - u(t)$.

Finally, based on (4) and (5), the NRFC is obtained from:

$$u(t) = \begin{bmatrix} \sum_{j=1}^{p} \mu_j K_i & I \end{bmatrix} \begin{bmatrix} x(t) \\ \sigma(t) \end{bmatrix}$$
 (6)

IV. STABILITY AND ROBUSTNESS ANALYSIS OF THE PROPOSED STRATEGY

Stabilization of the system (1) is studied in two cases: first in the absence of RFMRAW to calculate the controller gains K_i in (4), and second in presence of the RFMRAW to calculate the proposed controller as follows.

A. Dynamics of Closed-Loop System without anti-windup

From (1) and (4), the closed loop system dynamics is given by:

$$\dot{x}(t) = \sum_{j=1}^{p} \mu_{j} [(A_{i} + B_{i}K_{i})x(t) + (E_{1i}F_{i}(t)H_{1i} + E_{2i}F_{i}(t)H_{2i}K_{i})x(t) + B_{di}w(t)]$$

$$y(t) = \sum_{j=1}^{p} \mu_{j} [C_{i}x(t) + D_{i}sat(u(t))]$$
(7)

The main results are summarized in the following Theorem:

Theorem 1: Any equilibrium point of an uncertain fuzzy control system as given by (7) subject to the constrained states (3) and control inputs, with disturbances and parameter uncertainties is asymptotically stable in the whole domain by the controller (6), if the controller gains are set to $K_i = O_i Z_i^{-1}$, and there exists a positive definite symmetric matrix $P_i = Z_i^{-1}$ and scalars ι , δ and β_i , such that the following LMIs are satisfied

$$\begin{bmatrix} A_{i}Z_{i} + Z_{i}A_{i}^{T} + (B_{i}O_{i}) + (B_{i}O_{i})^{T} + \delta E_{i}E_{i}^{T} + \frac{1}{\delta}B_{di}B_{di}^{T} \\ + Z_{i}\delta & Z_{i}H_{1i}^{T} + O_{i}H_{2i}^{T} \\ H_{1i}Z_{i} + H_{2i}O_{i} & -\delta I \end{bmatrix} \leq 0$$

$$\begin{bmatrix} \iota\beta_{ji}N_{ji} - M_{ji}Z_{i}M_{ji}^{T} & \beta_{ji} \\ \beta_{ji} & 1 \end{bmatrix} \geq 0, \quad \begin{bmatrix} 1 & O_{li} \\ O_{li}^{T} & Z_{i} \end{bmatrix} \geq 0$$
(8)

where, $1 \le l \le c$, $1 \le j \le m$, O_l , N_{li} and M_{li} are the l^{th} row of O_i , N_i and M_i , respectively and ι is a constant. **Proof**: The closed-loop system in the absence of RFMRAW. (7) should be obtained first. Based on [10], the proof and the controller gains K_i are obtained.

B. Dynamics of Closed-Loop System in Presence of RFMRAW Compensator

To design the RFMRAW gains $(G_{di} \text{ and } L_{di})$ in (5) the closed-loop system with RFMRAW should be obtained first. Let $\varphi = sat(u) - u(t)$. K_i are obtained using Theorem 1. From (1) and (6), the closed loop system in the presence of RFMRAW compensator is given by:

$$\dot{x} = \sum_{j=1}^{p} \mu_j [\bar{A}_i x + B_i \varphi], \quad y = \sum_{j=1}^{p} \mu_j [\bar{C}_i x + D_i \varphi]$$

$$y_z = \sum_{j=1}^{p} \mu_j [\bar{C}_{zi} x + D_{zi} \varphi]$$
(9)

where $\bar{A}_i = A_i + B_i K_i$, $\bar{C}_i = C_i + D_i K_i$, $\bar{C}_i = C_{zi} + D_{zi} K_i$, $y(t) \in \Re^{g_y \times 1}$ are the measurement output and $y_z(t) \in \Re^{g_z \times 1}$ is the performance output.

The main results are summarized in the following Theorem:

Theorem 2: If there exist a symmetric positive definite matrix, Q, another matrix, Y, and a positive scalar, ϱ , such that the following conditions hold:

$$\begin{bmatrix} \Gamma_{1} & * & * & * \\ B_{i}^{T} & -I & * & * \\ \bar{C}_{zi}Q - D_{zi}Y & D_{zi} & -\bar{\varrho}I & * \\ \Gamma_{2} & -\bar{L}_{di} & D_{zi}^{T} - \bar{L}_{di}D_{zi}^{T} & \Gamma_{3} \end{bmatrix}$$

$$\leq 0$$

$$(\bar{A}_{i} + B_{i}\bar{G}_{di})Q + Q(\bar{A}_{i} + B_{i}\bar{G}_{di})^{T} - 2Q\lambda \leq 0$$

$$Q > 0$$

with $\Gamma_1 = \bar{A}_i Q + \bar{A}_i^T + B_i Y + (B_i Y)^T$, $\Gamma_2 = \bar{G}_{di} Q - Y - \bar{L}_{di} B_i^T$ and $\Gamma_3 = -\bar{L}_{di} + \bar{L}_{di}^T - 2I$ and * is the conjugate transposed, then considered equilibrium point of the closed-loop system (1) and (9) can be made asymptotically stable in the whole domain by the controller (6) and the anti-windup (5) with the gains set to $G_{di} = -(I - \bar{L}_{di})^{-1}\bar{G}_{di}$

and
$$L_{di} = -(I - \bar{L}_{di})^{-1}\bar{L}_{di}$$
.

Proof: In [19], several other algorithms are expressed, in particular using Lyapunov based procedure. By the same analysis procedure in the Theorem 1 and using the Schur complement method [20] and based on the analysis procedure [21], we can obtain (10) of Theorem 2.

V. OVERALL VSC-HVDC LINK EMBEDDED IN AC GRID MODELING AND DESCRIPTION

This section contains the dynamic equations describing the AC embedded HVDC system. The example in Fig. 1 was chosen to illustrate the case of a VSC-HVDC link embedded in AC grid. It is simple enough to follow directly the analytic modelling and capture the significant points, the parallel AC line ensures AC interactions between the two HVDC terminals while the two lines towards the infinite buses model the short-circuit power at each terminal. The average model of the converter proposed in [8] is used in this paper. The dynamic equations in dq coordinates of the AC embedded HVDC system in Figure 1 are [7]:

$$\frac{di_{1d}^{\infty}}{dt} = \omega i_{1q}^{\infty} + \frac{1}{L_{g1}} E - \frac{1}{2} \left(\frac{1}{L_{g1}} + \frac{1}{L_{AC}} \right) v_{DC1} \beta_{1d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC2} \beta_{2d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC2} \beta_{2d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC2} \beta_{2q}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC2} \beta_{2q}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC2} \beta_{2q}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC1} \beta_{1d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC1} \beta_{1d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC1} \beta_{1d}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC1} \beta_{1q}^{\infty} + \frac{1}{2} \frac{1}{L_{AC}} v_{DC1} \beta_{1q}^{\infty$$

where, i_{1d}^{∞} and i_{1q}^{∞} , $i_{2d}^{\infty}, i_{2q}^{\infty}$, β_{1d}^{∞} , β_{1q}^{∞} , i_{DC} v_{DC1} , β_{2d}^{∞} , β_{2q}^{∞} , v_{DC2} are variables for VSC on the left and right sides, respectively, L_{g1} , L_{g2} , L_{AC} , r_{DC} , L_{DC} , C_{1} , C_{2} , E and ω are constants. P_{2} , Q_{1},Q_{2} are the active and reactive powers, respectively: $Q_{1} = \frac{3}{2}(\frac{1}{2}(\beta_{1d}^{\infty}v_{DC1}i_{1q}^{\infty}) - \frac{1}{2}(\beta_{1q}^{\infty}v_{DC1}i_{1d}^{\infty}))$, $P_{2} = \frac{3}{2}(\frac{1}{2}(\beta_{2d}^{\infty}v_{DC2}i_{2d}^{\infty}) + \frac{1}{2}(\beta_{2q}^{\infty}v_{DC2}i_{2q}^{\infty}))$, $Q_{2} = \frac{3}{2}(\frac{1}{2}(\beta_{2d}^{\infty}v_{DC2}i_{2q}^{\infty}) - \frac{1}{2}(\beta_{2q}^{\infty}v_{DC2}i_{2d}^{\infty}))$,

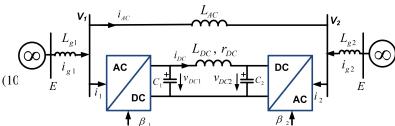


Fig. 1. HVDC power structure control model.

VI. SIMULATIONS RESULTS AND DISCUSSION

Three Matlab simulations and discussions on the model proposed in Section IV are presented.

Simulation 1: Proposed Strategy Validation Subject to 40% of Parameter Uncertainties, 35% of Disturbance and Saturation on Both States and Control

The objective here is to conceive the saturation on both states and the control for AC embedded HVDC system model. In this paper, we consider that the current variables (x_1, x_2, x_3, x_4) are constrained based on (3) as $-2000Amp \le x_1, x_2, x_3, x_4 \le 2000Amp$. Also, the control inputs $(-1 \le u_1, u_2, u_3, u_4 \le 1)$ are considered constrained.

The uncertainties of 40% are considered in the DC link resistance (r_{DC}) and AC grid inductances (L_{g1}, L_{g2}) and additive disturbance about 35%. Therefore, additive disturbance is given by $B_{di}sign(sin(0.3t))$, where the additive disturbance matrix is $B_{di} = \begin{bmatrix} 0.35 & 0.35 & 0.35 & 0.35 & 0.35 & 0.35 \end{bmatrix}^T$ (i=1,2,...,8).

Figure 2 shows the step response of the DC voltage (V_{DC1} [KV]). Reactive powers (Q_1 [MVA] and Q_2 [MVA]) are given in Figures 3 and 5, respectively. Figure 4 illustrates the active power (P_2 [MW]).

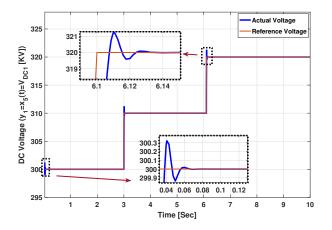


Fig. 2. Time response of the DC voltage $(y_1 = x_5 = V_{DC1} \text{ [KV]})$.

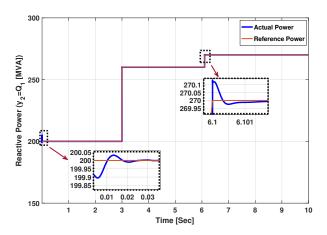


Fig. 3. Time response of the reactive power $(y_2 = Q_1 \text{ [MVA]})$.

From simulation 1 results, it cab be seen that the effectiveness of the proposed control approach.

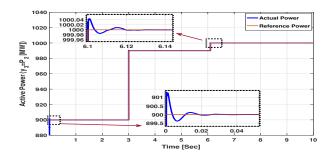


Fig. 4. Time response of the active power $(y_3 = P_2 \text{ [MW]})$.

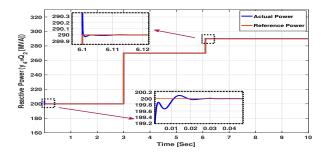


Fig. 5. Time response of the reactive power $(y_4 = Q_2 \text{ [MVA]})$.

Simulation 2: Proposed Overall Control Architecture to Illustrate the Robustness Compared to other Strategy

Simulation 2 demonstrates the effectiveness and robustness of the proposed overall control architecture proposed strategy compared with the RC strategy [10] methods already existing in the literature. For simulation runs, the same parameter and constraint conditions as in the preceding section are considered. The elements ΔA_i and ΔB_i achieve values within 42% of their nominal values corresponding to system parameter variations in $(L_{g1}$ and L_{g2} , r_{DC}). Figure 6 shows the time responses of the DC voltage $(V_{DC1}$ [KV]) for the proposed strategy (left) and Robust Controller (RC) [10] (right) strategy, respectively.

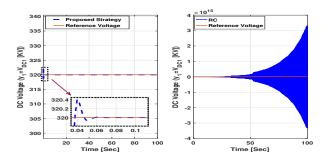


Fig. 6. Time response of the DC voltage $(y_1 = V_{DC1} \text{ [KV]})$ for the proposed strategy (left) and RC strategy (right).

It can be seen that the proposed strategy gives a good tracking performance while the RC strategy algorithm [10] cannot give a good robustness and leads to system instability.

Simulation 3: FRT Capability Validation

In order to validate the FRT capability, a short-circuit is applied at $t_s = 5sec$ in the middle of the AC parallel line and cleared at t_c =5.1 Sec. In simulation 3, the maximum level of uncertainty is assumed only for inductance, L_{g1} and reference variation in the reactive power is considered. Figure 7 shows the reactive power, Q_1 [MVA], response on left AC side of VSC-HVDC. It is observed from the results that the proposed algorithm gives good robustness in this case.

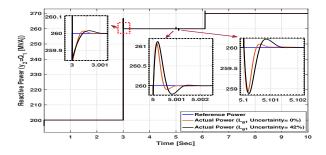


Fig. 7. Reactive power behavior ($y_2=Q_1[{\rm MVA}]$) on left AC side of VSC–HVDC considering inductance (L_{g1}) with and without uncertainty and 100 ms short-circuit at $t_s=5sec$ and reference variation in Q_1 .

The simulations results imply that the proposed strategy can fully control the plant whereas previous algorithms considered [10] failed to provide adequate responses and lead to system instability.

VII. CONCLUSION

This work presented a Robust Fuzzy Control Based on Model Recovery Anti-Windup Strategy in presence of parameter uncertainties, disturbances, and saturations on both states and control for a nonlinear VSC- HVDC link embedded in AC grid. Sufficient conditions for robust stabilization of the TS fuzzy model were derived in a LMI formalism. To implement the proposed strategy, a nonlinear Robust Fuzzy Controller subject to disturbance and parameters uncertainties is combined with an additional Robust Fuzzy Model Recovery Anti-windup to compensate for the effects of saturation on both states and the control. The overall proposed strategy is tested and compared with alternative frameworks existing in the literature in order to demonstrate the advantages of the proposed methodology. The obtained results confirm that, using the proposed approach: (i) the strategy can be easily implemented in real time because it is simple and implemented by only 8 fuzzy rules; (ii) it compensates the effect of the parameter uncertainties up to 42% (which is a higher level compared with [10]) and disturbance up to 40%; (iii) VSC-HVDC link embedded with AC grid with constraints on both states and the control is stable. Also, a good fault ride-through capability of the closed-loop system is demonstrated. This work compels improvements of the transient stability margin in the AC grid neighborhood which will be fully analyzed in subsequent work. In addition, the hypothesis of direct access to d and q components (ideal Phase-Locked-Loop (PLL)) will be no longer used as an estimator will be synthesized for this in conjuction with the controller.

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