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# **Comparison between Sputtering and Atomic Layer Deposition (ALD) of ferroelectric doped and undoped HfO**<sub>2</sub>

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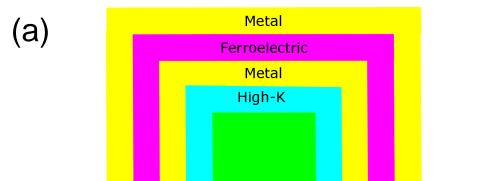
We study the conditions to stabilize the ferroelectric phase (f-phase) in the doped HfO<sub>2</sub>. It corresponds to the Pca2<sub>1</sub> space group and the orthorhombic III phase (o-III-phase). According to Böscke et al. [1], there is a transition from theorical tetragonal phase (t-phase) or to the monoclinic phase) or to the o-III-phase during cooling, respectively without a capping electrode or with a capping electrode. We compare the phases of our samples by X-rays Diffraction (XRD). We observe only amorphous phases in the case of samples deposited by ALD at 200°C (holder temperature) on Si, SiO<sub>2</sub>, Pt and TiN, whereas we observe a monoclinic phase for HfO<sub>2</sub> and Hf<sub>x</sub>Zr<sub>1-x</sub>O<sub>2</sub> samples deposited on Si/SiO<sub>2</sub>/Pt at 350° C (sample temperature) by sputtering. The phase doesn't seem to be related on the thickness as the HfO<sub>2</sub> is  $\sim 150$  nm thick. It leads us to believe we could stabilize the f-phase in-situ by sputtering in the future, which has never been done, to our knowledge, and compare its properties with AI:HfO<sub>2</sub> samples annealed ex-situ after ALD deposition. Moreover, using a Hf/Zr target has, as far as we are aware, also never been done.



**Applications** 

Ferroelectric Field Effect Transistor (FeFET)





Si substrate

Ferroelectric Random Access Memory (FeRAM) => embedded Non-Volatile Memory (eNVM) in MicroController Unit (MCU) (the "heart" of Internet of Things (IoT) devices)

# **Ferroelectric HfO**<sub>2</sub>

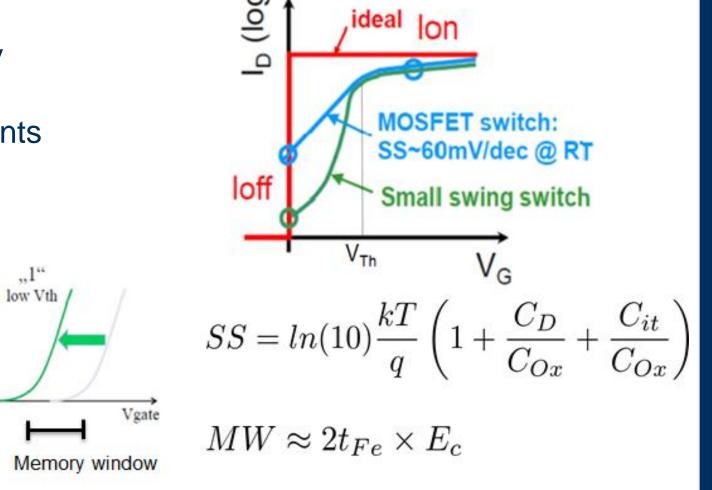
- Discovered in 2011 [1]
- o-III-phase and Pca2<sub>1</sub> space group [2]
- Stabilized between up and bottom electrodes (already tested : TiN, TaN, Pt, RuO, Ir)
- Ferroelectricity appears for a large sort of cation dopants (Zr<sup>4+</sup>, Y<sup>3+</sup>, Al<sup>3+</sup>, La<sup>3+</sup>, Gd<sup>3+</sup>, Sr<sup>2+</sup>,...) [2,3]

(b)

Memory window

## **Problems with other** ferroelectrics

- Cost
- Compatibility with Si processing
- Memory Window limitations lead to scalability issues
- "dead layer" effects lead to scalability issues



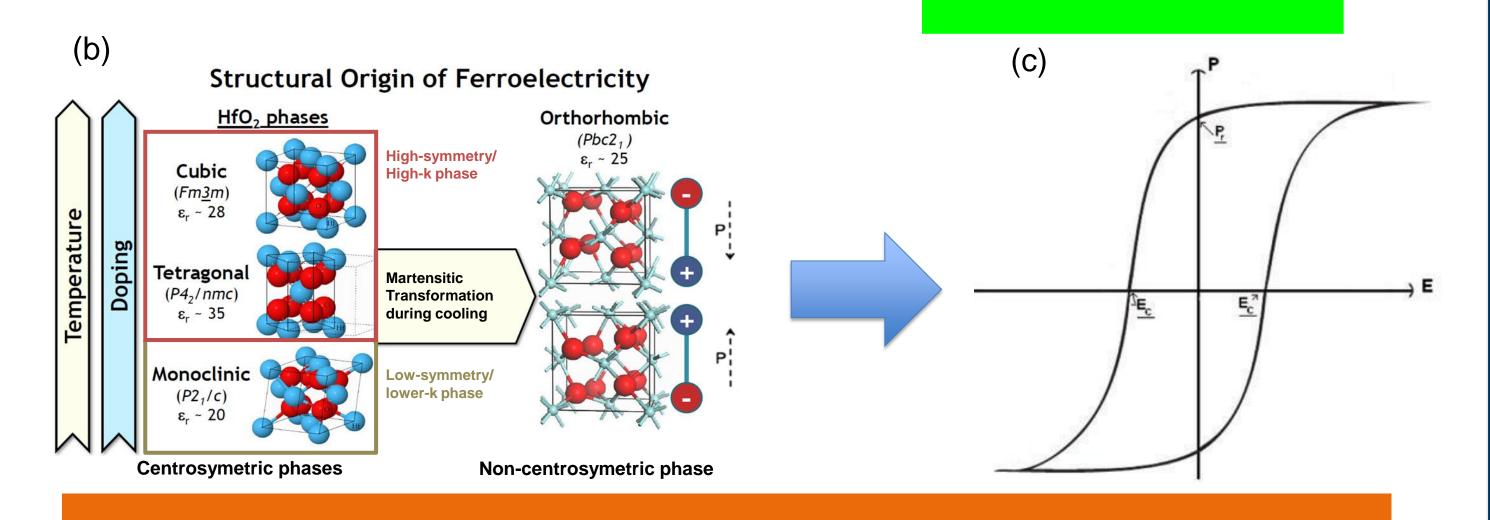
(a)

(a) Drain current vs. gate voltage and formula to calculate the thermodynamic limit of the switch slope. It is theoretically possible to obtain a sub-threshold slope for transistors using a ferroelectric oxide [4] (b) Drain current vs. gate voltage where it is represented the difference between the two threshold voltages in function of the ferroelectric state • Memory Window. The Electric Field E<sub>c</sub> is in the right range (see objectives) to induce ferroelectricity in the oxide with very small thickness (<10nm).

(a)

X

- I. Electronic component using the properties of ferroelectric doped HfO<sub>2</sub>
- 2. Studying the materials properties
- 3. Comparing different deposition technics



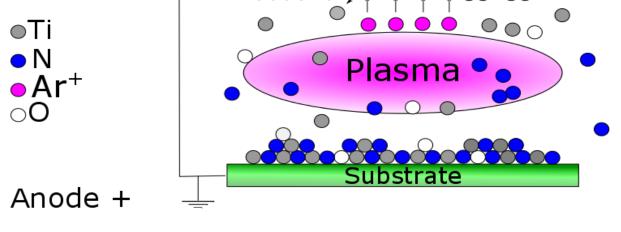
(a) NC-FinFET [5] (b) Structural origin of ferroelectricity in doped (or undoped) HfO<sub>2</sub> with a phase change from the tetragonal phase to the o-III-phase. (c) The structural phase change leads to the classical hysteresis for ferroelectric with a coercitive electric field E<sub>c</sub> in the order of 1MV/cm for less than 10nm of  $HfO_2$ .

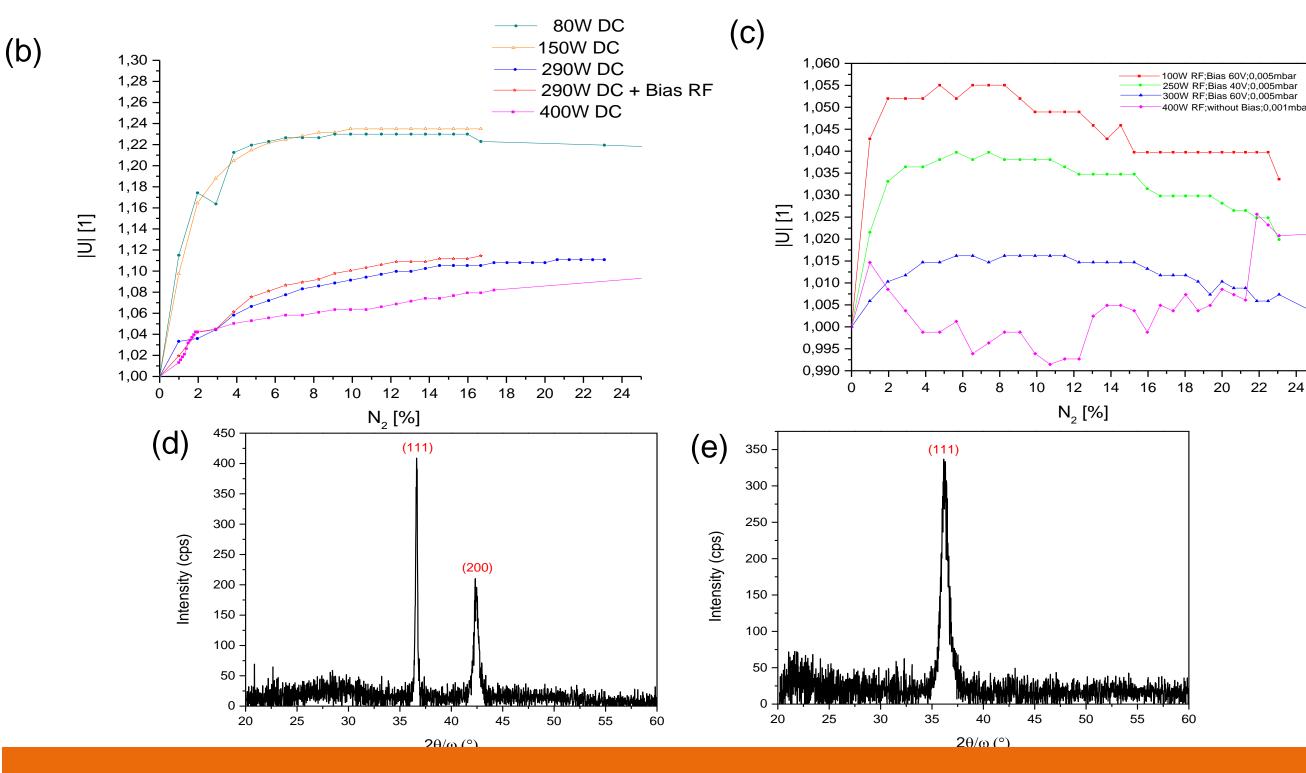
Electrodes

Cathode

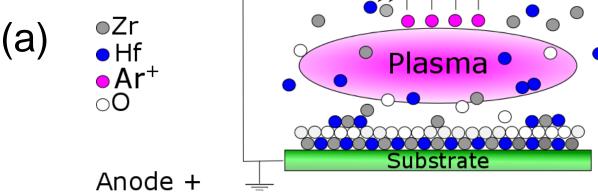
Cathode

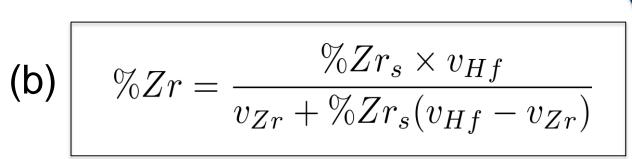
Titanium nitride (TiN) sputtering : (a) mecanisms; (b) and (c) Discharge vs. Nitrogen quantity give us the poisoned and metallic regims for (b) during current sputtering and (c) radiofrequency sputtering





(d) XRD of a ~1µm sample realized at Institut des Matériaux Jean Rouxel (Nantes) compared with a (e) Textured TiN sample realized at INL by sputtering in both cases.

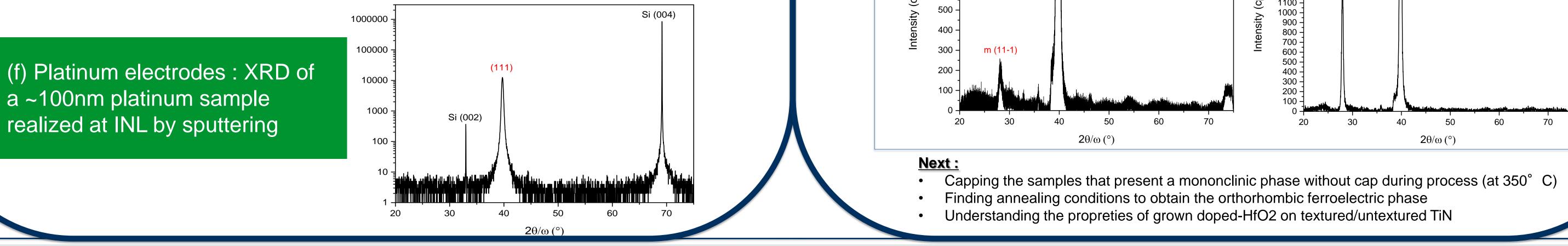




Oxides

(a) HfZrO<sub>2</sub> sputtering ideal mecanisms (b) Formula to deduce the quantity of necessary zirconium to theoritically realise stoichiometric  $HfZrO_2$ .

		Materials	Templates	Technics	Temperatures during deposition	phases
(C)	<text></text>	<ul> <li>HfO<sub>2</sub></li> <li>Al-doped HfO<sub>2</sub></li> </ul>	<ul> <li>Si</li> <li>Si/SiO<sub>2</sub></li> <li>Si/SiO<sub>2</sub>/Pt</li> <li>Si/SiO<sub>2</sub>/TiN</li> </ul>	ALD	200°C (holder temperature)	amorphous
		<ul> <li>HfO<sub>2</sub></li> <li>Zr-doped HfO<sub>2</sub></li> </ul>	<ul> <li>Si</li> <li>Si/SiO<sub>2</sub></li> <li>Si/SiO<sub>2</sub>/Pt</li> <li>Si/SiO<sub>2</sub>/TiN</li> </ul>	Sputtering	Room temperature	amorphous
		<ul> <li>HfO<sub>2</sub></li> <li>Zr-doped HfO<sub>2</sub></li> </ul>	• Si/SiO <sub>2</sub>	Sputtering	350°C (sample temperature)	amorphous
		<ul> <li>HfO<sub>2</sub> (b) (15nm)</li> <li>Zr-doped HfO<sub>2</sub> (c) (150nm)</li> </ul>	• Si/SiO <sub>2</sub> /Pt	Sputtering	350°C (sample temperature)	monoclinic
	(b) 900 800 700 (c) 600	Pt (111)	$q=3^{\circ}$ (C) 2000 1900 1800 1700 1600 1500 1400 $\widehat{y}$ 1200 $\widehat{y}$ 1100	) - ) - ) - ) - ) - ) - ) - ) - ) - ) -	Pt (111)	q=3°



### References

[1] T.S. Böscke et al., "Ferroelectricity in Hafnium Oxide Thin Films Ferroelectricity in Hafnium Oxide Thin Films," Applied Physics Letters 102903, no. 2011 (2011): 0–3, doi:10.1063/1.3634052. [2] Min Hyuk Park et al., "Ferroelectricity and Antiferroelectricity of Doped Thin HfO2 -Based Films," Advanced Materials, 2015, 1811–31, doi:10.1002/adma.201404531 [3] S Starschich and U Boettger, "An Extensive Study of the Influence of Dopants on the Ferroelectric Properties of HfO 2," Journal of Materials Chemistry C, 2016, doi:10.1039/C6TC04807B. [4] Sayeef Salahuddin and Supriyo Datta, "Use of Negative Capacitance to Provide Voltage Amplification for Low Power Nanoscale Devices," Nano Letters 8, no. 2 (2008): 405–10, doi:10.1021/nl071804g. [5] Kai-shin Li et al., "Sub-60mV-Swing Negative-Capacitance FinFET without Hysteresis," 2015, 620–23. [6] Erich H. Kisi and Christopher J. Howard, "Crystal Structure of Orthorhombic Zirconia in Partially Stabilized Zirconia," Journal of the American Ceramic Society, 72 (1989): 1657, doi:10.1361/asmhba0003801.







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