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## To cite this version:

Alain Cournier, Stephane Rovedakis, Vincent Villain. The First Fully Polynomial Stabilizing Algorithm for BFS Tree Construction. Information and Computation, 2019, 265, pp.26-56. 10.1016/j.ic.2019.01.005 . hal-02470990

HAL Id: hal-02470990
https://hal.science/hal-02470990
Submitted on 22 Oct 2021

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# The First Fully Polynomial Stabilizing Algorithm for BFS Tree Construction ${ }^{\text {and }}$, 动 

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#### Abstract

The construction of a spanning tree is a fundamental task in distributed systems which allows to resolve other tasks (i.e., routing, mutual exclusion, network reset). In this paper, we are interested in the problem of constructing a Breadth First Search (BFS) tree. Stabilization is a versatile technique which ensures that the system recovers a correct behavior from an arbitrary global state resulting from transient faults.

A fully polynomial algorithm has a round complexity in $O\left(d^{a}\right)$ and a step complexity in $O\left(n^{b}\right)$ where $d$ and $n$ are the diameter and the number of nodes of the network and $a$ and $b$ are constants. We present the first fully polynomial stabilizing algorithm constructing a BFS tree under a distributed daemon. Moreover, as far as we know, it is also the first fully polynomial stabilizing algorithm for spanning tree construction. Its round complexity is in $\Theta\left(d^{2}\right)$ and its step complexity is in $O\left(n^{6}\right)$.

To our knowledge, since in general the diameter of a network is much smaller than the number of nodes $(\log (n)$ in average instead of $n[1])$, this algorithm reaches the best compromise of the literature between the complexities in terms of rounds and in terms of steps.


Keywords: Distributed systems, Fault-tolerance, Stabilization, Spanning tree construction.

## 1. Introduction

The computation of a solution to a given problem in a distributed system requires the collaboration of a part or the whole system. To this end, some amount of information have to be exchanged between the elements of the system involved in the computation. One important aspect in the design of distributed algorithms is to solve a problem in an efficient way. In the context of distributed fault-tolerance, the communication and time complexities to solve a given problem by a distributed algorithm are important measures, expressed respectively in terms of number of exchanged messages and rounds. Indeed, every execution of an algorithm is decomposed into rounds, where a round contains a single computation step of each processor of

[^0]the system. Self-stabilization introduced first by Dijkstra in [3] and later publicized by several books [4,5] is one of the most versatile techniques to handle transient faults arising in distributed systems. A distributed algorithm is self-stabilizing if starting from any arbitrary global state (due to faults or attacks) the system is able to recover from this catastrophic situation in finite time without external (e.g., human) intervention. To ease the design of fault-tolerant algorithms, Dijkstra introduced in [3] also an abstraction model of the message passing model, the locally shared memory model. In this model, each processor can directly read the variables of its neighbors in a computation step (or steps), instead of exchanging messages. This model was largely used for the design of self-stabilizing algorithms. Since the introduction of the self-stabilization paradigm, a lot of effort has been put to design self-stabilizing algorithms with a low time complexity (or convergence time) required to reach a correct system state. This leads to the introduction of several stabilization alternatives, in particular Snap-stabilization which have been proposed by Bui et al. [6]. This form of stabilization characterizes self-stabilizing algorithms with a convergence time of 0 (see Definition 2). As explained by Cournier et al. in [7], the zero convergence time does not guarantee that all components of the system never work in a fuzzy manner. A snapstabilizing protocol ensures that any request is satisfied despite the arbitrary initial configuration, while a self-stabilizing protocol often needs to be repeated an unbounded number of times before guaranteeing the satisfaction of any request.

Although the time complexity given in (asynchronous) rounds allows to capture the execution rate of the slowest process in any execution, another crucial measure is the number of steps needed by an algorithm to compute the desired solution. In fact, the step complexity reflect more accurately the workload generated by a distributed algorithm. Moreover, one can observe that the communication complexity is deeply related to the number of steps. Indeed, this measure reflects the amount of information exchanged to compute the solution, especially in the context of stabilizing algorithms in which in a step a processor sends at least one message for incorrect local state detection in the neighborhood. Recently, reducing the communication complexity becomes a new challenge to increase the practical use of self-stabilizing algorithms. Contrary to non faulttolerant distributed algorithms, self-stabilizing distributed algorithms may introduce an overhead on the communication complexity before and after reaching a correct global state. Indeed, this is due to two facts: (i) self-stabilizing algorithms can start from an arbitrary state (not only from a defined initial state) of the system and converge to a correct global state, and (ii) they have to perpetually exchange information to always allow the correction of the nodes' state (the latter fact is inherent to any adaptive approach). Devismes et al. [8] and Masuzawa et al. [9, 10] study the communication efficiency of self-stabilizing algorithms after the convergence to a correct state of the system. The goal is to determine if it is possible to design self-stabilizing algorithms which work correctly while avoiding the communication between pairs of neighbors in the system. To this end, they introduced several complexity measures to analyze the communication efficiency of a protocol, among them $k$-efficiency and $\diamond$ - $k$-stable. The first measure establishes an upper bound $k$ on the number of neighbors with which each node communicates at each step. Note that every distributed self-stabilizing algorithm is obviously at most $\Delta$-efficient since each processor communicates with all its neighbors [8]. The second complexity measures is the same as the former but focusing only on computation steps after a correct system state is reached. The authors consider the maximal independent set, the maximal matching, the spanning tree construction and the minimal connected dominating set problem. They present self-stabilizing distributed algorithms achieving better communication complexities than existing algorithms, otherwise impossibility results are given. Kutten et al. [11] addressed also the problem of reducing the communication complexity of distributed self-stabilizing algorithms. Self-stabilizing
algorithms with low communication are proposed for several tasks: spanning tree construction, distributed reset and unison. The given algorithms have a low communication complexity during the whole algorithm's execution, i.e., before and after reaching a correct system state. To achieve low communication, randomized algorithms are designed (instead of deterministic ones) allowing to by-pass the constraint on the number of communicating neighbors. In fact, a reduction on the number of communicating neighbors in the case of deterministic algorithms induces an increase of the convergence time.

Contrary to the above cited works, we adopt another approach to achieve communication efficiency. In this paper, we address this problem by bounding and reducing the number of steps needed to solve a distributed problem, and we introduce a particular class of distributed algorithms, called fully polynomial algorithms, in the context of the locally shared memory model. These algorithms are characterized by efficient complexities to solve a distributed task, i.e., a round complexity polynomial in the network diameter and step complexity polynomial in the network size.

Definition 1 (Fully polynomial algorithms). A distributed algorithm is fully polynomial if it has a round complexity in $O\left(d^{a}\right)$ and a step complexity in $O\left(n^{b}\right)$ where $d$ and $n$ are the diameter and the number of nodes of the network and $a$ and $b$ are constants.

This class of distributed algorithms is particularly suitable in the context of large scale networks. Indeed, these systems are characterized by an ever growing size and a low diameter in average. In this context, it is of primary importance to design algorithms with a round complexity independent of the network size and a low communication complexity (e.g., achieved via a polynomial step complexity). Distributed fully polynomial algorithms allow to have an execution with a high degree of parallelism (round complexity defined by a polynomial function of the network diameter, and not dependent of the network size), while the communication complexity is bounded (step complexity defined by a polynomial function of the network size, since each processor executes at least a step). Having fault-tolerant distributed algorithms which belong to this class of algorithms is of theoretical and practical importance, since as large scale systems have a high number of processor they are highly subject to crash faults or topological changes.

So, an interesting question addressed in this paper can be the following: Do there exist fully polynomial self-stabilizing algorithms solving global distributed tasks?

Contributions. To our knowledge there exists no fully polynomial stabilizing algorithm solving global distributed tasks in the literature ${ }^{1}$. In this paper, we show that this class of algorithms related to global distributed tasks is not empty. To this end, we consider the global distributed task of constructing a spanning tree rooted at a designated node, due to its importance in distributed systems. As presented in the Related work section below, to our knowledge the existing self-stabilizing spanning tree constructions cannot belong to the class of algorithms defined in Definition 1. Indeed, the existing stabilizing algorithms have either a round and step complexity both polynomial in $n$, or a round complexity polynomial in $d$ but an exponential or unbounded step complexity, where $d$ and $n$ are respectively the diameter and the size of the network. Therefore, we present the first fully polynomial stabilizing algorithm for the construction of a spanning tree with a round complexity lower than $\Theta\left(\max \left(d^{2}, n\right)\right)$. Our algorithm computes a BFS tree in $\Theta\left(d^{2}\right)$ rounds with a polynomial number of steps in $O\left(n^{6}\right)$ (the step complexity is $O\left(m n^{4}\right)$ and

[^1]$m \ll n^{2}$ ) under a distributed daemon without any fairness assumptions (which is the weakest daemon), with $m$ the number of edges in the network. To our knowledge, since in general the diameter of a network is much smaller than the number of nodes $(\log (n)$ in average instead of $n$ [1]), this algorithm reaches the best compromise of the literature between the complexities in terms of rounds and in terms of steps. Moreover, the algorithm proposed in this paper is fully polynomial for any topology, i.e., we do not consider a particular network topology. Finally, this BFS tree construction is based on a snap-stabilizing algorithm given in this paper resolving a sub-problem, called the Question-Answer problem, in which each node requests a permission (delivered by a subset of network nodes) in order to perform a defined computation, which is of independent interest.

Related work. One basic task performed in every network is the transmission of information, which can be done by resolving the construction of a spanning tree (acyclic virtual structure with no cycle and interconnecting all the nodes of a network). In distributed systems, the construction of a spanning tree is commonly used to design algorithms resolving other distributed tasks, like routing, token circulation or message broadcasting in a network. Hence, there are a lot of works which study this task. There are many different spanning tree construction problems guaranteeing various properties, e.g., a spanning tree of minimum weight or a spanning tree of minimum diameter. A survey on several self-stabilizing tree constructions can be found in [12].

Arora and Gouda [13] are interested in designing an algorithm which allows to reset a network by resetting the state of the nodes when a fault is detected in a dynamic network. To this end, the authors present a self-stabilizing reset algorithm which constructs a BFS tree in $O\left(N^{2}\right)$ rounds, with $N$ an upper bound on the number of nodes in the network. Dolev, Israeli and Moran [14] give one of the first self-stabilizing algorithms for the construction of a spanning tree. In their work, a BFS tree is used to resolve the mutual exclusion problem. Afek, Kutten, and Yung [15] have proposed independently from [14] a self-stabilizing algorithm constructing a BFS tree. This algorithm uses the node identifiers to construct a BFS tree rooted at the node of highest identifier in the network in $O\left(n^{2}\right)$ rounds. Moreover, it incorporates a mechanism to transmit requests and acknowledgments for the addition of new nodes in a tree. The root of a tree allows the connection of new nodes if no higher identifier is detected in the network. Chen et al. proposed a self-stabilizing spanning tree construction algorithm [16], which was improved later to construct a BFS tree [17]. The time complexity of these two algorithms is $\Theta(n)$ rounds. Ducourthial et al. proposed a self-stabilizing algorithm for the construction of a BFS tree which is a modification of the algorithm proposed in [17]. As analyzed in [18], this algorithm has a time complexity of $\Theta(d)$ rounds, where $d$ is the diameter of the network. More recently, Burman and Kutten [19] give a solution to construct a Shortest Path tree in $O(d)$ rounds, extending to the message passing model a solution proposed by Awerbuch et al. [20]. Datta, Larmore, and Vemula [21] resolves the election problem by constructing a silent self-stabilizing BFS tree in $O(n)$ rounds. The silent property is to guarantee that when a legitimate configuration ${ }^{2}$ is reached the values stored in the registers do not change anymore. $O(d)$ additional rounds are needed to the algorithm to become silent. There are many other works on the self-stabilizing construction of a spanning tree with additional properties, e.g., DFS tree [22, 23]. There are also works which study the construction of a spanning tree with a low memory complexity. For example, Johnen and Beauquier give a self-stabilizing token circulation allowing to construct a DFS tree using

[^2]$O(\log \Delta)$ bits [24], whereas Johnen proposes a self-stabilizing algorithm for the construction of a BFS tree using $O(\Delta)$ bits [25], with $\Delta$ the maximum node degree in the network.

Table 1: Distributed stabilizing algorithms for the construction of spanning trees. $n, d$ and $\Delta$ are respectively the number of nodes, the diameter and the maximum degree in the network, while $N$ is an upper bound of $n, D$ is an upper bound of $d$, and $\operatorname{Max}$ is the maximum height value in the tree of a node in the initial configuration. The silent property for a self-stabilizing algorithm is to guarantee that when a legitimate configuration is reached the values stored in the registers do not change anymore.

|  | References | Round complexity | Step complexity | Memory complexity | $\begin{gathered} \text { Silent } \\ \text { property } \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| BFS | [13] | $O\left(N^{2}\right)$ | Undetermined | $O(\log (n))$ | Yes |
|  | [14] | $O(d)$ | Undetermined | $O(\Delta \log (n))$ | Yes |
|  | [15] | $O\left(n^{2}\right)$ | Undetermined | $O(\log (n))$ | Yes |
|  | [17] | $\Theta(n)$ | $\Omega\left(2^{n}\right)^{\dagger}$ | $O(\log (n))$ | Yes |
|  | [26] | $\Theta(d)$ | $O\left(n(M a x+d)^{n}\right)^{\dagger}$ | $O(\log (n))$ | Yes |
|  | [20] | $O(D)$ | $\Omega\left(2^{\frac{D}{2}}\right)^{\ddagger}$ | $O\left(\log ^{2}(n)\right)$ | Yes |
|  | [25] | $\Omega\left(d^{2}\right)$ | Undetermined | $O(\log (\Delta))$ | No |
|  | [19] | $O(d)$ | Undetermined | $O\left(\log ^{2}(n)\right)$ | Yes |
|  | [21, 27] | $O(n)$ | $\Omega\left(n^{\log _{2}(n)}\right)^{\mp}$ | $O(\log (n))$ | Yes |
|  | [28] | $O(n)$ | $\Omega\left(2^{\frac{n}{4}}\right)^{\mp}$ | $O(\log (n))$ | Yes |
|  | [7] | $\Theta\left(d^{2}+n\right)$ | $O\left(\Delta n^{3}\right)$ | $O(\log (n))$ | No |
|  | This paper | $\Theta\left(d^{2}\right)$ | $O\left(n^{6}\right)$ | $O(\log (n))$ | Yes |
| Any | [16] | $O(n)$ | $\Omega\left(2^{n}\right)^{\dagger}$ | $O(\log (n))$ | Yes |
|  | [29] | $O(n)$ | $\Theta\left(n^{2} d\right)$ | $O(\log (n))$ | Yes |
|  | [30] | $\Theta(n)$ | $\Theta\left(n^{2}\right)$ | $O(\log (n))$ | Yes |
| DFS | [22] | $O(d n \Delta)$ | Undetermined | $O(n \log (\Delta))$ | Yes |
|  | [23] | $O\left(n^{2}\right)$ | $O\left(n^{3}\right)$ | $O(\log (n))$ | Yes |
|  | [31] | $O(n)$ | $O\left(n^{2}\right)$ | $O(n \log (n))$ | Yes |
|  | [7] | $O(n)$ | $O\left(\Delta n^{3}\right)$ | $O(\log (\Delta+n))$ | No |
| ${ }^{\dagger}$ This is detailed in the analysis given in [18]. <br> $\ddagger$ This is detailed in the analysis given in [32]. <br> $\mp$ This is detailed in the analysis given in [33]. |  |  |  |  |  |

Some of the algorithms cited above are optimal in terms of rounds for the construction of an arbitrary spanning tree or a BFS tree. As discussed in the Introduction section, the number of steps required to compute a solution is an important criterion since it is interesting for the communication complexity. As demonstrated in the analysis given in [18, 32], the algorithms presented in $[16,17]$ have an exponential number of steps, whereas the one given in [26] has a finite number of steps (with a lower and upper bounds respectively of $\Omega\left(n^{2} M a x\right)$ and $O\left(n(M a x+d)^{n}\right)$ steps, where Max is the maximum height value of a node in the initial global state). Moreover, Awerbuch et al. [20] proposed a solution to construct a Shortest Path tree with an optimal time complexity of $O(D)$ rounds, however as demonstrated recently by Devismes et al. [32] it stabilizes in at least $\Omega\left(2^{D / 2}\right)$ steps (with $D$ an upper bound of the network diameter $d$ ). Datta, Larmore, and Vemula [21,27] resolves the election problem by constructing a BFS tree which stabilizes in $O(n)$ rounds. Later, the same authors proposed in [28] a second self-stabilizing algorithm for the election problem which is very similar to the first one proposed in [21, 27]. To elect a leader in the network, a breadth-first search spanning tree rooted at the elected leader is
also build. Notice that there is no explicit pointer to the parent but it can easily be computed. The main difference between these two algorithms is the way to manage fake leader identifiers, since the system can be placed in incorrect configurations. The algorithm proposed in [28] exploits a special value 0 , smaller than any node identifier, which is propagated in the network to clean fake identifiers. However, Altisen et al. [33] have shown recently exponential step complexities for these two algorithms. Indeed, the first algorithm proposed in [21,27] has a step complexity of at least $\Omega\left(n^{\log _{2}(n)}\right)$ steps, while the second algorithm proposed in [28] has a step complexity of at least $\Omega\left(2^{\frac{n}{4}}\right)$. Kosowski and Kuszner give a self-stabilizing algorithm to construct a spanning tree with a bounded number of steps $\left(\Theta\left(n^{2} d\right)\right.$ steps are needed) [29]. Recently, in [30] Cournier presented a new stabilizing solution for the construction of an arbitrary spanning tree improving the bound on the number of steps of [29]. This algorithm runs in $\Theta(n)$ rounds and $\Theta\left(n^{2}\right)$ steps. Cournier, Devismes, and Villain proposed a snap-stabilizing solution for the problem of Propagation of Information with Feedback (PIF) [34]. A spanning tree rooted at the source node with the information to propagate is constructed. This algorithm uses a question mechanism to ensure that every processor in the network belongs to the constructed spanning tree, and to guarantee that every processor receives the propagated information. Cournier, Devismes, and Villain give also an efficient transformer to obtain a snap-stabilizing version of a distributed algorithm [7]. They use this transformer to obtain a snap-stabilizing algorithm for the BFS tree problem which runs in $O\left(d^{2}+n\right)$ rounds and $O\left(\Delta n^{3}\right)$ steps, with $\Delta$ the maximum degree of a node in the network. Table 1 summarizes the time complexities (round and step complexities) of some self-stabilizing tree construction algorithms.

Notice that the algorithm presented in [7] does not satisfy the definition of a fully polynomial algorithm since it has a round complexity which is related with the network size. Moreover, the algorithm proposed in [26] has a round complexity only polynomial in $d$ and a bounded step complexity, but its step complexity is dependent on the maximum height value Max in the initial configuration. This value can be arbitrarily high (e.g., exponential on the network size) which does not satisfy the definition of a fully polynomial stabilizing algorithm. However, if one considers the latter algorithm in a non fault-tolerant context, then the algorithm satisfies the fully polynomial property since the round and step complexity are respectively $\Theta(d)$ rounds and $O(d n)$ steps (each node can only execute an action to improve its height, which can be done in time polynomial in $d$ ). Therefore, another interesting question can be: Does the stabilization property prevent some algorithms to satisfy the fully polynomial property?

Outline of the paper. The paper is organized as follows. In Section 2 we present the model assumed in this paper. Section 3 gives few insights on the difficulties to design fully polynomial stabilizing distributed algorithms for the spanning tree construction problem. In Section 4, we give a high-level overview of the fully polynomial stabilizing algorithm we propose and give an explanation about the time complexity to solve the BFS tree problem. We then present in detail our approach to construct a BFS tree in Section 5, based on a snap-stabilizing algorithm to the Question-Answer problem given in Section 6. We describe in Section 7 how these stabilizing algorithms are composed together. An example of execution of the algorithm is detailed in Section 8. Finally, Section 9 shows the correctness of our algorithm, and we conclude in the last section.

## 2. Model

Notations. We consider a network as andirected connected graph $G=(V, E)$ where $V$ is a set of nodes (or processors) and $E$ is the set of bidirectional asynchronous communication links. We state that $n$ is the size of $G(|V|=n)$. We assume that the network is rooted, i.e., among the processors, we distinguish a particular one, $r$, which is called the root of the network. In the network, $p$ and $q$ are neighbors if and only if a communication link $(p, q)$ exists (i.e., $(p, q) \in E)$. Every processor $p$ can distinguish all its links. To simplify the presentation, we refer to a link $(p, q)$ of a processor $p$ by the label $q$. We assume that the labels of $p$, stored in the set $N e i g_{p}$, are locally ordered by $\prec_{p}$. We also assume that $N e i g_{p}$ is a constant input from the system. $\Delta$ is the maximum degree of the network (i.e., the maximal value among the local degrees of the processors). A tree $T=\left(V_{T}, E_{T}\right)$ is an acyclic connected subgraph such that $V_{T} \subseteq V$ and $E_{T} \subseteq E$, where the root of tree $T$ is noted by $\operatorname{root}(T)$. Moreover, any processor has a parent in a tree $T$ which is the neighbor on the path leading to $\operatorname{root}(T)$. A processor $p \in V_{T}$ with at least two neighbors in tree $T$ is called an internal processor and a leaf processor otherwise.

Programs. In our model, protocols are semi-uniform, i.e., each processor executes the same program except $r$. We consider the local shared memory model of computation. In this model, the program of every processor consists of a set of variables and an ordered finite set of actions inducing a priority. This priority follows the order of appearance of the actions into the text of the protocol. A processor can write to its own variable only, and read its own variables and that of its neighbors. Each action is constituted as follows: $<$ label $>::<$ guard $>\rightarrow<$ statement $>$. The label is used as a name to refer to an action in the program. The guard of an action in the program of $p$ is a boolean expression involving variables of $p$ and its neighbors. The statement of an action of $p$ updates one or more variables of $p$. An action can be executed only if its guard is satisfied. The state of a processor is defined by the value of its variables. The state of a system is the product of the states of all processors. We will refer to the state of a processor and the system as a (local) state and (global) configuration, respectively. We note $\mathcal{C}$ the set of all possible configuration of the system. Let $\gamma \in \mathcal{C}$ and $A$ an action of $p(p \in V)$. $A$ is said to be enabled at $p$ in $\gamma$ if and only if the guard of $A$ is satisfied by $p$ in $\gamma$. Processor $p$ is said to be enabled in $\gamma$ if and only if at least one action is enabled at $p$ in $\gamma$. When several actions are enabled simultaneously at a processor $p$ : only the priority enabled action can be activated.

Let a distributed protocol $P$ be a collection of binary transition relations denoted by $\mapsto$, on $\mathcal{C}$. An execution of a protocol $P$ is a maximal sequence of configurations $e=\left(\gamma_{0}, \gamma_{1}, \ldots, \gamma_{i}, \gamma_{i+1}, \ldots\right)$ such that, $\forall i \geq 0, \gamma_{i} \mapsto \gamma_{i+1}$ (called a step) if $\gamma_{i+1}$ exists, else $\gamma_{i}$ is a terminal configuration. Maximality means that the sequence is either finite (and no action of $P$ is enabled in the terminal configuration) or infinite. All executions considered here are assumed to be maximal. $\mathcal{E}$ is the set of all possible executions of $P$. We can give in Definition 2 a definition of Snap-Stabilization.

Definition 2 (Snap-Stabilization). Let $\mathcal{T}$ be a task and $\mathcal{F}$ be a specification of $\mathcal{T}$. A protocol $P$ is snap-stabilizing for $\mathcal{F}$ if and only if starting from any configuration every execution of $P$ satisfies $\mathcal{F}$.

As we already said, each execution is decomposed into steps. Each step is shared into three sequential phases atomically executed: (i) every processor evaluates its guards, (ii) a daemon (also called scheduler) chooses some enabled processors, (iii) each chosen processor executes its priority enabled action. When the three phases are done, the next step begins.

A daemon can be defined in terms of fairness and distributivity. In this paper, we use the notion of unfairness: the unfair daemon can forever prevent a processor from executing an action except if it is the only enabled processor. Concerning the distributivity, we assume that the daemon is distributed meaning that, at each step, if one or more processors are enabled, then the daemon chooses at least one of these processors to execute an action.

We consider that any processor $p$ executed a disabling action in the computation step $\gamma_{i} \mapsto$ $\gamma_{i+1}$ if $p$ was enabled in $\gamma_{i}$ and not enabled in $\gamma_{i+1}$, but did not execute any protocol action in $\gamma_{i} \mapsto \gamma_{i+1}$. The disabling action represents the following situation: at least one neighbor of $p$ changes its state in $\gamma_{i} \mapsto \gamma_{i+1}$, and this change effectively made the guard of all actions of $p$ false in $\gamma_{i+1}$.

To compute the time complexity, we use the definitions of round and step. The definition of round captures the execution rate of the slowest processor in any execution. Given an execution $e(e \in \mathcal{E})$, the first round of $e$ (let us call it $e^{\prime}$ ) is the minimal prefix of $e$ containing the execution of one action (an action of the protocol or a disabling action) of every enabled processor from the initial configuration. Let $e^{\prime \prime}$ be the suffix of $e$ such that $e=e^{\prime} e^{\prime \prime}$. The second round of $e$ is the first round of $e^{\prime \prime}$, and so on. On the contrary, given an execution $e \in \mathcal{E}$ the definition of step allows to estimate more accurately the amount of computations needed by a distributed protocol to recover a correct behavior.

Therefore, the time complexity (or convergence time) of a stabilizing algorithm is the maximum time (in steps or rounds) over every possible execution under a considered daemon to reach a terminal (legitimate) configuration, starting from any initial configuration.

## 3. About the Spanning Tree problem

As presented in Table 1 in Related work, the existing self-stabilizing spanning tree constructions have either a polynomial step complexity but require $\Omega(n)$ rounds, or a round complexity polynomial on the network diameter but with an unbounded or exponential step complexity. So, in this section we focus on the difficulty to obtain a trade-off between the round and step complexities for the spanning tree construction problem.

A spanning tree $T=\left(V^{\prime}, E^{\prime}\right)$ of a graph $G=(V, E)$ is a connected sub-graph spanning all the processors of the network (i.e., (i) $V^{\prime}=V$, (ii) $E^{\prime} \subseteq E$ and (iii) for any pair of nodes $(x, y)$ there exists a unique path between $x$ and $y$ in $T$ ). As a consequence, $T$ is an acyclic sub-graph and we have that $\left|E^{\prime}\right|=|V|-1$. Most of the existing spanning tree approaches construct a spanning tree of the network rooted at a given node $r$. To this end, a solution is computed by selecting a neighbor $u \in V$ for each node $v \in V$ as its parent in the tree, i.e., the closest neighbor of $v$ on the path to the root in the computed tree $T$. Generally, a level (or height) is associated to each node $v$ in $T$ indicating the number of nodes (or hops) between $v$ and $r$. The maintenance of height values at each node is largely used to solve the spanning tree construction task since it allows to easily detect cycles (e.g., by comparing them with the network size or with other values in the neighborhood).

In the following, we consider three self-stabilizing approaches for the spanning tree problem whose techniques are the most related to the solution we propose in this paper. We give some insights regarding the time complexity (number of rounds and steps) needed to construct a solution to the considered problem. To compare these algorithms from the step complexity point of view, we will consider the number of changes on the height and parent for any node (except the root) until reaching a legitimate configuration.

### 3.1. Classic approach

A classic approach to construct a spanning tree rooted at a node $r$ is to use a simple rule based on network distances: Each node is connected to the neighbor closest (according to the network distances) to $r$ in the network. To this end, each node maintains its height in the tree which yields to the construction of a BFS tree. The root $r$ has a height equal to zero, while every node (except the root) selects as its parent the neighbor with the smallest height (and smallest identifier or channel port in case of neighbors with equal height). This approach was used in several works to design a self-stabilizing BFS algorithm, e.g., as the algorithm proposed by Ducourthial et al. in [26].

By induction on the network distances starting from the root node, it is easy to show that a BFS tree can be constructed in a self-stabilizing manner in $O(d)$ rounds, with $d$ the network diameter. Indeed, each new layer of nodes at distance $k$ from the root is stabilized in a constant number of additional rounds, when the layers at distance $j, 0 \leq j<k$, have been stabilized.

However, the use of this rule without any other mechanism can yield to a high step complexity. Indeed, this approach is dependent of the height values initially present in the network, as shown in Figure 1.

(a)

(b)

(c)

Figure 1: Example used to show that the classic spanning tree approach has a step complexity related to the maximum height value in the network.

In Figure 1(a), a special network is used which is composed of $k$ cycles (we take $k=$ $(n-2) / 3$ in Figure 1) of three nodes (this can be generalized by using cliques of at least three nodes) which are connected to the root $r$ via another node $x$. We can consider a special initial configuration of the system shown in Figure 1(b), where the root has a zero height and all the nodes have a height equal to one, except $x$ which has the maximum possible height value noted Max. Node $x$ is connected to $r$, and the parent link of the other nodes follows the $k$ cycles. From this particular initial configuration, we can have the following distributed execution. Each node in the $k$ cycles can execute concurrently the rule of the algorithm to increase their height until Max. So, after Max $\times 3 k$ steps a new configuration is reached where all the nodes have a height value equal to Max (except the root $r$ ). From this new configuration, each node can execute its algorithm to select the neighbor of minimum height and to update its height to be equal to its parent's height plus one. The following order is used to reach in $n-1$ additional steps the legitimate configuration shown in Figure 1(c): $x$ is executed to correct first its height to be equal to 1 , then for $1 \leq i \leq k$, nodes $a_{i}$ select $x$ as their parents with a height equal to 2 , nodes $b_{i}$ and $c_{i}$ select $a_{i}$ as their parent with a height equal to 3 . This approach leads to a step complexity of $M a x \times 3 k+n-1=\Omega(\operatorname{Max} \times n+n)$ steps, since we have $k=(n-2) / 3$.

We can notice that the number of parent link changes of a node is also dependent of the Max value, as illustrated in Figure 2. To demonstrate this point, we are interested in the node
$y$ in the network of Figure 2(a). Consider the initial configuration in Figure 2(b). We can have the following distributed execution. For $i, 1 \leq i \leq k$, each pair of nodes $a_{i}$ and $b_{i}$ execute concurrently their algorithm to increase their height value by one. This leads $y$ to be connected $k-1$ times to the new parent $b_{i+1}$. This phase can be iterated Max times (where $y$ is connected again to $b_{1}$ between each iteration). We can observe that the node $y$ suffers from $\Omega(\operatorname{Max} \times k)=$ $\Omega(\operatorname{Max} \times n)$ parent link changes, since $k=(n-3) / 2$ in Figure 2.


Figure 2: Example used to show that the number of parent link changes with the classic spanning tree approach is related to the maximum height value in the network.

As presented above with Figure 1 and 2, this approach has a step complexity dependent of the maximum initial value in the network, noted Max, which can be an exponential function of the network size. Therefore, to solve the spanning tree construction problem, we cannot establish a polynomial step complexity using only this simple rule. In the following, we present two other approaches for the spanning tree construction problem which have a polynomial step complexity. To achieve this goal, the crucial point considered in these approaches is to bound the number of parent link changes.

### 3.2. First approach with polynomial bound in steps

To our knowledge, Kosowski et al. proposed in [29] the first self-stabilizing algorithm to construct a spanning tree with a polynomial step complexity. Like the previous approach, height values are maintained for the selection of a parent in the neighborhood of each node. Contrary to the previous classic approach, this algorithm assigns a height to each node by increasing these values using the following rule: Each node $v$ can increase its height value if all its neighbors have an equal or higher height value. In this case, $v$ takes a new height equal to the maximum height in its neighborhood plus one. The idea behind this rule is to reach a configuration in which there is a path between the root and every node in the network with increasing height values (note that the difference on height values between two adjacent nodes in the spanning tree can be higher than one). The configurations which satisfy this property are legitimate and define a correct spanning tree, since no cycle is introduced on the described paths because of increasing height values. We can construct a rooted spanning tree by introducing a second rule (only enabled if the first one is not) to select as the parent of each node the neighbor of smallest height (and of minimum identifier or channel port in case of neighbors with equal height).

This approach has a round complexity of $O(n)$ rounds. This round complexity comes from the fact that for each node the height values must be corrected until having at least one path leading to the root with only increasing height values. The length of such a path can be of at most $n$ nodes, and each node having no neighbor with a smaller height must execute its algorithm.

(a)


(b)


Figure 3: An example used to show the lower bound on step complexity which matches the upper bound proved in [29]. Here we consider $k=3$, but this example can be generalized with a longer chain (nodes $x_{i}$ ) and a bigger clique (nodes $y_{i}$ ), with $0 \leq i \leq k$.

We will now analyze the step complexity of this algorithm by showing a lower bound of $\Omega\left(d n^{2}\right)$ steps which matches asymptotically the upper bound of $O\left(d n^{2}\right)$ steps shown in [29]. To this end, we consider the network topology given in Figure 3(a) which is composed of a clique of four nodes ( $y_{k}$ with $0 \leq k \leq 3$ ) connected to the root $r$ via a chain of three nodes ( $x_{k}$ with $0 \leq k \leq 3$ and $r=x_{0}$ ). Starting from the initial configuration of Figure 3(b), only the node $y_{0}$ can execute the first rule to increase its height value to 5 (maximum height value of neighbors plus one). This yields to the execution of the second rule for each node of the clique (except $y_{0}$ and $y_{1}$ ) to select $y_{1}$ as their new parent, which involves $k-1$ steps. This process is iterated in the clique when a new node execute its first rule. More formally, we have $j, 0 \leq j \leq k$, iterations of this process in the clique as following: $y_{j}$ executes the first rule to increase its height, then all the nodes $y_{i}$ (except $y_{0}$ and $y_{j}$ ) execute the second rule to select $y_{j+1}$ as their new parent. Thus, after $k(k-1)$ steps we reach the configuration given in Figure 3(c) in which no node $y_{k}$ can execute one of its rule. However, the system has not reached a legitimate configuration. Indeed, we have not increased the height values along the chain composed of nodes $x_{k}$. So, from configuration of Figure 3(c) only the node $x_{3}$ can execute its first rule to increase its height to 6 , which triggers a new phase defined by the procedure described above in the clique and involving $k(k-1)$ additional steps. Finally, we can observe that we have at most $k-1$ phases to reach the legitimate configuration given in Figure 3(d). In each phase, the node $x_{i}, 1 \leq i \leq k$, (by decreasing order of $i$ ) executes its first rule followed by the execution procedure described in the clique. Therefore, we have a step complexity of $\Omega\left(k^{3}\right)=\Omega\left(d n^{2}\right)$, since the first factor $k$ is dependent of the length of the chain and in the example of Figure 3 we have $k=(n-2) / 2$.

A crucial point with this second approach is that the step complexity of the algorithm is not function of the maximum height value initially present in the network. Moreover, we can notice that the step complexity can be bounded by the fact that the parent link can be changed at most $n^{2}$ for each node, as illustrated in the example of Figure 3. However, the number of parent link
changes for a node is important and can be a problem from an application point of view using the spanning tree to broadcast information. So, a natural question is to determine if it is possible to design a self-stabilizing algorithm with a smaller upper bound on parent link changes, which can also lead to a better step complexity. This is the motivation of the third approach presented in the next subsection.

### 3.3. An approach improving the bound in steps

Cournier [30] proposed a self-stabilizing spanning tree construction which improves the step complexity of the algorithm proposed in [29]. As discussed above, this work addresses the problem related to the maximum number of parent link changes per node. To achieve this goal, an additional mechanism is introduced to better control this aspect, allowing to reduce the upper bound on the step complexity.

This approach constructs an arbitrary spanning tree rooted at a designated node, noted $r$. As presented in the previous approaches, each node maintains a height and a parent pointer. A node is considered as the root of its tree if its parent has a higher height value than itself. These elements allow to define a forest of trees. To explain the additional mechanism used we introduce the notion of abnormal tree. A tree is called abnormal if the tree is rooted at a node $x$ such that $x \neq r$, in this case $x$ is an abnormal root. A tree is called correct if the tree is rooted at node $r$. In the algorithm proposed in [30], contrary to the previous approaches a node $v$ can change its parent link only when $v$ learns it belongs to an abnormal tree. Moreover, $v$ tries to connect to a tree which is not abnormal via a neighbor $u$ which does not learn it belongs to an abnormal tree. Every node can learn it belongs to an abnormal tree thanks to the use of the additional mechanism which works as follows. When a node $x$ detects it is an abnormal root, then it propagates down in its tree an Error status in the tree using a Propagation of Information with Feedback (PIF) protocol $[35,36,20,34]$. When every node in the subtree of $x$ learn they belong to an abnormal tree (i.e., when the PIF has finished), $x$ propagates down a Leave status allowing its descendants to leave the tree. At this point, the considered abnormal tree is frozen and no new node can be connected to it. Every node connected to the correct tree has a Correct status. Since a node with a Correct status cannot leave the tree it belongs to, in finite time (because the network size is finite) every node will belong to the correct tree rooted at $r$.

This approach has a round complexity of $O(n)$. Indeed, the additional mechanism allowing to freeze the abnormal trees, with the use of a PIF procedure, is performed in time function of the tree depth (i.e., in $O(n)$ rounds since the depth of a tree is bounded by $n$ ). So, in $O(n)$ rounds there is only a correct tree and abnormal trees in the forest, since all abnormal trees are frozen in $O(n)$ rounds. Moreover, no additional node can be connected to any abnormal tree of the forest. As a consequence, in at most $O(n)$ additional rounds every node in all the abnormal trees are connected to the correct tree, which is rooted at $r$.

To analyze the step complexity, we need to make some observations for each node in the network (except the root which can execute no action). First, no node with a Correct status can leave the tree it belongs to. As a consequence, the nodes in the correct tree never leave this one. Second, a node which learns it belongs to an abnormal tree can join another tree via a neighbor with a Correct status. Therefore, thanks to the mechanism used to freeze the abnormal trees a node belonging to an abnormal tree can join at most $n-1$ trees until joining the correct tree. Finally, the author shows that any node in a correct tree can perform at most a constant number of actions, these are used to maintain height values in the tree such that each node has a height value equal to its parent's height plus one. Putting it all together, this algorithm has a step complexity
of $O\left(n^{2}\right)$. Moreover, as with the approach proposed in [29] the step complexity is not dependent of the height values initially present in the network.

## 4. Outline of the proposed algorithm

In this section, we give an overview of the fully polynomial stabilizing approach proposed in this paper for the construction of a Breadth First Search tree. As stated in the previous sections, the main difficulty is to design an algorithm with a round complexity function of the network diameter with a polynomial step complexity.

The self-stabilizing algorithm we propose in this paper is based on the following principles to construct a BFS tree rooted at node $r$ :

1. First of all, each node $p$ always attempts to connect to the neighbor $q$ of minimum height belonging to the normal tree (i.e., the BFS tree rooted at $r$ ). To join the normal tree, $p$ must wait for a connection authorization from $q$ to select it as its new parent.
2. When a node $q$ receives a connection request from at least one neighbor $p$ which want to join the tree $q$ belongs to, $q$ has to check first that it belongs to the normal tree. This verification is performed by using a question-answer mechanism which allows $q$ to ask an authorization from the root of the tree it belongs to. Connection authorizations are only delivered to asking node belonging to the normal tree. Moreover, these authorizations are delivered by increasing order of the height of asking nodes in the normal tree. This allows to control the acceptance of new nodes from the lowest to the highest levels in the normal tree (defining the priority of requests). As soon as the verification is over for a asking node $q$ (i.e., a positive answer is delivered by the root $r$ ), $q$ delivers a connection authorization to its neighbors $p$ (out of the normal tree) allowing them to select $q$ as their new parent to join the normal tree.
3. If a node detects an inconsistency between its state and the state of its parents, then the node goes away from the tree it belongs to by setting its status to Error and selecting himself as a root. This allows the detection of abnormal trees.

Note that the use of connection authorizations to add new nodes in the normal tree allows to limit the number of parent changes performed by each node. Moreover, the construction of the BFS tree allows to reduce the cost of the verification (question-answer mechanism) executed in Point 2 above, since it is related to the diameter of the network in the worst case.

To construct a BFS tree, each processor must be connected to its closest neighbor toward the root $r$. As presented in Section 3, since there is a single root $r$ in the network this general approach allows to connect all the processors to the tree rooted at $r$ in a distributed and selfstabilizing manner in $O(d)$ rounds [26]. However due to a bad initial configuration, we also explained that the construction cannot be done using a polynomial step complexity because of a non polynomial number of connections of a node to a tree which is not rooted at $r$. This was the motivation to bound the number of connections to this kind of trees by introducing a mechanism to freeze these trees $[29,30]$. However, the counter part in these approaches is that their round complexity is driven by the mechanism used to freeze the abnormal trees, i.e., function of the network size. Therefore, in our approach we use an additional questioning mechanism allowing us to control the expansion of the tree rooted at $r$, while limiting the processor connections to abnormal trees.

Our algorithm $\mathcal{B F S}$ is composed of two sub-algorithms: the first sub-algorithm performs the connection of the processors to the spanning tree (see Section 5), while the second one allows
or not the connections of processors in order to obtain the desired tree (i.e., in our case a BFS tree) (see Section 6). The second sub-algorithm is considered as an oracle by the first one as explained below. As the approaches presented in Section 3, each node maintains a parent pointer to a neighbor and a level value indicating the number hops to the root in the tree it belongs to.

Abnormal trees detection. We consider that there is a processor $r$ designated as the root of the spanning tree. We consider a forest of trees and we use the notion of abnormal trees introduced in [30], i.e., trees rooted at a node with a level lower than its parent. The tree rooted at $r$ is called a normal tree. We also take the principle to detect the abnormal trees, but without the acknowledgement of all the descendants. That is, each processor $p$ checks if its level is higher than the one of its parent. If this constraint is not satisfied then $p$ takes the Error status and propagates this status in its subtree. That is, we do not use a PIF protocol, instead we only use a mechanism to propagate the Error status.

Connection of processors. Contrary to the approach proposed in [30], each node can leave the tree it belongs to without the permission of the root. Indeed, we do not use a PIF mechanism to have more flexibility for the connection of processors. However, before leaving the tree it belongs to a node must have the authorization from a neighbor to connect to him. Therefore, the connection of processors is controlled by a questioning mechanism viewed as an oracle by the first sub-algorithm, which delivers connection authorizations in a way allowing to construct a BFS tree. The connection procedure of a processor $p$ is as follows: A processor $q$ sends a request to the oracle, when $q$ detects a neighbor $p$ such that $p$ has an Error status or $p$ has a level higher than $q$ 's level plus one. If $q$ receives a response from the oracle then $p$ has the authorization to connect to $q$. Note that we consider that the oracle responds only to the processors which belong to the normal tree. Therefore, a node can only connect to the normal tree, except if $q$ has a response from the oracle because of a bad initial configuration.

Oracle. The questioning mechanism is used by each processor $q$ with a neighbor to connect in order to detect if $q$ really belongs to the normal tree or not. To this end, the request of $q$ is sent to the root of the tree $q$ belongs to. This request is forwarded by the ancestors of $q$ in the tree. If this request is received by the root $r$, then $r$ sends a (positive) response to $q$ which follows the path used to forward $q$ 's request, otherwise $q$ receives no response. This mechanism is a semialgorithm because only positive responses are sent, which allows to not execute useless actions. The construction of a BFS tree involves the connection of the nodes via the closest neighbor to the root. Therefore, the root $r$ must respond first to the request of processors of smallest level. To this end, the oracle takes into consideration the priority associated to each request, which is related to the height in the tree of the asking processor. Thus, a request sent by a processor $q^{\prime}$ closer than $q$ from $r$ erases $q$ 's request on the common path used to forward these two requests, even if $r$ still respond to the request of $q$. In this case, when the response to the request of $q^{\prime}$ is forwarded then $q$ 's request is forwarded again on the part of the path erased by the request of $q^{\prime}$. The responses are delivered level by level starting from the root. So, we can identify a classic method to construct a BFS tree based on waves used to add a new layer of processors with a synchronization to the root between two consecutive waves. Note that, a processor in a Error status cannot execute the actions related to the oracle, it can only execute the first sub-algorithm in order to hook to another neighbor with an authorization.

Time complexity considerations. First of all, we consider the round complexity of the whole algorithm. Algorithm $\mathcal{B F} \mathcal{F}$ uses a questioning mechanism which can be viewed as a synchronizer allowing to construct a BFS tree rooted at $r$ layer by layer, the addition of any new layer of processors is dependent on the acknowledgement to a permission request. The requesting processors closest to $r$ at height $k$ receive an acknowledgement to their request from $r$ in $O(k)$ rounds (see Corollary 1) which allows their neighbors to hook on to the normal tree. The same argument holds for the addition of each new layer. Moreover, the height of a BFS tree is lower than or equal to the network diameter. Therefore, summing up the round complexity associated to each layer we obtain a $O\left(d^{2}\right)$ round complexity to construct a BFS tree, with $d$ the network diameter. On the other hand, the mechanism we use for deleting the abnormal trees is obviously in $O(n)$ rounds, since the height of such a tree can be in $O(n)$. But any processor in an abnormal tree far from the root of this tree will become the neighbor of at least a processor of the normal BFS tree in $O\left(d^{2}\right)$ rounds and will hook to it even if the abnormal tree is not yet deleted. So the global round complexity is still $O\left(d^{2}\right)$ (see Lemma 19).
We give below the main arguments allowing to show that Algorithm $\mathcal{B F} \mathcal{S}$ has a polynomial step complexity in $O\left(\Delta m n^{3}+m n^{4}\right)$. We first consider the step complexity of the first sub-algorithm. The questioning mechanism is designed to avoid that a processor can hook to an abnormal tree several times by the same neighbor. Therefore, a processor can hook to an abnormal tree at most $\Delta$ times until reaching the normal tree (with $\Delta$ the maximum degree of a node) and at most $n$ times when it belongs to the normal tree until reaching its final position. This involves that there are at most $\Delta n+n^{2}$ connections until all the processors reach their correct position in the final BFS tree. We consider now the step complexity of the questioning mechanism. The connection of a processor is the result of a request, done by at most each of its neighbors (i.e., at most $\Delta$ requests per processor connection). So, to construct a BFS tree at most $\Delta m+m n$ requests are generated by the first sub-algorithm. A processor receives a response from the oracle in $O\left(n^{2}\right)$ steps, since the oracle handles first the closest requests in the tree. Note that a synchronization is performed for the parallel requests of same priority which yields a polynomial number of retransmissions. Thus, in $O\left(n^{3}\right)$ steps every requesting processor in the normal tree receives a response (there are at most $n$ concurrent requests in the network). Given an upper bound on the number of generated requests, we have to multiply this amount by the number of steps needed by the oracle to respond to these requests in order to obtain an upper bound to the total step complexity of Algorithm $\mathcal{B F S}$.
Notice that in one hand using a questioning mechanism allows us to save steps by avoiding the transmission of useless requests, but on the other hand we obtain a higher round complexity ( $O\left(d^{2}\right)$ instead of $O(d)$ with standard algorithms constructing BFS trees) due to the fact that permissions must be delivered before the add of new nodes to the constructed tree. Moreover, the step complexity (see Lemma 30) is not related with any initial value of a variable and it holds under any fairness assumptions.

## 5. Spanning Tree Construction

In this section, we are interested in the problem of constructing a tree spanning all the processors of the network. We consider a particular root processor, noted $r$, which is used to construct a spanning tree. More precisely, we consider the construction of a Breadth First Search (BFS) tree rooted at processor $r$. We can define a BFS tree as in Definition 3.

Definition 3 (BFS Tree). Let $G=(V, E)$ be a network and $r$ a node called the root. A graph $T=\left(V_{T}, E_{T}\right)$ of $G$ is called $a$ Breadth First Search tree if the following conditions are satisfied:

1. $V_{T}=V$ and $E_{T} \subseteq E$, and
2. $T$ is a connected graph (i.e., there exists a unique path in $T$ between any pair of nodes $\left.x, y \in V_{T}\right)$ and $\left|E_{T}\right|=|V|-1$, and
3. For each node $p \in V_{T}$, the path between $p$ and $r$ in $T$ is a shortest path (in hops) between $p$ and $r$ in $G$.

We give a formal specification to the problem of constructing a BFS tree, stated in Specification 1.

Specification 1 (BFS Tree Construction). Let $\mathcal{C}$ the set of all possible configurations of the system. An algorithm $\mathcal{A}_{\mathcal{B F S}}$ solving the problem of constructing a stabilizing BFS tree satisfies the following conditions:
[TC1] Algorithm $\mathcal{A}_{\mathcal{B F S}}$ reaches a set of terminal configurations $\mathcal{T} \subseteq \mathcal{C}$ in finite time, and
[TC2] In every configuration $\gamma \in \mathcal{T}$ there exists a spanning tree satisfying Definition 3.

### 5.1. Breadth first search tree algorithm

In this section, we present a snap-stabilizing algorithm, called $\mathcal{B F} \mathcal{S}$, to construct a BFS tree. Algorithm $\mathcal{B F} \mathcal{S}$ is a semi-uniform algorithm, this means that exactly one of the processors, called the root and denoted $r$, is distinguished. This distinguished processor is used in Algorithm $\mathcal{B} \mathcal{F S}$ as the root of the spanning tree.

Algorithm $\mathcal{B F S}$ is a composition of two algorithms. Algorithm 1 is based on the fact that a processor has to choose a neighbor with the minimal distance to the root as its parent in the tree. It is well known that this common idea is enough to get a round complexity in $O(d)$, but does not ensure a step complexity in $O\left(n^{b}\right)^{5}$. So we allow a processor to connect to a neighbor only if this neighbor is in the tree rooted at $r$ and in the shortest path to $r$. The detection of such neighbors is assigned to Algorithm 2 (see Section 6) which can be seen as an oracle by Algorithm 1. The second role of Algorithm 1 is to remove the abnormal trees, i.e., those that are not rooted at $r$.

### 5.1.1. Variables

We define below the variables used by Algorithm 1. For Algorithm 1, we characterize $r$ by the predicate Allowed (i.e., Allowed $(p) \equiv(p=r), \forall p \in V)$.

Shared variable. Each processor $p \in V$ has a local shared variable $p$. Req which is used by Algorithm 1 to monitor Algorithm 2 at $p$. This shared variable can take four values: ASK, $W A I T, R E P$, and $O U T$. By setting the shared variable $p$.Req to $A S K$, Algorithm 1 informs Algorithm 2 that a permission from the root of the tree that $p$ belongs to is needed at $p$. In this case, Algorithm 2 tries to send a request and to obtain a permission for $p$ if it is possible (i.e., if $p$ belongs to an allowed tree and this request has the highest priority during enough time for

[^3]an acknowledgment to return). If a permission is delivered to processor $p$, then Algorithm 2 sets this shared variable to $R E P$ in order to inform Algorithm 1. Then, every neighbor of $p$ can execute Algorithm 1 to join the tree that $p$ belongs to. When there is no neighbor of $p$ to connect, then Algorithm 1 sets $p . R e q$ to $O U T$ which allows Algorithm 1 to request another permission through Algorithm 2 if needed.

Local variables. Each processor $p \in V$ maintains three local variables:

- p. $P$ : it gives the parent of $p$ in the tree it belongs to, $p . P=\perp$ for processor $p=r$.
- p.L: it stores the level (or height) of $p$ in the tree it belongs to, $p . L=0$ for processor $p=r$.
- $p . S$ : it defines the status of processor $p$. It can take two values: $E$ if $p$ does not belong to a tree rooted at a processor $x$ satisfying Predicate $\operatorname{Allowed}(x), C$ otherwise. We have $p . S=C$ for processor $p=r$.


### 5.1.2. Algorithm description

As described before, we consider a forest $\mathcal{F}$ of trees and a distinguished processor $r$ which is the only processor authorized to deliver permissions in the network (i.e., Allowed $(p) \equiv(p=r)$ for every processor $p \in V$ ). We can notice that in a tree there is a strong constraint between the level of a processor and the level of its parent in the tree: For any processor $p \neq r$, the level of $p$ 's parent must be equal to $p$ 's level minus 1 . Therefore, the root of a tree in forest $\mathcal{F}$ is either (i) processor $r$, or (ii) a processor $p \neq r$ such that $p . L \leq(p . P) . L$ (it is used to detect cycles in the network). Since we want to construct a spanning tree, in case (ii) we say that processor $p$ is an abnormal root. Moreover, any processor $p \neq r$ in a tree in $\mathcal{F}$ rooted at an abnormal root belongs to an abnormal tree. Every processor $p \in V$ in an abnormal tree can execute $E$-action to change its Status to $E$ (i.e., $p . S=E$ ) and to inform its descendants in the tree (see the formal description of Algorithm 1). Note that to reduce the number of moves executed by Algorithm $\mathcal{B} \mathcal{F S}$, a processor $p \in V$ in an abnormal tree does not ask any permission. Processor $p$ waits until a neighbor $q$ in the tree rooted at $r$ authorizes $p$ to connect to $q$.

When a BFS tree is constructed, the following property is verified at each processor $p \in$ $V, p \neq r$ : The level of $p$ 's parent is equal to $p$ 's level minus 1 (i.e., $(p \neq r) \Rightarrow(p . L=$ $(p . P) . L+1)$ ). For processor $r$, we have the following constant values: $r$ has no parent and a level equal to zero (i.e., $(p=r) \Rightarrow(p . P=\perp \wedge p . L=0)$ ). Moreover, according to Claim 3 of Definition 3 we must have that the deviation on the level values between any processor $p \in V$ and its neighbors does not exceed one (i.e., $\forall q \in N e i g_{p},|q . L-p . L| \leq 1$ ). If one of these above constraints are not verified then a BFS tree is not constructed. Therefore, we have either at least one abnormal tree in $\mathcal{F}$ or there is a processor $p \in V$ with a neighbor $q$ such that $q . L-p . L>1$ (i.e., Predicate $G P-R E P(p)$ is satisfied at $p$ ). In these cases, processor $p$ executes $A$-action to set the shared variable $p$. Req to $A S K$ in order to ask the permission to allow $q$ to connect to $p$, if $p$ is not already asking a permission (i.e., we have $p \cdot \operatorname{Req}=O U T$ ). To this end, Algorithm 2 sends a request to the root of the tree.

Inputs for Algorithm 2. In order to allows Algorithm 2 to send a request the following inputs are given at processor $p$ : (i) $\operatorname{Child}(p)$ is the set of children of $p$ in the tree (i.e., $\operatorname{Child}(p) \equiv$ $\left\{q \in N e i g_{p}: q . P=p\right\}$ ), (ii) $\operatorname{Parent}(p)$ is the parent of $p$ in the tree (i.e., $\operatorname{Parent}(p) \equiv p . P$ ), (iii) $\operatorname{Height}(p)$ is the height in the tree of the requesting processor $p$, and (iv) $\operatorname{Allowed}(p)$ is a

```
Algorithm 1 Spanning Tree Construction for any \(p \in V\)
Inputs: \(N e i g_{p}\) : set of (locally) ordered neighbors of \(p\)
Shared variable: \(p . \operatorname{Req} \in\{A S K, W A I T, R E P, O U T\}\);
Macros:
    \(\operatorname{Child}(p) \quad=\quad\left\{q \in\right.\) Neig \(\left._{p}:: q \cdot P=p \wedge q \cdot L=p \cdot L+1\right\}\)
    Parent
    \(\operatorname{Height}(p)=\quad\) p.L
    \(=p . P\)
    \(C h P a r(p) \quad=\quad\left\{q \in N e i g_{p} \backslash \operatorname{Child}(p):: q \cdot S=C\right\}\)
    \(\operatorname{MinChPar}(p) \quad=\quad \min \{q \in C h \operatorname{Par}(p):: \forall t \in C h \operatorname{Par}(p), q . L \leq t . L\}\)
Global Predicates:
    GoodT \((p)\)
    \(\equiv \quad p . S \neq E \wedge(p \neq r \Rightarrow p . L=(p . P) \cdot L+1)\)
    \(\equiv \quad\left(\forall q \in N e i g_{p}::|p . L-q \cdot L|>1 \Rightarrow(p . L<q \cdot L \vee q \cdot S=E)\right)\)
    \(G P-R E P(p) \quad \equiv \quad\left(\exists q \in N e i g_{p}:: q . S=E \vee q . L-p . L>1\right)\)
    \(\operatorname{Start}(p) \quad \equiv \quad p . R e q=O U T \wedge G P-R E P(p)\)
    \(\operatorname{End}(p) \equiv p . R e q=R E P \wedge \neg G P-R E P(p)\)
Algorithm for \(p=r\)
    Constants: \(p . S=C ; p . P=\perp ; p . L=0 ;\)
    Predicates:
        Allowed \((p) \equiv\) true
    Actions:
        A-action \(\quad:: \quad \operatorname{Start}(p) \quad \rightarrow \quad\) p.Req \(:=\) ASK
        O-action \(\quad: \operatorname{End}(p) \quad \rightarrow \quad p \cdot R e q:=O U T\)
Algorithm for \(p \neq r\) :
    Variables: \(p . S \in\{C, E\} ; p . P \in N e i g_{p} ; p . L \in \mathbb{N} ;\)
    Predicates:
        Allowed \((p) \equiv\) false
        AbnormalTree \((p) \equiv p . S=C \wedge((p . P) . S=E \vee(p . P) . L \geq p . L)\)
        \(\operatorname{Connect}(p) \quad \equiv \quad\left(\exists q \in N e i g_{p}:: q \cdot \operatorname{Req}=R E P \wedge q=\operatorname{MinChPar}(p)\right.\)
                                    \(\wedge(p . S=C \Rightarrow p . L-q . L>1))\)
    Actions:
        E-action \(:: \quad\) AbnormalTree \((p) \quad \rightarrow \quad\) p.S \(:=E\);
        C-action \(\quad: \operatorname{Connect}(p) \quad \rightarrow \quad\) p.S:=C;p.P:=MinChPar \((p)\);
        Connect \((p) \longrightarrow\) p.S \(:=(p . P) . L+1 ; p . R e q:=O U T\)
        A-action \(:: \operatorname{Start}(p) \quad \rightarrow \quad\) p.Req \(:=\) ASK;
        O-action \(:: \operatorname{End}(p) \quad \rightarrow \quad\) p.Req \(:=O U T\);
```

predicate which notifies if $p$ can deliver permissions (i.e., Allowed $(p) \equiv(p=r)$ ). We remind that Allowed $(p)$ must be satisfied only at processor $p=r$ in Algorithm 2 to allow that eventually every processor joins the tree rooted at $r$, since eventually the processors cannot join another tree in the forest $\mathcal{F}$.

In the case a permission is delivered at processor $p$ (i.e., we have $p . \operatorname{Req}=R E P$ ), then each neighbor $q$ of $p$ can execute $C$-action to connect to $p$. However to construct a BFS tree without an overcost on moves, processor $q$ waits for until its neighbor $x$ with the smallest level in a normal tree gives a connection authorization to $q$ by executing $C$-action (i.e., we have $x . \operatorname{Req}=R E P \wedge x=\operatorname{MinChPar}(q))$. When processor $q$ executes $C$-action then it sets its variables $q . P$ and $q . L$ according to its new parent in the tree, and it changes its status to Status $C$ and its shared variable $q . R e q$ to $O U T$. Finally, if there is no neighbor for which processor $p$ needs a permission (i.e., Predicate $G P-R E P(p)$ is no more satisfied at $p$ ), then $p$ executes $O$-action to set its shared variable $p$.Req to $O U T$. This informs Algorithm 2 that the permission can be removed at $p$, then this allows $p$ to ask a new permission later.

## 6. Question-Answer problem

In this section, we present a snap-stabilizing algorithm to implement the oracle used by the BFS tree construction given in Section 5. Formally, this oracle has to solve the Question-Answer
problem which can be stated as following, a formal specification is given in Specification 2.
Given a static forest $\mathcal{F}$ of trees in a network $G=(V, E)$, a set of processors $D e \subseteq V$ requesting a permission to make a defined computation and a set of processors $A P \subset V$ authorized to deliver permissions. Each $p \in A P$ is a root of a tree $T \in \mathcal{F}$. The Question-Answer problem is to deliver a permission (or acknowledgement) to a processor $p$ in a tree $T \in \mathcal{F}$ if and only if the root $q$ of $T$ is in $A P$.

Specification 2 (Question-Answer). Let $G=(V, E)$ be a network and $\mathcal{F}$ the static forest of trees in $G$. Let a tree $T \in \mathcal{F}$ and $\operatorname{root}(T)$ the root of $T$. $T$ is an allowed tree if $\operatorname{root}(T) \in A P$ and not allowed otherwise. A protocol $P$ which resolves the Question-Answer problem satisfies:

- Liveness 1: During an infinite execution, if a processor has to send infinitely often a request and it cannot send its request in an allowed tree, then there exist an infinite number of requests which were sent.
- Liveness 2: For every execution suffix, if a processor in an allowed tree has sent a request at time $t$, then there exist at least one processor in the same tree which receives an acknowledgement to its own sent request at time $t^{\prime}>t$.
- Safety 1: Every processor which has sent a request receives at most one acknowledgement causally related to its sent request.
- Safety 2: Every processor in a not allowed tree which has sent a request never receives an acknowledgement.

Remark that only semi-algorithms can satisfy Specification 2, that is no acknowledgement is sent to processors in a not allowed tree, from Property (Safety 2) of Specification 2.

### 6.1. Question-Answer algorithm

In this section, we present a snap-stabilizing algorithm for the Question-Answer problem, a formal description is given by Algorithm 2. This is a non-uniform algorithm because some rules are only executed by a subset of processors $p \in V$ satisfying a local Predicate $\operatorname{Allowed}(p)$ (i.e., $p$ can deliver a permission or not).

### 6.1.1. Variables

We define below the different variables used by Algorithm 2.
Shared variable. Each processor $p \in V$ has a local shared variable $p$.Req which allows an external algorithm to require the Question-Answer algorithm at $p$. This shared variable can take four values: $A S K, W A I T, R E P$, and $O U T$. By setting the shared variable $p . R e q$ to $A S K$ in the external algorithm, $p$ requests a permission through the Question-Answer algorithm to its root of the tree. To this end, Question-Answer algorithm tries to send a request to the root of the tree and sets the shared variable $p$.Req to $W$ AIT. The request sent by requesting processors with the lowest level (or height) in the tree will reach the root and then receive a permission (an acknowledgement). When $p$ receives an acknowledgement, it sets $p$.Req to REP. Finally, the external algorithm must set $p$.Req to $O U T$ to request another permission through QuestionAnswer algorithm.

Local variables. Each processor $p \in V$ maintains two local variables:

- $p . Q$ : it defines the status of the Question-Answer algorithm at processor $p$. There are three distinct status: $R, W$, and $A$. Status $R$ notifies that $p$ transmits a request to the root of the tree, whereas Status $W$ indicates that $p$ waits for an acknowledgement from the root for the transmitted request. The third status, Status $A$, indicates that $p$ has received an acknowledgement from the root.
- p.HQ: it stores at $p$ the height of the processor which has sent the request.


### 6.1.2. Algorithm description

To simplify the presentation of the algorithm, consider a forest of allowed trees (i.e., trees rooted at nodes $p$ satisfying Predicate $\operatorname{Allowed}(p))$ and a fixed set of requests. In the following, we explain the way our algorithm handles requests focusing on a single tree $T$ of the forest, but this is the same for the other trees since the requests in each tree are handled independently. In the algorithm, the requests sent by nodes of lowest height in the tree are handled in priority.

When a processor $p$ has a local request requested by the external algorithm (i.e., $p . R e q=$ $A S K), p$ can execute $Q R$-action to set its variables $p$.Req, $p . Q$, and $p . H Q$ to $W A I T, R$, and to $\operatorname{Height}(p)$ respectively, in order to send its request to the root of the tree it belongs to. The external algorithm is informed that the request is sent since $p . R e q=W A I T$. Otherwise, an internal processor $p$ in the tree with no local request (i.e., $p . \operatorname{Req} \neq R E P$ ) could have to transmit requests from its children (the request from a requesting descendant of lowest height first) in the following cases:

- a child of $p$ given by $C h_{p}$ (see Algorithm 2) is sending a request with a highest priority (i.e., $\left(C h_{p}\right) . H Q<p . H Q$ );
- the acknowledgement received for the transmitted request is no more needed at $p$ (all its children waiting it have transfered the acknowledgment, see Predicate $\operatorname{Transmit}(p)$ );
- $p$ is waiting for an acknowledgement for a request and a new request is transmitted by a child of $p$ with the same height (see Predicate $\operatorname{Retransmit}(p)$ ).

In all these above cases, $p$ executes $Q R C$-action to set $p . Q$ to $R$ and $p . H Q$ to the lowest height among requesting descendant of $p$ (i.e., $p \cdot H Q=\left(C h_{p}\right) \cdot H Q$ ).

A processor $p$ waits for an acknowledgement for a current request when its parent has transmitted the request (see Predicate $\operatorname{Wait}(p)$ ). Moreover, all $p$ 's children transmitting the same request (i.e., with the same height) have to wait for an acknowledgement. Hence, Status $W$ allows to remove bad requests due to an incorrect initial configuration and to synchronize request transmissions of same priority. In this case, $p$ sets its variable $p \cdot Q$ to $W$ using $Q W$-action.

When the root $\operatorname{root}(T)$ of the tree $T$ has no local request and is waiting for an acknowledgement for requesting descendant(s) (see Predicate Answer), then it executes $Q A$-action to set its variable $\operatorname{root}(T) \cdot Q$ to $A$. This permission is propagated down in the tree to the requesting descendant(s) following the path(s) used to transmit the request. Finally, a processor $p$ waiting for an acknowledgement to a local request (i.e., $p . Q=W$ and $p \cdot R e q=W A I T$ ) executes $Q A$ action to receive the acknowledgement and sets the shared variable $p \cdot \operatorname{Req}$ to $R E P$ to notify to the external algorithm of the delivered permission. Note that as soon as a received acknowledgement is no more needed at a processor $p$ (i.e., $p$.Req is set to $O U T$ by the external algorithm), then another request transmitted by a child of $p$ can be transmitted by $p$ up in the tree.

```
Algorithm 2 Question-Answer algorithm for any \(p \in V\)
Inputs: Neig \(_{p}\) : set of (locally) ordered neighbors of \(p\)
        \(\operatorname{Child}(p)\) : set of neighbors considered as children of \(p\) in the tree
        Allowed \((p)\) : predicate which indicates if \(p\) is able to acknowledge to a request,
        \(\operatorname{Parent}(p)\) : parent of \(p\) in the tree, equal to a processor \(q \in N \operatorname{Neig}_{p}\) if \(\neg \operatorname{Allowed}(p)\) or equal to \(\perp\) otherwise :
        \(\operatorname{Height}(p)\) : height of \(p\) in the tree;
Shared variable: \(p . R e q \in\{A S K, W A I T, R E P, O U T\}\);
Variables: \(p . Q \in\{R, W, A\} ; p . H Q \in \mathbb{N}\);
Macros:
    \(\underset{R C(p)}{ } \quad=\quad\{q \in \operatorname{Child}(p):: q \cdot Q \in\{R, W\}\}\)
    \(\operatorname{PrioRC}(p)=\{q \in R C(p):: \forall t \in R C(p), q \cdot H Q \leq t . H Q\}\)
    \(C h_{p}=\min \{q \in \operatorname{PrioRC}(p)\}\)
Global Predicates:
    \(\operatorname{Transmit}(p) \equiv p \cdot Q=A \wedge(\forall q \in \operatorname{Child}(p):: q \cdot Q=W \Rightarrow q \cdot H Q \neq p \cdot H Q)\)
    \(\operatorname{Retransmit}(p) \equiv p \cdot Q=W \wedge(\exists q \in \operatorname{Child}(p):: q \cdot Q=R \wedge q \cdot H Q=p \cdot H Q)\)
    \(\operatorname{Error}(p) \equiv p \cdot Q \neq A \wedge[(p . \operatorname{Req} \notin\{A S K, W A I T\} \wedge p . H Q=\operatorname{Height}(p)) \vee(p . H Q \neq \operatorname{Height}(p)\)
    \(\wedge(p \cdot \operatorname{Req} \neq R E P \Rightarrow(\forall q \in \operatorname{Child}(p):: q \cdot H Q=p \cdot H Q \Rightarrow q \cdot Q=A)))]\)
    \(\operatorname{Request}(p) \equiv p . \operatorname{Req}=A S K \wedge\left(|\operatorname{PrioRC}(p)|>0 \Rightarrow \operatorname{Height}(p) \leq\left(C h_{p}\right) \cdot H Q\right)\)
    RequestT \((p) \equiv p . \operatorname{Req} \neq R E P\)
    \(\wedge|\operatorname{PrioRC}(p)|>0 \wedge\left[\left(\left(C h_{p}\right) \cdot H Q \geq p \cdot H Q \Rightarrow \operatorname{Transmit}(p)\right) \vee \operatorname{Retransmit}(p)\right]\)
Algorithm:
    Predicates:
        \(\operatorname{Wait}(p) \quad \equiv \quad(\) Allowed \((p) \wedge p \cdot Q=R \wedge(\forall q \in \operatorname{Child}(p):: q \cdot H Q=p \cdot H Q \Rightarrow q \cdot Q=W)) \vee\)
                        \((\neg \operatorname{Allowed}(p) \wedge \operatorname{Parent}(p) \cdot Q=R \wedge p \cdot Q=R \wedge \operatorname{Parent}(p) \cdot H Q=p \cdot H Q\)
                        \(\wedge(\forall q \in \operatorname{Child}(p):: q \cdot H Q=p \cdot H Q \Rightarrow q \cdot Q=W))\)
        \(\operatorname{Answer}(p) \quad \equiv \quad(\operatorname{Allowed}(p) \wedge p \cdot Q=W) \vee\)
        \(\begin{array}{cll}\text { Actions: } \\ Q E \text {-action } & :: \quad \operatorname{Error}(p)\end{array} \quad \rightarrow \quad p \cdot Q:=A ; p \cdot H Q:=\operatorname{Height}(p)\);
        \(Q R\)-action \(:: \operatorname{Request}(p) \quad \rightarrow \quad p . Q:=R ; p . H Q:=\operatorname{Height}(p) ; p . \operatorname{Req}=W\) AIT
        \(Q R C\)-action \(:: \quad\) Request \((p) \quad \rightarrow \quad p . Q:=R ; p . H Q:=\left(C h_{p}\right) . H Q\);
        if \(p . H Q<\operatorname{Height}(p) \wedge p . \operatorname{Req}=W A I T\)
        then \(p\). Req \(:=A S K ; \mathrm{fi}\)
        QW-action \(\quad: \quad W \operatorname{Wait}(p) \quad \rightarrow \quad p \cdot Q:=W\);
        \(Q A\)-action \(:: \operatorname{Answer}(p) \quad \rightarrow \quad p . Q:=A\);
        if \(p\). Req \(=W A I T\) then \(p\). Req \(:=R E P ;\) fi
```

However, a processor must be able to detect wrong requests due to an incorrect initial configuration. A request treated by a processor $p$ is a wrong request in the following cases (see Predicate $\operatorname{Error}(p))$ :

- $p$ is sending a local request whereas it has no local request (i.e., $p . Q \neq A \wedge p . \operatorname{Req} \notin$ $\{A S K, W A I T\}$ and $p . H Q=\operatorname{Height}(p))$;
- $p$ is transmitting a request from a child, however no child of $p$ has a request with the same height (i.e., $p . Q \neq A \wedge p . H Q \neq \operatorname{Height}(p) \wedge(\forall q \in \operatorname{Child}(p), q . H Q=p . H Q \Rightarrow q . Q=$ $A)$ ).

When a processor $p$ detects a wrong request, then $p$ executes $Q E$-action. This action has the highest priority among the actions at $p$, and it resets $p$ 's state like if an acknowledgement to a local request was received, i.e., to set $p . Q$ to $A$ and $p . H Q$ to $\operatorname{Height}(p)$ (without changing the state of the shared variable $p$.Req).

A questioning mechanism close to the mechanism presented here was used in [34] to design a snap-stabilizing solution to the problem of Propagation of Information with Feedback (PIF) with a round complexity in $O(n)$ and a step complexity in $O\left(\Delta n^{3}\right)$. However, solving the PIF problem involves a strong synchronization in the network to insure that all the nodes in the network belong
to the same broadcast tree before initiating the feedback phase. Indeed, each time a node is added to the broadcast tree the questioning mechanism is reset leading to a $O(n)$ round complexity. Contrary to this questioning mechanism, here our mechanism needs a weakest synchronization to resolve the Question-Answer problem. Let $D e$ be the set of requesting nodes and $h_{\text {min }}$ the height of closest requesting nodes from the root in $T$. The first requests acknowledged by $\operatorname{root}(T)$ are the requests from nodes at height $h_{\text {min }}$. Then, if the set of requests is static then the requests at height $h_{\min }+1$ are acknowledged by $\operatorname{root}(T)$ (if any) and so on. In fact, only a synchronization for the requests of requesting nodes at height $h_{\min }$ (whose requests are of highest priority) in tree $T$ is required leading to a round complexity function of the height of $T$. The transmission of a request requires $O(n)$ steps, however this transmission can be interrupted only by a requesting node with the same height in $T$, that is at most $|D e|$ times.

The following corollary summarizes the above discussion:
Corollary 1. Let $T$ be an allowed tree and $h_{\text {min }}$ be the height of the closest requesting nodes in De from the root in $T$. In $O\left(h_{\min }\right)$ rounds and $O(n|D e|)$ steps, at least one requesting node in $D e$ receive an acknowledgement from $\operatorname{root}(T)$ to its request.

## 7. Composition and complexities

Algorithm $\mathcal{B F S}$ is obtained by the composition of Algorithm 2 and Algorithm 1. These two algorithms are composed together at each processor $p \in V$ with a conditional composition (first introduced in [37]): Algorithm $\left.1 \circ\right|_{\operatorname{Cond}(p)}$ Algorithm 2, where each guard $g$ of the actions of Algorithm 2 at each processor $p \in V$ has the form $\operatorname{Cond}(p) \wedge g$ with Predicate $\operatorname{Cond}(p)$ defined below (see Algorithm 1 for the description of predicates): $\operatorname{Cond}(p) \equiv \operatorname{GoodT}(p) \wedge \operatorname{GoodL}(p)$.

Using this composition, each processor $p \in V$ can execute Algorithm 2: (i) to transmit requests and acknowledgements only if the tree containing $p$ is locally correct (i.e., Predicate $\operatorname{Good} T(p)$ is satisfied), and (ii) to ask a permission if needed (i.e., Predicate $\operatorname{Good} L(p)$ is satisfied). Indeed, actions in Algorithm 2 can be locked to avoid processors belonging to a tree not rooted at $r$ (abnormal tree) to transmit useless requests since no acknowledgement can be received (only $r$ can deliver acknowledgements). Therefore, processors in abnormal trees can only execute actions in Algorithm 1 to hook on to another tree in the forest via a neighbor with a permission (acknowledgement delivered by Algorithm 2). Moreover, actions of Algorithm 2 and Algorithm 1 can be enabled at $p$ simultaneously. In this case, Algorithm 2 is executed before Algorithm 1 at $p$.

## 8. An example of execution

In this section we give an example of execution of Algorithm $\mathcal{B F S}$. To this end, we consider the network with the initial configuration illustrated in Figure 4(a). In Figure 4, the values of the variables of each processor $p$ is given in three parts: the first pair $(p . L, p . S)$ related to Algorithm 1, followed by the shared variable $p . \operatorname{Req}$, and a last pair $(p . Q, p . H Q)$ related to Algorithm 2.

In the following, we explain the evolution of the system states by (synchronous) round until reaching a terminal and legitimate configuration illustrated in Figure 4(h):


Figure 4: Network considered for the example of execution of Algorithm $\mathcal{B F} \mathcal{F}$. For each processor $p$, the values of the variables are separated in three parts: the first pair $(p . L, p . S)$ related to Algorithm 1, followed by the shared variable $p$. Req, and a last pair ( $p . Q, p . H Q$ ) related to Algorithm 2. (a) initial configuration with a forest of trees (dotted lines are edges not used in the forest), (b)-(g) several intermediate states with modifications in bold text until reaching the terminal configuration given in (h).

## Round 0.

1. First of all, the processors belonging to an abnormal tree are informed by the abnormal root of this situation. So, every processor $y_{i}$ and $z_{i}, 1 \leq i \leq 4$, execute $E$-action of Algorithm 1 to propagate the Error status $E$ in the abnormal trees.
2. After this propagation of Error status, the processors $x_{2}$ and $x_{4}$ have neighbors satisfying Predicate $G P-R E P()$. So, these two processors execute $A$-action of Algorithm 1 to set the shared variable Req to $A S K$ for sending their local request to the questioning mechanism (i.e., Algorithm 2). The configuration obtained after the execution of these actions is illustrated in Figure 4(b).

## Round 1.

1. The questioning mechanism sets the shared variable Req to $W$ AIT by the execution of $Q R$-action of Algorithm 2 at $x_{2}$ and $x_{4}$ to inform these processors that their requests are taken into account. These processors cannot send another request (local or not) until this shared variable has $O U T$ value. Moreover, by the execution of $Q R$-action the variables $Q$ and $H Q$ (related to the sent request) are set to $R$ and 1 for $x_{2}$ or 2 for $x_{4}$ (level of $x_{2}$ and $x_{4}$ ).
2. Then processors $x_{1}$ and $r$ transmit the request of highest priority (i.e., with the lowest $H Q$ value) from their descendants by executing $Q R C$-action of Algorithm 2. So, $x_{1}$ transmits the request initiated by $x_{4}$, while $r$ transmits the request from $x_{2}$. The new configuration reached after all these actions is given in Figure 4(c).

## Round 2.

1. The processors on the requesting path related to the request initiated by $x_{2}$ and $x_{4}$ execute $Q W$-action of Algorithm 2 to notice that the request has been transmitted by the parent in the tree. So, the variable $Q$ is set to value $W$ at $x_{2}$ and $r$, this is also the case for $x_{4}$.

## Round 3.

1. The authorization for $x_{2}$ 's request is given by the root $r$ of the normal tree and is propagated down following the requesting path of $x_{2}$ 's request. So, $r$ and $x_{2}$ execute $Q A$-action of Algorithm 2 to set variable $Q$ to value $A$. Moreover, since $x_{2}$ receives an authorization for its local request then the shared variable $R e q$ is also set to value $R E P$ to inform Algorithm 1 that neighbor processors can be connected to $x_{2}$.
2. Processor $z_{2}$ executes $C$-action of Algorithm 1 to hook to $x_{2}$ because of the authorization given to $x_{2}$. So, $z_{2}$ sets its variables $S, P$ and $L$ to $C, x_{2}$ and 2 ( $x_{2}$ 's level plus one) respectively. The new reached configuration is illustrated in Figure 4(d).

## Round 4.

1. The request initiated by $x_{4}$ is transmitted to $r$, since the authorization for $x_{2}$ 's request has been propagated down. So, $r$ executes $Q R C$-action of Algorithm 2 to set variables $Q$ and $H Q$ to $R$ and 2 respectively.
2. The processors on the requesting path related to $x_{4}$ 's request execute $Q W$-action of Algorithm 2 to notice that the request has been transmitted by the parent in the tree.
3. Processor $x_{2}$ executes $O$-action of Algorithm 1 to set its shared variable $R e q$ to $O U T$ since all its neighbors are connected (no neighbor satisfying Predicate $G P-R E P()$ ) and the obtained authorization is no more needed. This allows $x_{2}$ to send a new request if needed.
4. Processor $z_{2}$ has neighbors which satisfy Predicate $G P-R E P()$. So, it executes $A$-action of Algorithm 1 to set the shared variable Req to $A S K$ to send a local request to the questioning mechanism.

## Round 5.

1. $r$ executes $Q W$-action of Algorithm 2 to notice that $x_{4}$ 's request has been correctly transmitted. So, the variable $Q$ is set to value $W$ at $r$.
2. The questioning mechanism set the shared variable $R e q$ to $W A I T$ by executing $Q R$ action of Algorithm 2 at $z_{2}$ to inform Algorithm 1 at $z_{2}$ that the local request is taken into account. Moreover, by the execution of $Q R$-action the variables $Q$ and $H Q$ are set to $R$ and 2.
3. $x_{2}$ executes $Q R C$-action to transmit the request initiated by $z_{2}$. So, variables $Q$ and $H Q$ are set to $R$ and 2 respectively.

## Round 6.

1. At this point of the execution, $x_{4}$ and $z_{2}$ have initiated two requests of same priority ( $x_{4}$ and $z_{2}$ have the same level). A synchronization is done by the questioning mechanism in order to synchronize the connection of processors at layer 3 of the constructed BFS. To this end, the transmission of the request is reseted at all the processors on the common requesting path of $x_{4}$ 's and $z_{2}$ 's request. So, here only $r$ execute $Q R C$-action of Algorithm 2 to set the variable $Q$ to $R$.
2. $z_{2}$ executes $Q W$-action of Algorithm 2 to notice that its local request has been correctly transmitted by its parent in the tree. So, the variable $Q$ is set to value $W$ at $z_{2}$. The configuration obtained after the execution of all these actions is given in Figure 4(e).

## Round 7.

1. $x_{2}$ and $r$ execute $Q W$-action of Algorithm 2 to notice that the request initiated by $z_{2}$ has been correctly transmitted by its parent in the tree. So, the variable $Q$ is set to value $W$ at $x_{2}$ and $r$.

## Round 8.

1. The authorization for the request of $x_{4}$ and $z_{2}$ is given by the root $r$ and is propagated down following the requesting path corresponding to the request of $x_{4}$ and $z_{2}$. So, $r$, $x_{1}, x_{4}$ execute $Q A$-action of Algorithm 2 to set variable $Q$ to value $A$ for $x_{4}$ 's request, the same is done by $x_{2}$ and $z_{2}$ for $z_{2}$ 's request. Moreover, since $x_{4}$ and $z_{2}$ receive an authorization for their local request then the shared variable $R e q$ is set to $R E P$ at $x_{4}$ and $z_{2}$ to inform Algorithm 1 that neighbor processors can be connected to $x_{4}$ and $z_{2}$.
2. Processors $z_{1}, z_{3}$ and $z_{4}$ execute $C$-action of Algorithm 1 to hook to $z_{2}$ because of the authorization given to $z_{2}$. The same is done at $y_{3}$ and $y_{4}$ to hook to $x_{4}$. So, variables $S$, $P$ and $L$ are set to $C, z_{2}$ (or $x_{4}$ accordingly) and 3 . The new reached configuration is illustrated in Figure 4(f).

## Round 9.

1. Processors $x_{4}$ and $z_{2}$ execute $O$-action of Algorithm 1 to set their shared variable Req to $O U T$ since all their neighbors are connected (no neighbor satisfying Predicate $G P$ $R E P())$ and the obtained authorization is no more needed. This allows $x_{4}$ and $z_{2}$ to send a new request if needed.
2. Processors $y_{3}$ and $y_{4}$ have neighbors which satisfy Predicate $G P-R E P()$. So, $A$-action of Algorithm 1 is executed to set the shared variable Req to $A S K$ to send a local request to the questioning mechanism.

## Round 10.

1. The questioning mechanism set the shared variable Req to $W$ AIT by the execution of $Q R$-action of Algorithm 2 at $y_{3}$ and $y_{4}$ to inform Algorithm 1 that the local request is taken into account. Moreover, by the execution of $Q R$-action the variables $Q$ and $H Q$ are set to $R$ and 3 at $y_{3}$ and $y_{4}$.
2. $x_{4}$ and $x_{1}$ execute $Q R C$-action to transmit the request initiated by $y_{3}$ and $y_{4}$ of same priority. So, variables $Q$ and $H Q$ are set to $R$ and 3 respectively. The new obtained configuration is given in Figure 4(g).

## Round 11.

1. $y_{3}, y_{4}, x_{4}, x_{1}$ and $r$ execute $Q W$-action of Algorithm 2 to notice that the requests initiated by $y_{3}$ and $y_{4}$ have been correctly transmitted by their parent in the tree. So, the variable $Q$ is set to value $W$.

## Round 12.

1. The authorization for the request of $y_{3}$ and $y_{4}$ is given by the root $r$ and is propagated down following the requesting path corresponding to the request of $y_{3}$ and $y_{4}$. So, $r, x_{1}, x_{4}, y_{3}$ and $y_{4}$ execute $Q A$-action of Algorithm 2 to set variable $Q$ to value $A$. Moreover, since $y_{3}$ and $y_{4}$ receive an authorization for their local request then the shared variable Req is set to $R E P$ at $y_{3}$ and $y_{4}$ to inform Algorithm 1 that neighbors can be connected to $y_{3}$ and $y_{4}$.
2. Processor $y_{2}$ executes $C$-action of Algorithm 1 to hook to $y_{3}$ because of the authorization given to $y_{3}$. The same is done at $y_{1}$ to hook to $y_{4}$. So, variables $S, P$ and $L$ are set to $C$, $y_{3}$ (or $y_{4}$ accordingly) and 4 .

## Round 13.

1. Finally, processors $y_{3}$ and $y_{4}$ execute $O$-action of Algorithm 1 to set their shared variable Req to $O U T$ since all their neighbors are connected (no neighbor satisfying Predicate $G P-R E P())$ and the obtained authorization is no more needed. The new and legitimate configuration reached by the system is illustrated in Figure 4(h).

## 9. Correctness proof

In this section, we show the correctness of the two algorithms proposed in this paper. In a first part, we prove that the Question-Answer algorithm (Algorithm 2) is stabilizing under a weakly fair daemon (Section 9.1.2), and then its correctness under an unfair daemon by showing a polynomial step complexity (Section 9.1.3). In a second part, we consider the BFS algorithm (Algorithm 1). We show its correctness under a weakly fair daemon (Section 9.2.2), and then under an unfair daemon (Section 9.2.3).

### 9.1. Proof of Question-Answer algorithm

In the following proofs, we consider a more general context than the one used to present Algorithm 2 for the Question-Answer problem. So $\operatorname{Height}(p)$ could be replaced by any priority which can be independent from the height of a requesting processor $p$ (which is a particular case).

To prove the correctness of the Question-Answer algorithm (Algorithm 2), we first introduce some useful definitions, and we show that in an illegitimate configuration there is always at least one enabled processor (Theorem 1). Then, we establish that in at most $O(k+1)$ rounds the request and the corresponding answer of every processor at height $k$ in the normal tree are transmitted by the Question-Answer algorithm (Lemmas 8, 9, and 10). We show that the Question-Answer algorithm is a silent algorithm (Lemma 11), which allows us to conclude that the Question-Answer algorithm satisfies Specification 2 under a weakly fair daemon (Lemma 12). Finally, we consider an unfair daemon and given a set of local requests from processors belonging to the normal tree $T$, we prove that these requests and their corresponding answers are transmitted in at most $O\left(n^{3}\right)$ steps (Corollary 4).

### 9.1.1. Definitions

Definition 4 (Path). The sequence of processors $\mathcal{P}(x, y)=<p_{0}=x, p_{1}, \ldots, p_{k}=y>$ is called a path if $\forall i, 1 \leq i \leq k, \operatorname{Parent}\left(p_{i}\right)=p_{i-1}$. The processors $p_{0}$ and $p_{k}$ are termed as the extremities of $\mathcal{P}$. The length of $\mathcal{P}$ is noted $|\mathcal{P}|=k$.

Definition 5 (Allowed tree). A tree $T$ rooted at processor $p$ such that $(p=\operatorname{root}(T) \wedge \operatorname{Allowed}(p))$ is called an allowed tree. Any tree $T^{\prime}$ rooted at processor $q$ such that $\left(q=\operatorname{root}\left(T^{\prime}\right) \wedge \neg \operatorname{Allowed}(q)\right.$ is called $a$ not allowed tree.

In the following, we consider a static forest $\mathcal{F}$ of trees constructed in the network $G=(V, E)$.
Definition 6 (Request priority). Given a tree $T$ in forest $\mathcal{F}$ and $k$ processors in $T$ sending $a$ request. Let $R=\left\{R_{p_{1}}, \ldots, R_{p_{k}}\right\}$ be the set of requests sent by processors $p_{i}, 1 \leq i \leq k$, in $T$. A request $R_{p_{i}}$ has a higher priority than request $R_{p_{j}}, 1 \leq i, j \leq k$, if $\operatorname{Height}\left(p_{i}\right)<$ Height $\left(p_{j}\right)$. A request $R_{p_{i}}$ sent (or transmitted) by a processor $p \in T$ is of highest priority in the neighborhood of $p$ if $\forall q \in N e i g_{p} \backslash\{\operatorname{Parent}(p)\}$ the request $R_{p_{j}}$ sent (or transmitted) by $q$ we have $\operatorname{Height}\left(p_{j}\right)>\operatorname{Height}\left(p_{i}\right)$.

In the reminder, we make the hypothesis that the extern algorithm (Algorithm $\mathcal{A}$ ) sets in finite time the shared variable $p$. Req from $R E P$ to $O U T$ when a permission delivered at $p$ is no more needed.

### 9.1.2. Proof assuming a weakly fair daemon

The following theorem proves that any execution of Question-Answer algorithm is deadlockfree.

Theorem 1. Let the set of configurations $\mathcal{B} \subseteq \mathcal{C}$ such that there is at least one processor $p \in V$ in an allowed tree which has a request to send or has sent a request and it does not receive an acknowledgement in every configuration $\gamma \in \mathcal{B} . \forall \gamma \in \mathcal{B}, \exists q \in V$ such that $q$ is enabled in $\gamma$.

Proof. Assume, by the contradiction, that $\exists \gamma \in \mathcal{B}$ such that $\forall q \in V$ no action is enabled at $q$ in $\gamma$. Assume then that there exists at least one allowed tree $T$ in $\gamma$ in which $\exists p \in T$ such that $p . R e q=A S K$. Consider the processor $p \in T$ with the request of highest priority in $T$, i.e.,
$(\forall x \in T:: x . \operatorname{Req}=A S K \wedge \operatorname{Height}(p)<\operatorname{Height}(x))$. In this case, either $p . \operatorname{Req}=A S K$ and $Q R$-action is enabled at $p$, a contradiction, or $\exists q \in \mathcal{P}(\operatorname{root}(T), p)$ such that $q . H Q \neq p . H Q$. Moreover, since $p$ 's request is of highest priority in $T$ then $q$ satisfies $p \cdot H Q=\left(C h_{q}\right) \cdot H Q$ and $|\operatorname{Prio} R C(q)|>0$. We assume that $p . R e q \neq R E P$, otherwise by hypothesis $p . R e q$ is set to $O U T$ in finite time. In this case, either we have $(p . H Q<q \cdot H Q \Rightarrow \operatorname{Request} T(q))$ and $Q R C$-action is enabled at $q$, a contradiction. Otherwise, $q$ has transmitted a request with the same priority, i.e., we have $p \cdot H Q=q \cdot H Q$ and $q \cdot Q=W$ (see Predicate Retransmit $(q)$ ), and $Q R C$-action is enabled at $q$, a contradiction. The execution of $Q R$-action sets $p$.Req to $W$ AIT. Hence, by contradiction, $p \cdot Q=R, p \cdot H Q=\operatorname{Height}(p)$ and $p \cdot R e q=W A I T$ at $p$ and $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot H Q=p . H Q$. If $\exists q \in \mathcal{P}(\operatorname{root}(T), p)$ such that $q \cdot Q=W$ then $Q R C$-action is enabled at $q$ (see Predicate $\operatorname{Retransmit}(q)$ ), a contradiction. Thus, $\forall x \in$ $\mathcal{P}(\operatorname{root}(T), p), x \cdot H Q=p \cdot H Q \wedge x \cdot Q=R$. Then, we have $(\operatorname{Parent}(p) \cdot H Q=p \cdot H Q \wedge$ $p . H Q=\operatorname{Height}(p)) \Rightarrow W \operatorname{ait}(p)$ and $Q W$-action is enabled at $p$, a contradiction. If $\exists q \in$ $\mathcal{P}(\operatorname{root}(T), p)$ such that $q \cdot Q=R \wedge(\exists s \in \operatorname{Child}(q):: s . Q=W)$ then $Q W$-action is enabled at $q$, a contradiction. Hence, by contradiction, $\forall x \in \mathcal{P}(\operatorname{root}(T), p), x . H Q=p . H Q \wedge x \cdot Q=$ $W$. Thus, $Q A$-action is enabled at $\operatorname{root}(T)$, a contradiction. If $\exists q \in \mathcal{P}(\operatorname{root}(T), p)$ such that $\operatorname{Parent}(q) \cdot Q=A \wedge q \cdot Q=W$ then $Q A$-action is enabled at $q$, a contradiction.

Lemma 1. Let an allowed tree $T$ in a static forest $\mathcal{F}$. After executing $Q E$-action at a processor $p \in T, Q E$-action is disabled at $p$ until $p$ sends or transmits another request.

Proof. Assume, by the contradiction, that $Q E$-action is enabled at a processor $p \in T$ before $p$ sends or transmits another request. After the first execution of $Q E$-action, we have $p \cdot Q=A$ at $p$. If $p$ can execute $Q E$-action again then this implies that we have $p . Q \neq A$ (because $(p . Q=A \Rightarrow \neg \operatorname{Error}(p)))$. Since we assume that $p$ does not execute $Q R$-action and $Q R C$ action, then this implies that $p \cdot Q=W$ obtained by executing $Q W$-action at $p$, a contradiction because $\operatorname{Wait}(p) \Rightarrow p \cdot Q=R$ at $p$.

Lemma 2. Let an allowed tree $T$ in a static forest $\mathcal{F}$. When $Q R$-action is enabled at processor $p \in T$, it remains enabled until $p$ executes it and $p$ remains in $T$.

Proof. Let $\gamma \mapsto \gamma^{\prime}$ be a step. Assume, by the contradiction, that $Q R$-action is enabled at $p$ in $\gamma$ and not in $\gamma^{\prime}$ (i.e., $\neg \operatorname{Request}(p)$ in $\gamma^{\prime}$ ) but $p$ did not execute $Q R$-action in $\gamma \mapsto \gamma^{\prime}$. According to the hypothesis of the lemma, we assume that $p$ has no child with a request of priority higher than $p$ 's request (i.e., $\left.\operatorname{Height}(p) \leq\left(C h_{p}\right) \cdot H Q\right)$. $Q R$-action is the enabled action at $p$ which has the highest priority, otherwise according to Lemma 1 after executing $Q E$-action then it is disabled at $p$. Moreover, we assume that $p$ remains in $T$ in $\gamma^{\prime}$, so $p$. Req $=A S K$ in $\gamma^{\prime}$. Since $p$ did not move in $\gamma \mapsto \gamma^{\prime}$, we have $p . H Q \neq \operatorname{Height}(p)$. Thus, $\operatorname{Request}(p)$ is satisfied in $\gamma^{\prime}$, a contradiction.

Lemma 3. Let any allowed tree $T$ in a static forest $\mathcal{F}$. Every processor $p \in T$ transmits the request with highest priority in its neighborhood.

Proof. According to formal description of Algorithm 2, to transmit a request a processor executes $Q R$-action or $Q R C$-action. Assume, by the contradiction, that there is a processor $p \in T$ which does not transmit a request. That is, $Q R$-action and $Q R C$-action are disabled or they are not the enabled actions of highest priority at $p$.

We first show that $Q R$-action and $Q R C$-action are enabled at $p$. We must consider two cases: $p$ has a local request to send with a priority higher than its children requests or $p$ has a
request from a child to transmit of highest priority. If $p$ has a local request to send then $p . \operatorname{Req} \in$ $\{A S K, W A I T\}$. Since $Q R$-action is not enabled at $p$, this implies that $p$.Req $=W A I T$ and $p$ has already sent its request, a contradiction. In first case, $p$ 's request has the highest priority in $p$ 's neighborhood (i.e., $|\operatorname{PrioRC}(p)|=0$ or $\operatorname{Height}(p) \leq\left(C h_{p}\right) \cdot H Q$ ). So $Q R$ action is enabled at $p$, a contradiction. Otherwise, $p$ has a child request with a priority higher than the priority of its local request (i.e., we have $p . \operatorname{Req} \neq R E P$ and $|\operatorname{PrioRC}(p)|>0$ ). Consider the child $q$ of $p$ such that $C h_{p}=q$. We have that $Q R$-action is disabled and by contradiction $Q R C$-action is not enabled at $p$. Thus, to have $\neg \operatorname{Request} T(p)$ this implies we have $\left(C h_{p}\right) \cdot H Q \geq p . H Q$ and we must consider two sub-cases at $p: p \cdot Q \neq A$ or $\exists s \in \operatorname{Child}(p)$ such that $s . Q=W \wedge s . H Q=p . H Q$. Either $p \cdot Q \neq A$ then this implies that $\left(C h_{p}\right) \cdot H Q=p \cdot H Q$ and $p$ has already transmitted the request of $q$, a contradiction. Or $\exists s \in \operatorname{Child}(p)$ such that $s . Q=W \wedge s . H Q=p . H Q$. This implies that either $s=q$ and $p$ has already transmitted $q$ 's request or $s \neq q$ and $s . H Q<q . H Q$, a contradiction because $\left(C h_{p}\right)=q$. Thus, $Q R$-action or $Q R C$-action is enabled at every processor $p \in T$ which has a local request or a request from a child to transmit of highest priority.

We must show that $Q R$-action or $Q R C$-action is the enabled action of highest priority for every processor $p \in T$ which has a local request or a request from a child to transmit of highest priority. If $Q R$-action or $Q R C$-action are not the action of highest priority at $p$ then this implies that $Q E$-action is always enabled. According to Lemma 1, after executing $Q E$-action it is not enabled at $p$ (unless $Q R$-action or $Q R C$-action is executed), a contradiction. So, $Q E$-action is disabled at $p$. According to Lemma 2, $Q R$-action is enabled until it is executed at every processor $p \in T$ having a request of priority higher than its children requests (i.e., Height $\left.(p) \leq\left(C h_{p}\right) . H Q\right)$. Otherwise, we have $Q R$-action is disabled. Therefore, since $Q E$ action and $Q R$-action are disabled then $Q R C$-action is the enabled action of highest priority for every processor $p \in T$ which has a request of highest priority from a child to transmit.

Corollary 2. Let an allowed tree $T$ in a static forest $\mathcal{F}$. The request with highest priority in $T$ is transmitted to $\operatorname{root}(T)$.

Lemma 4. Let any allowed tree $T$ in a static forest $\mathcal{F}$. Every processor $p \in T$ waits for an acknowledgement if p's parent transmits the request of highest priority in p's neighborhood.

Proof. According to formal description of Algorithm 2, to wait for an acknowledgement to a request a processor executes $Q W$-action. Assume, by the contradiction, that there is a processor $p \in T$ which does not wait for an acknowledgement while $p$ transmits the request of highest priority in its neighborhood. That is, QW-action is disabled or it is not the enabled action of highest priority at $p$.

We first show that $Q W$-action is enabled at $p$. According to Lemma 3, for processor $p$ we have $p \cdot Q=R, p \cdot H Q=\operatorname{Height}(p)$, and $p \cdot R e q=W A I T$ if $p$ has sent a local request, or $p \cdot Q=R, p \cdot H Q \neq \operatorname{Height}(p)$, and $p \cdot R e q \neq R E P$ otherwise. We must consider two cases: $p$ 's parent has not transmitted $p$ 's request or there is a child of $p$ with a request of same priority which is not waiting for the acknowledgement (i.e., $\neg[\operatorname{Parent}(p) \cdot Q=R \wedge \operatorname{Parent}(p) \cdot H Q=p . H Q]$ or $\exists q \in \operatorname{Child}(p)$ such that $q \cdot H Q=p \cdot H Q \wedge q \cdot Q \neq W)$. Note that for $\operatorname{root}(T)$ only the second case must be considered. If $\neg[\operatorname{Parent}(p) \cdot Q=R \wedge \operatorname{Parent}(p) \cdot H Q=p \cdot H Q]$ then this implies that the request transmitted by $p$ 's parent is not the request of highest priority in the neighborhood of $p$ 's parent (since its parent has transmitted another request), a contradiction with assumption of lemma to prove. Otherwise, $\exists q \in \operatorname{Child}(p)$ such that $q \cdot H Q=p \cdot H Q \wedge q \cdot Q \neq W$. Then there is a path $\mathcal{P}(p, s)$ in $T$ such that $x . H Q=\operatorname{Height}(s)=q \cdot L Q$ for every processor $x \in \mathcal{P}(p, s)$.

Moreover, there is a processor $y \in \mathcal{P}(p, s)$ such that $y \cdot Q=W$ and $\operatorname{Parent}(y) \cdot Q=R$. Thus, $Q W$-action is enabled at $\operatorname{Parent}(y)$ from the first case. By induction on the length of path $\mathcal{P}(p, s)$ when every processor $x$ has executed $Q W$-action then $q \cdot Q=W$, a contradiction.

We must show that $Q W$-action is the enabled action of highest priority for every processor which transmits the request of highest priority in its neighborhood also transmitted by their parent. Assume, by the contradiction, that $Q W$-action is not the enabled action of highest priority at $p$. Suppose that $Q E$-action is the enabled action of highest priority at $p$. According to Lemma 1, after executing $Q E$-action it is not enabled at $p$, a contradiction. So, $Q E$-action is disabled at $p$. Suppose that $Q R$-action or $Q R C$-action is enabled at $p$, a contradiction because we assume that $p$ has transmitted the request of highest priority in its neighborhood (i.e., $((p . R e q=$ $W A I T \wedge p \cdot Q \neq A) \Rightarrow \neg \operatorname{Request}(p))$ or $((p \cdot \operatorname{Req} \neq \operatorname{REP} \wedge p \cdot Q \neq A) \Rightarrow \neg \operatorname{Request} T(p)))$.

Corollary 3. Let an allowed tree $T$ in a static forest $\mathcal{F}$. $\operatorname{root}(T)$ waits for an acknowledgement for the request of highest priority in $T$.

Lemma 5. Let an allowed tree $T$ in a static forest $\mathcal{F}$. A processor $p \in T$ waiting for an acknowledgement to a transmitted request transmits again the request of a child with the same priority, if it is the request of highest priority in p's neighborhood.

Proof. According to formal description of Algorithm 2, a processor $p \in T$ waiting for an acknowledgement executes $Q R C$-action to transmit again a request from a child with the same priority.

As there is a child request of highest priority in $p$ 's neighborhood transmitted by $p$, then we have $p . \operatorname{Req} \neq R E P \wedge|\operatorname{PrioRC}(p)|>0$. Assume, by the contradiction, that $p$ does not execute $Q R C$-action to transmit again the request with the same priority. Either for every child $q$ of $p$ we have $q \cdot Q \neq R$ or $q \cdot H Q \neq p \cdot H Q$ because $(\forall q \in \operatorname{Child}(p):: q \cdot Q \neq R \vee q \cdot H Q \neq p \cdot H Q) \Rightarrow$ $\neg \operatorname{Retransmit}(p)$. Either $q \cdot Q \neq R$ then $q$ has no request to transmit because its request was already transmitted (i.e., q. $Q=W$ ) or an acknowledgement was received (i.e., $q \cdot Q=A$ ), a contradiction. Or $q \cdot H Q \neq p \cdot H Q$ then the request to transmit again by $p$ is not of highest priority in $p$ 's neighborhood (i.e., $\left(C h_{p}\right) \cdot H Q \neq q \cdot H Q$ ), a contradiction with the hypothesis of the lemma.

Lemma 6. Let any allowed tree $T$ in a static forest $\mathcal{F}$ and a processor $s \in T$ sending the request of highest priority in $T$. Every processor $p \in \mathcal{P}(\operatorname{root}(T), s)$ transmits the acknowledgement to the request of $s$.

Proof. According to formal description of Algorithm 2, to transmit the acknowledgement to a request a processor executes $Q A$-action. Assume, by the contradiction, that there is a processor $p \in \mathcal{P}(\operatorname{root}(T), s)$ which does not transmit the acknowledgement to the request of $s$. That is, $Q A$-action is disabled or it is not the enabled action of highest priority at $p$.

We first show that $Q A$-action is enabled at $p$. According to Lemma 4, for processor $p$ we have $p \cdot Q=W$, $p \cdot H Q=\operatorname{Height}(s)$, and $p . \operatorname{Req}=W A I T$ if $p=s$, or $p \cdot Q=W$, $p . H Q=\operatorname{Height}(s) \neq \operatorname{Height}(p)$ and $p . \operatorname{Req} \neq R E P$ otherwise. We must consider two cases: $p=\operatorname{root}(T)$ or $p \neq \operatorname{root}(T)$. Consider processor $\operatorname{root}(T)$, if $Q A$-action is disabled then this implies that $\operatorname{root}(T) \cdot Q \neq W$, a contradiction with the assumption that for every processor $q \in \mathcal{P}(\operatorname{root}(T), s)$ we have $q \cdot Q=W$. Now, $p \neq \operatorname{root}(T)$. Consider $p$ is the child of $\operatorname{root}(T)$ such that $p \in \mathcal{P}(\operatorname{root}(T), s)$. If $Q A$-action is disabled at $p$ then either $\operatorname{Parent}(p) . Q \neq A$ or
$\operatorname{Parent}(p) \cdot H Q \neq p \cdot H Q$ or $p \cdot Q \neq W$, a contradiction because $\operatorname{root}(T) \cdot Q=A$ from first case and we assume for every processor $q \in \mathcal{P}(\operatorname{root}(T), s)$ we have $p \cdot Q=W, p \cdot H Q=\operatorname{Height}(s)$. Otherwise, $p \neq \operatorname{root}(T)$ and $p$ is not the child of $\operatorname{root}(T)$. By induction on the length of path $\mathcal{P}(\operatorname{root}(T), s)$, the arguments used for processor $p$ can be applied for every processor $q \in$ $\mathcal{P}(\operatorname{root}(T), s)$. Thus, $Q A$-action is enabled for every processor $q \in \mathcal{P}(\operatorname{root}(T), s)$.

We must show that $Q A$-action is the enabled action of highest priority for every processor $p \in \mathcal{P}(\operatorname{root}(T), s)$. Assume, by the contradiction, that $Q A$-action is not the enabled action of highest priority at $p$. According to Lemma 1, after executing $Q E$-action it is not enabled at $p$, a contradiction. So, $Q E$-action is disabled at $p$. Suppose that $Q R$-action or $Q R C$-action is enabled at $p$, a contradiction because we assume that $p$ has transmitted the request of highest priority in its neighborhood (i.e., $((p . \operatorname{Req}=W A I T \wedge p . Q \neq A) \Rightarrow \neg \operatorname{Request}(p))$ or $((p . \operatorname{Req} \neq R E P \wedge p . Q \neq A) \Rightarrow \neg \operatorname{Request} T(p)))$. Suppose that $Q W$-action is enabled at $p$, a contradiction because $p \cdot Q \neq R$.

Lemma 7. Let an allowed tree $T$ in a static forest $\mathcal{F}$. Between the reception of two acknowledgements to a request, every processor $p \in T$ has sent a new request.

Proof. According to formal description of Algorithm 2, to receive an acknowledgement to a request in an allowed tree $T$ a processor executes $Q A$-action. Assume, by the contradiction, that there is a processor $p \in T$ which receives two acknowledgements for the same request. That is, $Q R$-action and $Q R C$-action are not executed by $p$ between two consecutive executions of $Q A$-action.

After the first execution of $Q A$-action by $p$, we have $p \cdot Q=A$ in configuration $\gamma_{i}$. To execute $Q A$-action in step $\gamma_{j-1} \mapsto \gamma_{j}$, with $i<j$, this implies we had $p \cdot Q=W$ in $\gamma_{j-1}$ because $\operatorname{Answer}(p) \Rightarrow p \cdot Q=W$. Thus, $Q W$-action was executed in step $\gamma_{j-2} \mapsto \gamma_{j-1}$. However, to execute $Q W$-action in step $\gamma_{j-2} \mapsto \gamma_{j-1}$ this implies we had $p \cdot Q=R$ in $\gamma_{j-2}$ because $\operatorname{Wait}(p) \Rightarrow p . Q=R$. So, by formal description of Algorithm $2 Q R$-action or $Q R C$ action was executed in step $\gamma_{j-3} \mapsto \gamma_{j-2}$, with $i<j-3$, a contradiction.

Lemma 8. Let an allowed tree $T$ in a static forest $\mathcal{F}$ and a processor $p \in T$ at height $k$ with a local request of highest priority in $T$. From any configuration, in at most $k+1$ rounds $p$ 's request is transmitted to $\operatorname{root}(T)$.

Proof. We show by induction the following proposition: If at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$ and $\exists p \in T$ at height $k$ such that $p . \operatorname{Req}=$ $A S K$, then in at most $j+1$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=R \wedge q \cdot H Q=\operatorname{Height}(p)$ at height $\geq k-j$ in $T$.
In the base case $j=0$ and we consider $p$. According to Lemma 2, if $\operatorname{Request}(p)$ is satisfied at $p$ then $p$ executes $Q R$-action and we have $p \cdot Q=R$ and $p \cdot H Q=\operatorname{Height}(p)$ at $p$. Consider that in first configuration of round $0 p$ satisfies $\operatorname{Error}(p)$, then $p$ can execute $Q E$-action and as the daemon is weakly fair at the end of round 0 we have $p \cdot Q=A$ and $p \cdot H Q=\operatorname{Height}(p)$. At the first configuration of round $1, p$ satisfies $\operatorname{Request}(p)$ and it can execute $Q R$-action. Since the daemon is weakly fair, thus the proposition is verified because at the last configuration of round 1 we have $p \cdot Q=R$ and $p . H Q=\operatorname{Height}(p)$ at $p$.
Induction case: We assume that in round $j=k-1$ the proposition is true for any processor at height $h, k-j \leq h \leq k$ in $\mathcal{P}(\operatorname{root}(T), p)$. We have to show that if at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$, then in round $j+1$ for any processor $q \in \mathcal{P}(\operatorname{root}(T), p)$ at height $h, k-(j+1) \leq h \leq k$, we have $q \cdot Q=R \wedge q \cdot H Q=$
$\operatorname{Height}(p)$. So, we consider the processor $x \in \mathcal{P}(\operatorname{root}(T), p)$ at height $k-(j+1)$ in $T$. If QE-action is enabled at $x$ in the beginning of round $j$ then as the daemon is weakly fair we have $(x . Q=A \wedge x . H Q=\operatorname{Height}(x)) \Rightarrow \neg \operatorname{Error}(x)$ at the first configuration of round $j+1$. Since there is no processor $s \in T, s \neq p$ at height lower than $k$ such that $Q R$-action is enabled at $s$, then $|\operatorname{PrioRC}(x)|>0$ and $C h_{x}=q$ such that $q \cdot Q=R$ and $q \cdot H Q=\operatorname{Height}(p)$. Either $\operatorname{Height}(p)<x . H Q$, then $Q R C$-action is enabled at $x$ in round $j+1$. Or $\operatorname{Height}(p) \geq x . H Q$, then as the daemon is weakly fair we have $(\forall s \in \operatorname{Child}(x):: s . Q=W \Rightarrow s . H Q \neq x . H Q)$, so $\operatorname{Transmit}(x)$ is satisfied (remind that $x$ has no request to send so $x \cdot \operatorname{Req} \neq R E P$ and $x \cdot Q=A$ ) and $Q R C$-action is enabled at $x$ in round $j+1$. In all the above cases, as the daemon is weakly fair in the last configuration of round $j+1$ so we have $x \cdot Q=R$ and $x \cdot H Q=\operatorname{Height}(p)$ at $x \in \mathcal{P}(\operatorname{root}(T), p)$, which verifies the proposition. Therefore, since $|\mathcal{P}(\operatorname{root}(T), p)|=k$ in at most $k+1$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=R \wedge q \cdot H Q=\operatorname{Height}(p)$.

Lemma 9. Let an allowed tree $T$ in a static forest $\mathcal{F}$ and a processor $p \in T$ at height $k$ with a local request of highest priority in $T$ transmitted to root $(T)$. In at most $k+1$ additional rounds, every processor $q \in T$ waits for an acknowledgement if $q$ transmits $p$ 's request.

Proof. According to Lemma 8, since $p . R e q=W A I T \wedge p . Q=R \wedge p . H Q=\operatorname{Height}(p)$ at processor $p \in T$ at height $k$ then in at most $k+1$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=$ $R \wedge q \cdot H Q=\operatorname{Height}(p)$.

We show by induction the following proposition: If at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$, and $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=R \wedge$ $q \cdot H Q=\operatorname{Height}(p)$, then in at most $j+1$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=W \wedge$ $q \cdot H Q=\operatorname{Height}(p)$ at height $\geq k-j$ in $T$.
In the base case $j=0$ and we consider $p$. We have $(\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=R \wedge q \cdot H Q=$ $\operatorname{Height}(p))$, in particular for $\operatorname{Parent}(p)$ and $p$. Thus, the proposition is verified for $p$ because $Q W$-action is enabled at $p$ in round 0 , and in the first configuration of round 1 we have $p \cdot Q=W$ and $p . H Q=\operatorname{Height}(p)$ at $p$ (since the daemon is weakly fair).
Induction case: We assume that in round $j=k-1$ the proposition is true for any processor at height $h, k-j \leq h \leq k$ in $\mathcal{P}(\operatorname{root}(T), p)$. We have to show that if at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$, then in round $j+1$ for any processor $q \in \mathcal{P}(\operatorname{root}(T), p)$ at height $h, k-(j+1) \leq h \leq k$, we have $q \cdot Q=W \wedge q \cdot H Q=$ Height $(p)$. By induction hypothesis, in the first configuration of round $j+1$ we have for any processor $s \in \mathcal{P}(\operatorname{root}(T), p)$ at height $\geq j$ we have $s . Q=W \wedge s . H Q=\operatorname{Height}(p)$. Thus, $\exists s \in \operatorname{Child}(q), s . Q=W \wedge s . H Q=q \cdot H Q$, and $q \cdot Q=R$ so $Q W$-action is enabled at $q$ in round $j+1$. So, since the daemon is weakly fair we have $q \cdot Q=W$ and $q \cdot H Q=\operatorname{Height}(p)$ at $q$, in the last configuration of round $j+1$, which verifies the proposition. Therefore, since $|\mathcal{P}(\operatorname{root}(T), p)|=k$ in at most $k+1$ additional rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=$ $W \wedge q \cdot H Q=\operatorname{Height}(p)$.

Lemma 10. Let an allowed tree $T$ in a static forest $\mathcal{F}$ and a processor $p \in T$ at height $k$ with a local request of highest priority in $T$ transmitted to $\operatorname{root}(T)$. In at most $k+1$ additional rounds, every processor $q \in T$ transmits the acknowledgement to $p$ 's request if $q$ has transmitted $p$ 's request.

Proof. According to Lemmas 8 and 9 , in at most $2(k+1)$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p)$, $q \cdot Q=W \wedge q \cdot H Q=\operatorname{Height}(p)$.

We show by induction the following proposition: If at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$, and $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=W \wedge$ $q \cdot H Q=\operatorname{Height}(p)$, then in at most $j+1$ rounds we have $x \cdot Q=A \wedge x \cdot H Q=\operatorname{Height}(p)$ at processor $x \in \mathcal{P}(\operatorname{root}(T), p)$ of height $\leq j$ in $T$.
In the base case $j=0$ and we consider $x=\operatorname{root}(T)$. We have $(\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=$ $W \wedge q \cdot H Q=\operatorname{Height}(p))$, in particular for $\operatorname{root}(T)$. The proposition is verified for $x$ because we have $(x . Q=W \Rightarrow \operatorname{Answer}(x))$ and $Q A$-action is enabled at $x$ in round 0 . Thus, in the first configuration of round 1 we have $x . Q=W \wedge x . H Q=\operatorname{Height}(p)$ at $x$ (since the daemon is weakly fair).
Induction case: We assume that in round $j$ the proposition is true for every processor at height $\leq j$ in $\mathcal{P}(\operatorname{root}(T), p)$. We have to show that if at height less than $k$ in $T$ there is no processor $q \in T$ such that $Q R$-action is enabled at $q$, then in round $j+1$ for processor $x \in \mathcal{P}(\operatorname{root}(T), p)$ at height $j+1$, we have $x . Q=A \wedge x . H Q=\operatorname{Height}(p)$. By induction hypothesis, in the first configuration of round $j+1$ we have $\operatorname{Parent}(x) \cdot Q=A \wedge \operatorname{Parent}(x) \cdot H Q=x \cdot H Q \wedge x \cdot Q=W$ at $x$, so $Q A$-action is enabled at $x$ in round $j+1$. Therefore, since the daemon is weakly fair we have $x . Q=A$ and $x . H Q=\operatorname{Height}(p)$ at $x$, in the first configuration of round $j+1$ which verifies the proposition. Moreover, we have $|\mathcal{P}(\operatorname{root}(T), p)|=k$ because $p$ is at height $k$ in $T$. According to formal description of Algorithm 2, if $x . H Q=\operatorname{Height}(x)$ when $Q A$-action is executed at $x$ then we have $x . \operatorname{Req}=R E P$. So we have $x=p$, and in most $k+1$ additional rounds we have $p \cdot \operatorname{Req}=R E P \wedge p \cdot Q=A \wedge p \cdot H Q=\operatorname{Height}(p)$.

Lemma 11. Let the set of configurations $\mathcal{B} \subseteq \mathcal{C}$ such that in every $\gamma \in \mathcal{B}$ there is no request and every processor $p \in V$ has received an acknowledgement. In every configuration $\gamma \in \mathcal{B}$, for every processor $p \in V$ no action of Algorithm 2 is enabled.

Proof. Since there is no request in $\gamma$ then for every processor $p \in V$ we have $p . \operatorname{Req} \neq A S K$ and $p . R e q \neq W A I T$. Moreover, observe that according to formal description of Algorithm 2 for every processor $p \in V$ we have $p \cdot Q \neq A$ either when $p \cdot R e q=W A I T$ or when $p \cdot R e q=$ OUT or $p . \operatorname{Req}=A S K$ with a descendant $x$ of $p$ such that $x . R e q=W A I T$. However, as $\forall p \in V, p . R e q \neq A S K$ and therefore $p . R e q \neq W A I T$ this implies we have $\forall p \in V, p \cdot Q=A$ in $\gamma$.

Assume, by the contradiction, that $\exists \gamma \in \mathcal{B}$ such that $\exists p \in V$ with an enabled action of Algorithm 2. If $Q E$-action is enabled at $p$ then this implies that $p . Q \neq A$, a contradiction. If $Q R$-action is enabled at $p$ then this implies that $p . R e q=A S K$, a contradiction since $\forall p \in$ $V, p . R e q \neq A S K$. If $Q R C$-action is enabled at $p$ then there is a child $q$ of $p$ such that $q . Q \neq A$ (i.e., $|\operatorname{PrioRC}(p)|>0)$, a contradiction because $((\forall p \in V, p . Q=A) \Rightarrow|\operatorname{PrioRC}(p)|=0)$. If $Q W$-action is enabled at $p$ then this implies that $p \cdot Q=R$, a contradiction because $\forall p \in$ $V, p \cdot Q=A$. If $Q A$-action is enabled at $p$ then this implies that $p \cdot Q=W$, a contradiction because $\forall p \in V, p \cdot Q=A$.

Lemma 12. Let a tree $T$ in a static forest $\mathcal{F}$. From any configuration where a processor $p \in T$ executes $Q R$-action, the execution satisfies Specification 2.

Proof. We have to show that starting from any configuration the execution of Algorithm 2 verifies all the properties of Specification 2.

We first show that Property (Liveness 1) of Specification 2 is satisfied. Let an allowed tree $T$ in a static forest $\mathcal{F}$. From any configuration according to Lemmas 2 and 8 a processor in $T$ which has a local request of highest priority in $T$ sends this request to $\operatorname{root}(T)$ in finite time
with Algorithm 2. Assume, by the contradiction, that there is a processor $p \in T$ which has infinitely often a request to send but it can not send its request to $\operatorname{root}(T)$, although there are a finite number of requests sent in $T$. This implies either that an infinite time is needed to send a request from $p$ to $\operatorname{root}(T)$, a contradiction with Lemmas 2 and 8 , or the request sent by $p$ is never the request of highest priority in $T$, a contradiction with the hypothesis of a finite number of requests sent in $T$. This satisfies Property (Liveness 1) of Specification 2.

We now show that Property (Liveness 2) of Specification 2 is satisfied. Let a processor $p \in T$ which has sent a request in an allowed tree $T$ and waits for the acknowledgement to its request. According to Theorem 1, the execution of Algorithm 2 is not done. Moreover, by Lemma 6 a processor which has sent a request with highest priority in $T$ receives an acknowledgement from $\operatorname{root}(T)$ in finite time. Thus, at least one processor receives an acknowledgement from $\operatorname{root}(T)$ in a finite time, the processor waiting for the acknowledgement to the request of highest priority in $T$. This satisfies Property (Liveness 2) of Specification 2.

We now show that Property (Safety 1) of Specification 2 is satisfied. According to Lemma 6, a processor $p$ which has sent a local request in an allowed tree $T$ receives at least one acknowledgement to its request. Moreover, by Lemma 7 a processor $p$ receives at most one acknowledgement to a sent request. This satisfies Property (Safety 1) of Specification 2.

We now show that Property (Safety 2) of Specification 2 is satisfied. Assume, by the contradiction, that there is a processor $p$ sending a request in a not allowed tree $T$ which receives an acknowledgement from $\operatorname{root}(T)$. Since $\operatorname{root}(T)$ is the root of a not allowed tree, we have $\neg \operatorname{Allowed}(\operatorname{root}(T))$ and $\operatorname{Parent}(\operatorname{root}(T)) \in \operatorname{Neig}_{\text {root }(T)}$. So, there is a cycle in $T$ because every processor in $T$ has a parent. Moreover, if $p$ receives an acknowledgement from $\operatorname{root}(T)$ then $\operatorname{root}(T)$ can execute $Q A$-action. This implies that $\operatorname{Parent}(\operatorname{root}(T)) \cdot Q=A$ because $\operatorname{Answer}(p) \Rightarrow \operatorname{Parent}(\operatorname{root}(T)) \cdot Q=A . \quad$ So, either $\operatorname{root}(T) \cdot Q=R$ or $\operatorname{root}(T) \cdot Q=$ $W \wedge \operatorname{root}(T) \cdot H Q \neq \operatorname{Parent}(\operatorname{root}(T)) \cdot H Q$ then $\operatorname{Parent}(\operatorname{root}(T))$ executes $Q R C$-action (because $\operatorname{Transmit}(\operatorname{Parent}(\operatorname{root}(T))) \Rightarrow \operatorname{Request}(\operatorname{Parent}(\operatorname{root}(T))))$, a contradiction. Otherwise, we have $\operatorname{Parent}(\operatorname{root}(T)) \cdot Q=A \wedge(\forall q \in \operatorname{Child}(\operatorname{Parent}(\operatorname{root}(T))), q \cdot Q=$ $W \wedge q \cdot H Q=\operatorname{Parent}(\operatorname{root}(T)) \cdot H Q)$ given by an initial configuration of the system, a contradiction. This satisfies Property (Safety 2) of Specification 2.

By Theorem 1 and Lemmas 11 and 12, the result below follows:
Theorem 2. Algorithm 2 is snap-stabilizing for Specification 2 under a weakly fair daemon.

### 9.1.3. Proof assuming an unfair daemon

Lemma 13. Let any allowed tree $T$ in a static forest $\mathcal{F}$ and any processor $p \in T$ with a local request of highest priority in $T$. If there is no new request with higher or equal priority than $p$ 's request in $T$, then $p$ 's request is transmitted to $\operatorname{root}(T)$ in at most $2 n$ steps, with $n$ the number of processors in the network.

Proof. According to Lemma 3, if there is no new request with higher or equal priority than $p$ 's request in $T$ then every processor $q \in \mathcal{P}(\operatorname{root}(T), p) \backslash\{p\}$ executes $Q R C$-action to transmit $p$ 's request to $\operatorname{root}(T)$. Observe that $|\mathcal{P}(\operatorname{root}(T), p)| \leq n$ and $Q R$-action is disabled at every processor $q \in \mathcal{P}(\operatorname{root}(T), p) \backslash\{p\}$. Suppose that for every processor $q$ the enabled action of highest priority is $Q E$-action, then after executing $Q E$-action we have $q \cdot Q=A$ and $q \cdot H Q=$ Height $(q)$ and $Q E$-action is disabled at $q$ according to Lemma 1. Then, $Q R C$-action is the enabled action of highest priority at $q$. As $|\mathcal{P}(\operatorname{root}(T), p)| \leq n$, in at most $2 n$ steps $p$ 's request is transmitted to $\operatorname{root}(T)$.

Lemma 14. Let any allowed tree $T$ in a static forest $\mathcal{F}$ and any processor $p \in T$ with a local request of highest priority in $T$ transmitted to $\operatorname{root}(T)$. If there is no new request with higher or equal priority than $p$ 's request in $T$, then $p$ receives an acknowledgement from $\operatorname{root}(T)$ in at most $2 n$ steps, with $n$ the number of processors in the network.

Proof. We assume there is no new request with higher or equal priority than $p$ 's request in $T$. Thus according to Lemma 3, we have $q \cdot Q=R$ and $q \cdot H Q=\operatorname{Height}(p)$ for every processor $q \in \mathcal{P}(\operatorname{root}(T), p)$. Moreover, the following actions are disabled for every processor $q \in \mathcal{P}(\operatorname{root}(T), p): Q E$-action because there exists a child $s$ of $q$ such that $s . H Q=q \cdot H Q \wedge$ $s . Q \neq A$ (in case of $p, p \cdot \operatorname{Req}=W A I T) ; Q R$-action because $q \cdot \operatorname{Req} \neq A S K$ or $\operatorname{Height}(q)>$ $q \cdot H Q=\left(C h_{q}\right) \cdot H Q$; and $Q R C$-action because $q \cdot Q=R \wedge(\forall s \in \operatorname{Child}(q), s \cdot Q=W \wedge$ $s . H Q=q \cdot H Q)$. According to Lemmas 4 and 6 , since there is no new request with higher or equal priority than $p$ 's request in $T$ thus every processor $q \in \mathcal{P}(\operatorname{root}(T), p)$ executes $Q W$ action to wait for an acknowledgement to $p$ 's request and then executes $Q A$-action to transmit the acknowledgement from $\operatorname{root}(T)$ to $p$. Observe that $|\mathcal{P}(\operatorname{root}(T), p)| \leq n$, thus in at most $2 n$ steps $p$ receives the acknowledgement from $\operatorname{root}(T)$ to its local request.

Lemma 15. Let any allowed tree $T$ in a static forest $\mathcal{F}$. In at most $O\left(n^{2}\right)$ steps, at least one processor $p$ with a local request receives an acknowledgement from $\operatorname{root}(T)$ to its request.

Proof. Assume without loss of generality that forest $\mathcal{F}$ is composed of a single tree $T$ containing the $n$ processors of the network. By Lemma 3, a request of highest priority stops the transmission of the acknowledgement of a request of lowest priority at a processor $q \in T$ because $\left(C h_{q}\right) \cdot H Q<q \cdot H Q \Rightarrow \operatorname{Request} T(q)$. Moreover, by Lemma 7 it is also the case at a processor $q \in T$ if there is a new request with the same priority than the previous request of highest priority because $\operatorname{Retransmit}(q) \Rightarrow \operatorname{Request} T(q)$. According to Lemma 13, if there is no new request with higher or equal priority than $p$ 's request in $T$ then in at most $2 n$ steps the processor $p$ receives an acknowledgement. However, since there is at most $n$ requests in parallel in $T$ then the acknowledgement of $p$ 's request can be stopped at most $n-1$ times.

Corollary 4. Let a static forest of trees $\mathcal{F}$ and a given set of requests. If there is no new request in $\mathcal{F}$ then in at most $O\left(n^{3}\right)$ steps every processor with a local request has received an acknowledgement to its request.

Proof. First observe that given a static forest $\mathcal{F}$, we can have a local request from at most each processor in $\mathcal{F}$, i.e., at most $n$ processors have a local request to send. According to Lemma 15, in at most $O\left(n^{2}\right)$ steps at least one processor sending a request receives an acknowledgement and as we have at most $n$ processors with a local request in $\mathcal{F}$, then the corollary follows.

### 9.2. Proof of Spanning Tree algorithm

To prove the correctness of the BFS algorithm (Algorithm 1), we first introduce some useful definitions, and we show that in an illegitimate configuration there is always at least one enabled processor (Theorem 3). Then, we establish that in at most $\Theta\left(d^{2}\right)$ rounds the BFS algorithm constructs a BFS tree (Lemmas 19 and 20). We show that the BFS algorithm is a silent algorithm (Lemma 21), which allows us to conclude that the BFS algorithm satisfies Specification 1 under a weakly fair daemon (Lemma 22). To establish the step complexity of the BFS algorithm under an unfair daemon, we first give several additional definitions, particularly we define a topological change in the forest of trees which mainly represents the parent changes. First of all, we
show that any processor can be connected at most once to the normal tree via the same neighbor (Lemma 24). Based on this result, we then prove that at most $2 \Delta m+m n$ requests are initiated by the BFS algorithm to construct a BFS tree (Lemma 29) starting from any configuration, since in any execution there are at most $2 \Delta n+n^{2}$ topology changes (Corollary 9) and each topology change produce at most $\Delta$ requests (Lemma 28). Finally, we show that starting from any configuration the BFS algorithm constructs a BFS tree in at most $O\left(\Delta m n^{3}+m n^{4}\right)$ steps (Lemma 30), since each request is handled using at most $O\left(n^{3}\right)$ steps by the Question-Answer algorithm.

### 9.2.1. Definitions

We give below the definitions used in this section, in particular we define precisely the notion of tree and normal tree.

Definition 7 (Tree). $\forall p \in V$ such that $\operatorname{Allowed}(p) \vee(p . P) . L \geq p . L$, we define a set Tree $(p)$ of processors as follows: $\forall q \in V, q \in \operatorname{Tree}(p)$ if and only if there exists a unique path $\mathcal{P}(p, q)$.

Definition 8 (Normal tree). A tree $T$ rooted at processor $\operatorname{root}(T)$ is called a normal tree if it contains only processors $p$ such that either (i) $(p=\operatorname{root}(T) \wedge \operatorname{Allowed}(p))$, or (ii) $(p . S=$ $C \wedge p . L=(p . P) . L+1)$. Any tree $T^{\prime}$ rooted at processor root $\left(T^{\prime}\right)$ such that $\neg$ Allowed $\left(\operatorname{root}\left(T^{\prime}\right)\right)$ is called an abnormal tree.

In the following, we consider there is only one processor $p \in V$ which is allowed to send an acknowledgement to a request, the root $r$, i.e., $\operatorname{Allowed}(p) \equiv(p=r)$. Therefore, there is only one normal tree, the tree $\operatorname{Tree}(r)$ rooted at $r$. Moreover, given two processors $u, v \in V$ we define by $d_{H}(u, v)$ the distance (in hops) between $u$ and $v$ in the subgraph $H$.

Remark 1. The system always contains one normal tree: the tree rooted at processor $r$.
Remark 2. All actions of Question-Answer algorithm are disabled for every processor $p \in$ $V \backslash\{r\}$ such that $p . S=E$ or $p . L \neq(p . P) . L+1$ or $\left(\exists q \in N e i g_{p}:: p . L>q . L+1\right)$.

The above remark comes from the conditional composition of Algorithm $\mathcal{B F S}$. In the two first cases, a processor $p$ cannot execute Question-Answer algorithm because Predicate $\operatorname{Good} T(p)$ is not satisfied, whereas the third case does not satisfy Predicate $\operatorname{GoodL}(p)$.

Definition 9 (Locally healthy processor). Let a tree $T \in \mathcal{F}$. A processor $p \in T$ is called locally healthy if $p$ satisfies the following predicate: $p \cdot S=C \wedge p \cdot L=(p \cdot P) \cdot L+1 \wedge \neg G P$ $R E P(p)$.

### 9.2.2. Proof assuming a weakly fair daemon

Theorem 3. Let the set of configurations $\mathcal{B} \subseteq \mathcal{C}$ such that every configuration $\gamma \in \mathcal{B}$ satisfies Definition 3. $\forall \gamma \in(\mathcal{C}-\mathcal{B}), \exists p \in V$ such that $p$ is enabled in $\gamma$.

Proof. Assume, by the contradiction, that $\exists \gamma \in(\mathcal{C}-\mathcal{B})$ such that $\forall p \in V$ no action is enabled at $p$ in $\gamma$. Since $\gamma \notin \mathcal{B}$, there is at least one abnormal tree $T$ in $\gamma$. Consider first every node $p \in T$ such that $p . S=C$. According to formal description of Algorithm 1, every processor $p \in V, p \neq r$, has a parent (i.e., $p . P \in N e i g_{p}$ ). So, if $p=\operatorname{root}(T)$ then we have $(p . P) . L \geq$ p.L (see Definition 7), and $E$-action is enabled at $p$, a contradiction. If $\exists p \in T$ such that $(p . P) . S=E$, then $E$-action is enabled at $p$, a contradiction. Now, in any abnormal tree $T$
we have $\forall p \in T, p . S=E$. Since $\gamma \notin \mathcal{B}$, then there is at least one abnormal tree $T$ or $\exists q \in$ Neig,$q . L-p . L>1$ for a processor $p \in V$. So, $\exists p \in \operatorname{Tree}(r)$, such that $\operatorname{GP}-R E P(p)$. In this case, either $p \cdot R e q=O U T$ then $A$-action is enabled at $p$, a contradiction. Hence, by the contradiction, $\forall p \in \operatorname{Tree}(r), p \cdot R e q \neq O U T$. If $p . \operatorname{Req}=A S K$ then according to Lemma 18 in a finite time $p . R e q=R E P$. Thus, we assume that $p . R e q=R E P$. Either, $G P-R E P(p)$ then there exists a processor $q \in N e i g_{p}$ such that $C$-action is enabled at $q$ since $\left((G P-R E P(p) \wedge p . \operatorname{Req}=R E P) \Rightarrow\left(\exists q \in N e i g_{p}\right.\right.$, Connect $\left.\left.(q)\right)\right)$, a contradiction. Or, $\neg G P-$ $R E P(p)$ then $O$-action is enabled at $p$, a contradiction.

Lemma 16. Let an abnormal tree $T$ of height $h$. From any configuration, in at most $h+1$ rounds we have $\forall p \in T, p . S=E$.

Proof. We show by induction the following proposition: In at most $j+1$ rounds, we have $\forall p \in T,\left(d_{T}(\operatorname{root}(T), p) \leq j \Rightarrow p . S=E\right)$.
In base case $j=0$. Consider any processor $p$ such that (p.P).L $\geq p . L$. If $p . S \neq E$ then $E$-action is enabled at $p$ in round 0 . Therefore, since the daemon is weakly fair then in the first configuration of round 1 , we have $p . S=E$ at $p$ which verifies the proposition.
Induction case: We assume that in round $j=h-1$ we have $\forall q \in T,\left(d_{T}(\operatorname{root}(T), q) \leq j \Rightarrow\right.$ $q \cdot S=E)$. We have to show that in round $j+1$ we have $\forall p \in T,\left(d_{T}(\operatorname{root}(T), p) \leq j+1 \Rightarrow\right.$ $p . S=E$ ). Consider any node $p \in T$ of height $j+1$ in $T$. By induction hypothesis, we have $(p . P) . S=E$ and if $p . S \neq E$ then $E$-action is enabled at $p$ in round $j$. Thus, since the daemon is weakly fair then in the first configuration of round $j+1$ we have $p . S=E$ and we have also $\forall q \in T,\left(d_{T}(\operatorname{root}(T), q) \leq j \Rightarrow q \cdot S=E\right)$. Therefore, in at most $h+1$ rounds we have $\forall p \in T,\left(d_{T}(\operatorname{root}(T), p) \leq h \Rightarrow p . S=E\right)$.

Lemma 17. Let a normal tree $T$ in a static forest $\mathcal{F}$ and a processor $p \in T$ at height $k$ with a local request. From any configuration, in at most $O\left(k^{2}\right)$ rounds the request of $p$ is transmitted to $\operatorname{root}(T)$.

Proof. We show by induction the following proposition: For any node $p \in T$ at height $j \geq 0$ in $T$ such that $p \cdot \operatorname{Req}=W A I T$, in at most $O\left(j^{2}\right)$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=$ $R \wedge q . H Q=\operatorname{Height}(p)$.
In the base case $j=0$ and we consider $p=\operatorname{root}(T)$. According to Lemma 8 , in at most $j+1=1$ round we have $p \cdot Q=R \wedge p \cdot H Q=\operatorname{Height}(p)$, which verifies the proposition.
Induction case: We assume that for $j=k-1$ after $O\left(j^{2}\right)$ rounds for each node $p \in T$ at height $k-1$ in $T$ such that $p \cdot \operatorname{Req}=W A I T$ we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=R$ and $q \cdot H Q=\operatorname{Height}(p)$. Consider any node $p \in T$ of height $j+1$ in $T$. We have to show that in at most $O\left((j+1)^{2}\right)$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p),(\operatorname{Height}(q) \leq j+1 \Rightarrow$ $(q \cdot Q=R \wedge q \cdot H Q=H e i g h t(p)))$. According to Lemmas 9 and 10 , in at most $O(j)$ additional rounds we have $x . \operatorname{Req} \neq R E P$ and $x . Q=A$ at each node $x$ of height $j$ in $T$ (in particular at node $x=p . P$. According to Lemma 8, in at most $j+1$ additional rounds we have $\forall q \in$ $\mathcal{P}(\operatorname{root}(T), p),(\operatorname{Height}(q) \leq j+1 \Rightarrow(q \cdot Q=R \wedge q \cdot H Q=H e i g h t(p)))$. Thus, in at most $j^{2}+(j-1)+(j+1)<O\left(j^{2}\right)$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p),(\operatorname{Height}(q) \leq j+1 \Rightarrow$ $(q \cdot Q=R \wedge q \cdot H Q=\operatorname{Height}(p)))$, and the proposition is verified at $p$ on height $j+1$ in $T$.

Lemma 18. Let a normal tree $T$ in a static forest $\mathcal{F}$ and a processor $p \in T$ at height $k$ with a local request. From any configuration, in at most $O\left(k^{2}\right)$ rounds $p$ receives an acknowledgement to its local request.

Proof. Let a processor $p \in T$ such that $p . \operatorname{Req}=A S K$ of height $k$ in $T$. According to Lemma 17, from any configuration in at most $O\left(k^{2}\right)$ rounds we have $\forall q \in \mathcal{P}(\operatorname{root}(T), p), q \cdot Q=$ $R \wedge q \cdot H Q=H e i g h t(p)$. Thus, we can apply Lemma 10 and in at most $k+1$ additional rounds we have $p \cdot \operatorname{Req}=R E P \wedge p \cdot Q=A \wedge p \cdot H Q=\operatorname{Height}(p)$ at $p$.

Lemma 19. From any configuration, in at most $O\left(d^{2}\right)$ rounds Algorithm $\mathcal{B F S}$ reaches a configuration $\gamma \in \mathcal{C}$ satisfying Definition 3, with $d$ the diameter of the network.

Proof. Note that by definition of Predicate $\operatorname{Allowed}(p) \equiv(p=r)$ and according to Property (Safety 2) of Specification 2, only the nodes sending a request in the tree rooted at $r$ can receive an acknowledgement to a request. Moreover, we have the following constant values at $r: r \cdot S=$ $C, r . P=\perp$, and $r . L=0$.

We first show by induction on the distances of the network the following proposition: in at most $O\left(j^{2}\right)$ rounds, $\forall p \in V,\left(d_{G}(r, p) \leq j \Rightarrow\left(p \in \operatorname{Tree}(r) \wedge\left(\forall q \in N e i g_{p}, q \in \operatorname{Tree}(r) \wedge\right.\right.\right.$ $q \cdot L-p . L \leq 1)$ ) .

In base case $j=0$. We have first that $r \in \operatorname{Tree}(r)$. To verify the proposition at $r$, we must consider any neighbor $q$ of $r$ in the network such that $r=\operatorname{MinChPar}(q)$.

- First consider that $q \notin \operatorname{Tree}(r), q \in T$ with $T$ an abnormal tree of the forest. Either case (A) $d_{G}(\operatorname{root}(T), q) \leq j$ then according to Lemma 16 in at most $O(1)$ rounds $q$ has detected it is in an abnormal tree, thus we have $(q \cdot S=E \Rightarrow G P-R E P(r))$. In this case, E-action is not enabled at $q$ and according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)=O\left(1^{2}\right)$ rounds we have $r \cdot R e q=R E P$ at $r$. Thus, $q$ can execute $C$-action, so in $O(1)$ additional rounds we have $q \cdot S=C, q \cdot P=r$, and $q . L=1$ at $q$. Or case (B) $d_{G}(\operatorname{root}(T), q)>j$, we must consider two sub-cases: (B1) $d_{G}(\operatorname{root}(T), q)=j+1=2$ or $(\mathrm{B} 2) d_{G}(\operatorname{root}(T), q)>2$.
- In the sub-case $(\mathrm{B} 1), d_{G}(\operatorname{root}(T), q)=2$. According to Lemma 16 in at most $j+1=O(1)$ rounds $q \cdot P$ has detected it is in an abnormal tree $T$ and we have $(q \cdot P) \cdot S=E$, thus $q$ can execute $E$-action and in $O(1)$ additional rounds we have $q . S=E$ leading to the case (A).
- In the sub-case $(\mathrm{B} 2), d_{G}(\operatorname{root}(T), q)>2$. E-action is not enabled at $q$ and $d_{G}(\operatorname{root}(T), q)>2 \Rightarrow q . L-r . L>1$. Thus, according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)=O\left(1^{2}\right)$ rounds we have $r . \operatorname{Req}=R E P$ at $r$. Then, $C$-action is the enabled action with the highest priority at $q$ and in $O(1)$ additional rounds it is executed by $q$ to obtain $q \cdot S=C, q \cdot P=r$, and $q \cdot L=1$.
- Otherwise, consider that $q \in \operatorname{Tree}(r)$ then we have $q \cdot S=C$ and $E$-action is not enabled at $q$. We must consider the case such that $q . L-r . L>1$ at $q$. We have $(q . L-r . L>1 \Rightarrow$ $G P-R E P(r))$ and according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)=O\left(1^{2}\right)$ rounds we have $r$.Req $=$ REP at $r$. So $q$ can execute $C$-action and in $O(1)$ additional rounds we have $q \cdot S=C, q \cdot P=r$, and $q \cdot L=1$ at $q$.

Therefore, since the daemon is weakly fair in at most $O(1)$ rounds for every neighbor $q$ of $r$ the parent of $q$ is $r$ (i.e., $q \in \operatorname{Tree}(r)$ ) and $q . L-r . L \leq 1$, which verifies the proposition.

Induction case: We assume the proposition is verified for every node at distance $j-1$ from $r$ in the network. We have to show the proposition is also verified for every node at distance $j$ from
$r$. Consider any node $p$ at distance $j$ from $r$. By induction hypothesis, we have $p \in \operatorname{Tree}(r)$. Let any node $q \in N e i g_{p}$ such that $p=\operatorname{MinChPar}(q)$.

- First consider that $q \notin \operatorname{Tree}(r), q \in T$. Either case (A) $d_{G}(\operatorname{root}(T), q) \leq j$, then according to Lemma 16 in at most $j+1$ rounds $q$ has detected it is in an abnormal tree $T$ and we have $(q \cdot S=E \Rightarrow G P-R E P(p))$. In this case, $E$-action is not enabled at $q$ and according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)$ rounds we have $p \cdot R e q=R E P$ at $p$. Thus, $q$ can execute $C$-action, so in $O(1)$ additional rounds we have $q \cdot S=C, q \cdot P=p$, and $q \cdot L=p . L+1$ at $q$. Or case (B) $d_{G}(\operatorname{root}(T), q)>j$, we must consider two sub-cases: $(\mathrm{B} 1) d_{G}(\operatorname{root}(T), q)=j+1$ or $(\mathbf{B} 2) d_{G}(\operatorname{root}(T), q)>j+1$.
- In the sub-case $(\mathrm{B} 1), d_{G}(\operatorname{root}(T), q)=j+1$. According to Lemma 16 in at most $j+1$ rounds $q \cdot P$ has detected it is in an abnormal tree $T$ and we have $(q \cdot P) \cdot S=E$, thus $q$ can execute $E$-action and in $O(1)$ additional rounds we have $q \cdot S=E$ leading to the case (A).
- In the sub-case $(\mathrm{B} 2), d_{G}(\operatorname{root}(T), q)>j+1$. E-action is not enabled at $q$ and $d_{G}(\operatorname{root}(T), q)>j+1 \Rightarrow q . L-p . L>1$. Thus, according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)$ rounds we have $p . \operatorname{Req}=R E P$ at $p$. Then, $C$-action is the enabled action with the highest priority at $q$ and in $O(1)$ additional rounds it is executed by $q$ to obtain $q \cdot S=C, q \cdot P=p$, and $q \cdot L=p \cdot L+1$.
- Otherwise, consider that $q \in \operatorname{Tree}(r)$ then we have $q \cdot S=C$ and $E$-action is not enabled at $q$. We must consider the case such that $q \cdot L-p . L>1$ at $q$. We have $(q \cdot L-p . L>1 \Rightarrow$ $G P-R E P(p))$ and according to Property (Liveness 2) of Specification 2 and to Lemma 18 in at most $O\left(j^{2}\right)$ rounds we have $p . \operatorname{Req}=R E P$ at $p$. So $q$ can execute $C$-action and in $O(1)$ additional rounds we have $q \cdot S=C, q \cdot P=p$, and $q \cdot L=p \cdot L+1$ at $q$.

Therefore, since the daemon is weakly fair in at most $O\left(j^{2}\right)$ rounds for every neighbor $q$ of $p$ we have $q \in \operatorname{Tree}(r)$ and $q . L-p . L \leq 1$, which verifies the proposition. Note that, at distance $j$ from $r$ when the proposition is verified for any processor $p \in \operatorname{Tree}(r)$ then $p$ can execute $O$-action. So, since the daemon is weakly fair in at most $O\left(j^{2}\right)$ rounds we have $\forall p \in V,\left(d_{G}(r, p) \leq j \Rightarrow\right.$ $(p \in \operatorname{Tree}(r) \wedge p . \operatorname{Req}=O U T)$.

We now show that the configuration $\gamma$ reached by Algorithm $\mathcal{B F S}$ in $O\left(d^{2}\right)$ rounds verifies Definition 3. Let $d$ the diameter of the network $G$. In the proof above, any processor $p \in V$ at distance $d$ from $r$ belongs to the subgraph $\operatorname{Tree}(r)$ in at most $O\left(d^{2}\right)$ rounds, otherwise $G$ is not a connected network. Moreover, there is a path between any processor $p \in V$ and $r$ in $\operatorname{Tr} e e(r)$, so the subgraph $\operatorname{Tr} e e(r)$ is connected. Observe that, the subgraph $\operatorname{Tr} e e(r)$ is a spanning tree of the network $G$. Indeed, every processor $p \in V$ has a parent in Tree $(r)$ except $r$ which has no parent (i.e., there is an unique path between $p$ and $r$ ) and Tree $(r)$ is connected, so the subgraph Tree $(r)$ contains no cycle. Thus, these remarks imply that the configuration $\gamma$ verifies Claims 1 and 2 of Definition 3. To show the last Claim of Definition 3, assume by the contradiction that $\operatorname{Tree}(r)$ is not a breadth first search tree. This implies that $\exists p \in \operatorname{Tree}(r)$ such that $\left(\exists q \in N e i g_{p}:: q . L<(p . P) . L\right)$. That is, we have $p . L-q . L>1$ which contradicts the proposition verified by every processor $p \in \operatorname{Tree}(r)$ according to the induction proof above. Therefore, Claim 3 of Definition 3 is verified, which finishes to show the lemma.

Corollary 5. From any configuration, in at most $O\left(d^{2}\right)$ rounds there is no abnormal tree in forest $\mathcal{F}$, with $d$ the diameter of the network.

Now, in the following lemma we will establish a lower bound on the round complexity of Algorithm $\mathcal{B F S}$.

Lemma 20. Algorithm $\mathcal{B F \mathcal { F }}$ reaches a configuration $\gamma \in \mathcal{C}$ satisfying Definition 3 in $\Omega\left(d^{2}\right)$ rounds, with $d$ the diameter of the network.

Proof. To show the lower bound we will use the network illustrated in Figure 5(a). This is a star network where the root $r$ is at one extremity of a branch of the star. This topology can be generalized by extending the length $k$ of the branches (here $k=2$ ) or by adding more branches to the star, however the lower bound is preserved.


Figure 5: An example used to show the lower bound on round complexity of Algorithm $\mathcal{B F} \mathcal{F}$. (a) the network topology, (b) the initial configuration, and (c) the legitimate configuration.

We consider the initial configuration given in Figure 5(b) with $\left\lceil\frac{n}{2}\right\rceil$ trees in the forest $\mathcal{F}$. Moreover, for each processor $p$, the values of the variables are separated in three parts: the first pair ( $p . L, p . S$ ) related to Algorithm 1, followed by the shared variable $p . R e q$, and a last pair $(p . Q, p . H Q)$ related to Algorithm 2. In the initial configuration, each processor has the same state: $(0, C), O U T,(A, 0)$.

We will now determine the number of rounds necessary to Algorithm $\mathcal{B F S}$ to reach a legitimate configuration:

- In the first round, every processor $p, p \neq r$, executes $E$-action of Algorithm 1 to set $p . S$ to $E$, since we have $(p . P) . L \geq p . L \Rightarrow$ AbnormalTree $(p)$.
- By recurrence following the increasing order on network distances $i, 0 \leq i \leq d-1$ :

1. Processors at distance $i$ send a local request to Algorithm 2 and receive an acknowledgement from Algorithm 2 after $3 i+3$ rounds according to Lemmas 8, 9 and 10, since the request of processors $i$ has the highest priority in the network.
2. Processors at distance $i+1$ can hook to their neighbor at distance $i$ since an acknowledgement from the root $r$ has been given. To this end, in round $3 i+4$ these processors $p$ execute $C$-action of Algorithm 1 to set their variables $p . S, p . P$ and $p . L$ accordingly.
3. Finally, in round $3 i+5$ processors at distance $i$ execute $O$-action of Algorithm 1 to set their variable $\operatorname{Req}$ to $O U T$ since all their neighbors at distance $i+1$ are connected (i.e., they have no neighbor satisfying $G P-R E P())$. This allows to reach the legitimate configuration in Figure 5(c).

By summing up all the rounds, Algorithm $\mathcal{B F S}$ needs $3 i+5$ rounds to add each layer $i+1$ to the BFS tree. Since there are $d$ layers in a BFS tree, we have the following equation which shows the lemma: $\sum_{i=0}^{d-1} 3 i+5=3 \times \frac{d(d+1)}{2}+5=\Omega\left(d^{2}\right)$ rounds.

From Lemmas 19 and 20, we have the following result.
Corollary 6. From any configuration, in $\Theta\left(d^{2}\right)$ rounds Algorithm $\mathcal{B F S}$ reaches a configuration $\gamma \in \mathcal{C}$ satisfying Definition 3, with $d$ the diameter of the network.

Lemma 21. In every configuration $\gamma \in \mathcal{C}$ satisfying Definition 3, for every processor $p \in V$ no action of Algorithm 1 is enabled in $\gamma$.

Proof. Observe first that since $\gamma$ satisfies Definition 3, then for every processor $p \in V$ we have $\forall q \in N e i g_{p},|p . L-q . L| \leq 1$. Moreover, there is a single tree spanning every processor $p \in V$, thus there exists no abnormal tree and by Definition 7 for every processor $p \in V$ we have $p \cdot S=C \wedge p \cdot L=(p \cdot P) \cdot L+1$. These two observations imply that every processor $p \in V$ is locally healthy in $\gamma$ (see Definition 9).

Assume, by the contradiction, that $\exists \gamma \in \mathcal{C}$ satisfying Definition 3 such that $\exists p \in V$ with an enabled action of Algorithm 1 at $p$. If $E$-action is enabled at $p$ then $(p . P) . S=E$ or $(p . P) . L \geq$ $p . L$, a contradiction because $p$ is a locally healthy processor in $\gamma$. If $C$-action is enabled at $p$ and $p . S=C$ then $\exists q \in N e i g_{p}$ such that $p . L-q . L>1$, a contradiction because $p$ is locally healthy. If $A$-action is enabled at $p$ then $\exists q \in N e i g_{p}$ such that either $q \cdot S=E$, a contradiction because we have $\forall p \in V, p . S=C$ in $\gamma$, otherwise $\exists q \in N e i g_{p}, q . L-p . L>1$, a contradiction because $p$ is locally healthy. Finally, if $O$-action is enabled at $p$ then $p . R e q=R E P$ and $p$ can execute $O$-action in step $\gamma \mapsto \gamma^{\prime}$. In configuration $\gamma^{\prime}$, we have $p . S=C \wedge p . \operatorname{Req}=O U T$ so $O$-action is disabled. Moreover, there is no request because every processor $p \in V$ is locally healthy in $\gamma^{\prime}$, a contradiction.

By Lemmas 11 and 21, we have the following corollary.
Corollary 7. In every configuration $\gamma \in \mathcal{C}$ satisfying Definition 3, every action of Algorithm $\mathcal{B F S}$ is disabled at each processor $p \in V$ in $\gamma$.

Lemma 22. From any configuration, the execution satisfies Specification 1.
Proof. We have to show that starting from any configuration the execution of Algorithm $\mathcal{B F S}$ verifies Property [TC1] and [TC2] of Specification 1.

According to Lemma 19 and Corollary 7, from any configuration Algorithm $\mathcal{B F S}$ reaches a configuration $\gamma \in \mathcal{C}$ in finite time and $\gamma$ is a terminal configuration, which verifies Property [TC1] of Specification 1. Moreover, according to Lemma 19 the terminal configuration $\gamma$ reached by Algorithm $\mathcal{B F S}$ satisfies Definition 3, which verifies Property [TC2] of Specification 1.

Theorem 3 and Lemma 22 imply the following theorem.
Theorem 4. Algorithm $\mathcal{B F S}$ is snap-stabilizing for Specification 1 under a weakly fair daemon.

### 9.2.3. Proof assuming an unfair daemon

Definition 10 (Topological change). Given a forest $\mathcal{F}$ of trees in a configuration $\gamma \in \mathcal{C}$. A topological change in $\mathcal{F}$ is obtained by the execution of one of the following actions at a processor $p \in V$ in step $\gamma \mapsto \gamma^{\prime}$ : p executes $E$-action, or $p$ executes $C$-action.

Remark 3. For every processor $p \in \operatorname{Tree}(r), E$-action is disabled at $p$.

Remark 4. E-action, C-action, and $A$-action are disabled at every locally healthy processor $p \in V$.

Proposition 1. Every $p \in V$ is hooked on to the neighbor $q$ such that $\forall s \in N e i g_{p}, q . L \leq s . L$.
Proof. According to formal description of Algorithm 1, a processor hooks on to a neighbor using C-action. Assume, by the contradiction, that there is a processor $p \in V$ such that $\exists s \in$ Neig,$(p . P) . L>s . L$. We must consider two cases: $s$ is in an abnormal tree or not. If $s$ is in an abnormal tree then either $s . S=E$ then $s \notin \operatorname{MinChPar}(p) \Rightarrow \neg \operatorname{Connect}(p)$ a contradiction, or $s . S=C$ then by Property (Safety 2) of Specification $2 s$ never receives an acknowledgement and we have that $s . \operatorname{Req} \neq R E P \Rightarrow \neg \operatorname{Connect}(p)$, otherwise $C$-action is enabled at $p$, a contradiction. If $s$ is in a normal tree then by Property (Liveness 2) of Specification 2 we have that $s . R e q=R E P$ and $C$-action is enabled at $p$, a contradiction.

Lemma 23. Let any abnormal tree $T \in \mathcal{F}$ and the set of processors $B=\{p \in V: p \notin$ $T \wedge\left(\exists q \in N^{\prime}\right.$ eig $\left.\left.g_{p}:: q \in T\right)\right\}$. In an execution, only processors in $B$ can hook on to $T$.

Proof. Consider any abnormal tree $T \in \mathcal{F}$ in configuration $\gamma \in \mathcal{C}$. According to formal description of Algorithm 1, a processor $p$ must execute $C$-action to hook on to a tree, i.e., there is a neighbor $q$ such that $q \cdot R e q=R E P$. Suppose that every processor $q \in B$ executes $C$-action and they are hooked on to $T$ in configuration $\gamma_{k}$. Note that after executing $C$-action, we have $q \cdot R e q=O U T$ at every processor $q \in B$. Assume, by the contradiction, that there is a processor $p \notin T$ in configuration $\gamma_{k}$ which hooks on to $T$ in step $\gamma_{k} \mapsto \gamma_{k+j}, j>0$. This implies that $p$ hooks on to a neighbor $q \in B$ (by definition of $B$ ) such that $q \cdot R e q=R E P$, a contradiction by Property (Safety 2) of Specification 2 because $q$ can not receive an acknowledgement from $\operatorname{root}(T)$ since $T$ is an abnormal tree.

Corollary 8. Let any abnormal tree $T \in \mathcal{F}$ and the set of processors $B=\{p \in V: p \notin$ $\left.T \wedge\left(\exists q \in N e i g_{p}:: q \in T\right)\right\}$. In an execution, at most $|B|$ processors can hook on to $T$.

Proposition 2. Let a processor $p \in V$ which hooks on to a tree $T$ in configuration $\gamma_{i} \in \mathcal{C}$. If another processor $q \in V$ hooks on to $T$ by $p$ in $\gamma_{i+j}, j>0$, then $T$ is a normal tree.

Proof. According to Lemma 23, the expansion of an abnormal tree $T^{\prime}$ is limited at distance one from $T^{\prime}$. After $p$ hooks on to $T$, to allow the processor $q$ to hook on to $T$ by $p$ then $p$ receives an acknowledgement from $\operatorname{root}(T)$. Therefore, $T$ is a normal tree by Specification 2.

Lemma 24. Let any abnormal tree $T \in \mathcal{F}$. A processor $p \in V$ can hook on to $T$ at most once by the same neighbor $q \in T$.

Proof. Assume, by the contradiction, that there is a configuration $\gamma_{k} \in \mathcal{C}$ such that there is a processor $p \in V$ which hooks on to $T$ by the same neighbor $q \in T$ a second time. To hook on to $T, p$ must execute $C$-action, i.e., there is a neighbor $x \in T$ of $p$ such that $x . S=C$ and $x . R e q=R E P$. According to Proposition $1, p$ hooks on to the neighbor $x \in V$ such that $x . S=C \wedge\left(\forall s \in N e i g_{p}, x . L \leq s . L\right)$. Suppose that $p$ hooks on to $T$ by the neighbor $q$ a first time in step $\gamma_{i-1} \mapsto \gamma_{i} \in \mathcal{C}$, then $p$ hooks on to another neighbor $s$ of $p, s \neq q$, in step $\gamma_{j-1} \mapsto \gamma_{j} \in \mathcal{C}, j>i$. Now, we must consider several cases in configuration $\gamma_{k}, i<j<k$. If $p$ is hooked on to $s$ in $\gamma_{j}$ because $q . S=E$ and $s . R e q=R E P$ in $\gamma_{i}$ then since $q \in T$ we have $q . S=E$ in $\gamma_{k}$ and $q \notin \operatorname{MinChPar}(p) \Rightarrow \neg \operatorname{Connect}(p)$, a contradiction. Otherwise
$s . S=q . S=C$ and $p$ is hooked on to $s$ in $\gamma_{j}, i<j<k$, because $s . L<q . L$ and $s . \operatorname{Req}=R E P$. When $p$ hooks on to $q$ the first time in step $\gamma_{i-1} \mapsto \gamma_{i}$, we have $s . S=E$ or $s . L>q . L$. Since we have $s . S=C \wedge s . L<q . L \wedge s . \operatorname{Req}=R E P$ and $p$ hooks on to $s$ in step $\gamma_{j-1} \mapsto \gamma_{j}$, this implies that $s$ is in a normal tree in $\gamma_{j}$ according to Proposition 2. Thus, we have $s . S=C \wedge s . L<q . L$ in $\gamma_{k}$ and $q \notin \operatorname{MinChPar}(p) \Rightarrow \neg \operatorname{Connect}(p)$, a contradiction.

Lemma 25. In an execution, every processor $p \in V \backslash\{r\}$ produces at most $2 \Delta$ topological changes in forest $\mathcal{F}$ while $p \notin \operatorname{Tree}(r)$, with $\Delta$ the maximum degree of a processor in the network.

Proof. To hook on to a tree, a processor $p \in V$ must execute $C$-action. According to Lemma 24, $p$ cannot hooks on to an abnormal tree $T \in \mathcal{F}$ twice by the same neighbor $q$ of $p$. Since a processor can have at most $\Delta$ neighbors, $p$ can hook on at most $\Delta$ times to an abnormal tree. Observe that $E$-action has a higher priority than $C$-action and $E$-action can be executed between two executions of $C$-action, i.e., at most $\Delta$ times while $p \notin \operatorname{Tree}(r)$. Therefore, by Definition 10 the lemma follows.

Lemma 26. In an execution, every processor $p \in V \backslash\{r\}$ produces at most $n$ topological changes in forest $\mathcal{F}$ while $p \in \operatorname{Tree}(r)$, with $n$ the number of processors in the network.

Proof. Observe that for every processor $p \in \operatorname{Tree}(r)$ we have $p . S=C$. Moreover, by Remark 3 for every processor $p \in \operatorname{Tree}(r)$ we have that $E$-action is disabled. So, by Definition 10 the only topological change in $\mathcal{F}$ that a processor $p \in \operatorname{Tree}(r)$ can produce is to execute $C$-action in order to reduce its level in $\operatorname{Tree}(r)$. Thus, by Proposition 1 each execution of $C$-action by a processor $p \in \operatorname{Tree}(r)$ in step $\gamma_{i} \mapsto \gamma_{i+1}$ implies that $p$ hooks on to the neighbor with the lowest level in $\gamma_{i+1}$ and $p . L$ in $\gamma_{i}$ is higher than $p . L$ in $\gamma_{i+1}$. Therefore, since the size of Tree(r) is bounded by $n$ then any processor $p$ can hook on to at most $n-1$ processors by executing $C$-action while $p \in \operatorname{Tree}(r)$.

Lemma 27. In an execution, every processor $p \in V \backslash\{r\}$ produces at most $2 \Delta+n$ topological changes in forest $\mathcal{F}$.

Proof. This comes from Lemmas 25 and 26.
Corollary 9. From any configuration, Algorithm 1 produces at most $2 \Delta n+n^{2}$ topological changes in forest $\mathcal{F}$.

Lemma 28. In an execution, each topological change in forest $\mathcal{F}$ generates at most $\Delta$ requests.
Proof. Let any processor $p \in V$ which produces a topological change in forest $\mathcal{F}$. By Definition 10 , we must consider two cases: $p . S=E$ (in this case $p \neq r)$ or $p . S=C \wedge(\exists q \in$ $N e i g_{p}, q . L-p . L>1$ ). If $p . S=E$ then we can have $p . S=E \Rightarrow G P-R E P(q)$ at a neighbor $q$ of $p$, so since a processor can have at most $\Delta$ neighbors this can generate at most $\Delta$ requests. Otherwise we have $p . S=C \wedge\left(\exists q \in N e i g_{p}, q . L-p . L>1\right)$ at $p$, then $p$ sends a request in order to allow each neighbor $q$ such that $q . L-p . L>1$ to hook on to $p$. Therefore, at most $\Delta$ requests are generated by a topological change at $p$.

Lemma 29. From any configuration, Algorithm 1 produces at most $2 \Delta m+m n$ requests to reach a configuration satisfying Definition 3.

Proof. This comes from Corollary 9 and Lemma 28.
Corollary 10. In an execution, $A$-action and $O$-action are executed at most $2 \Delta m+m n$ times in the network.

Lemma 30. From any configuration, at most $O\left(\Delta m n^{3}+m n^{4}\right)$ steps are needed by Algorithm $\mathcal{B F S}$ to reach a configuration satisfying Definition 3.

Proof. By Corollary 9, from any configuration Algorithm 1 generates at most $2 \Delta n+n^{2}$ topological changes to reach a configuration satisfying Definition 3. Thus, by Definition 10 this implies that $E$-action and $C$-action are executed at most $\Delta n+n^{2}$ times. Moreover, by Corollary 10 from any configuration $A$-action and $O$-action are executed at most $2 \Delta m+m n$ times to send a local request. According to Corollary 4 , an acknowledgement to a request is received in at most $O\left(n^{3}\right)$ steps. Therefore, from any configuration in at most $O\left(\Delta m n^{3}+m n^{4}\right)$ steps a legitimate configuration is reached.

## 10. Conclusion

In this paper we define a particular class of distributed algorithms, called fully polynomial algorithms, having good properties suitable for large scale systems. These algorithms are characterized by a round complexity polynomial on the network diameter and a step complexity polynomial on the network size. Moreover, we show that this class of distributed algorithms is not empty for the global distributed task of spanning tree construction. To this end, we proposed the first fully polynomial stabilizing algorithm constructing a Breadth First Search tree in $\Theta\left(d^{2}\right)$ rounds and in $O\left(n^{6}\right)$ steps for any topology network, with $d$ the diameter and $n$ the number of nodes in the network. Moreover, a distributed daemon without any fairness assumptions is considered.

Several open questions follow from this work:

- There is a need to classify other global distributed tasks such as leader election, Propagation of Information with Feedback, routing table construction ..., to determine if there exist also fully polynomial algorithms for these problems.
- We show an upper bound on the step complexity of our algorithm. However, is it possible to improve the step complexity of the algorithm while preserving a round complexity of $O\left(d^{2}\right)$ ?
- In the same way, is it possible to design a fully polynomial stabilizing algorithm for the spanning tree construction problem with an optimal round complexity of $\Theta(d)$ ?

The positive result proposed in this paper for the spanning tree construction problem allows to consider the design of stabilizing algorithms for large scale systems. Therefore, it is crucial to continue the investigation of totally effective fault-tolerant distributed algorithms (with optimal round and step complexities) for global tasks.

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[^0]:    ${ }^{\text {AT }}$ A preliminary version of this paper has been published in OPODIS [2].
    Whe This work has been partially supported by the ANR project SPADES (08-ANR-SEGI-025).
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    Preprint submitted to Elsevier

[^1]:    ${ }^{1}$ Except the abstract version of this paper [2].

[^2]:    ${ }^{2} \mathrm{~A}$ configuration from which every execution suffix satisfies the specification of the problem to solve.

[^3]:    ${ }^{5}$ Indeed, this approach is used in [17] to construct a BFS tree with a round complexity in $\Theta(d)$ but with a step complexity in $\Omega\left(\operatorname{Max} \times n^{2}\right)$, as demonstrated in [18]. However, Max is an upper bound of $n$ and can be arbitrary high with respect to $n$ so the step complexity can be at least exponential. Note that the gap between the lower and the upper bound (see Table 1) of the step complexity leads us to think that the lower bound in [18] is not tight.

