



## Assessment of device RF performance and behavior using TCAD simulation

Soumya Ranjan Panda, Sébastien Fregonese, Marina Deng, Anjan Chakravorty, Thomas Zimmer

### ► To cite this version:

Soumya Ranjan Panda, Sébastien Fregonese, Marina Deng, Anjan Chakravorty, Thomas Zimmer. Assessment of device RF performance and behavior using TCAD simulation. IEEE BEE BRANCH, Dec 2019, Bordeaux - Talence, France. hal-02404058

HAL Id: hal-02404058

<https://hal.science/hal-02404058>

Submitted on 11 Dec 2019

**HAL** is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

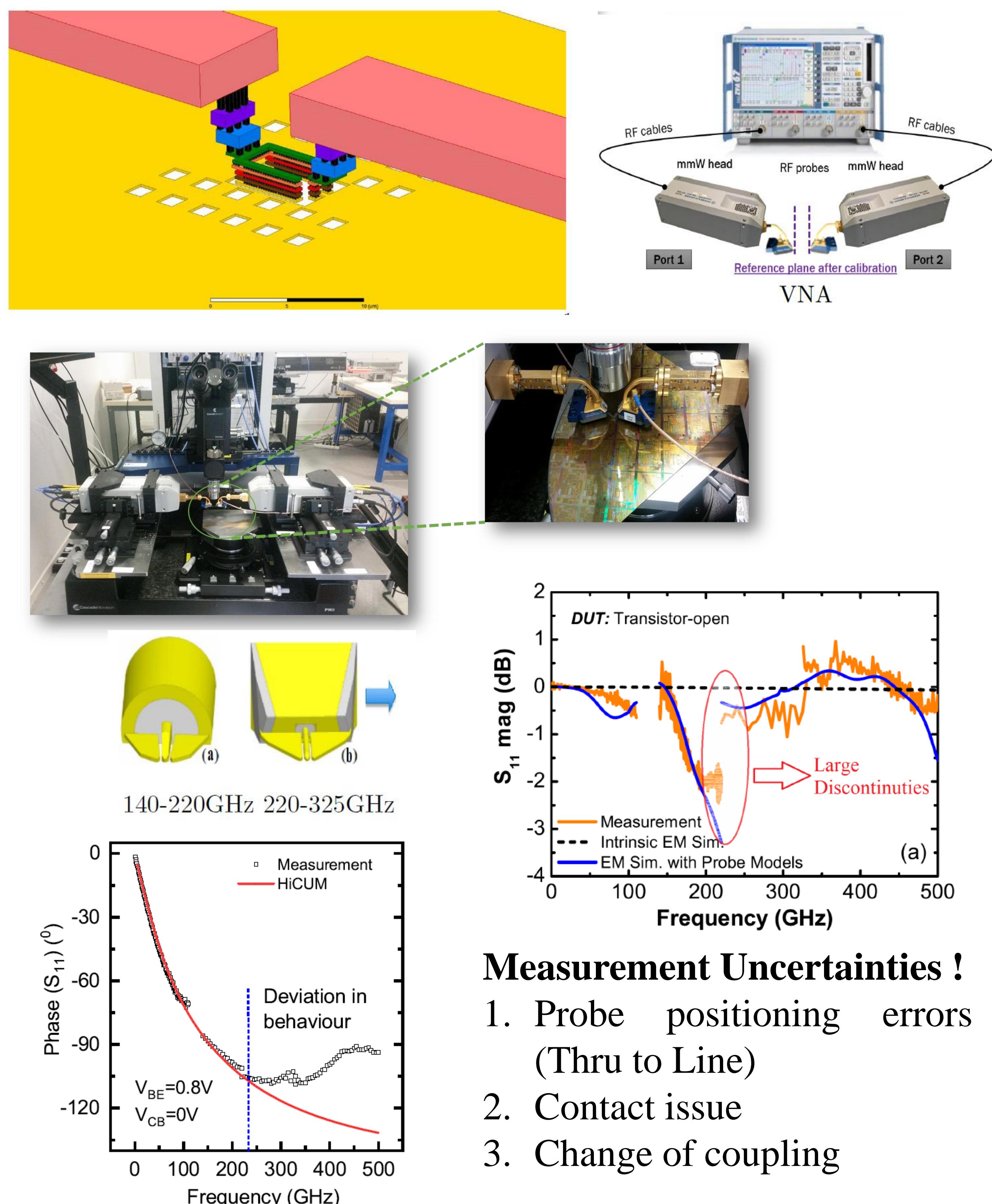
# Assessment of device RF performance and behavior using TCAD simulation

Soumya Ranjan Panda<sup>1,2</sup>, Sébastien Fregonese<sup>1</sup>, Marina Deng<sup>1</sup>, Anjan Chakravorty<sup>2</sup>, Thomas Zimmer<sup>1</sup>

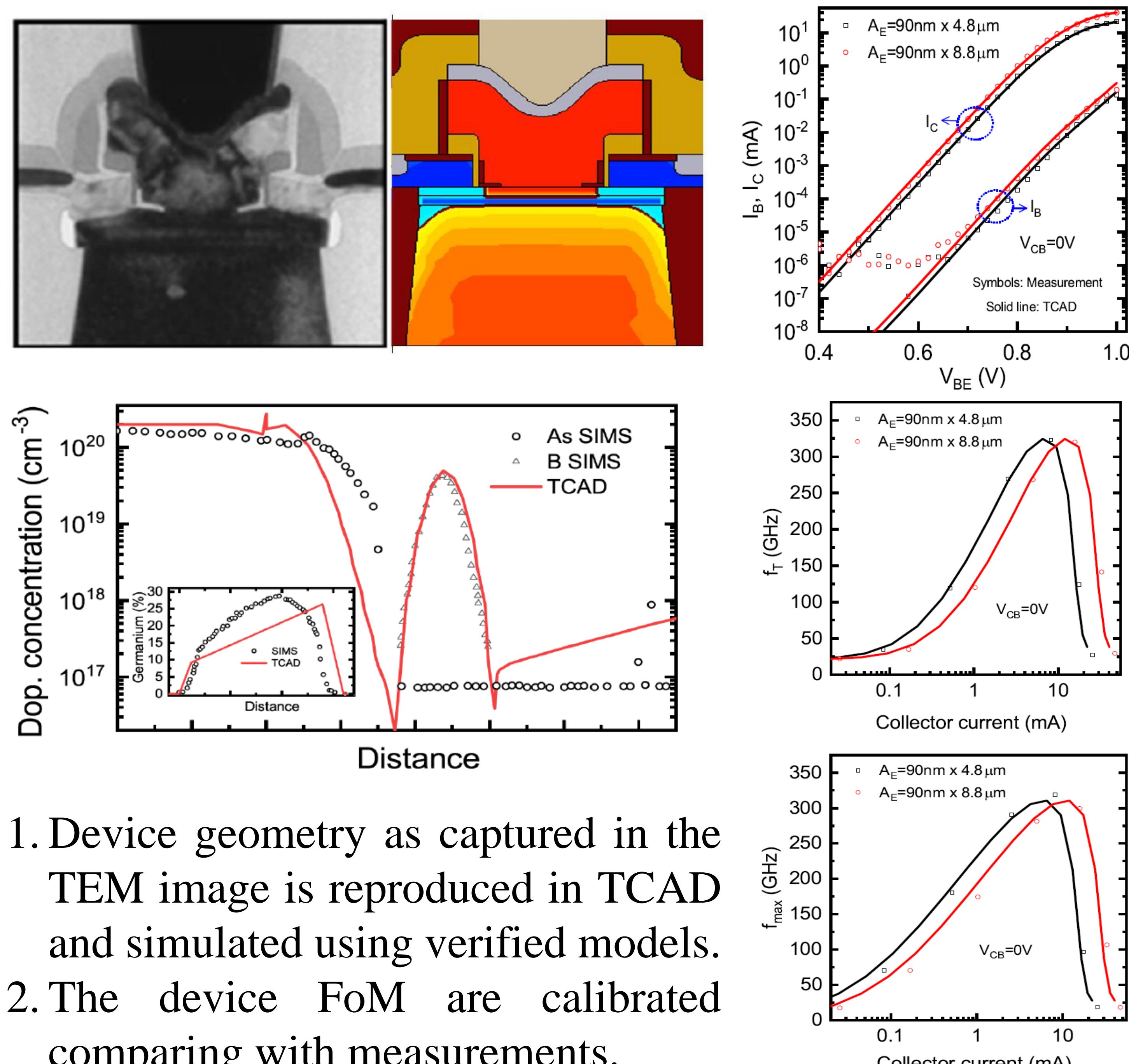
<sup>1</sup>IMS Laboratory, University of Bordeaux, France <sup>2</sup>IIT Madras, Chennai, India

## Introduction

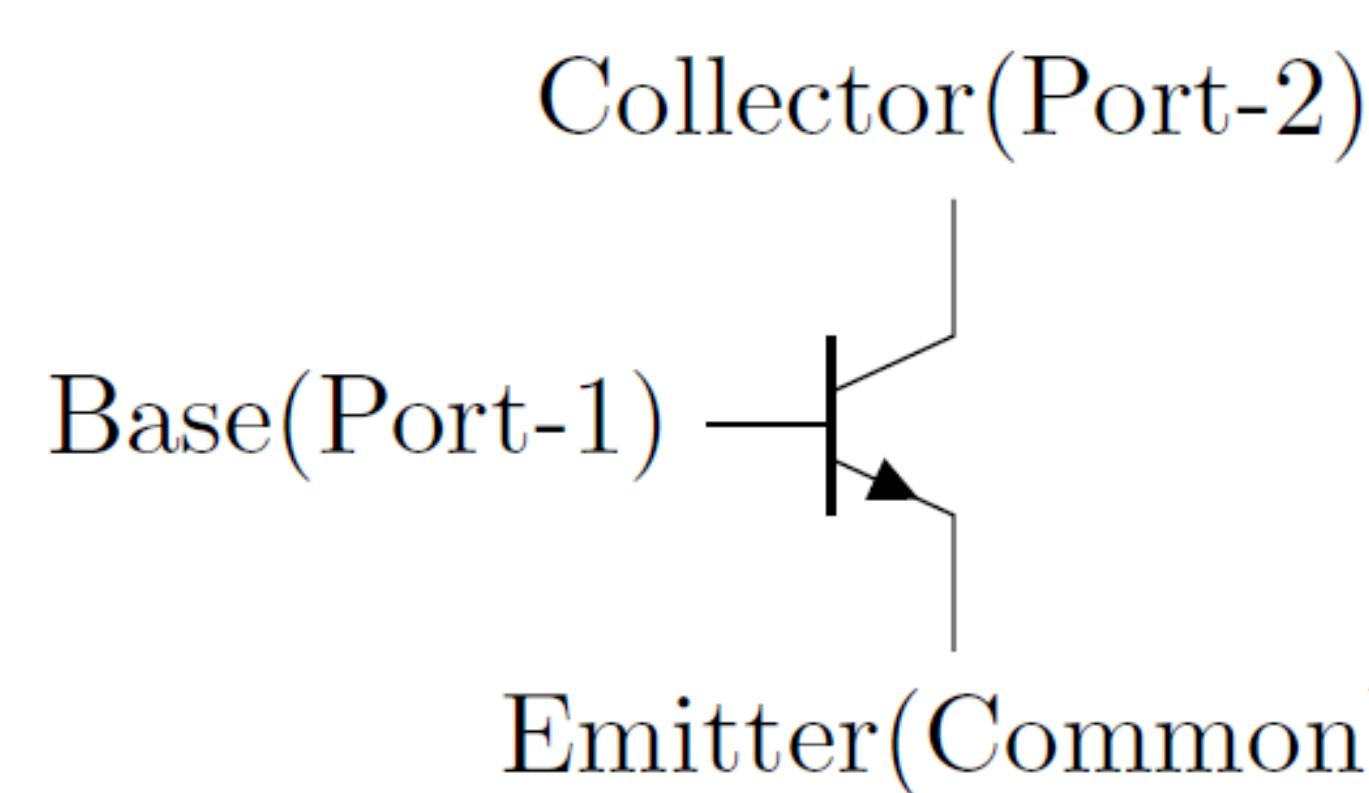
Characterization above 110 GHz is still a challenge. So TCAD simulation has been done to estimate the measurements.



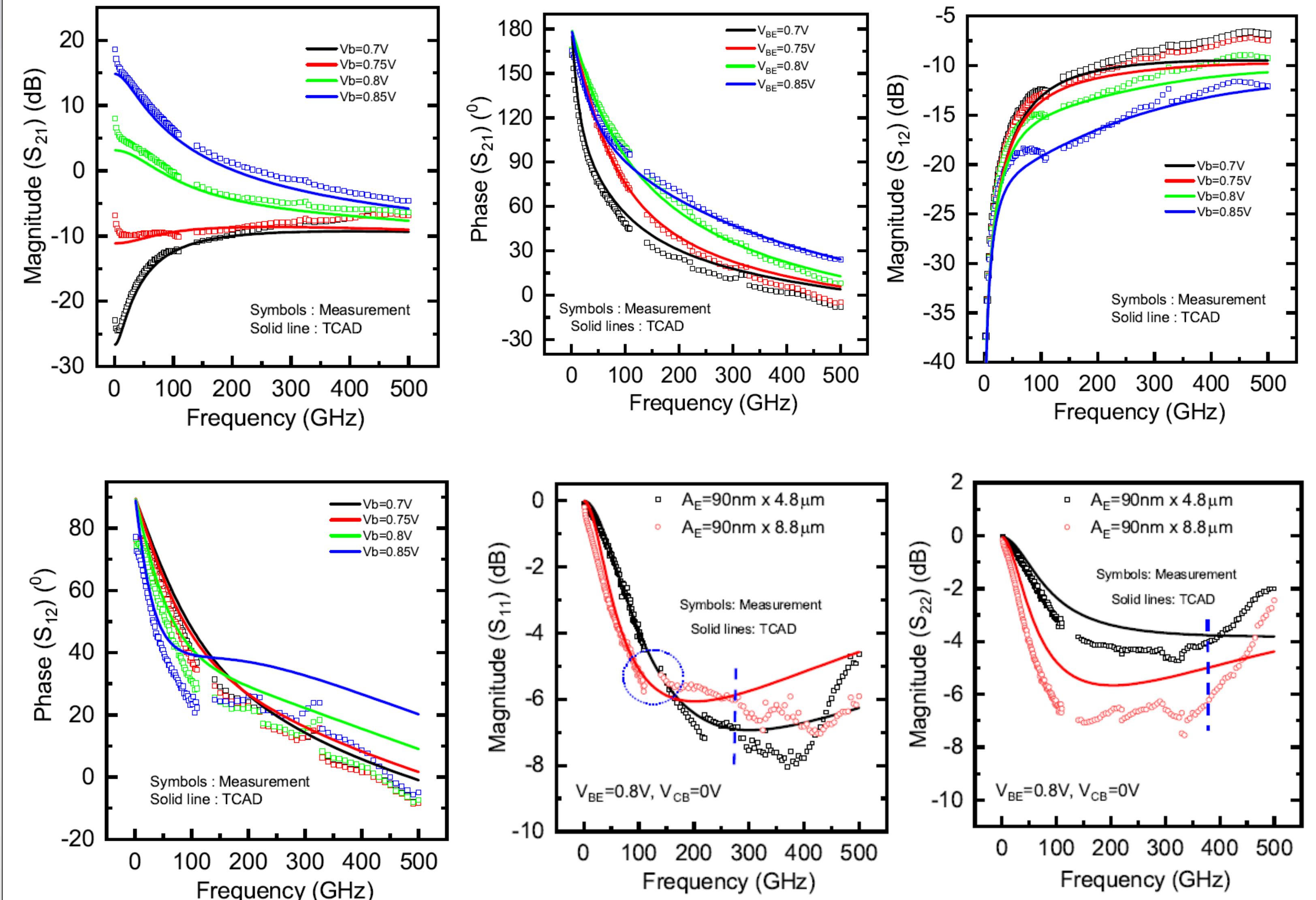
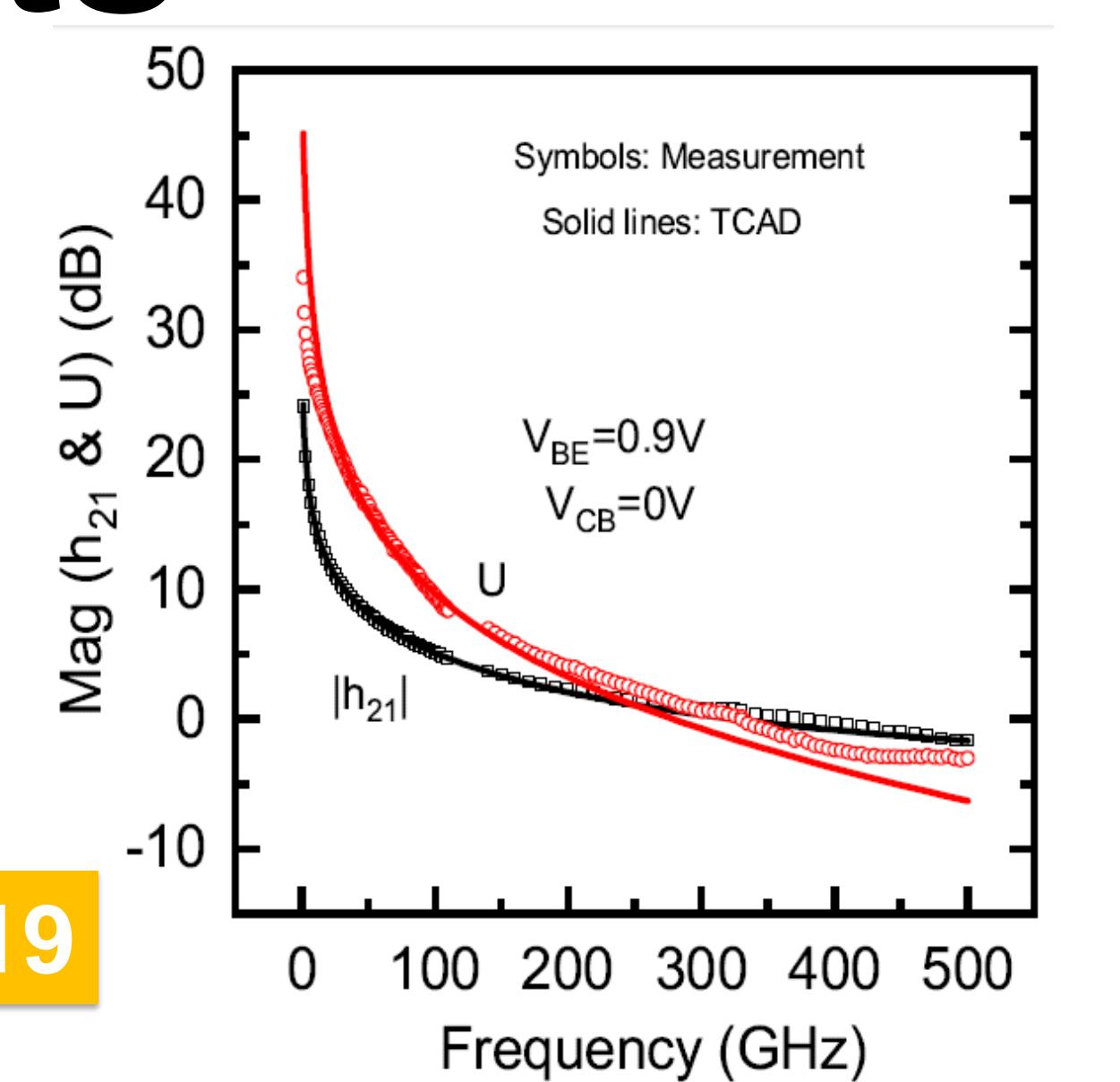
## TCAD Calibration



## Results



Published in BCITS 2019



The comparisons are done w.r.t bias variation and device dimension variation.

## Conclusions

1. No clear upper frequency limit up to which the measurements are representing the intrinsic transistor (after de-embedding).
2. An urgent need for improvement of test structure design, calibration methodologies and de-embedding procedures when entering the THz domain.

## Acknowledgement

The authors would like to thank ST Microelectronics, French Nouvelle Aquitaine and EU for the wafers and funding respectively.

## Bibliography

1. VU Van-Tuan et al. "TCAD calibration of high-speed Si/SiGe HBTs in 55-nm BiCMOS", ECS Transactions, 2016.
2. Peter Ashburn, SiGe heterojunction bipolar transistors, John Wiley & Sons, 2004.
3. S. Fregonese et al., On-wafer characterization of silicon transistors up-to 500 GHz, IEEE Transactions on MTT, 2018.

Contact : [soumya-ranjan.panda@u-bordeaux.fr](mailto:soumya-ranjan.panda@u-bordeaux.fr)