

All 2-Input Logic Gates and a Half-Adder in a Single Reconfigurable Plasmonic Cavity

Upkar Kumar¹, Aurélien Cuche¹, Christian Girard¹, Sviatlana Viarbitskaya², Gérard Colas des Francs², Sreenath Bolisetty³, Raffaele Mezzenga³, Alexandre Bouhelier², and Erik Dujardin^{1*}

¹CEMES CNRS UPR 8011 and University of Toulouse, 29 rue J. Marvig, 31055 Toulouse, France.

²Laboratoire Interdisciplinaire Carnot de Bourgogne, CNRS UMR 6303, Université de Bourgogne Franche-Comté, 9 Av. A. Savary, 21000 Dijon, France.

³ETH Zurich, Department of Health Sciences and Technology, Schmelzberg-strasse 9, CH-8092 Zurich, Switzerland.

*Correspondence to: erik.dujardin@cemes.fr.

Abstract:

Main text

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Supplementary Materials:

[Materials and Methods](#)

[Figures S1-S6](#)

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Author Contributions. ED, AB designed the experiments. RM, SB synthesized the Au microplatelets. UK, ED conducted the sample preparation and nanofabrication steps. AB, SV implemented the experimental optical set-up. UK, SV performed the optical experiments. ED, UK processed the data. CG, AC, UK, GCdF developed the simulations tools and performed all GDM calculations. ED developed the gate mapping and drafted the manuscript, which was amended by all co-authors. All co-authors have given approval to the final version of the manuscript.

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