

DEPARTMENT OF COMPUTER & ELECTRICAL ENGINEERING

Technology Trends and Electromagnetic Compatibility of Integrated Circuits



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> www.etienne-sicard.fr www.ic-emc.org





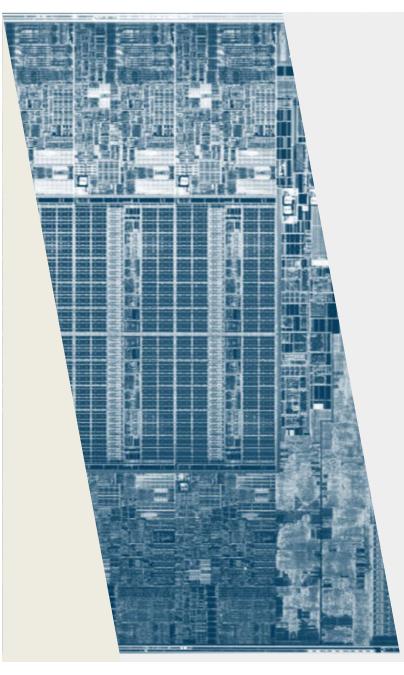




- I. A BRIEF HISTORY OF IC EMC
- II. GENERAL TRENDS
- III. TECHNOLOGY TRENDS
- IV. EMC CHALLENGES
- V. GOOD NEWS FOR EMC
- VI. CONCLUSION







A BRIEF HISTORY OF IC-EMC



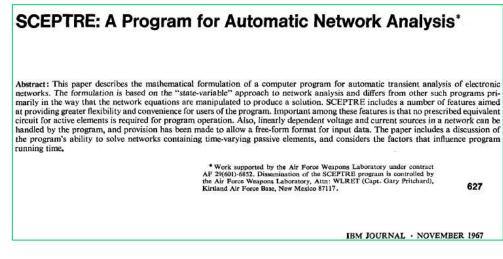


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- Air Force Weapon Laboratory, USA
- SPECTRE : Simulation software developed at IBM (Sedore, 1965)
- Effects of the electromagnetic fields trigged by nuclear explosions on electronic devices
- Correlate simulations and experimental measurements obtained on an electromagnetic impulse test-bench.
- Ancestor of SPICE











- IEC TC 47 was proposed in 1959 to cope with the needs of industry in semiconductor devices.
- Role : preparing international standards for the design, manufacture, use, reuse, and testing of discrete semiconductor devices, integrated circuits, sensors, electronic component assemblies, interface requirements, and micro-electromechanical devices, using environmentally sound practices.



Commission Electrotechnique Internationale International Electrotechnical Commission Международная Электротехническая Комиссия





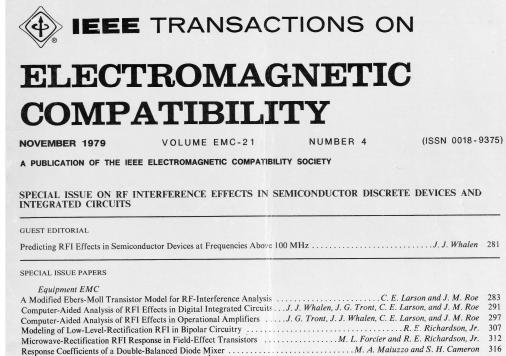


IEEE TRANS ON EMC, 1979

SPECIAL ISSUE ON RF

INTERFERENCE IN ICS

- Guest editor: James J. Whalen
- Rising risk of interference between
 ICs and electromagnetic sources
- Very High Frequency (30 MHz -300 MHz)
- Visionary editorial about future problems due to higher speed and integration



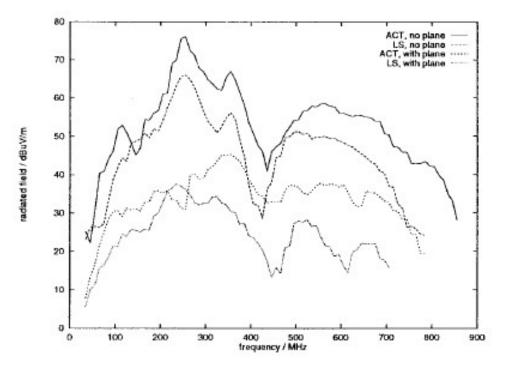




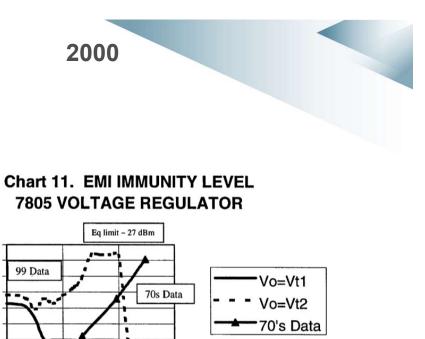


IEEE TRANS ON EMC, 1998 TECHNOLOGY COMPARISONS (M.P. ROBINSON)

- Robinson (Univ. York) compared the radiated emissions produced by different families of logic circuits.
- Significant behavioral differences
 were observed
- Upto 1GHz







INTEGRATED CIRCUIT ELECTROMAGNETIC IMMUNITY HANDBOOK, BY NASA, USA

Comparison between IC families •

(ALS, HC, Regulators)

Upto 10 GHz

NASA/CR-2000-210017



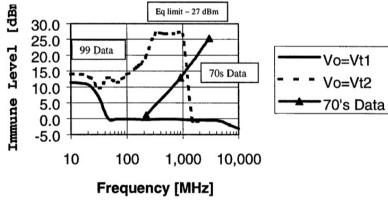
Integrated Circuit Electromagnetic Immunity Handbook

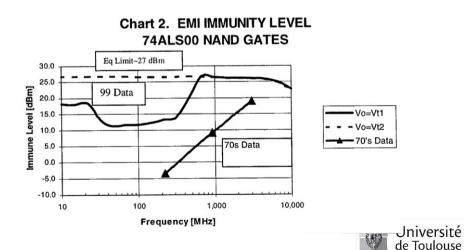
J.G. Sketoe Boeing Information, Space and Defense Systems, Seattle, WA



Prepared for Marshall Space Flight Center under Contract NAS8-98217 and sponsored by The Space Environments and Effects Program

7805 VOLTAGE REGULATOR







FIRST INTERNATIONAL WORKSHOP **DEDICATED TO EMC OF ICS (2002)**

- **Organized by INSA Toulouse France**
- 70 attendees •
- 30 papers •
- Invited guests: Bob ROSS, Mentor • Graphics & IBIS, Ross CARLTON, Motorola & IEC







2002

The progresses in technology, the continuous increase in dock frequencies and circuit complexity lead to complete system-on-chip solutions. However, the level of parasitic emission increases with the technology scale down.

Furthermore, the supply reduction and ever smaller noise margins tend to increase the susceptibility of the IC to external electromagnetic appression.

The purpose of the workshop EMC Compo 2002 is to address the emission and susceptibility issues for integrated circuits, either logic, analog or mixed logic/analog. The most recent advances in measurement methods, models, standards, tools and design methodologies will be discussed during the workshop.

A technical exhibition will include EMC products and services related to integrated dircuits.

Paper submission

Complete papers should be submitted in a A4, 4 pages format. induding title, abstract, illustrations and references. Electronic submission is encouraged



The workshop is specifically focused on EMC for integrated circuits, with the following subtopics:

- Measurement methods for emission of integrated drauits
- · Measurement methods for susceptibility of integrated circuits
- Models & standards for predicting emission & susceptibility of integrated circuits.
- Use of IC models in Printed circuit board simulation
- · Education and e-learning related to EMC for Ics.
- Design methodologies for improved EMC behavior of Ics
- Tools to handle EMC at IC level
- · Prospective aspects of EMC in future generation ICs

Dead-line Final paper submission

Sept 15th, 2002 Notification of acceptance Oct 15th, 2002



Flavio Canavero, Politecnico Torino Christian Carel, See Thales Mart Coepen Phillips Lionel Courau, ST Microelectronics Bernard Demoulin, University of Lille Jean-Marc Dienot, IUT Tarbes Franco Fiori, Politecnico Torino Hervé Grauby, Gerac Todd H Hubing, University Missouri Rolla Claude Huet, EADS Airbus Werner John, Mesale Elva B. Joffe, IEEE EMC Joachim Kruecken, Motorola Gmbh Jean-Luc Levant, Atriel Nantes Marc Lubineau, Alcate/ Scace Christian Marot, Slamens VDO Jean-Claude Perrin, Texas Instruments Mohamed Ramdani, ESEO Miguel Roca, University of Balearic Islands Anton Sauer, Lina Fraunhofer Dresden Etienne Sigard, INSA Amaury Soubeyran, EADS CCR Thomas Steinecke, Infreen

Committee Sonia Bendhia, INSA

Francois Vernieres, lerset Boris Traa, Phillos

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té

se



2002



Commission Electrotechnique Internationale International Electrotechnical Commission Международжая Электротехническая Комиссия

RELEASE OF IC-EMC MEASUREMENT STANDARDS

- IEC 61967, Integrated circuits Measurement of electromagnetic emissions 150 kHz to 1 GHz, 2002
- Generic IC EMC Test Specification,
 Version 1.0, Bosch, Infineon, Siemens
 VDO, 2004
- IEC 62132, Integrated circuits
 Measurement of electromagnetic
 immunity 150 kHz to 1 GHz, 2005

Limit class	150 Ω method		1Ω method		TEM
	glob al	local	globa l	local	cell method
Ι	8-H	6-F	10-K	8-H	Ι
П	10-K	8-H	12-M	10-K	L
III	12-M	10-K	14-0	12-M	Ν
С	Customer-specific				

TABLE II IMMUNITY LIMIT CLASSES

Immunity limit	DP forward po	TEM [E-field] V/m		
classes	global pin	local pin	entire IC	
I	18	0	200	
П	24	6	400	
ш	30	12	800	
С	Customer-specific			

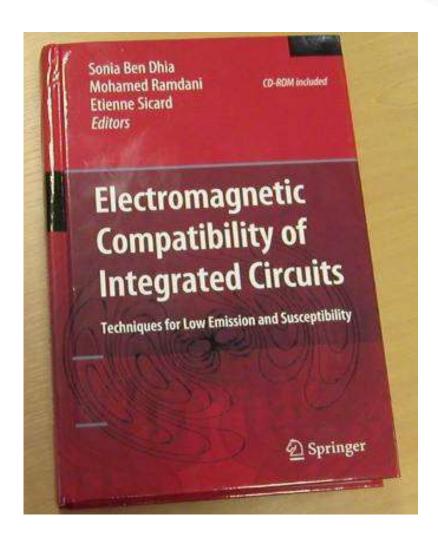






EMC OF ICS", SPRINGER, USA, 2006

- Based on EMC Compo 2004
 contributors
- Ben Dhia, Ramdani and Sicard compile the first book only dealing with EMC at integrated circuit level.
- The book includes a description of measurement, model standards, techniques for low emission and susceptibility











RELEASE OF IC-EMC MODELLING STANDARDS

 IEC 62433 : EMC IC modelling Models of integrated circuits for RF immunity behavioural simulation

> General modelling framework : 2011 Conducted immunity modelling (ICIM-CI) : 2016

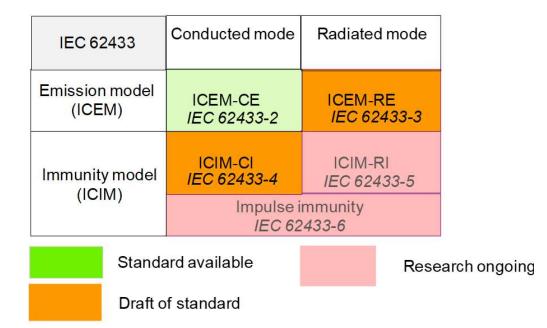
Conducted emissions modelling (ICEM-

CE): 2017

Radiated emissions modelling (ICEM-RE) :

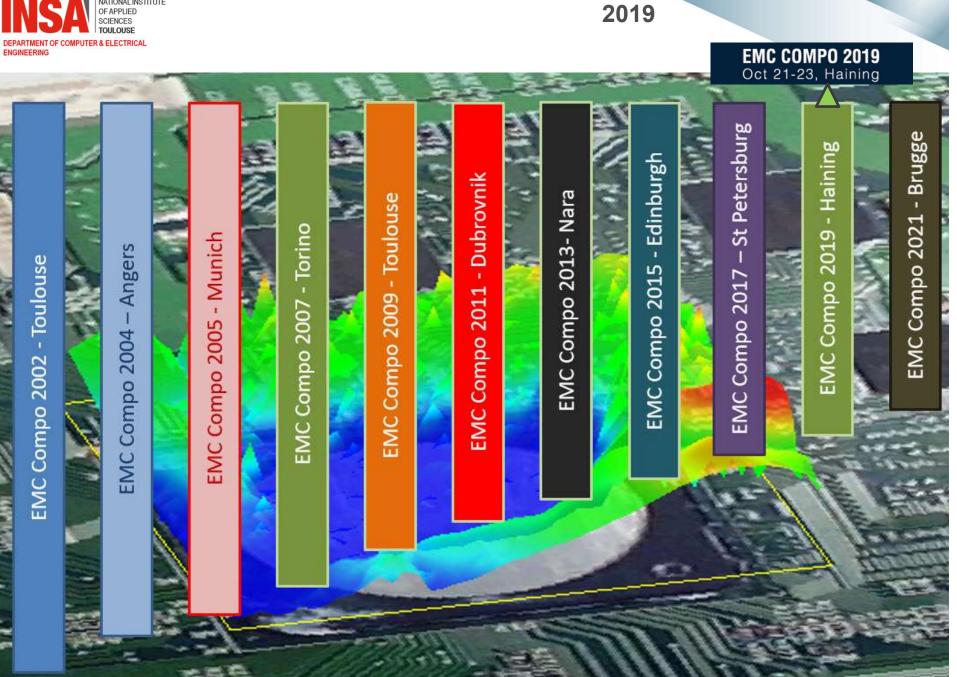
2017

Universal data exchange format in XML









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GENERAL TRENDS

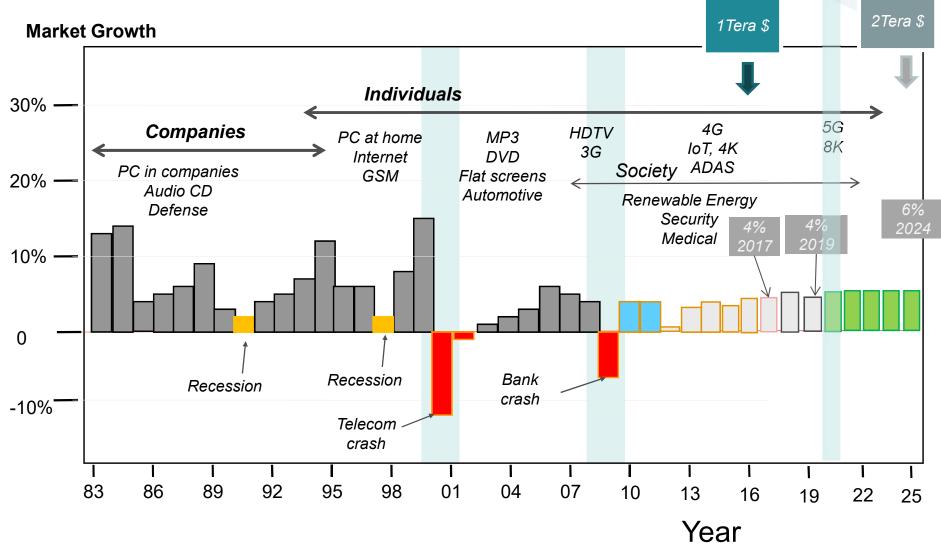
















THE ELECTRONIC MARKET GROWTH

Share of system sales

2024 vs 2019

- Vision 2024
 - Increasing disposable income,
 - expanding urban population,
 - growing internet penetration and
 - availability of strong distribution network

Smartphones PC TV Automotive Internet of Things **Tablets** Game Medical consoles Servers Growth -10% 0 10% 20%





MOBILE SUBSCRIBERS

https://gsmaintelligence.com/

Oct. 2019

GLOBAL DATA

Mobile connections, including licensed cellular loT Sep 2019

9,319,577,335 5,146,873,987 \$1.04T \$8.45 · 5.5%



Unique mobile subscribers

Sep 2019

Revenue/year ARPU/month FY 2018 FY 2018 - 1.9% -0.8%



.. Oct 2014 (-5 years)

7,180,500,225 3,645,727,439 \$1.13T \$12.15 - - 3.97%

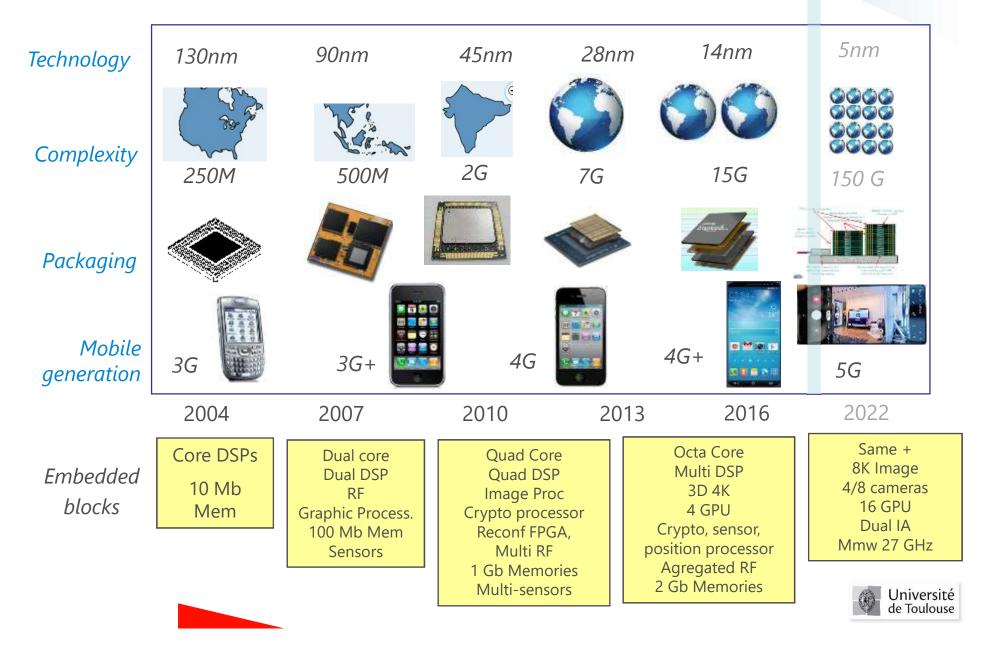
Source: GSMA Intelligence 2019, current year-end data except interpolated subscribers and connections







TOWARDS 5G EMC COMPO 2019 Oct 21-23, Haining



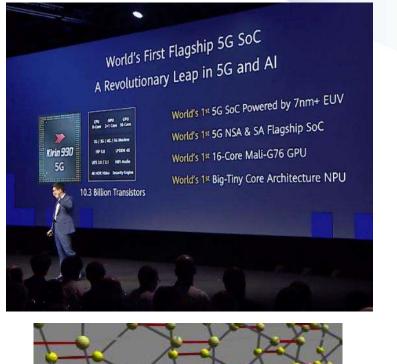


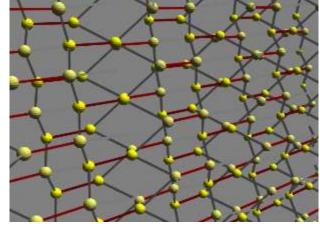
- Kirin 990 HiSilicon for Huawei
- Octa Processor + Dual IA + 16 GPU + Modem 5G
- 7nm+ process
- Non-Standalone network (now)
- Also support Stand-Alone network
- 10 Billion devices





TOWARDS 5G

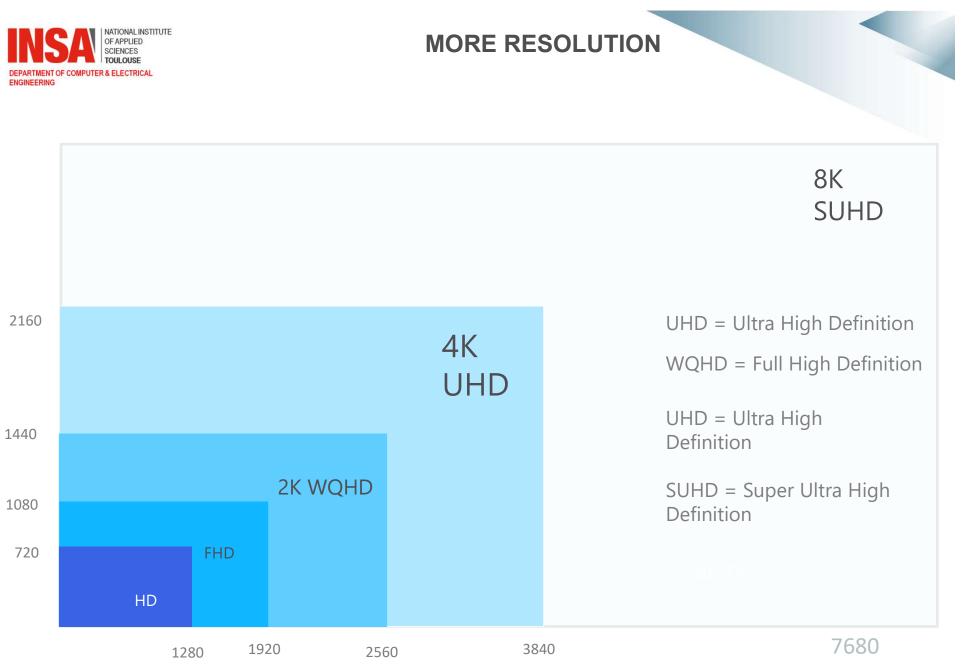




Silicon lattice: 0.25 nm "7nm"=30 atoms











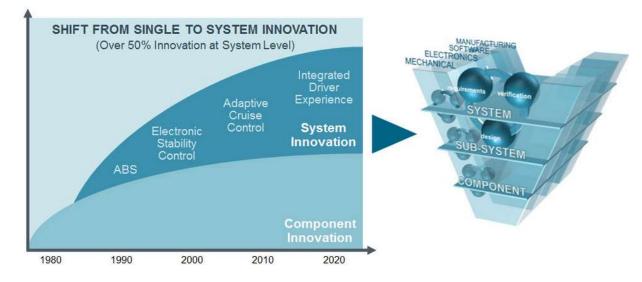


Automatic Cars

- require highly intensive and reliable computing
- Trend towards hybrid/electrical car for low carbon emission
- Multi-sensor approach
- A lot of EMC concerns
 pending

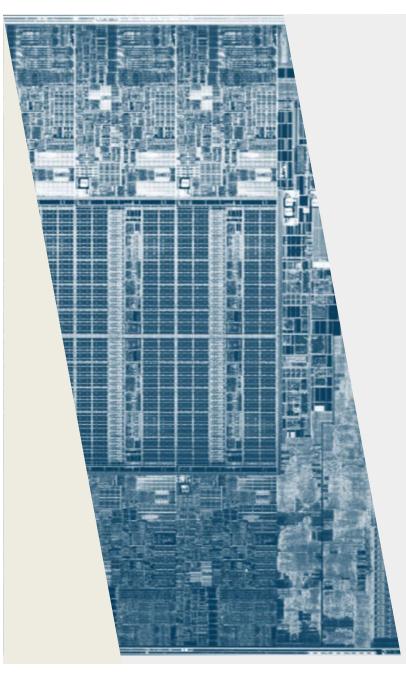
AUTOMATIC DRIVE









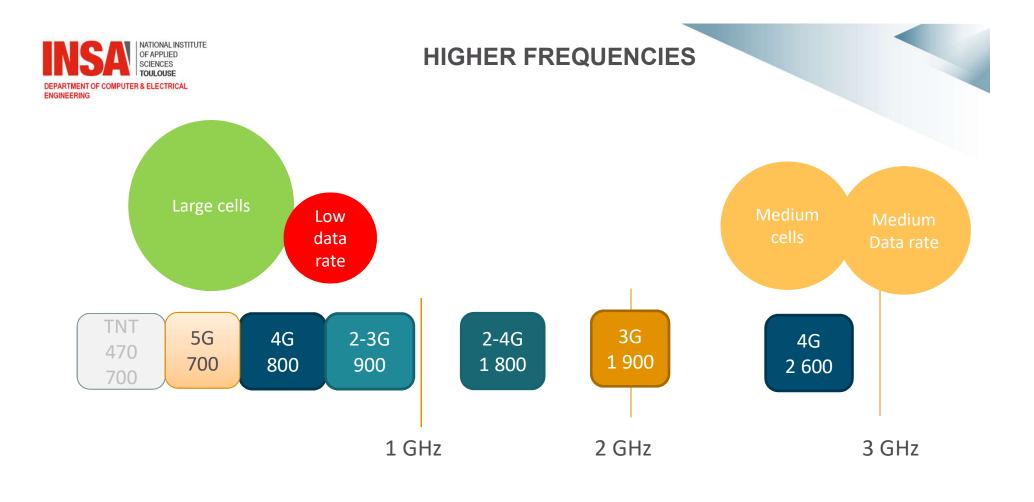


TECHNOLOGY TRENDS

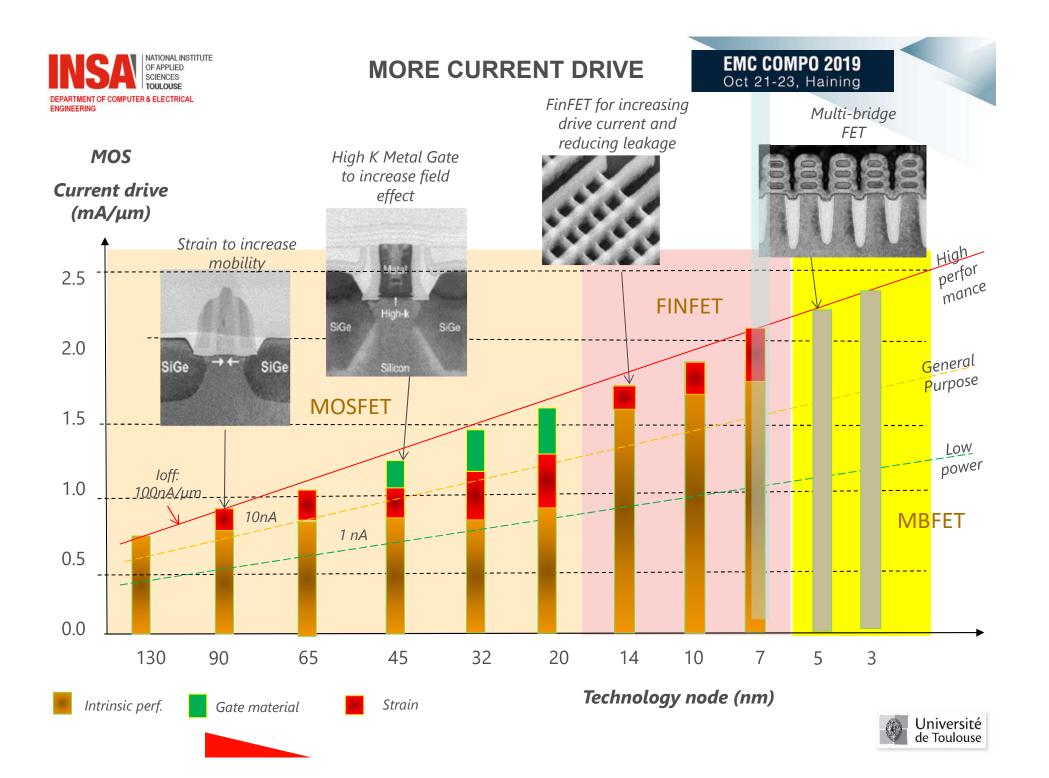


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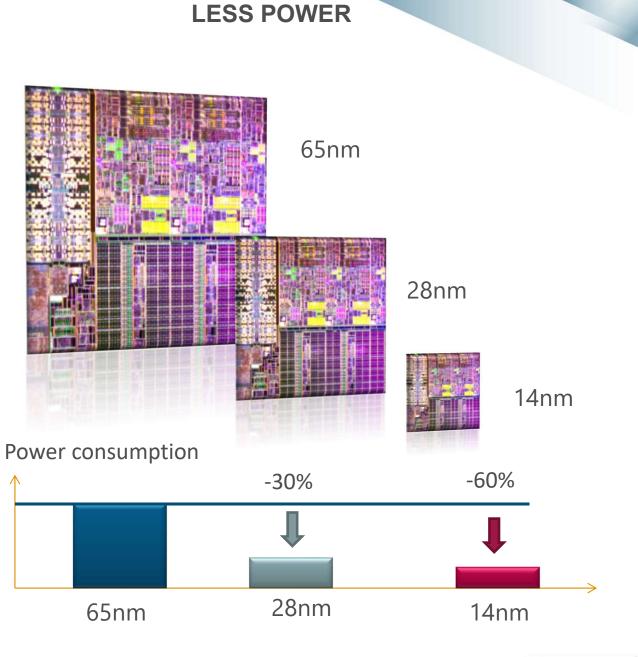






Scale down benefits

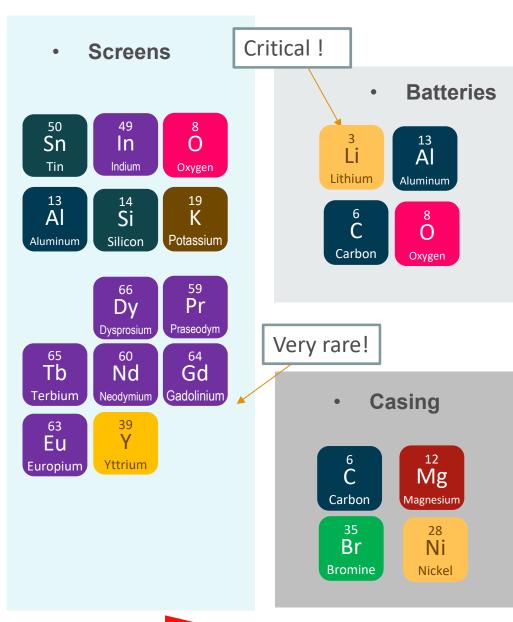
- Less power
 consumption
- More space for integration
- Maybe faster
- Maybe cheaper

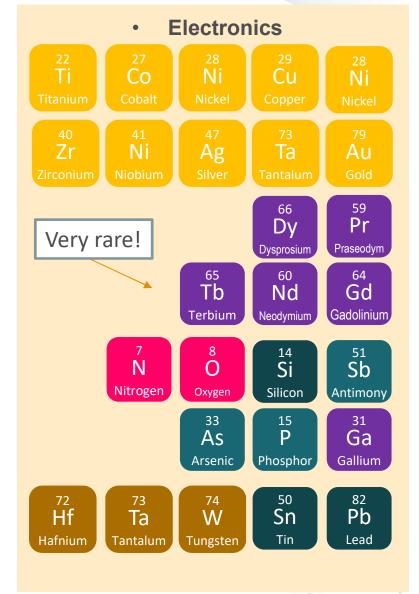






MORE MATERIALS







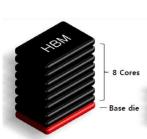


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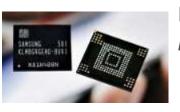
Increased memory/ processor bandwidth High performance computing & Al Mobiles & high resolution games Servers & Security

MORE MEMORY BANDWIDTH

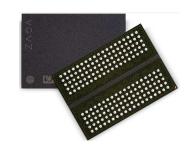


High Performance Computing & Networking *High Bandwidth Memory*





Mobile Low Power DDR5



Graphics Graphics DDR6



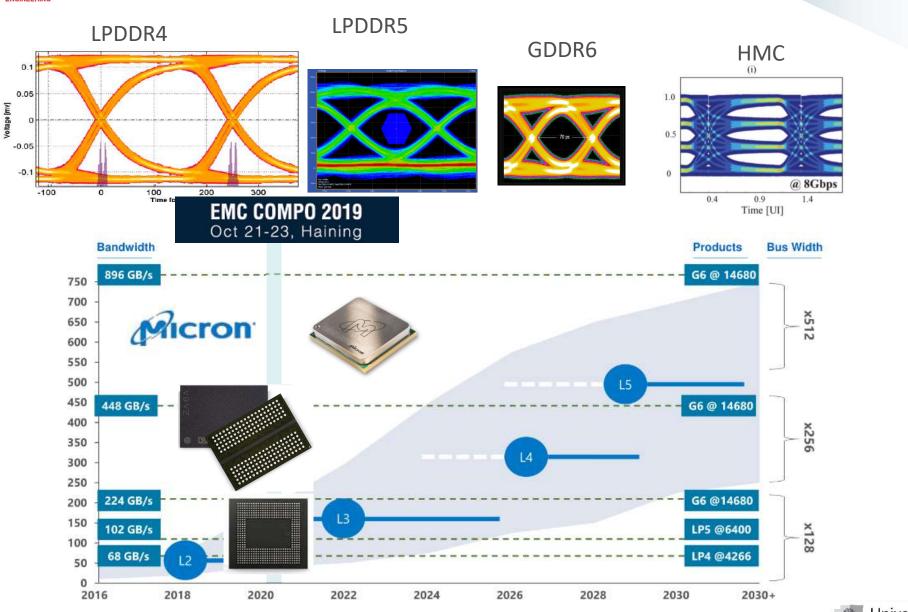








MORE MEMORY BANDWIDTH

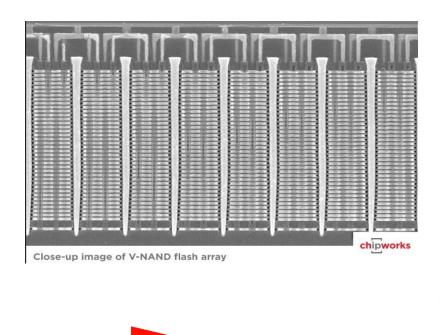


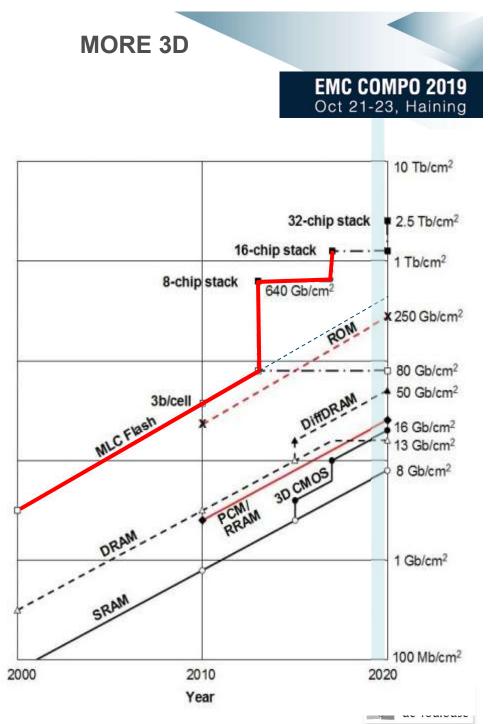
Université de Toulouse

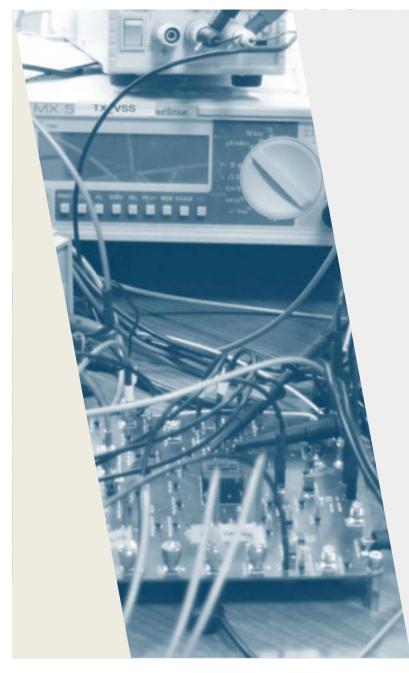


Technology

- 2.5D high bandwidth and high density DRAM with TSV and Si Interposer
- 1 tera-bit/cm2 achieved 5 years ahead from roadmaps







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EMC CHALLENGES

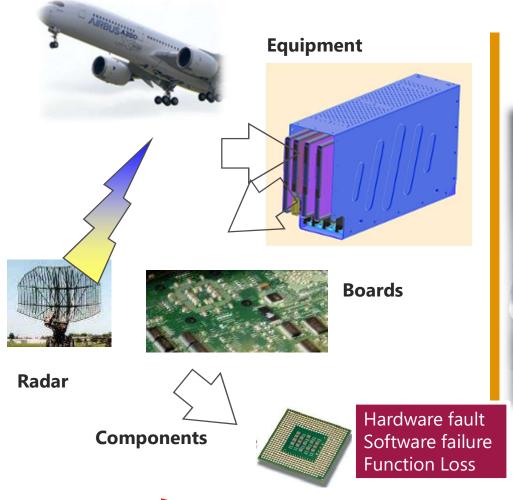






EMC – TWO KEYWORDS • EMISSION Personal







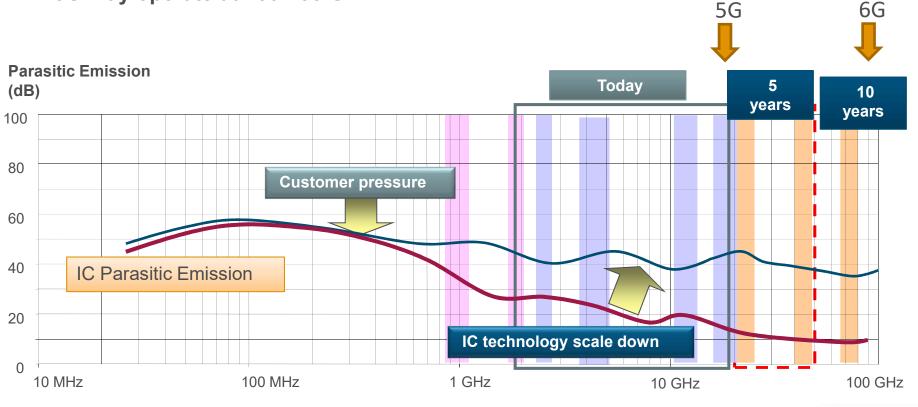




CHALLENGES IN EMC OF ICS

Increased frequencies

- EMC at IC level usually characterized upto 6 GHz
- Millimeter-wave 5G operates at 27 GHz
- 6G may operate at 100-200 GHz

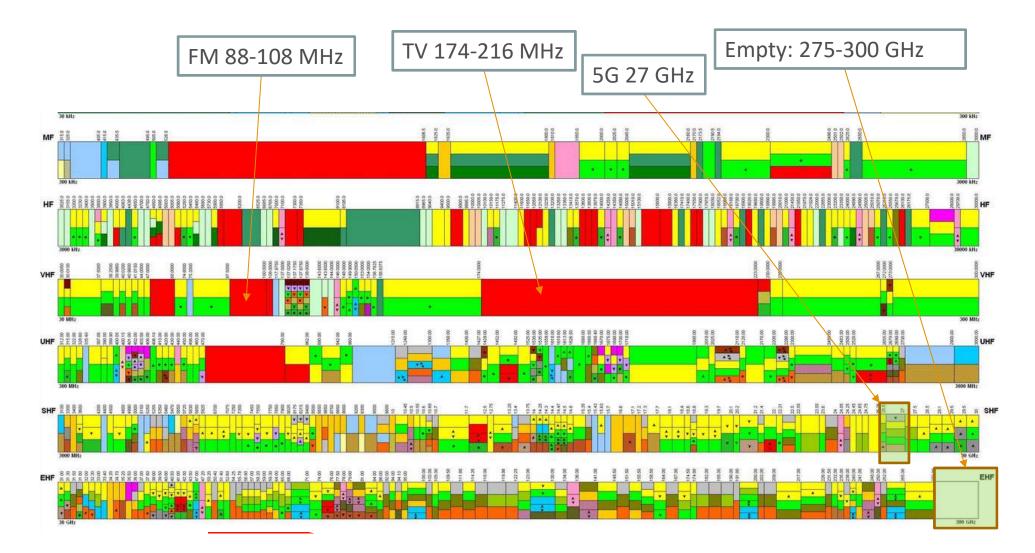






THE FREQUENCY SPECTRUM IS FULL

Giant number of communication systems upto 275 GHz

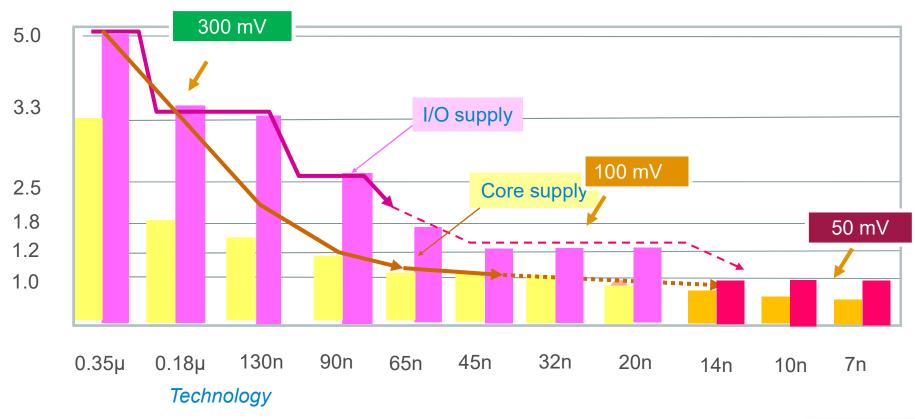




Supply (V)

LOWER NOISE MARGINS

- Reduced supply voltage in nano-cmos lcs
- Reduced voltage swing



34

Susceptibility Issues





PRESSURE OF LOWER COSTS

Costs

- Pressure for cost reduction
- EMC test costs increased
- Late analysis of EMC
 usually lead to extra costs
- Off-the-shelf components are becoming very popular, but EMC performances are usually unknown
- At best, incomplete IBIS
 models



PROS	CONS	
No development time required; The product is available as soon as it is purchased	No competitive advantage since it is available to all competitors	
Gain cost efficiencies with a mass market product	Custom functionality not available; You conform to the app rather than the app conforming to you	
Upgrades and enhancements provided by the app owner, oftentimes at no additional charge	App support is contingent on the developing company's viability	



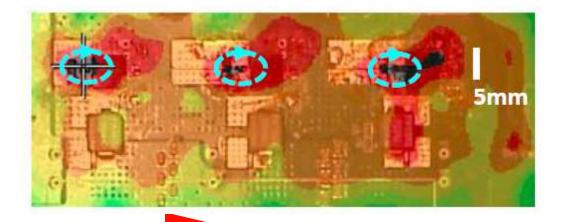


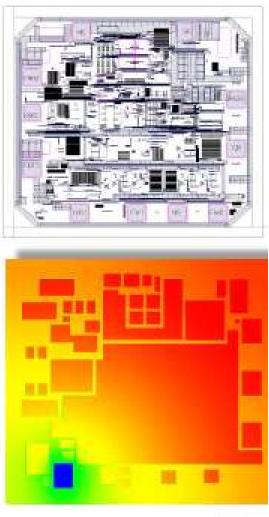


MORE POWER DOMAINS

More Electric Systems (cars, airplanes)

- Power IC contains 100+ pockets and 10+ power domains
- Standard TCAD tools cannot cope with chip complexity and dimensions
- Simulation of EMC requires simplification & approximations for model-order-reduction







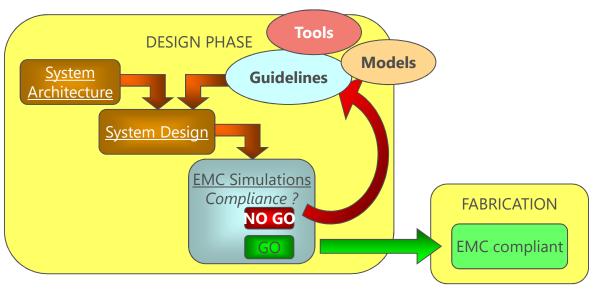


MORE ANTICIPATION

Design

- EMC concerns move from
 system-level to IC-level
- EMC/interference is taken into account from the very early design phases
- Increased use of simulation & modeling
- Measurement should (preferably) only confirm a good EMC modeling / simulation work









PEOPLE & TOOLS

People

- Lack of EMC engineers with knowledge on Ics
- Not enough engineers who can design and simulate chip-package-board together
- Not enough engineers who can handle signal & power integrity together
- Non-EMC aware project management

Tool Efficiency

- Communication and data sharing between IC, package, and PCB design level very difficult
- Limits of CAD and computational resources
- Efficient modeling and simulation methods simulating "Tera-device" problems not available







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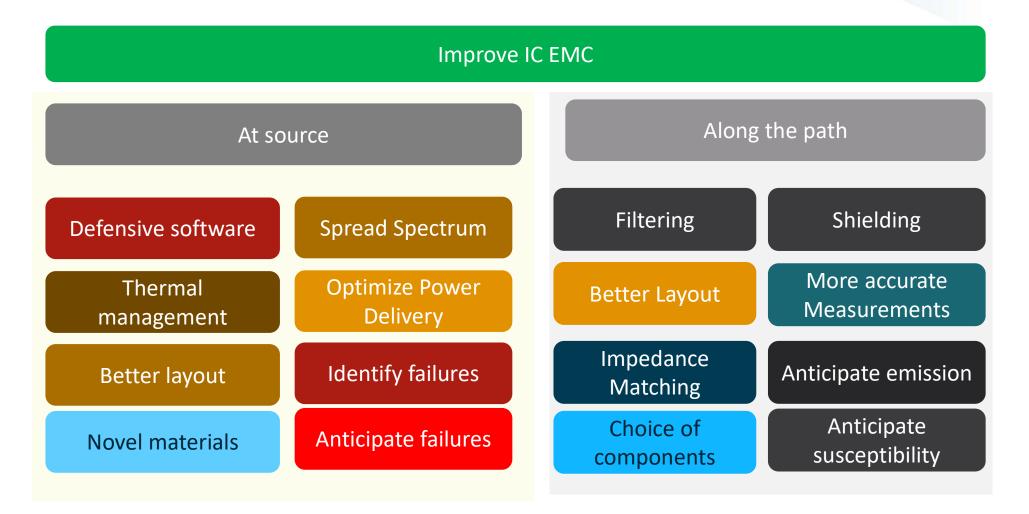
GOOD NEWS FOR EMC







MANY SOLUTIONS EXIST



• Selection of papers published in 2018, 2019 in IEEE Transaction on EMC







META-MATERIALS

0

6

12

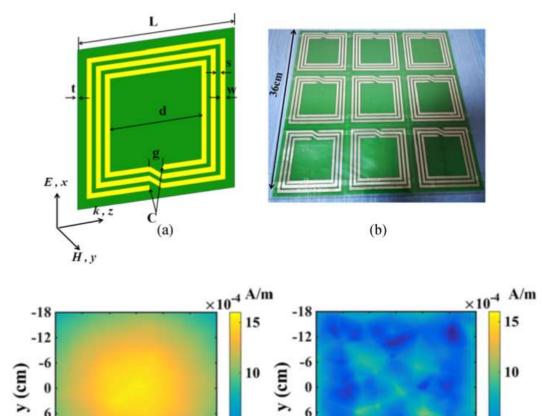
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-18 -12 -6 0 6 12 18

x (cm)

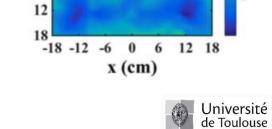
Shielding

- **Investigation of Negative** • and Near-Zero Permeability Metamaterials, Conghui Lu, IEEE Trans. EMC, 2019
- **Reduction of 15 dB thanks** • to meta-materials



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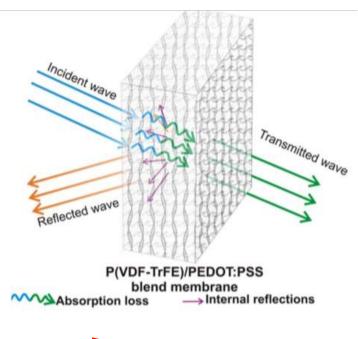
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EMI SHIEDLING

Shielding

- Flexible, Thin Composite Film to Enhance the Electromagnetic Compatibility of Biomedical Electronic Devices Vikas Rathi, 2019
- Electromagnetic Shielding Effectiveness: 20-30 dB



Material	Mechanical Property	Thickness (mm)	EMISE(dB)	Reference
MWCNT/ polyurethane	flexible	1.5	29	25
PVDF/CNT/ graphene	flexible	0.1	27.58	2
poly(propylene) /polystyrene	hard	2	20	26
P(VDF -TrFE) / PEDOT:PSS	flexible	0.08	35	This work





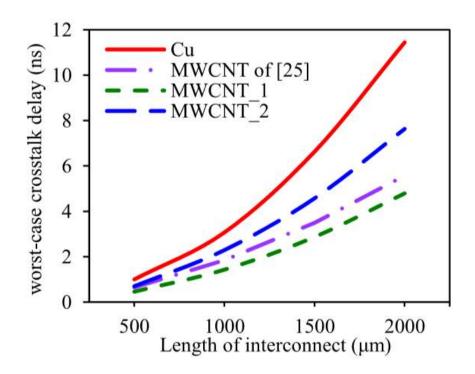


ON-CHIP CROSSTALK – CNT VS CU

Novel materials

Shielding

- Efficient Passive Shielding of Multi-Wire Carbon Nano-Tube Interconnects to Reduce Crosstalk Effects, M. H. Moaiyeri, 2019
- More compact pitch, less crosstalk with CNT as compared to Copper



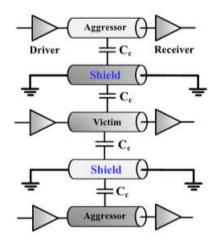
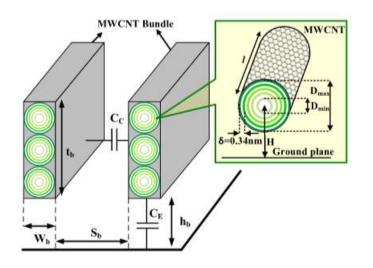


Fig. 1. Schematic of the bus used for investigating the crosstalk effects.



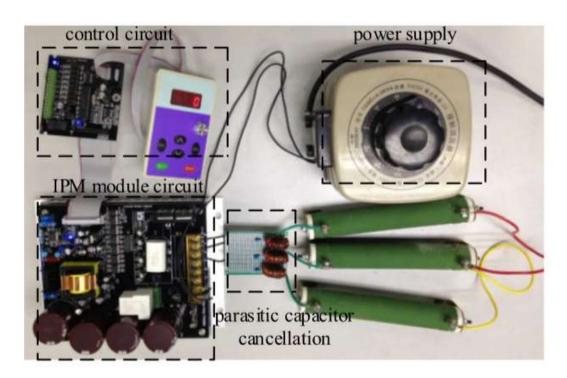


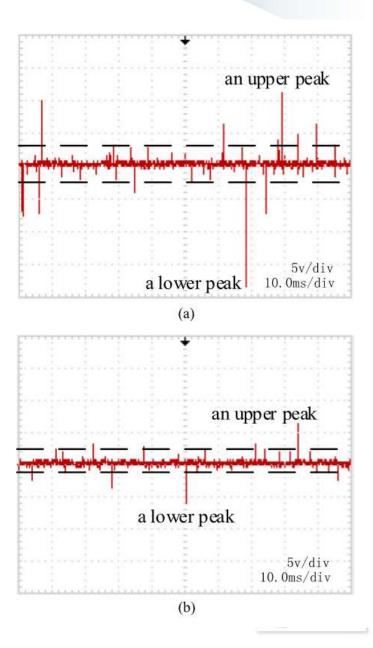


FILTERING IN POWER MODULES

Filtering

- Common-Mode Noise Reduction by Parasitic Capacitance Cancellation in the Three-Phase Inverter Haiwei Zhang, 2019
- Devices for solar panels







EMC PERFORMANCES AFTER AGING

Anticipate failures

- A New Methodology for EMC Prediction of Integrated Circuits After Aging, Chaimae Ghfiri, 2019
- Degradation of Ring Oscillator frequency
- Reduction of conducted emission

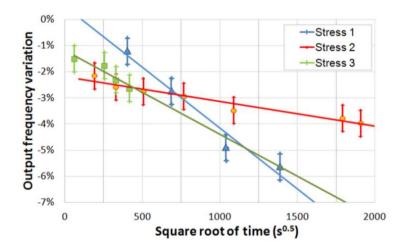


Fig. 3. Evolution of the output frequency of the ring oscillator for each stress condition.

Stress N° VCCINT (V) VCCO(V) $T_i (°C)$ 2,3 4,1 115 1 2 2,3 4,8 135 3 2.8 5.2 115

 TABLE II

 Selected Stress Conditions for the M-HTOL Test

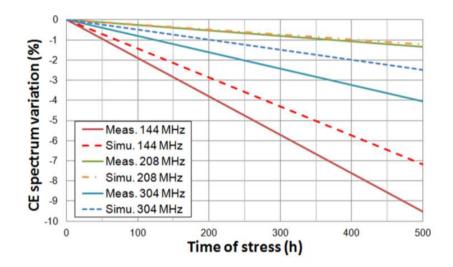


Fig. 7. Comparison between the variation of the measured and simulated CE spectrum from the initial state and after 500 h aging.

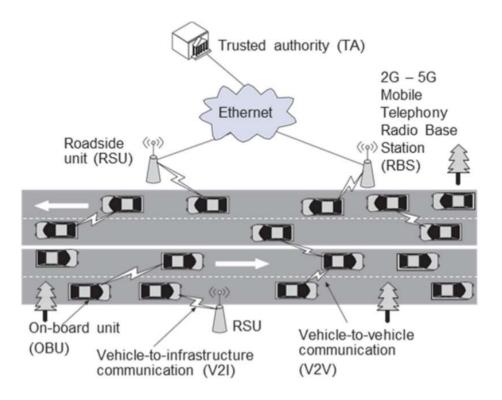


Resilience to intentional EMI in 5G vehicules

Anticipate failures

- Resilience to Intentional Electromagnetic Interference Is Required for Connected Autonomous Vehicles, Per Angskog, IEEE EMC 2019
- Analyse vulnerability of critical
 Active Road Safety warnings

 approaching emergency vehicle warning
 intersection collision warning
 stop-sign violation warning
 wrong-way driving warning
 collision risk warning
 road-work warning
 road condition warning



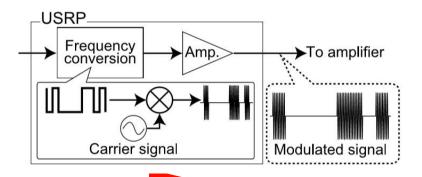


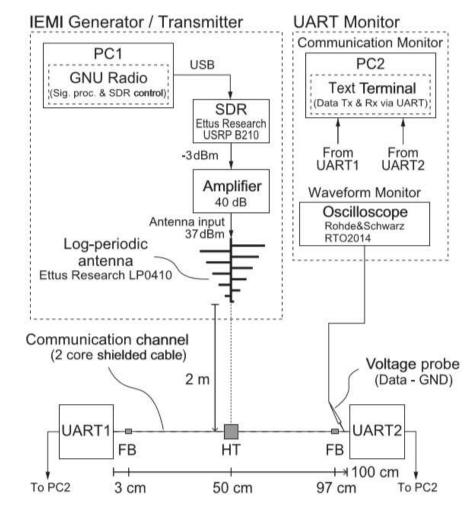


DATA INJECTION ATTACK ON UART

Identify failures

- Data Injection Attack Against
 Electronic Devices With Locally
 Weakened Immunity Using a
 Hardware Trojan Shugo Kaji, 2019
- Changes in electrical signals equivalent to the communication between UARTs are obtainable by data injection attacks









SOFT FAILURE ON USB 3.0

Identify failures

 Identification of Soft Failure Mechanisms Triggered by ESD Stress on a Powered USB 3.0 Interface, Sebastian Koch, 2019

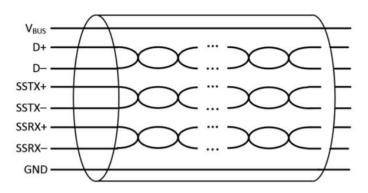
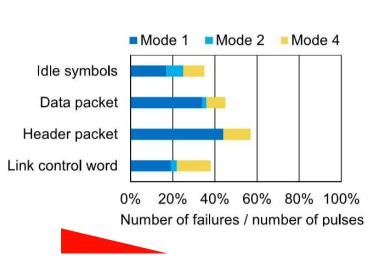
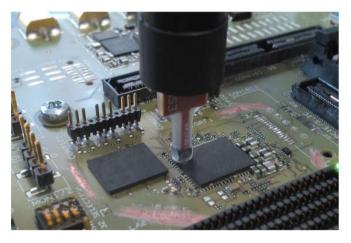


Fig. 1. USB 3.0 interface with its three differential signaling pairs, 5 V $V_{\rm BUS}$, and GND pin.





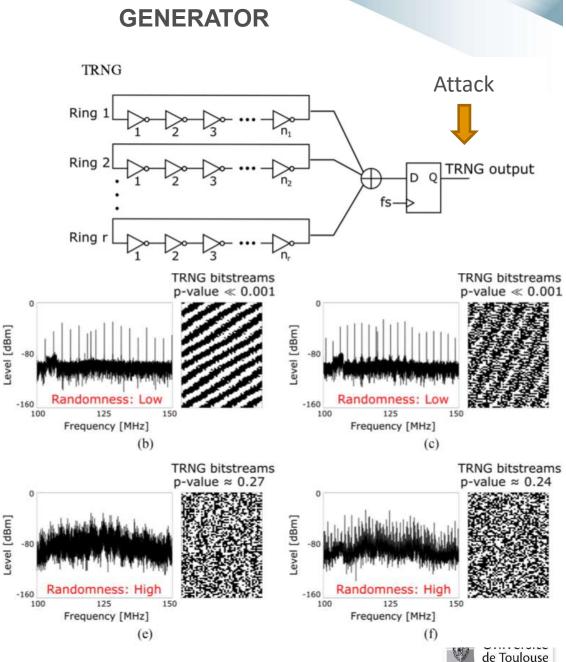




ATTACK ON RANDOM NUMBER GENERATOR

Identify failures

- EM Information Security
 Threats Against Ring Oscillator-Based True Random Number
 Generators, S. Osuka, 2019
- The disturbance frequency could be kept out of the IC by dynamically controlling the load impedance
- EMC countermeasures, such as ferrite cores, may be able to prevent interference



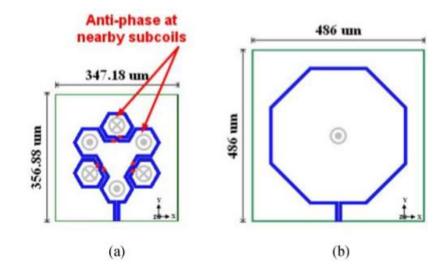


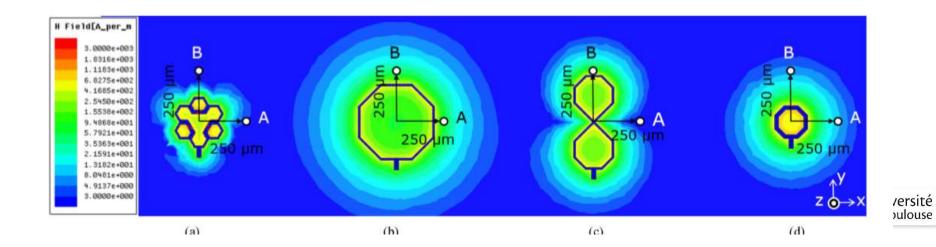


PLANAR HONEYCOMB INDUCTOR

Better layout

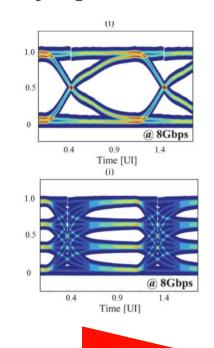
- Characterization of a Planar Honeycomb-Based Inductor on Crosstalk/EMI Suppression Tzuen-Hsi Huang, 2019
- Honeycomb shape: 15–30 dB reduction
- Same 1nH inductor



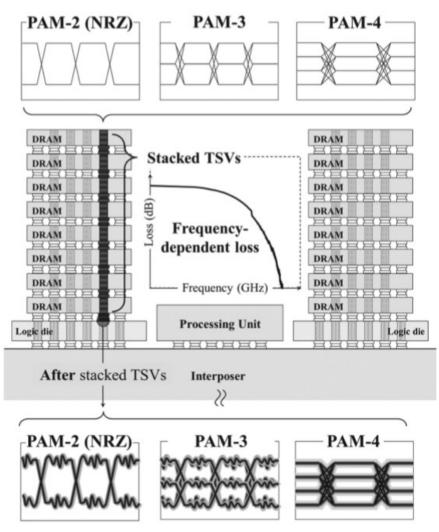




A Novel Eye-Diagram
 Estimation Method for Pulse
 Amplitude Modulation With
 N-Level (PAM-N) on Stacked
 Through-Silicon Vias
 Junyong Park, 2019







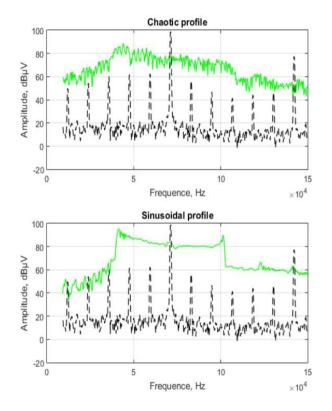


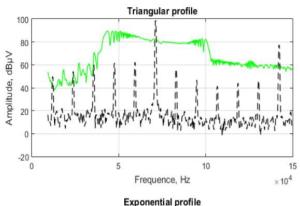


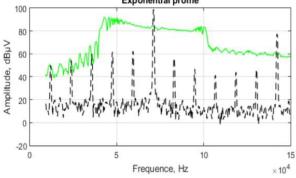
REDUCING NOISE BY PEAK REDUCTION

Spread Spectrum

- A Review of SpreadSpectrum-Based
 PWM Techniques—
 A Novel Fast Digital
 Implementation
 Rabiaa Gamoudi,
 2018
- Optimal spread of Energy by chaotic profile











FAST DYNAMIC POWER & CONDUCTED NOISE ESTIMATION

Anticipate emission

- A New Methodology to Build the • Internal Activity Block of ICEM-CE for Complex Integrated Circuits Chaimae Ghfiri, 2018
- **Deterministic vs statistical model**

of a FPGA IA

Static Timing

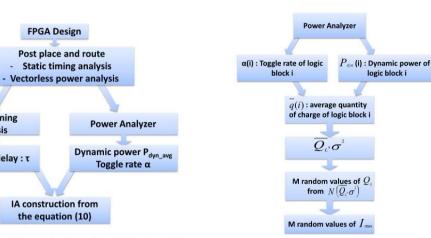
Analysis

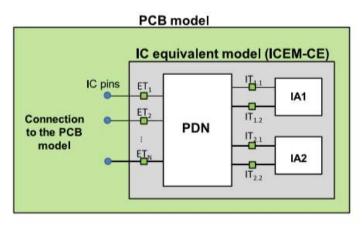
Data path delay : τ

FPGA Design

Post place and route

- Static timing analysis





Structure of ICEM-CE model. Fig. 1.

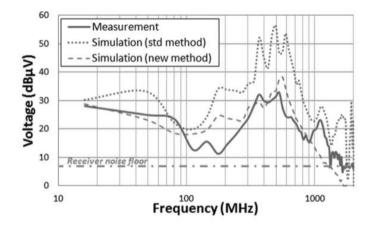
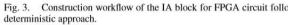


Fig. 23. Comparison between the measured and simulated spectral envelope CE with a 150 Ω probe according to the standard and new IA extraction method (delay Line configuration).



IA construction from

the equation (10)

Fig. 4. Workflow of the IA construction using the statistical approach.

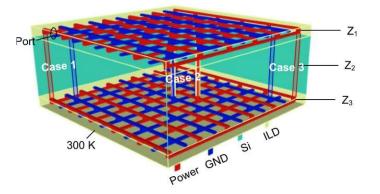


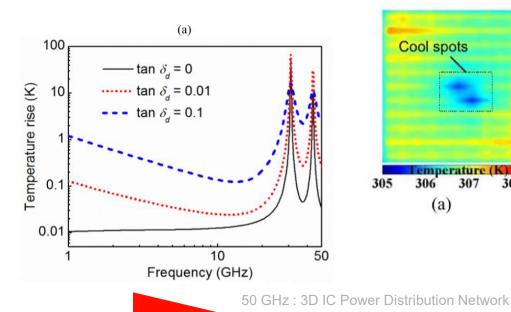


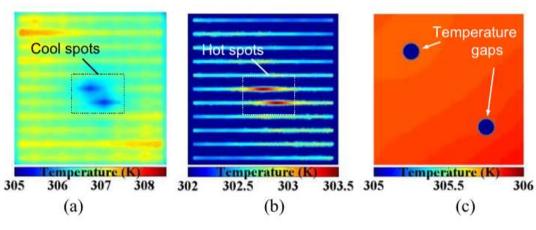
ELECTROTHERMAL EFFECTS IN 3D-ICS

Thermal management

- LI et al.: HIGH-FREQUENCY ELECTROTHERMAL CHARACTERIZATION OF TSV-BASED PDN
- Effects of 20-40 GHz resonance of local hot spots







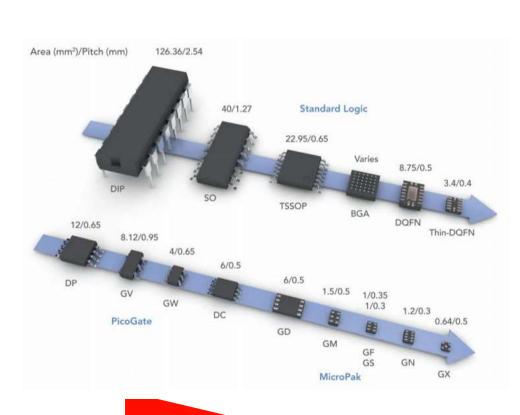


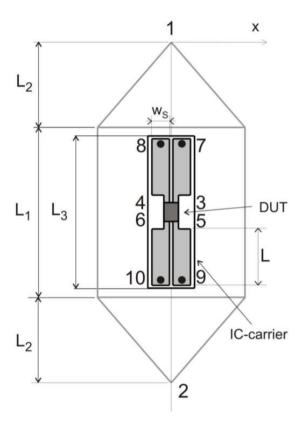


EMC CHARACTERIZATION OF VERY SMALL ICS

A Test Structure for the EMC Characterization of Small Integrated Circuits, Michele Perotti, 2019

• Stripline method adaptation to very small ICs



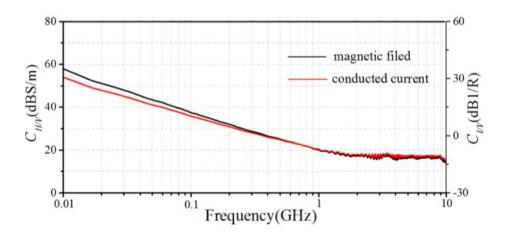


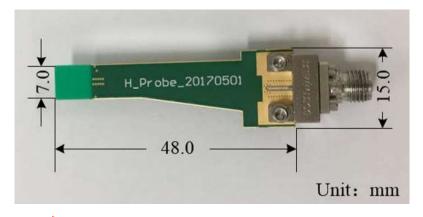


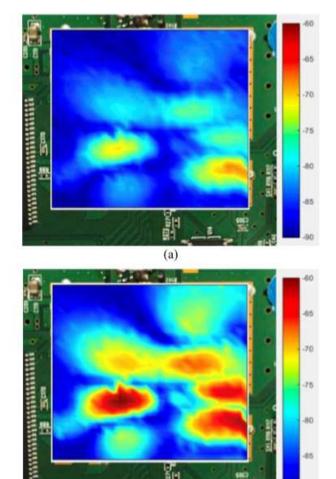


NEAR FIELD CURRENT PROBES

- YAN et al.: NONCONTACT WIDEBAND CURRENT PROBES, IEEE EMC 2018
- More precision, more resolution, upto 10 GHz







(b)

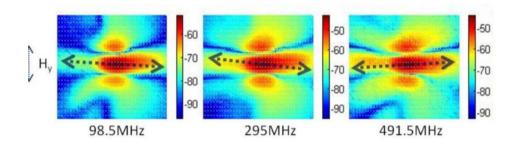
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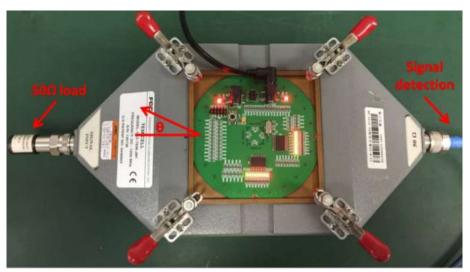


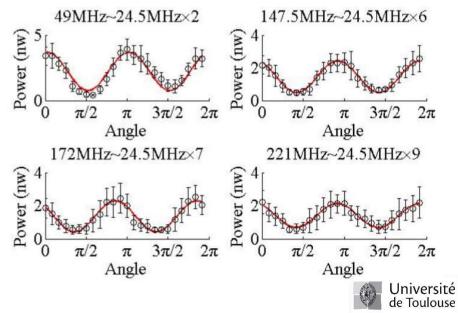
ROTATING TEST BOARD

More accurate Measurements

- Extracting the Electromagnetic
 Radiated Emission Source of an
 Integrated Circuit by Rotating the
 Test Board in a TEM Cell
 Measurement Wenxiao Fan, 2019
- Maximum power is not always matched to 0, 90, 180 or 270°
- Verified using near-field scan









ANALYSIS OF SIMILAR ICS AND STUDY OF EM INTERFERENCE

Choice of components

- Modeling and Analysis of Large-Signal RFI Effects in MOS Transistors Clovis Pouant, 2019
- 2 MOS references BSS83 and 3N170 compared in terms of rectifying effects with RFI 1 MHz – 3GHz

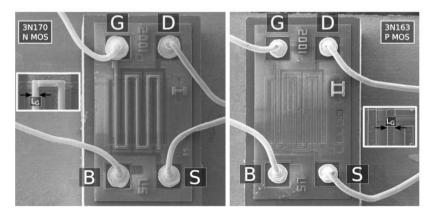


Fig. 6. 3N170 (left) and 3N163 (right) dies pictures obtained with a SEM and geometrical parameter measurement.

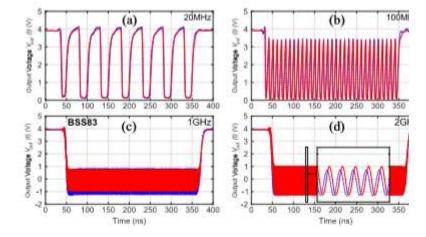


Fig. 12. Measured (dashed blue line) and simulated (solid red line) B: output voltages $V_{out}(t)$ for an input signal of 4-V peak. (a) Response at 20 M (b) 100 MHz, (c) 1 GHz, and (d) 2 GHz. $R_L = 1 \text{ k}\Omega$ and $V_{DD} = 4 \text{ V}$.

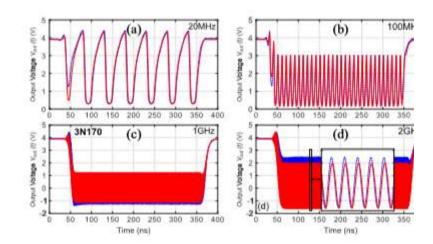


Fig. 13. Measured (dashed blue line) and simulated (solid red line) 31 output voltages $V_{out}(t)$ for an input signal of 6-V peak. (a) Response at 20 M (b) 100 MHz, (c) 1 GHz, and (d) 2 GHz. $R_L = 1 \text{ k}\Omega$ and $V_{DD} = 4 \text{ V}$.

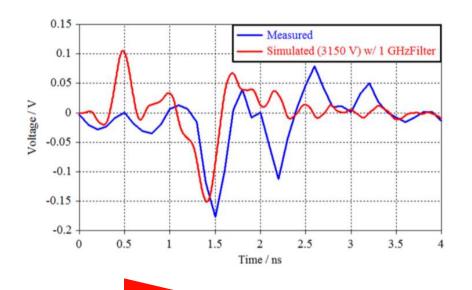


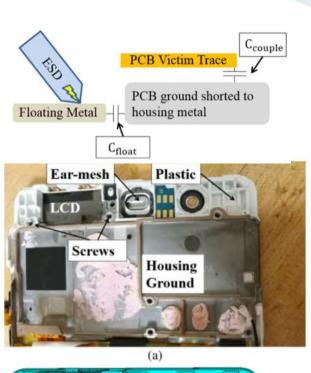


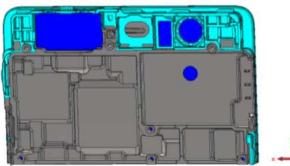
ESD ON SMARTPHONE – FULL WAVE SIMULATION

Anticipate susceptibility

- Full-Wave Simulation of System-Level
 Disruption During Secondary ESD Events
 in a Smartphone, Darwin Zhang Li, 2019
- 5 billon initial mesh, reduced to 8 million
- Prediction of discharge current & victim noise







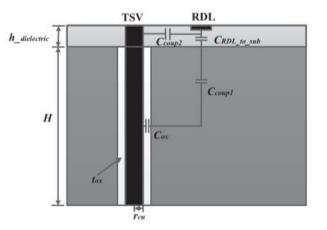




COUPLING IN 3D-ICs

Anticipate susceptibility

- Analysis of the Coupling Capacitance Between TSVs and Adjacent RDL Interconnections Zheng Mei, 2019
- Analysis of coupling & noise for different
 situations



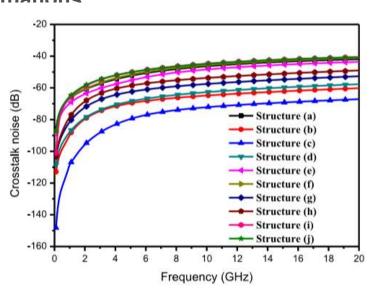
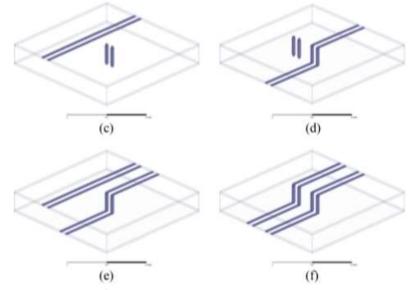
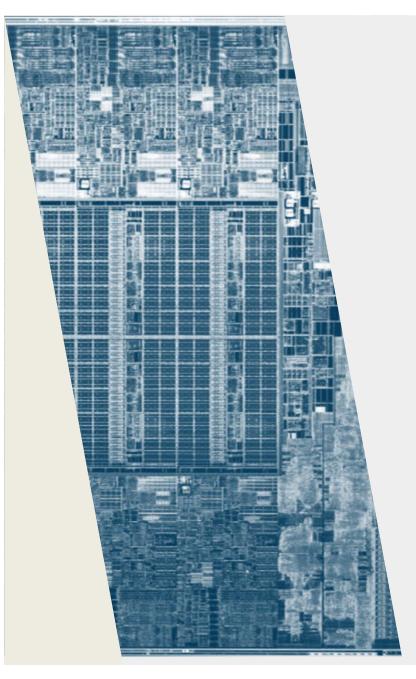


Fig. 9. Crosstalk noise of different types of TSV structures.







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CONCLUSION







CONCLUSION

- Steady growth of the electronics market
- 5G, autonomous drive, ultra high resolution screens
- Technology trends towards nm-scale, 10-100 giga transistors
- Numerous EMC challenges
- Active EMC-IC community
- Many improvements in emission & immunity
- Anticipation of EMC risk in many domains







MORE INFORMATION

Books

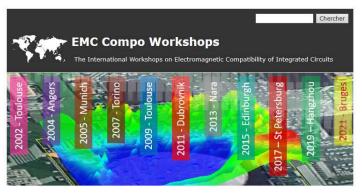


Freeware



www.ic-emc.org

Workshops



www.emccompo.org

Standards www.iec.ch

- IEC 61967, 2001, Integrated Circuits Emissions
- IEC 62132, 2003, Integrated circuits Immunity
- IEC 62433, 2008, Integrated Circuit Model

October 19







DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING

Thank you for your attention

非常感謝您的邀請

to Prof. Erping Li



