Versatile Boxes: a Multi-Purpose Algebra of High-Level Petri nets
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Abstract
This paper introduces a model of composable Petri nets, called Versatile Boxes, that has all the high-level features already introduced in the family of the Petri Box Calculus (mainly: data and time representation) as well as a new interruption capability. This allows for defining processes that are able to interrupt their execution at any point, just as a program can raise an exception. By choosing a carefully tuned level of generality, we are able to obtain a model that is much simpler than previous approaches, while still allowing to give the semantics of the usual programming language constructs. We believe that our model has the required characteristics for a very general use, hence its name. On the other hand, its is not very complex and so it should be easy to understand or implement and efficient for verification or simulation purpose.

1. INTRODUCTION
The Petri Box Calculus (PBC) and its successor PNA [1, 2] are two classes of Petri nets that can be composed like terms in process algebra. Various high-level variants of them have been introduced. The most noticeable one is the model of M-nets [6] that can be seen as a high-level version of the Petri net part of PBC. It allows to handle data values directly as tokens and to apply high-level operations on them. However, M-nets lack a syntactic level that allows for building large complicated nets without the need to draw them. (Actually, a syntax exists but is at least as complex as has the net model itself [8].) This problem has been addressed through several successive extensions of PBC by introducing classes of high-level Petri nets provided with a user-friendly syntax. In particular: the Asynchronous Box Calculus [3] introduces buffered communication; the Box Calculus with Data [4] allows for high-level data in the model; the Causal Time Calculus [9] addresses timed systems. The model presented in this paper, called Versatile Boxes (v-boxes for short), will cover all those aspects in a unified presentation and will provide various improvements while keeping the model simple to implement.

The main aim of the present paper is to push further this evolution by introducing an exception-like feature. The goal is to be able to define processes whose execution can be interrupted at any point. This question has been already addressed for M-nets (but not at syntactical level), leading to a very general preemption mechanism [7]. However, it has been necessary to introduce priorities between Petri net transitions in order to be able to preempt an M-net in presence of nested parallelism. The resulting model is an exponential abbreviation of regular Petri nets; moreover, it is not usable with most existing software tools. In this paper, we use a simpler setting inspired from what exists in most programming languages: parallelism is restricted to well identified threads of execution (or tasks), each one being a sequential process. Moreover, we do not consider a general preemption mechanism, where any process can preempt any other, but rather an interrupt mechanism by which a process can terminate itself. This is also what happens in most programming languages where a thread can be stopped only by itself.

As a simple motivating example, let us just consider the break or exit statement used in many programming languages to stop a loop at any point (we could similarly consider interruptions or the return instruction). This commonly used statement cannot be modelled by any of the previous models, except using that from [7] but at the price explained above. We show in the conclusion how this can be modelled in a simple way using v-boxes.

The sequel is organised as follows: we first introduce basic definitions, in particular that of the class of labelled Petri nets used throughout the paper; then, we define the operators that allow to compose these nets; finally, we introduce the syntactic level and its Petri net semantics. As a conclusion, we will summarise the new or improved features of v-boxes and present future directions of work.

In order to focus on the new aspects, some technical aspects will be simplified.

2. BASIC DEFINITIONS
A multiset over a set $X$ is a function $\mu : X \rightarrow \mathbb{N}$. We shall use the following operations on multisets: comparison $\mu \leq \mu'$, element inclusion $x \in \mu$ and sum $\mu + \mu'$. A subset of $X$ may be treated as a multiset over $X$, by identifying it with its characteristic function. A finite multiset may
be written in extended set notation, e.g., \{a,a,b\}. We denote by \(\mathbb{N}^X\) the set of all the finite multisets over \(X\).

### 2.1. Distinguished sets

We consider several sets used in the following: except when specified, they are assumed pairwise disjoint.

- \(\mathbb{D}\) is the set of data values, it includes in particular the integers, the booleans \(\top\) and \(\bot\), and the Petri net tokens •, ○ and •, \(\mathbb{D}\) also holds the integer-like value \(\omega\) that is greater than any integer, and such that \(\omega + 1 \equiv \omega\) and \(\omega - 1 \equiv \omega\). This set of values corresponds to the data stored in a Petri net, • and ○ being specifically used to model the control flow while • is involved in net compositions. \(\mathbb{D}\) is assumed closed under tuple aggregation, that is, \(\mathbb{D}^i \subseteq \mathbb{D}\) for all \(i \geq 1\), allowing to model structured data.

A distinguished subset of \(\mathbb{D}\) is \(\mathbb{D}^\text{df} \equiv \mathbb{N} \cup \{\omega\}\) that is the set of clock counts, corresponding to the value associated to a counter of clock ticks. We shall use increasing such counters to measure the passing of time as well as decreasing ones to model timeouts.

- \(\mathbb{V}\) is the set of variables, that are denoted by \(x, y, x_1, \ldots\). In order to allow for renaming variables at will, \(\mathbb{V}\) is assumed infinite. These variables will be used to label Petri nets, for instance in order to represent values carried by arcs.

- \(\mathbb{I} \equiv \mathbb{I}_s \cup \mathbb{I}_t \cup \mathbb{I}_p \cup \{\epsilon\}\) is the set of identifiers that are names given to nodes. \(\epsilon\) is the anonymous identifier. Transitions are named from \(\mathbb{I}_t\) \(\cup\) \(\{\epsilon\}\) and places from \(\mathbb{I}_p\) \(\cup\) \(\{\epsilon\}\). \(\mathbb{I}_p\) is involved in processes naming; for all \(\text{name} \in \mathbb{I}_p\) we assume that \(\text{name}_\text{enter}\) and \(\text{name}_\text{exit}\) also belong to \(\mathbb{I}_p\). We also assume that for all \(\text{name} \in \mathbb{I}_s\), there exists a corresponding \(\text{name} \in \mathbb{V}\), with a mapping \(\text{var}\) such that \(\text{var}\{\text{name}\} \equiv \text{name}\).

- \(\mathbb{S} \equiv \{\epsilon, x, i, b, w, t, v\}\) is the set of place statuses. In a Petri net, they are used to distinguish respectively the entry, exit, internal, buffer, watch, timeout and variable places. The entry, exit and internal places are used to model the control flow of a Petri net and are so called control flow places. The others are called data places as they store all the data manipulated by the Petri net. More precisely, buffer places will hold multisets of values that can be added or retrieved from buffers. Watch places will model counters of clock ticks used to measure the passing of time. Timeout places will be downward ticks counters used to model timeouts. Last, variable places will hold a single value (but possibly structured) as variables in a program.

- \(\mathbb{P} \subseteq \mathbb{D}\) is the set of process identifiers, that allows to distinguish several concurrent threads of execution in a Petri net. \(\mathbb{P}\) can be assumed finite or infinite depending on the execution policy that one wants to use.

### 2.2. Labelled Petri nets

A labelled Petri net is a tuple \(N = (S, T, \ell)\) where \(S\) is a finite set of places, \(T\) is a finite set of transitions such that \(S \cap T = \emptyset\), and \(\ell\) is the labelling function on places, transitions and arcs, i.e., elements from \((S \times T) \cup (T \times S)\). This labelling is defined as follows:

- for a place \(s \in S\), \(\ell(s) \equiv s\gamma\{(s)\}\eta(s)\) where \((s) \in S\) is the status of \(s\), \((s) \subseteq \mathbb{D}\) is the type of \(s\) and \(\eta(s) \in \mathbb{I}_s \cup \mathbb{I}_p \cup \{\epsilon\}\) is the name of \(s\);

- for a transition \(t \in T\), \(\ell(t) \equiv \gamma(t)\eta(t)\) where \(\eta(t) \in \mathbb{I}_t \cup \{\epsilon\}\) is the name of \(t\) and \(\gamma(t)\) is an evaluable boolean expression, called the guard of \(t\) (a condition for its execution);

- for an input arc \((s,t) \in S \times T\), \(\ell(s,t)\) is a finite multiset over \(\mathbb{D} \cup \mathbb{V}\), representing the values consumed in \(s\) when \(t\) is executed;

- for an output arc \((t,s) \in S \times T\), \(\ell(t,s)\) is a finite multiset of values, variables or evaluable expressions representing the values produced in \(s\) when \(t\) is executed. The difference with input arcs is that computation of new values is allowed by using expressions.

The usual graphical representation for Petri nets is to have circles for the places, squares for the transitions and directed links for the arcs. We shall omit arcs with empty directed links for the arcs. Let \(\sigma, \tau\) be two arcs, \(\mathbb{S}\) a set, \(\mathbb{D}\) and \(\mathbb{V}\) the data values set. \(\mathbb{S}\) is the set of \(\mathbb{S}\) elements.

### 3. LABELLED NETS OPERATORS

This section introduces various operators on the labelled Petri nets defined above, allowing to build complex systems from simpler blocks.

Let \(N_1\) and \(N_2\) be two nets, we will define four binary control flow operators \(\triangleright, \Box, \circ\) and \(\triangleright\) with the following intuitive semantics:

- the sequence \(N_1 \triangleright N_2\) allows to execute \(N_1\), followed by \(N_2\);
- the choice \(N_1 \Box N_2\) allows to execute either \(N_1\) or \(N_2\);
- the iteration \(N_1 \circ N_2\) allows any number of executions of \(N_1\), followed by one execution of \(N_2\);
- the trap \(N_1 \triangleright N_2\) allows to execute \(N_1\) then, only if \(N_1\) finishes abnormally, \(N_2\) is executed, i.e., \(N_2\) can trap an error occurring in \(N_1\) (but not outside).
The notion of normal or abnormal termination of a net will correspond to have respectively a token  or  in its exit places. Producing a token  corresponds to raising an exception in a program: the regular flow of operations is interrupted and the exception may be caught by a handler (a trap in our framework).

The name hiding  makes anonymous the name-labelled nodes in . This operator is used to disable the name-based node merges that take place when two nets are combined. For instance, in such a case, the variable places of a given name that come from each net have to be merged in order to ensure that each variable is modelled by only one place. By using the name hiding operator, one can disable further merges of such a place, which results in making a variable local to a sub-net.

The task declaration operator  (name : N) turns a net  into a task called name. This allows to start several concurrent executions of ; they are basically independent but may interact through shared data places.

Finally, the parallel composition  allows  and  to evolve concurrently. This operator will never be nested inside another one.

In order to define correctly those operators, we assume that the labelled Petri nets considered as arguments have the following properties (but we may build nets that do not respect these properties):

- R1: exactly one entry place and one exit place;
- R2: control flow places have the type \{ , \};
- R3: control flow places as well as the transitions connected to them are anonymous.

### 3.1. Operators nets

The four control flow operators will be defined using operator nets. They are particular labelled Petri nets used to guide the construction of the net resulting from the application of each operator. Intuitively, an operator net defines the structure of the result that is built by replacing dedicated transitions of the operator net by the operand nets.

Figure 1 shows the four operator nets corresponding to our four control flow operators. In order to perform an operation, each transition \( t_i \) has to be replaced by the \( i \)-th operand net. The labels on the arcs indicate what tokens are allowed:  or any (with ).

Consider for instance the top-left net, that specifies the sequence operator, and take \( N_1 \parallel N_2 \); the sequential composition of \( N_1 \) and \( N_2 \). \( N_1 \) is started by putting one token in its entry place, if it is , transition \( t_1 \) and thus net \( N_1 \) is executed. When it terminates, it produces a token in the internal place. Again, if this is , \( t_2 \) and thus \( N_2 \) is executed until it puts a token in the exit place. But, if \( N_1 \) terminates with a token , transition \( t_2 \) is executed instead of \( t_2 \), which results in skipping \( N_2 \) because we have to propagate the error until it is trapped. Similarly, if the net is started with a  in the entry place, this token is directly propagated to the exit place through transition \( t_1 \). The situation is similar for the choice and iteration operators. Consider now the trap operator net \( N_t \), it has the same structure as \( N_1 \) but the tokens on the arcs differ: here, if \( N_1 \) terminates with \( \bullet \) this token is directly propagated to the exit place; but if \( N_1 \) produces a , this token allows to execute \( t_2 \) and thus \( N_2 \), which is the expected behaviour of the trap. Notice that the arcs going out of transitions \( t_i \) allow any token to be produced as the result of each \( N_i \).

In order to know how the labelling of an arc in an operator net influences the labellings of the arcs in the resulting nets, we define a function \( \Theta \) that maps a pair of arc labels (in operator and operand net) to the label that will be used in the resulting net. This function is defined by the array given in figure 2.

Consider for instance transition \( t_1 \) in operator net \( N_o \) and assume an operand net \( N_t \) that has an entry places \( e \) with outgoing arcs to two transitions:

\[
\begin{align*}
e \rightarrow^{(\bullet)} t_1 & \quad \text{and} \quad e \rightarrow^{(\circ)} t_2.
\end{align*}
\]

When \( N_t \) replaces \( t_1 \), its place \( e \) becomes the entry place of the resulting net that will also include \( t_1 \) and \( t_2 \). But the operator net allows only \{ \} to enter \( t_1 \) and thus \( N_t \).
This is enforced by $\theta$ that defines the labels of the arcs outgoing from $e$ in the resulting net as:

$$e \cdot \theta([-\bullet],[\bullet]) \rightarrow t_1$$
$$e \cdot \theta([-\bullet],[\circ]) \rightarrow t_2$$

So the arc to $t_1$ is preserved while the arc to $t_2$ is relabelled with $\{\bullet\}$. As a result, $t_2$ becomes a dead transition because $\bullet$ is not in the type of control flow places (by restriction R2). So, tokens $\circ$ introduced in $e$ will go through transition $t_2'$ (coming from $N_2$) in the resulting net, which is the expected behaviour. Because any token is allowed to go out of $\bullet 1$, the arcs to the exit place of $N_1$ will not be changed (see the $\{\bullet\}$ column of the array that defines $\theta$). Consider now transition $\bullet 2$, substituted with the same net $N_1$; we have:

$$e \cdot \theta([\circ],[\bullet]) \rightarrow t_1$$
$$e \cdot \theta([\circ],[\circ]) \rightarrow t_2$$

So, the precise meaning of the arc to $\bullet 2$ is actually "take one token $\circ$ in the internal place and start the net that replaces $\bullet 2"$. Token $\circ$ is thus directly consumed by $t_1$ which corresponds to the regular start of $N_1$.

### 3.2. Control flow operators

Let $N_1 \equiv (S_1, T_1, \ell_1)$ and $N_2 \equiv (S_2, T_2, \ell_2)$ marked respectively by $M_1$ and $M_2$. Take $\circ \in \{\circ, \square, \#\}$ and operator net $N_0 \equiv (S_0, T_0, \ell_0)$. We assume that $S_1$, $S_2$, $T_1$, $T_2$, $S_0$ and $T_0$ are pairwise disjoint (nodes in $N_1$ or $N_2$ may be renamed if needed). The composition $N_1 \circ N_2$ is $N \equiv (S, T, \ell)$ marked by $M$ defined as follows.

First, the nodes from $N_1$ and $N_2$ are copied to $N$, together with the arcs connecting them. So, for $1 \leq i \leq 2$, for all $s \in S$ and all $t \in T_i$:

- $s$ is also a place in $S$ with $\ell(s) \equiv \ell_i(s)$ and $M(s) \equiv M_i(s)$;
- $t$ is also a transition in $T$ with $\ell(t) \equiv \ell_i(t)$;
- $\ell(s,t) \equiv \ell_i(s,t)$ and $\ell(t,s) \equiv \ell_i(t,s)$.

The transitions of the operator net that will not be replaced by a $N_i$ are also copied to the result: for all $t \in T_0 \setminus \{\bullet 1, \bullet 2\}$, we also have $t \in T$ with $\ell(t) \equiv \ell_0(t)$.

Then, the non-anonymous transitions are merged. For all $\eta \in \pi_1$, all the transitions $t_1, \ldots, t_n$ in $T$ such that $\eta(t_i) = \eta$ (for $1 \leq i \leq n$) are merged, yielding $t'$ such that $\gamma(t') \equiv \gamma(t_1) \wedge \cdots \wedge \gamma(t_n)$ and $\eta(t') \equiv \eta$. The $t'$s $(1 \leq i \leq n)$ are then removed from $T$.

Moreover, the non-anonymous data places are merged. For all $\alpha \in I_s$ and all $a \in \{b, w, v\}$, all the places $s_1, \ldots, s_m$ in $S$ such that $\eta(s_i) = \alpha$ and $\sigma(s_i) = a$ (for $1 \leq i \leq m$) are merged, yielding $s'$ such that $\sigma(s') \equiv a$, $\eta(s') \equiv \alpha$ and $\tau(s') \equiv \tau(s_{1}) \cup \cdots \cup \tau(s_{m})$. The marking of $s'$ is dependent on the status of the merged places: if $a = b$, then $M(s') \equiv M(s_1) + \cdots + M(s_m)$ as a buffer place can accumulate tokens; otherwise, the composition is only allowed if all the places have the same marking. This marking becomes that of $s'$. The $s_i$'s $(1 \leq i \leq m)$ are then removed from $S$.

Finally, entry and exit places are combined in order to enforce the desired control flow. To do so, we rely on operator net $N_3$: we consider in turn each place $s_o$ of $N_0$ and collect in a set $P$ places originated from each $N_i$.

The places in $P$ should be merged in order to become the realisation of $s_o$ in $N$. So, for $1 \leq i \leq 2$:

- if $\ell(s_o, \bullet i) \neq \emptyset$ then the entry place $e_i$ of $N_i$ is added to $P$;
- if $\ell(s_o, \pi i) \neq \emptyset$ then the exit place $x_i$ of $N_i$ is added to $P$.

Notice that $e_i$ and $x_i$ exist because of restriction R1. The places in $P$ are merged, yielding a new place $s'_o$ whose label is that of $s_o$. The composition is allowed only if all the places in $P$ have the same marking. This marking becomes that of $s'_o$. The places in $P$ are then removed from $S$. The last operation related to $s'_o$ is to use $\theta$ in order to adjust the labelling of the arcs between $s'_o$ and the transitions originated from each $N_i$; moreover, $s'_o$ has to be connected to the transitions originated from $N_0$:

- for $1 \leq i \leq 2$, for all $t \in T_i$ that is connected to the entry $e_i$ or exit place $x_i$ of $N_i$:
  - $\ell(s'_o, t) \equiv \theta(\ell(s_o, \bullet i) \setminus \{\bullet i\}, \ell(e_i, t))$;
  - $\ell(t, s'_o) \equiv \theta(\ell(s_o, \pi i) \setminus \{\pi i\}, \ell(t, x_i))$;
- for all $t \in T_0 \setminus \{\bullet 1, \bullet 2\}$, $\ell(s'_o, t) \equiv \ell(s_o, t)$ and $\ell(t, s'_o) \equiv \ell(t, s_o)$.

Optionally, the transitions in $T$ having an attached arc labelled by $\{\bullet\}$ being dead, they can be removed.

### 3.3. Task declaration

Let $N \equiv (S, T, \ell)$ be a labelled net and $\text{task} \in \pi_0$ be a task identifier, the task declaration $\langle \text{task} : N \rangle$ is a net that is able to concurrently execute instances of $N$. These instances are identified by a value in $\pi$ so that they can run independently. This could not be obtained by just
putting several tokens in the entry place of $N$ because they would be all identical. Instead, (almost) every place $s \in S$ will be given the type $P \times \pi(s)$ in order to associate each token to a process identifier. The arcs connected to these places will be changed accordingly. However, we do not modify the non-anonymous places that may be merged later on if the task is composed with other nets. These places have to be considered as external shared resources that are not involved in the construction of the task.

When a net is turned into a task, its entry and exit places are changed into buffer places with the type $P \times \{\bullet, \circ\}$ (because of restriction R2, $\{\bullet, \circ\}$ is the type of these places). So, starting an instance of the task can be achieved by putting in the entry-like place a pair $(p, \bullet)$ where $p$ is a process identifier. The process identifier is carried along the net using a fresh variable $\pi \in \mathbb{V}$, i.e., that is not used in any annotation of $N$. When the exit-like place becomes marked, the process identifier can be retrieved there, together with the termination status of the corresponding thread (\bullet or \circ). In order to find easily the new entry-like and exit-like places, they are named respectively task-enter and task-exit, where task is the identifier used in the task declaration. Notice that it is possible to start a thread with a token $(p, \circ)$ that may be trapped inside $N$. Moreover, the user is let responsible for consistently choosing process identifiers.

If $N$ is marked by $M$, the net $N' \equiv \langle \text{task} : N \rangle \equiv (S', T', \ell')$ and its marking $M'$ are defined as follows:

- each transition $t \in T$ is also a transition in $T'$ with $\ell'(t) \equiv \ell(t)$;
- each place $s \in S$ such that $\eta(s) \neq \epsilon$ is also a place in $S'$ with $\ell'(s) \equiv \ell(s)$ and $M'(s) \equiv M(s)$; moreover, for all $t \in T$, $\ell'(s,t) \equiv \ell(s,t)$ and $\ell'(t,s) \equiv \ell(t,s)$;
- for each place $s \in S$ such that $\eta(s) = \epsilon$, there is a place $s'$ in $S'$ with $M'(s') \equiv \{(p,v) \mid p \in P, v \in M(s)\}$ and
  - $\ell'(s') \equiv (P \times \pi(s)) \bowtie \text{task-enter}$, if $\sigma(s) = \epsilon$,
  - $\ell'(s') \equiv (P \times \pi(s)) \bowtie \text{task-exit}$, if $\sigma(s) = \pi$,
  - $\ell'(s') \equiv (P \times \pi(s)) \bowtie \sigma(s) \epsilon$, otherwise;
- moreover, for all $t \in T$, $\ell'(s',t) \equiv \{(\pi, \alpha) \mid \alpha \in \ell(s,t)\}$ and $\ell'(t,s') \equiv \{(\pi, \alpha) \mid \alpha \in \ell(t,s)\}$.

It may be remarked that the task declaration produces a net that does not respect restrictions R1 to R3.

### 3.4. Name hiding, parallel composition

Both operators can work on any labelled Petri net that do not need to respect restrictions R1 to R3.

The net $N/\text{name}$ is a copy of $N$ in which the nodes labelled by name are given the anonymous name $\epsilon$. The name hiding is commutative (and idempotent) and so naturally extends to sets of names.

The parallel composition $N_1 || N_2$ is defined as a simplified control flow operation: first, the nodes and arcs from both nets are copied, and second, non-anonymous nodes with the same name are merged, exactly as for control flow operators.

### 4. VERSATILE BOXES

This section introduces a syntax whose semantics is given in terms of a function box that constructs a labelled Petri net when given an expression. This syntax can be considered as a kind of assembly language having just the features required to express the constructs usually found in programming languages.

#### 4.1. Syntax

A box expression is a term on the syntax given in figure 3. The most basic process is an atomic action that corresponds to a test and set, for instance $(x > 4 \Rightarrow x := 0)$ is an atomic action that tests the value of $x$ to be greater than 4 and resets it to 0. Such an atomic action is blocked until its condition becomes true. An interrupt action is a variant of an atomic action that can be considered as the raising of an exception: when it is executed, the sequel of the process is by-passed until the exception is trapped. Conditions and assignments in actions are related to declarations, inside a scope or as a global resource. Each declaration in a scope may introduce a variable, a buffer, a watch or a timeout with the expected meaning. Moreover, a clock may be declared in order to provide the ticks counted by watches and timeouts; each clock is independent from the others. Resources can declare tasks, global data or clocks (scopes declare local objects). Processes can then be combined using operators that correspond to those defined at Petri net level.

#### 4.2. Lexical scoping

Declarations are lexically scoped: an identifier can be used only if it is the name of a previously declared object. Moreover, the reuse of an already declared identifier results in its hiding in favour of the newly declared one. In order to cope with lexical scoping, we will use environments allowing to remember the declared objects while computing the semantics of nested processes. Formally, an environment $E$ is a mapping from a subset of $\mathbb{I} \setminus \{\epsilon\}$ to $\{v, b\} \times 2^D \times N^D \cup \{(t, w) \times \mathbb{I}_t \times \mathbb{C}\} \cup \mathbb{I}_c$. We denote by $\text{name} \in E$ the fact that $\text{name}$ is in the domain of $E$, by $\emptyset$ the environment with empty domain and by $+$ the combination of environments defined by: if $\text{name} \in E_2$ then $(E_1 + E_2) (\text{name}) \equiv E_2 (\text{name})$, else if $\text{name} \in E_1$ then $(E_1 + E_2) (\text{name}) \equiv E_1 (\text{name})$, else $\text{name} \notin E_1 + E_2$. Intuitively, $E (\text{name})$ corresponds to how
\[
P ::= S \quad \text{sequential process} \\
| R \parallel P \quad \text{process with a resource} \\
| \{ \text{task} : S \} \quad \text{task resource} \\
| D \quad \text{data resource} \\
R ::= S ; S \quad \text{sequence} \\
| S \oplus S \quad \text{iteration} \\
| S \Box S \quad \text{choice} \\
| S \triangleright S \quad \text{trap} \\
| \llbracket D \mid S \rrbracket \quad \text{scope} \\
| \llbracket B \Rightarrow A \rrbracket \quad \text{atomic action} \\
S ::= \begin{array}{l}
\text{variable name} : V := v \\
\text{buffer name} : V := Z \\
\text{watch name} : \text{tick} := c \\
\text{timeout name} : \text{tick} := c \\
\text{clock tick} \\
\end{array} \quad \text{variable declaration} \\
| D, D \quad \text{multiple declarations} \\
B ::= \begin{array}{l}
\text{“boolean expression”} \\
\text{assignment} \\
\emptyset \\
\end{array} \quad \text{condition} \\
| A ::= \begin{array}{l}
\text{name} := \text{“expression”} \\
A, A \quad \text{multiple assignments} \\
\emptyset \\
\end{array} \quad \text{assignment} \\
\begin{array}{l}
P \mid R \mid S \mid D \mid B \mid A \\
\end{array} \quad \text{expression and its environment and yields a labelled Petri net}
\]

\text{Figure 3.} The syntax of box expressions, where name \in \mathbb{I}_s, \text{task} \in \mathbb{I}_r, \text{tick} \in \mathbb{I}_t, V \in \mathbb{D}, v \in V, Z \in \mathbb{N}^v, c \in \mathbb{C} \text{ and underlined words are keywords}. Both “boolean expression” and “expression” are evaluable expressions.

\text{name} was last declared: a variable or a buffer on a given set with an initial value corresponds to a triple from \{v, b\} \times \mathbb{D} \times \mathbb{N}^D; a watch or a timeout corresponds to a triple from \{w, t\} \times \mathbb{I}_r \times \mathbb{C}; a clock corresponds to its name in \mathbb{I}_t.

### 4.3. Denotational semantics

The denotational semantics of a box expression is defined by the function box that takes as arguments an expression and its environment and yields a labelled Petri net. This function is defined recursively on the syntax. The semantics of a process \(P\) is the net obtained from box(\(P, \emptyset\)) in which one token \(\bullet\) is added to each entry place (there will be actually only one such place).

#### 4.3.1. Control flow operators

The semantics of a syntactical control flow operator is simply the application of the corresponding operator at Petri net level. So, for \(\circ \in \{\circ, \oplus, \Box, \triangleright\}\), we have:

\[
\text{box}(S_1 \circ S_2, E) \triangleq \text{box}(S_1, E) \circ \text{box}(S_2, E)
\]

#### 4.3.2. Scope

The semantics of a scope consists essentially in enriching the environment with the new declarations and hiding the corresponding names in the Petri net obtained from the process part of the scope:

\[
\text{box}(\llbracket D \mid S \rrbracket, E) \triangleq (\text{init}(D) \parallel \text{box}(S, E + \text{env}(D)) \parallel \text{term}(D)) / \{\text{name} \mid \text{name} \in \text{env}(D)\}
\]

where function env(\(D\)) is defined as:

\[
\text{env(\text{name})} : V := v \triangleq (\text{name} \rightarrow (v, V, \{v\})) \\
\text{env(\text{buffer})} : V := Z \triangleq (\text{name} \rightarrow (b, V, Z)) \\
\text{env(\text{watch})} : \text{tick} := c \triangleq (\text{name} \rightarrow (w, \text{tick}, c)) \\
\text{env(\text{timeout})} : \text{tick} := c \triangleq (\text{name} \rightarrow (t, \text{tick}, c)) \\
\text{env(\text{clock tick})} \triangleq (\text{tick} \rightarrow \text{tick}) \\
\text{env}(D_1, D_2) \triangleq \text{env}(D_1) + \text{env}(D_2)
\]

The Petri nets init(\(D\)) and term(\(D\)) are intended to initialise and terminate the declared variables, watches and timeouts. Buffers are initialised directly at the level of the marking and they are not terminated as they are allowed to keep their values between two executions of the scope. The net init(\(D\)) is defined as:

- it has a single unmarked entry place \(e_D\) such that \(\ell(e_D) \triangleq e(\bullet, o)\);\(\varepsilon\);
- it has a single unmarked exit place \(x_D\) such that \(\ell(x_D) \triangleq x(\bullet, o)\);\(\varepsilon\);
- it has a single transition \(t_D\) such that \(\ell(t_D) \triangleq \top\);\(\varepsilon\);
- there is one arc from \(e_D\) to \(t_D\) and one arc from \(t_D\) to \(x_D\) both labelled by \{\bullet\};
- for each \text{name} declared in \(D\) such that \text{name} \in \mathbb{I}_s and \text{env}(D)(\text{name}) = (x, y, z), there is one place \(s_{\text{name}}\) defined as:
  - if \(x \in \{v, b\}\) then \(\ell(s_{\text{name}}) \triangleq x y \text{name}\), otherwise \(\ell(s_{\text{name}}) \triangleq x \mathbb{N}^D \text{name}\).
  - if \(x = b\) then \(s_{\text{name}}\) is marked by \(z\), else if \(x \in \{w, t\}\) then \(s_{\text{name}}\) is marked by \{\(\omega\)\}, else it is unmarked,
  - if \(x \neq b\) then there is an arc from \(t_D\) to \(s_{\text{name}}\) labelled by \(z\),
  - if \(x \in \{w, t\}\) there is also an arc from \(s_{\text{name}}\) to \(t_D\) labelled by \{\(\omega\)\}.

The net term(\(D\)) is based on init(\(D\)) with the difference that the arcs attached to the places \(s_{\text{name}}\) are directed the other way and labelled with variables in order to restore the initial markings. More precisely: there is an arc from each \(s_{\text{name}}\) to \(t_D\) labelled by \{\text{var}(\text{name})\} if \(x \neq b\) and by \(\emptyset\) otherwise; if \(x \in \{w, t\}\) there is also an arc from \(t_D\) to \(s_{\text{name}}\) labelled by \{\(\omega\)\}. 

4.3.3. Resources

The semantics of a process with a data or task resource is defined as:
\[
\begin{align*}
\text{box}((\text{task} : S) \parallel P, E) & \equiv (\text{task} : \text{box}(S,E)) \parallel P \\parallel E \\
\text{box}(P, E + \text{env}(\langle \text{task} : S \rangle)) & \equiv \text{box}(P) \parallel \text{box}(E + \text{env}(\langle \text{task} : S \rangle))
\end{align*}
\]

where \(\text{env}(D)\) has been defined above and:
\[
\text{env}(\langle \text{task} : S \rangle) \equiv (\text{task}_{\text{enter}} \mapsto (b, b \times \{\circ, \bullet\}, \emptyset))
\]

Moreover, the nets for data resources are as follows:
- \(\text{box}(\text{variable name} : V := v, \emptyset)\) is composed of a single place labelled by \(v \in V\) name and marked by \(\{v\}\);
- \(\text{box}(\text{buffer name} : V := Z, \emptyset)\) is composed of a single place labelled by \(b \in b\) name and marked by \(\{Z\}\);
- \(\text{box}(\text{watch name} : \text{tick} := c, \emptyset)\), resp. \(\text{box}(\text{timeout name} : \text{tick} := c, \emptyset)\), is composed of a single place labelled by \(w \in w\) name, resp. by \(t \in t\) name, and marked by \(\{c\}\);
- \(\text{box}(\text{clock tick}, \emptyset)\) is an empty Petri net;
- \(\text{box}(D_1, D_2, \emptyset) \equiv \text{box}(D_1, \emptyset) \parallel \text{box}(D_2, \emptyset)\).

As defined in the previous section, the net operation \(\langle\text{task} : N\rangle\) involves a cross-product of \(P\) with the existing marking of \(N\). This potential explosion can be avoided at syntactic level by never nesting a watch, timeout or non-empty buffer declaration inside a task resource (the resulting net in this case has an empty marking).

4.3.4. Atomic action

The semantics of \((B \Rightarrow A)\) is mainly one anonymous transition guarded by \(B\) and surrounded by the data places corresponding to the current declarations. These places are connected to the transition in order to read the values needed by both the guard and the assignments and to write these values back or assign new ones appropriately. A buffer is treated differently: consulting its value consumes one of its tokens while assigning it adds the result of the assigned expression to the buffer. Finally, non-anonymous transitions are added to model the needed clock ticks.

More precisely, \(\text{box}(\langle B \Rightarrow A \rangle, E)\) is a labelled Petri net \(N \equiv (S, T, \ell)\) marked by \(M\) such that:
- there is a single entry place \(e \in S\) such that \(\ell(e) \equiv e \in \{\circ, \bullet\} \epsilon\) and \(M(e) \equiv \emptyset\);
- there is a single exit place \(x \in S\) such that \(\ell(x) \equiv x \in \{\circ, \bullet\} \epsilon\) and \(M(x) \equiv \emptyset\);
- there is a transition \(t \in T\) such that \(\ell(t) \equiv B \epsilon\);
- \(\ell(e,t) \equiv \{\bullet\}\) and \(\ell(t,x) \equiv \{\bullet\}\);
- for all \(\text{name} \in \mathbb{E}\) such that \(\text{name} \in E\), there is a place \(s_{\text{name}}\) in \(S\) such that:
  - if \(E(\text{name}) = (s, V, Z) \equiv \{v, b\} \times 2^2 \times \mathbb{N}\) then \(\ell(s_{\text{name}}) \equiv s \in V \text{ name and } M(s_{\text{name}}) \equiv \emptyset\);
  - if \(E(\text{name}) = (s, \text{name}, c) \equiv \{w, t\} \times \mathbb{I} \times \mathbb{C}\) then \(\ell(s_{\text{name}}) \equiv s \in \mathbb{C} \text{ name and } M(s_{\text{name}}) \equiv \{\omega\}\);
- for all \(\text{tick} \in \mathbb{I}\) such that \(\text{tick} \in E\), there is a transition \(s_{\text{tick}}\) in \(T\) such that \(\ell(s_{\text{tick}}) \equiv \top \text{ tick and, for all place } s_{\text{tick}}\) added to \(S\) at the previous step (as \(s_{\text{name}}\) with name = tick):
  - if \(E(\text{tick}) = (w, \text{tick}, v)\) then \(\ell(s_{\text{tick}}, \text{tick}) \equiv \{\text{var}(\text{tick})\}\) and \(\ell(t_{\text{tick}}, s_{\text{tick}}) \equiv \{\text{var}(\text{tick}) + 1\}\);
  - if \(E(\text{tick}) = (t, \text{tick}, v)\) then \(\ell(s_{\text{tick}}, \text{tick}) \equiv \{\text{var}(\text{tick})\}\) and \(\ell(t_{\text{tick}}, s_{\text{tick}}) \equiv \{\text{var}(\text{tick}) - 1\}\);
- for all \(\text{name} \notin \mathbb{E} \{\epsilon\}\) involved in \(A\) through an assignment \(\text{var}(\text{name}) := \text{expr}\) and such that \(E(\text{name}) \neq (b, V, Z)\):
  - if \(\text{name} \notin \mathbb{I}\) then \(\text{box fails as a clock can be used only through timeouts or watches},\)
  - if \(\text{name} \notin E\) then \(\text{box fails as name is used without being declared},\)
  - otherwise, \(\ell(s_{\text{name}}, t) \equiv \{\text{var}(\text{name})\}\) and \(\ell(t, s_{\text{name}}) \equiv \{\text{expr}\}\);
- for all other \(\text{name} \in \mathbb{I} \{\epsilon\}\) involved in \(A\) or in \(B\) and such that \(E(\text{name}) \neq (b, V, Z)\):
  - if \(E(\text{name}) \notin \mathbb{I}\) or name \(\notin E\) then \(\text{box fails},\)
  - otherwise, \(\ell(s_{\text{name}}, t) \equiv \{\text{var}(\text{name})\}\) and, \(\ell(t, s_{\text{name}}) \equiv \{\text{var}(\text{name})\}\);
- for all \(\text{name} \in \mathbb{I}\) involved in \(A\) or \(B\) such that if \(E(\text{name}) = (b, V, Z)\):
  - if name appears as \(\text{var}(\text{name}) := \text{expr}\) then \(\ell(t, s_{\text{name}}) \equiv \{\text{expr}\}\),
  - if \(E(\text{name})\) appears in \(B\) or in the right side of an assignment then \(\ell(s_{\text{name}}, t) \equiv \{\text{var}(\text{name})\}\);
- arcs not defined above are labelled by \(\emptyset\).

One can easily check that restrictions R1 to R3 are respected.

4.3.5. Interrupt action

\(\text{box}(\langle B \Rightarrow A \rangle, E)\) is built like \(\text{box}(B \Rightarrow A, E)\) except that the arc from transition \(t\) to the exit place is labelled by \(\{\circ\}\) instead of \(\{\bullet\}\).

5. CONCLUSION

We presented a new model of composable high-level Petri nets provided with a syntax that we called Versatile Boxes or v-boxes. This model unifies and improves various existing evolutions of the Petri Box Calculus.

(1) It features buffers as introduced in [3, 4]. Moreover, v-boxes distinguish multi-valued buffers from one-valued...
variables, the latter being more traditional in programming languages.

(2) As in [9], v-boxes are able to model timed systems. However, this new version separates watches from timeouts. It allows to remove the requirement in [9] that watches must be bounded in order to model timeouts. Moreover, v-boxes can have multiple clock ticks defined locally while [9] only allowed for one global clock.

(3) The parallel composition cannot be nested in v-boxes while this is possible in the other models. This allows a definition of a novel interrupt/trap mechanism much more efficient than the approach proposed in [7]. Moreover, this actually does not restrict the expressive power but only enforces a structured programming policy. Indeed the effect of power but only enforces a structured programming policy much more efficient than the approach proposed in [7].

(4) Unlike the other models, v-boxes do not propose a transition synchronisation operator. That would be absolutely no problem to have it as it could be directly borrowed from [4] or [9]. We simply felt no need for introducing it. Indeed, it has been possible to define the semantics of all the usual programming constructs without the help of a transition synchronisation. The simpler name-based transition merging scheme introduced in this paper is actually sufficient to ensure the required merges of tick transitions.

Theoretical properties and practical applications of the model are currently being investigated.

(1) Our first studies show that v-boxes basically have all the properties usually required, in particular: clean and safe markings under any evolution (see [3]). By the way, another advantage of removing nested parallelism is that we obtain 1-boundedness of control flow places, instead of 2-boundedness plus auto-concurrency free transitions as found in [3].

(2) Moreover, a prototype implementation is in progress and turns out to be not more difficult to realise than a previous prototype for CTC [10], despite the generalisation and the new features.

(3) Finally, we have already successfully translated to v-boxes significant constructs of the Ada programming language: exceptions, loops with multiple exits, tasks and sub-programs. For instance, an exit (i.e., break) statement can be modelled using an interrupt action with a trap after the loop. A simple example is given in figure 4. In general, there might be other reasons for executing an interrupt action, in particular the execution of a return statement or the occurrence of an exception. These cases can however be distinguished in the right part of each trap operation by consulting a global variable that is set when each interrupt action is executed.

Future works will address the mechanisms of object-oriented languages by adapting the approach proposed in [5]. Another direction will be to give a SOS semantics to the syntactic level of v-boxes and to prove its consistency with the Petri nets semantics.

REFERENCES


