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A Time-Of-Flight Gamma Camera Data Acquisition System for Hadrontherapy Monitoring

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Abstract— A collimated gamma camera coupled to a beam hodoscope is currently under development within the CLaRyS collaboration to provide ion-range verification during hadrontherapy. We propose a μ TCA-based data acquisition (DAQ) system that consists of dedicated front-end boards, a multi-optical DAQ board, a reference clock board and a computer. Several specific developments have been performed in the front-end boards, namely a TDC (time-to-digital converter) based system synchronization, an adjustable-depth measureddata buffer, an histogram-based fast time-of-flight estimation and an easy adaptation for different applications. All these DAQ developments have been recently tested with a reduced-size hodoscope and collimated camera in the Mediterranean Protontherapy Institute in Nice.

Index Terms—Collimated γ-Camera, DAQ, TDC, prompt gamma, hadrontherapy verification

I. INTRODUCTION

Several methods have been proposed to perform ion-range verification for Quality Assurance in hadrontherapy. Prompt gamma detection is one of them, and has lead to the development of two gamma camera prototypes, coupled with a beam hodoscope upstream of the patient (in a French research collaborative project involving IPNL Lyon, LPSC Grenoble, CPPM Marseille, CREATIS Lyon): a collimated camera (with multi-parallel slit collimator) and a Compton camera (CC)[1].

The hodoscope consists of two crossed arrays of scintillating

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fibers to allow for spatial and time tagging of incident ions. Spatial information is used for image reconstruction, and temporal information allowing time-of-flight (TOF) measurement serves to reduce neutron background and thus to enhance signal-to-noise ratio. The two gamma camera prototypes have been designed to employ the same absorber consisting of BGO (Bismuth Germanate) blocks.

Both prototypes require a data acquisition (DAQ) system with the main following specifications: 1 μ s trigger delay for Compton camera, time resolution better than 1 ns, ~100 and 500 kHz trigger rate for collimated camera and Compton camera respectively, and ~500 Mbits/s final storage rate.

We present here a DAQ system designed for this purpose. It has recently been tested with a reduced-size hodoscope and collimated camera in the Mediterranean Protontherapy Institute in Nice.

II. ARCHITECTURE AND OPERATION

The designed DAQ system mainly consists of the following parts (shown in Figure 1):

- Two 64-channel Hodoscope Front-End Readout (HFER) boards [2] for 64-channel Reduced-Size (RS) hodoscope,
- Six 24-channel absorber front-end readout boards (called ASM) [3] for thirty-six 4-pixel BGO block,
- A Micro Telecommunications Computing Architecture (μTCA)-based DAQ system mainly consisting of an Advanced Mezzanine Card board (AMC40) [2] and a PC for data acquisition, slow control and monitoring



• A 40-MHz reference clock and reset generator [2].

Figure 1. Scheme of the DAQ system.

A. System description

Both HFER and ASM boards (front-end boards) perform position determination and time of arrival (TOA) measurement. In addition, the ASM board allows signal charge measurement. The AMC40 board serves as data transfer station for every DAQ components except the clock-and-reset generator board. It connects to the front-end boards via a 3 Gbit/s signal-channel optical link (up to 36 links available) and connects to the PC through a 1 Gbits/s Ethernet link.

The DAQ system operates as follows: when a prompt gamma (PG) is detected in a BGO block, its associated ASM board issues a trigger signal to every HFER board for coincidence event searching. Coincidence events correspond to hodoscope and trigger events located within a TOF window centered on the PG TOA. The front-end boards send the data to the PC for data storage.

These boards incorporates several multi-phase time-to-digital converters (MPTDC): one for tagging the global reset signal and the other ones for tagging the detected signal. The final detected-signal's timestamp is obtained by its measured timestamp minus reset-signal's timestamp. Compared to conventional synchronization methods resetting coarse time which leads to potential meta-stability of one clock tick, this method can reduce reset error to 1 least-significant bit (LSB) of TDC.

B. Frond-end boards description

The front-end boards have the same basic structure, consisting of a FPGA, an optical transceiver, a clock jitter cleaner and a multi-channel analog readout module. The main difference between the two boards is that the former incorporates two 32-channel readout ASICs, and the later employs 8-channel DRS4 (Domino Ring Sampling) mezzanines (3 for each board).

In addition to the three MP-TDCs with 312.5 ps LSB, the firmware implementation of HFER board consists of a specific adjustable-depth buffer for the measured data and a histogram-based fast TOF estimation, which is adapted for different applications and eases fast system debugging. It includes also a coincidence-event searching, zero-suppression for useful data and NIOSII-based slow control.

Besides four MP-TDC with 625 ps LSB, the firmware implementation of ASM board also consists of a charge measurement as well as NIOSII-based slow control.

III. BEAM TESTS

The reduced-size multi-collimated camera system has been tested in September 2018, with the 65 MeV proton beam line. The setup consists of a RS hodoscope associated to one HFER board, six BGO absorber blocks associated to one ASM board, a PMMA target and the μ TCA readout system. The experiment was performed as follows: this proton passed through RS hodoscope and then hit a PMMA target where PG were emitted. The PG passing through the collimator were then absorbed by a BGO block.

Another beam-test has been performed in March 2019 to evaluate the performance of the HFER readout board. The main difference in respect to the previous setup is that this setup was composed of two plastic scintillators located upstream and downstream of the RS hodoscope to provide the trigger signal (coincidence of the two plastics).

IV. RESULTS AND DISCUSSION

For the first beam test, two different measurements with the target shifted by 1 cm along the beam direction were completed with a trigger rate ranging from 300 to 400 kHz. The final storage rate was about 200 Mbits/s after sending all the data to a PC. The mono-dimensional interaction position profiles along the beam direction are shown on Fig 2.a: the red curve corresponds to the initial position while the blue one corresponds to the shifted position. The maximum is closely related to the Bragg peak position, a few millimeters upstream. A shift of 2-3 bins is qualitatively visible between the two PG profiles. PG profiles will be improved by fine tuning the BGO blocks and ASM board parameters.



Figure 2: a) Mono-dimensional interaction position profile along the beam direction obtained for two acquisitions with the PMMA target set in two different positions; b) TOF spectrum obtained.

The TOF distributions (Fig 2.b) shows the beam time structure, with bunches delivered every 40 ns. Two bunches are clearly visible in the presented distribution, and a third one is cut by the end of the coincidence window. The main peak (close to 40 ns) corresponds to the bunch that has caused the emission of the PG inducing the trigger signal.

The second beam test was performed with several beam intensities resulting in data rate from 13 kHz to 30 MHz. The measured efficiency was about 75% for data rate lower than 13 MHz and slightly decreases down to 65% at 30 MHz.

V. CONCLUSION

The proposed DAQ system adopted several special techniques for an easy adaptation to different applications. The beam tests show that the system can operate with 312.5 ns LSB timing resolution, a storage rate of ~200 Mbits/s for a count rate of 400 kHz. Another beam test for the full version of the collimated camera coupled to a RS hodoscope is in preparation.

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