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Memoryless systems generate the class of all discrete systems

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Abstract

Automata are machines, which receive inputs, accordingly update their internal state, and produce output, are a common abstraction for the basic building blocks used in engineering and science to describe and design complex systems. These arbitrarily simple machines can be wired together—so that the output of one is passed to another as its input—to form more complex machines. Indeed, both modern computers and biological systems can be described in this way, as assemblies of transistors or assemblies of simple cells. The complexity is in the network, i.e., the connection patterns between simple machines. The main result of this paper is to show that the range of simplicity for parts as compared to the complexity for wholes is in some sense complete: the most complex automaton can be obtained by wiring together direct-output memoryless components. The model we use—discrete-time automata sending each other messages from a fixed set of possibilities—is certainly more appropriate for computer systems than for biological systems. However, the result leads one to wonder what might be the simplest sort of machines, broadly construed, that can be assembled to produce the behavior found in biological systems, including the brain.

Keywords: automata, category theory, discrete system, memoryless system, monoidal category, wiring diagram.

1 Introduction

Automata represent systems that receive inputs, alter their internal states, and produce outputs. The state set of the automaton is to be interpreted as the set of all potential memories, or storable experiences. In automata theory, the state set is typically finite. In this case, one can view this memory capacity as limited. On the contrary, when the memory of the automaton is not assumed to be limited (human brain), or its capacity can always be extended (RAM-machines or Turing machines as models of computers in computation theory), the automaton should have an infinite state set.

In the theory of dynamical systems, we use a generalisation of automata in which the size of the state space is not restricted to finiteness, or even to countability. Dynamical systems with the behaviour of an automaton, that is, taking inputs in discrete time, are called discrete systems. The state space of such a system acts as a sort of memory of the inputs. Each input influences the current state of the automaton, and the current state is the result of the system’s own form—how it deals with inputs—together with the system’s history.

One can imagine a dynamical system whose state space is that of all possible input-histories; a new input simply appends to the existing history to become a new history.

On the other hand, one can imagine the "opposite" kind of system: one that completely forgets the previous inputs. These systems are referred to as "simple-reflex" in [RN10, p. 49], reactive, or memoryless in this paper. The transitions of these automata depend only on the input, as no experience is stored. The system decides according to the current perception of the world, rather than current perception together with past perception. In fact, these memoryless systems could act by making a single distinction in the input—a yes/no Boolean response—and nothing more; we call these Boolean reactive systems.

In this paper, we will study the links between systems that have memory and those that do not. More precisely, we prove that systems with memory can be simulated by wiring together systems without memory. Our result provides a theoretical framework that supports artificial neural network approaches. Memory is carried by connections, and not only by individuals, within a compositional hierarchy of parts. In particular, feedback generates memory. This result is already known by electronics and computer scientists (transistors), but this article formally proves and generalises this intuitive result to any kind of automaton in discrete time, which takes any kind of inputs and returns any kind of outputs. As a special case, when the automata are boolean (which can compare to transistors), we generate the class of finite automata (which can compare to computers).

This article lies between two fields of mathematics: category theory and dynamical systems. Regarding category theory, we need no more than the basic definitions of categories, functors, natural transformations, (symmetric) monoidal categories, and monoidal functors. Regarding dynamical systems from a category-theoretic point of view, Section 2 recalls the notions of \mathcal{C} -typed-finite sets, \mathcal{C} -boxes, wiring diagrams and discrete systems inside a \mathcal{C} -box, with reference to [Spi16].

In Section 3, we introduce discrete systems and a specific mapping that will serve our purposes (Section 3.1). We then introduce two equivalence relations between discrete systems. Both are bigger than the usual bisimulation used in automata theory (in the sense of set inclusion). One corresponds to an external point of view; two systems are equivalent if they transform input streams into output streams in the same way (Section 3.2). The other relation corresponds to an internal point of view: two systems are equivalent if they have "the same structure" (in a sense that is defined in Section 3.3). We prove that these are just two perspectives on the same relation.

This equivalence relations plays a crucial role in the two results of Section 4. First, we show that any discrete system is equivalent to some wiring-together of memoryless systems (Section 4.2). Second, we show that any discrete system with a finite state set is equivalent to a combination of finitely many Boolean reactive systems (Section 4.3).

1.1 Notation

In this article, we will use the following notation.

- Let \mathbb{N} denote the set of all natural numbers, $\mathbb{N} := \{0, 1, 2, \dots\}$.
- By default, the variable n will refer to a natural number: $n \in \mathbb{N}$. We will also see the integers n as their set-theoretic counterparts, that is $0 = \emptyset$, and $n = \{0, 1, \dots, n-1\}$; in that context, $i \in n$ simply means $i \in \{0, \dots, n-1\}$. Note that the set n contains exactly n elements and this is what really matters in this notation.
- When the size of a sequence $(x_0, x_1, \dots, x_{n-1})$ does not matter, it will be denoted \bar{x} , which makes it easier to write and read. If each x_i is an element of the same set X , then we will write $\bar{x} \in X$, instead of defining an $n = \text{length}(\bar{x})$ and writing $\bar{x} \in X^n$. If $x_i \in X_i$ for possibly different X_i , and if there exists a compact notation \widehat{X} for $X_0 \times X_1 \times \dots \times X_{n-1}$, then we will write $\bar{x} \in \widehat{X}$ too.

- Sets is the usual category of sets.

2 Boxes and wiring diagrams

In this section, we will present the background necessary for the understanding of this paper, namely that of dynamical systems from a categorical point of view. It will be reduced to the absolute minimum used in this article. For more background on monoidal categories and functors, see [BW98] and [Bor94]. Our approach is different from the one in [BKP⁺17]. The dynamical systems presented here are defined as a generalisation of automata whose input and output spaces are predetermined. We will define a category of lists, a category of boxes, and diverse operations on them.

In this section, \mathcal{C} will be any category with finite products, that is: for any finite sequence $(A_i)_{i \in n} \in \mathcal{C}$, the product $\prod_{i \in n} A_i$ always exists (typically **Sets**). Most of the following notions were already defined in [Spi16]; we only recall them without proving their properties. Examples can be found in the longer version of this article [BPS18].

2.1 The category of typed finite sets

Before defining proper boxes, we need to define the notion of input and output ports. These will eventually be the sides of our boxes.

Definition 2.1 (Category of \mathcal{C} -typed finite sets [Spi16]). The category $\mathbf{TFS}_{\mathcal{C}}$ of \mathcal{C} -typed finite sets is defined as follows:

Objects: An object is any pair (P, τ) such that P is a finite set and $\tau : P \rightarrow \text{Ob}_{\mathcal{C}}$ is a function

Morphisms: A morphism from (P, τ) to (P', τ') is a function $\gamma : P \rightarrow P'$ such that $\tau = \tau' \circ \gamma$

Identities: The identity morphism on (P, τ) is the identity function of the set P

Composition: The composition of morphisms is the usual composition of functions

An object in $\mathbf{TFS}_{\mathcal{C}}$ is called a \mathcal{C} -typed finite set; a morphism in $\mathbf{TFS}_{\mathcal{C}}$ is called a \mathcal{C} -typed function.

We can rewrite a \mathcal{C} -typed finite set (P, τ) as the finite sequence $\langle \tau(p_0), \dots, \tau(p_{n-1}) \rangle$, where $P = \{p_0, \dots, p_{n-1}\}$. A \mathcal{C} -typed finite set is simply a list of objects in \mathcal{C} , indexed by a finite set P . If $\mathcal{C} = \mathbf{Sets}$, a **Sets**-typed finite set is a list of sets.

A \mathcal{C} -typed function $\gamma : (P, \tau) \rightarrow (P', \tau')$ can be then seen as a means to obtain the former list $\langle \tau(p_0), \dots, \tau(p_{n-1}) \rangle$ from the latter list $\langle \tau'(p'_0), \dots, \tau'(p'_{n-1}) \rangle$, by reordering, duplicating or even ignoring its elements. As $\tau = \tau' \circ \gamma$, the list $\langle \tau(p_0), \dots, \tau(p_{n-1}) \rangle$ can be rewritten $\langle \tau'(\gamma(p_0)), \dots, \tau'(\gamma(p_{n-1})) \rangle$. Beware of the inversion: γ goes from (P, τ) to (P', τ') and we see it as a transformation of the list (P', τ') into the list (P, τ) .

Definition 2.2 (Sum of typed finite sets [Spi16]). Let $(P_0, \tau_0), (P_1, \tau_1) \in \mathbf{TFS}_{\mathcal{C}}$ be two \mathcal{C} -typed finite sets.

We define their *sum* by $(P_0, \tau_0) + (P_1, \tau_1) = (P_0 + P_1, \tau_0 + \tau_1)$ as the usual disjoint union of sets $P_0 + P_1$ and $\tau_0 + \tau_1$ as τ_i on P_i for $i \in 2$.

Definition 2.3 (Sum of typed functions [Spi16]). Let $\gamma_i : (P_i, \tau_i) \rightarrow (P'_i, \tau'_i)$ ($i \in 2$) be two \mathcal{C} -typed functions.

We define their *sum* as the \mathcal{C} -typed function $\gamma_0 + \gamma_1 : (P_0, \tau_0) + (P_1, \tau_1) \rightarrow (P'_0, \tau'_0) + (P'_1, \tau'_1)$ such that $\forall x \in P_0 + P_1, (\gamma_0 + \gamma_1)(x) = \gamma_i(x)$ if $x \in P_i$ ($i \in 2$).

We can view the sum $(P, \tau) + (P', \tau')$ as the concatenation of the lists $\langle \tau(p_0), \dots, \tau(p_n) \rangle$ and $\langle \tau'(p'_0), \dots, \tau'(p'_n) \rangle$, that is, the list $\langle \tau(p_0), \dots, \tau(p_n), \tau'(p'_0), \dots, \tau'(p'_n) \rangle$, and the sum of \mathcal{C} -typed functions as a action on each part of the concatenated list.

Proposition 2.4. *The category $\mathbf{TFS}_{\mathcal{C}}$ has the following properties:*

- *The sum of \mathcal{C} -typed finite sets is a coproduct.*
- *There is only one \mathcal{C} -typed finite set (P, τ) where $P = \emptyset$. We denote it by 0.*
- *$\mathbf{TFS}_{\mathcal{C}}$ has a symmetric monoidal structure for the sum $+$, with 0 as the unit.*

Proof. See [Spi16]. □

2.2 Dependent products

In this subsection, we define the dependent product functor. If a \mathcal{C} -typed finite set can be viewed as a list of objects of \mathcal{C} , then the dependent product of this list is simply the product of its elements.

Definition 2.5 (Dependent product [Spi16]). We define the *dependent product* as the functor $\widehat{\cdot} : \mathbf{TFS}_{\mathcal{C}}^{\text{op}} \rightarrow \mathcal{C}$ such that:

Action on objects: $\widehat{(P, \tau)} = \prod_{p \in P} \tau(p)$

Action on morphisms: If $\gamma : (P, \tau) \rightarrow (P', \tau')$, then $\widehat{\gamma} : \widehat{(P', \tau')} \rightarrow \widehat{(P, \tau)}$ is defined as the function $\widehat{\gamma} : \prod_{p' \in P'} \tau'(p') \rightarrow \prod_{p \in P} \tau(p)$ such that $\forall (a_{p'})_{p' \in P'} \in \widehat{(P', \tau')}$, $\widehat{\gamma}((a_{p'})_{p' \in P'}) = (a_{\gamma(p)})_{p \in P}$.

The interpretation of the dependent product is actually quite straightforward: the dependent product of a \mathcal{C} -typed finite set, viewed as a list, is the product of the elements of the list in the same order as they appear in the list. The dependent product is thus a functor that packages the usual operations of diagonal $A \rightarrow A \times A$, projection $A \times B \rightarrow A$, and swapping $A \times B \rightarrow B \times A$.

We remind that \mathcal{C} has finite products; as a consequence, the dependent product always exists.

Proposition 2.6. *There is a natural isomorphism $\widehat{(P_0, \tau_0) + (P_1, \tau_1)} \cong \widehat{(P_0, \tau_0)} \times \widehat{(P_1, \tau_1)}$; in other words, the dependent product functor sends coproducts in $\mathbf{TFS}_{\mathcal{C}}$ to products in \mathcal{C} .*

Proof. See [Spi16]. □

This property is also quite intuitive: if one views the coproduct in $\mathbf{TFS}_{\mathcal{C}}$ as the concatenation of lists, and the dependent product as the product of the elements of the list, then the dependent product of the concatenation of two lists is the product of the dependent products of each lists.

2.3 The category of boxes and wiring diagrams

The category $\mathbf{TFS}_{\mathcal{C}}$ is not the main purpose of this article; however its properties will be useful for the rest of this article.

In the following, by abuse of notation, we will write $X \in \mathbf{TFS}_{\mathcal{C}}$ for (X, τ) , and \widehat{X} for (\widehat{X}, τ) .

Definition 2.7 (\mathcal{C} -box [Spi16]). We call \mathcal{C} -box any pair $X = (X^{\text{in}}, X^{\text{out}}) \in \mathbf{TFS}_{\mathcal{C}} \times \mathbf{TFS}_{\mathcal{C}}$.

A \mathcal{C} -box is a pair of \mathcal{C} -typed finite sets $(X^{\text{in}}, X^{\text{out}})$, where X^{in} represent the list of inputs ports, and X^{out} represent the list of outputs ports.

Definition 2.8 (Wiring diagram [Spi16]). Let $X = (X^{\text{in}}, X^{\text{out}})$ and $Y = (Y^{\text{in}}, Y^{\text{out}})$ be \mathcal{C} -boxes.

A wiring diagram $\varphi : X \rightarrow Y$ is a pair of \mathcal{C} -typed functions $(\varphi^{\text{in}}, \varphi^{\text{out}})$ such that:

- $\varphi^{\text{in}} : X^{\text{in}} \rightarrow Y^{\text{in}} + X^{\text{out}}$
- $\varphi^{\text{out}} : Y^{\text{out}} \rightarrow X^{\text{out}}$

The \mathcal{C} -typed function φ^{in} tells what feeds the input ports of the box X : each input port of X is either connected to an input port of Y or to an output port of X (in case of feedback); the \mathcal{C} -typed function φ^{out} tells what feeds the output ports of Y : each output port of Y is connected to some output port of X .

We can now compose the wiring diagrams:

Definition 2.9 (Composition of wiring diagrams [Spi16]). Let $\varphi : X \rightarrow Y$ and $\psi : Y \rightarrow Z$ be two wiring diagrams. We define their *composition*, denoted $\psi \circ \phi$, as the pair $((\psi \circ \phi)^{\text{in}}, (\psi \circ \phi)^{\text{out}})$, where $(\psi \circ \phi)^{\text{in}}$ is defined such that the following diagram commutes:

$$\begin{array}{ccc}
 X^{\text{in}} & \xrightarrow{(\psi \circ \phi)^{\text{in}}} & Z^{\text{in}} + X^{\text{out}} \\
 \varphi^{\text{in}} \downarrow & & \uparrow Z^{\text{in}} + \nabla_{X^{\text{out}}} \\
 Y^{\text{in}} + X^{\text{out}} & & \\
 \psi^{\text{in}} + \text{id}_{X^{\text{out}}} \downarrow & & \\
 Z^{\text{in}} + Y^{\text{out}} + X^{\text{out}} & \xrightarrow{\text{id}_{Z^{\text{in}} + \varphi^{\text{out}} + \text{id}_{X^{\text{out}}}}} & Z^{\text{in}} + X^{\text{out}} + X^{\text{out}}
 \end{array}$$

and $(\psi \circ \phi)^{\text{out}}$ is defined such that the following diagram commutes:

$$\begin{array}{ccc}
 Z^{\text{out}} & \xrightarrow{(\psi \circ \phi)^{\text{out}}} & X^{\text{out}} \\
 \psi^{\text{out}} \searrow & & \nearrow \varphi^{\text{out}} \\
 & Y^{\text{out}} &
 \end{array}$$

Definition 2.10 (Category of \mathcal{C} -boxes and wiring diagrams [Spi16]). The *category* $\mathcal{W}_{\mathcal{C}}$ of \mathcal{C} -boxes and wiring diagrams is defined as follows:

Objects: An object in $\mathcal{W}_{\mathcal{C}}$ is a \mathcal{C} -box

Morphisms: A morphism between two \mathcal{C} -boxes X and Y is a wiring diagram $\phi : X \rightarrow Y$

Identities: An identity morphism on X is the identity wiring diagram

Composition: The composition of wiring diagrams is the composition defined in definition 2.9

2.4 Monoidal structure of the category of boxes

The category $\mathcal{W}_{\mathcal{C}}$ has a monoidal structure for the parallel composition of boxes, that corresponds to the intuitive idea of parallellising boxes.

Definition 2.11 (Parallel composition of boxes [Spi16]). Let $X = (X^{\text{in}}, X^{\text{out}})$ and $Y = (Y^{\text{in}}, Y^{\text{out}})$ be two \mathcal{C} -boxes.

The *parallel composition*, or *sum*, of X and Y , denoted $X \boxplus Y$, is the box $X \boxplus Y = (X^{\text{in}} + Y^{\text{in}}, X^{\text{out}} + Y^{\text{out}})$, where $+$ is the sum of \mathcal{C} -typed finite sets (cf. Definition 2.2).

The parallel composition of two \mathcal{C} -boxes summarises to the concatenation of both input ports, and both output ports.

Definition 2.12 (Parallel composition of wiring diagrams [Spi16]). Let $\varphi : X \rightarrow Y = (\varphi^{\text{in}}, \varphi^{\text{out}})$ and $\psi : Y \rightarrow Z = (\psi^{\text{in}}, \psi^{\text{out}})$ be two wiring diagrams.

The *parallel composition*, or *sum*, of φ and ψ , denoted $\varphi \boxplus \psi$, is the wiring diagram $\varphi \boxplus \psi = (\varphi^{\text{in}} + \psi^{\text{in}}, \varphi^{\text{out}} + \psi^{\text{out}})$, where $+$ is the sum of \mathcal{C} -typed functions (cf. Definition 2.3).

Proposition 2.13. *The category $\mathcal{W}_{\mathcal{C}}$ has the following properties:*

- The closed box \square , defined as $\square = (0, 0)$, where 0 is \mathcal{C} -typed finite set $(\emptyset, \emptyset \rightarrow \mathcal{C})$ defined in 2.4, is the unit for the sum of boxes \boxplus .
- $\mathcal{W}_{\mathcal{C}}$ has a symmetric monoidal structure for the sum of boxes \boxplus , with \square as the unit.

Proof. See [Spi16]. □

2.5 Dependent product of boxes

The aim of this section is to extend the notion of dependent product (Definition 2.5) to \mathcal{C} -boxes and wiring diagrams.

Definition 2.14 (Dependent product of a \mathcal{C} -box [Spi16]). The *dependent product* \widehat{X} of the \mathcal{C} -box $X = (X^{\text{in}}, X^{\text{out}})$ is the pair $\widehat{X} = (\widehat{X}^{\text{in}}, \widehat{X}^{\text{out}})$.

Remark 2.15. The dependent product of $X_0 \boxplus X_1$ is $\widehat{X_0 \boxplus X_1} = (\widehat{X_0}^{\text{in}} \times \widehat{X_1}^{\text{in}}, \widehat{X_0}^{\text{out}} \times \widehat{X_1}^{\text{out}})$.

Definition 2.16 (Dependent product of wiring diagrams [Spi16]). The *dependent product* \widehat{X} of the wiring diagram $\varphi : X \rightarrow Y$ is the pair $\widehat{\varphi} = (\widehat{\varphi}^{\text{in}}, \widehat{\varphi}^{\text{out}})$.

Remark 2.17. The dependent product $\varphi_0 \boxplus \varphi_1$ is $\widehat{\varphi_0 \boxplus \varphi_1} = (\widehat{\varphi_0}^{\text{in}} \times \widehat{\varphi_1}^{\text{in}}, \widehat{\varphi_0}^{\text{out}} \times \widehat{\varphi_1}^{\text{out}})$.

Proposition 2.18. *Let $\varphi : X \rightarrow Y$ and $\psi : Y \rightarrow Z$. The dependent product of $\psi \circ \varphi$ is the pair $\widehat{\psi \circ \varphi} = (\widehat{(\psi \circ \varphi)}^{\text{in}}, \widehat{(\psi \circ \varphi)}^{\text{out}})$ where:*

$$\bullet \widehat{(\psi \circ \varphi)}^{\text{in}}(x, z) = \widehat{\varphi}^{\text{in}}\left(\widehat{\psi}^{\text{in}}\left(z, \widehat{\varphi}^{\text{out}}(x)\right), x\right)$$

$$\bullet \widehat{(\psi \circ \varphi)^{\text{out}}}(x) = \widehat{\psi^{\text{out}}}\left(\widehat{\varphi^{\text{out}}}(x)\right)$$

Proof. See [Spi16]. □

Remark 2.19. The dependent product of \mathcal{C} -boxes and wiring diagrams could be described in terms of monoidal functors; however the codomain of this functor is not $\mathcal{C} \times \mathcal{C}$ as expected, but a category that has the same objects (pairs of objects (A, B) of \mathcal{C}) but whose morphisms are pairs of morphisms $(\widehat{f^{\text{in}}}, \widehat{f^{\text{out}}}) : (A_0, B_0) \rightarrow (A_1, B_1)$ such that $\widehat{f^{\text{in}}}$ is the morphism $\widehat{f^{\text{in}}} : A_1 \times B_0 \rightarrow A_0$ in \mathcal{C} and $\widehat{f^{\text{out}}}$ is the morphism $\widehat{f^{\text{out}}} : B_0 \rightarrow B_1$ in \mathcal{C} . The composition law is the one given in Proposition 2.18.

Until now, we have only defined a category of \mathcal{C} -boxes, with interesting properties. These \mathcal{C} -boxes are exactly as their name suggests: empty boxes. The extension of the dependent product to \mathcal{C} -boxes is a necessary step in order to define the "inhabitants" of \mathcal{C} -boxes.

3 Discrete systems and their equivalences

In this section and in the rest of this paper, we will consider the special case where $\mathcal{C} = \mathbf{Sets}$. Thus, in general, we will simply call "boxes" what we introduced as "Sets-boxes". We denote the symmetric monoidal category of boxes as $\mathscr{W}_{\mathbf{Sets}}$.

3.1 Definition and basic properties

The notions introduced in this section come from [Spi16]. The properties stated here are proven in the same article.

Definition 3.1 (Discrete systems [Spi16]). Let $X = (X^{\text{in}}, X^{\text{out}}) \in \mathscr{W}_{\mathbf{Sets}}$ be a box.

A *discrete system for the box X* , or *discrete system for short*, is a 4-tuple $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ where:

- $S_F \in \mathbf{Sets}$ is the *state set* of F
- $f^{\text{rdt}} : S_F \rightarrow \widehat{X^{\text{out}}}$ is its *readout function*
- $f^{\text{upd}} : \widehat{X^{\text{in}}} \times S_F \rightarrow S_F$ is its *update function*
- $s_0 \in S_F$ is its *initial state*

We denote by $\text{DS}(X)$ the set of all discrete systems for the box X .

Remark 3.2. In Proposition 2.13, we defined the closed box $\square = (0, 0)$, where 0 denotes $(\emptyset, \tau : \emptyset \rightarrow \mathbf{Sets})$. Its dependent product is $\widehat{\square} = (\prod_{p \in \emptyset} \tau(p), \prod_{p \in \emptyset} \tau(p)) \cong (1', 1')$, where $1'$ is any typed finite set of the form $(1, \tau : 1 \rightarrow \mathbf{Sets})$. As a consequence, we have:

$$\text{DS}(\square) \cong \left\{ (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \mid S \in \mathbf{Sets}, f^{\text{rdt}} : S \rightarrow 1, f^{\text{upd}} : 1 \times S_F \rightarrow S_F, s_{F,0} \in S_F \right\}$$

In other words, an inhabitant of a closed box is a dynamical system with no inputs and no outputs, just a set S and a function $S \rightarrow S$.

Remark 3.3. From a set-theoretic point of view, $\text{DS}(X)$ is too big to be a set. A potential solution is to define the $\text{DS}(X)$ within a set big enough for our purposes; for example, the set V_{ω_1} from the von Neumann hierarchy of sets, which contains the usual sets,

vector spaces, measurable spaces, Hausdorff spaces, fields, etc. used in mathematics ($V_{\omega \times 2}$ suffices [Kun99, Lemma 2.9]).

In the following, we will continue to write $\text{DS}(X)$ (and similarly for mappings) with the state set in $S_F \in \mathbf{Sets}$ for the sake of understandability, but in case set-theoretic problems emerge, we should not write $S_F \in \mathbf{Sets}$ but $S_F \in V_{\omega_1}$.

Note that discrete systems can be viewed as a generalisation of automata. They have no final states, the transition function is always a function, i.e. all discrete systems are deterministic, the input alphabet can be infinite, and the transition function is always defined on every input and every state. Discrete systems are not automata that recognize a language, but rather, automata that take any input stream and return an output stream based on the states it transitioned to; that is, discrete systems are a generalisation of transducers as defined in [Sip12]. Alternatively, discrete systems exactly correspond to the *sequential automata* in [AT90].

We previously viewed general boxes (objects in $\mathscr{W}_{\mathbf{Sets}}$) as empty frames. Discrete systems are the objects that "live" inside. One can draw a parallel with programming: a \mathcal{C} -box is the signature of the function, that is, its accepted types of inputs and outputs, and the discrete system is the actual code of the function.

In the rest of the article, we will often represent a discrete system $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ as the following two-arrow graph:

$$F : \quad \widehat{X^{\text{in}}} \times S_F \xrightarrow{f^{\text{upd}}} S_F \xrightarrow{f^{\text{rdt}}} \widehat{X^{\text{out}}}$$

The first function describes how a state and an input are transformed into a new state; the second describes how the state is output, or "read out". In general, the initial state $s_{F,0} \in S_F$ will not be represented in these diagrams, though it is implicitly there.

Discrete systems are part of the more general class of *dynamical systems*. We can define other types of dynamical systems depending on the category \mathcal{C} that we are interested in. If \mathcal{C} is the category \mathbf{Euc} of Euclidean spaces, then we will refer to *continuous systems*. For more examples, see [Spi16].

Definition 3.4 (DS-application of a wiring diagram [Spi16]). Let $\varphi : X \rightarrow Y$ be a wiring diagram. Let $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X)$.

The *DS-application of φ to F* , denoted $\text{DS}(\varphi)(F)$, is the discrete system $\text{DS}(\varphi)(F) = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0}) \in \text{DS}(Y)$ such that:

- $S_G = S_F$
- $g^{\text{rdt}} : s \mapsto \widehat{\varphi^{\text{out}}}(f^{\text{rdt}}(s))$
- $g^{\text{upd}} : (y, s) \mapsto f^{\text{upd}}(\widehat{\varphi^{\text{in}}}(y, f^{\text{rdt}}(s)), s)$
- $s_{G,0} = s_{F,0}$

We can view $\text{DS}(\varphi)(F)$ as the discrete system we obtain from F by implementing the wiring diagram φ .

Definition 3.5 (Parallel composition of discrete systems [Spi16]). Let X_0, X_1 be boxes and let $F_i = (S_{F_i}, f_i^{\text{rdt}}, f_i^{\text{upd}}, s_{F_i,0}) \in \text{DS}(X_i)$ ($i \in 2$) be discrete systems.

The *parallel composition of F_0 and F_1* , denoted $F_0 \boxtimes F_1$, is the discrete system $(S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0}) \in \text{DS}(X_0 \boxplus X_1)$ such that:

- $S_G = S_{F_0} \times S_{F_1}$

- $s_{G,0} = (s_{F_0,0}, s_{F_1,0})$
- $g^{\text{rdt}} = f_0^{\text{rdt}} \times f_1^{\text{rdt}} : S_{F_0} \times S_{F_1} \rightarrow \widehat{X_0^{\text{out}}} \times \widehat{X_1^{\text{out}}}$
- $g^{\text{upd}} : \widehat{X_0^{\text{in}}} \times \widehat{X_1^{\text{in}}} \times S_{F_0} \times S_{F_1} \rightarrow S_{F_0} \times S_{F_1}$ makes the following diagram commute:

$$\begin{array}{ccc}
\widehat{X_0^{\text{in}}} \times \widehat{X_1^{\text{in}}} \times S_{F_0} \times S_{F_1} & \xrightarrow{g^{\text{upd}}} & S_{F_0} \times S_{F_1} \\
\downarrow \cong & & \downarrow = \\
\widehat{X_0^{\text{in}}} \times S_{F_0} \times \widehat{X_1^{\text{in}}} \times S_{F_1} & \xrightarrow{f_0^{\text{upd}} \times f_1^{\text{upd}}} & S_{F_0} \times S_{F_1}
\end{array}$$

We also define the parallel composition of $\text{DS}(X_0)$ and $\text{DS}(X_1)$, denoted $\text{DS}(X_0) \boxtimes \text{DS}(X_1)$, by:

$$\text{DS}(X_0) \boxtimes \text{DS}(X_1) = \{F_0 \boxtimes F_1 \mid F_0 \in \text{DS}(X_0), F_1 \in \text{DS}(X_1)\}.$$

Proposition 3.6. *Parallel composition $(F_0, F_1) \mapsto F_0 \boxtimes F_1$ provides a natural map $\text{DS}(X_0) \times \text{DS}(X_1) \rightarrow \text{DS}(X_0 \boxplus X_1)$.*

Proof. See [Spi16]. □

Theorem 3.7. *Definitions 3.1, 3.4, and 3.5 define a lax monoidal functor $\text{DS}: \mathcal{W}_{\text{Sets}} \rightarrow \text{Sets}$.*

Proof. See [Spi16] □

3.2 An external equivalence relation on dynamical systems

Via the monoidal functor DS , a box contains a specified sort of discrete system (depending on the ports of the box). For an exterior spectator, the content of the box does not matter; what matters is the way it transforms input streams to output streams. Thus, even if two boxes contain different discrete systems, for example one with an infinite state set, and the other with a finite state set, as long as they both give the same output in response to the same input, then they are viewed as "equivalent" from an external point of view.

The following definitions formalise this idea.

Definition 3.8 (Input and output streams). Let $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X)$.

An *input stream* (for X) is a finite sequence $\overline{x^{\text{in}}} = (x_i^{\text{in}})_{i \in \mathbb{N}} \in (\widehat{X^{\text{in}}})^n$, where $n \in \mathbb{N}$.

The *output stream produced by F when given $\overline{x^{\text{in}}}$* , denoted $F(\overline{x^{\text{in}}})$, is the stream $\overline{x^{\text{out}}}$ defined by the following recursive system:

$$\begin{cases}
s_0 & = s_{F,0} \\
s_{i+1} & = f^{\text{upd}}(x_i^{\text{in}}, s_i) \\
x_i^{\text{out}} & = f^{\text{rdt}}(s_{i+1})
\end{cases}$$

We refer to the state s that F reaches after having processed the input stream $\overline{x^{\text{in}}}$ as *resulting state of F* , and denote it $F_{\text{res}}(\overline{x^{\text{in}}})$. Formally, if $\overline{x^{\text{in}}} = (x_i^{\text{in}})_{i \in \mathbb{N}}$, then according to the previous recursive system, the resulting state of F is $F_{\text{res}}(\overline{x^{\text{in}}}) = s_n$.

Remark 3.9. According to the notation proposed in section 1.1, $\overline{x^{\text{in}}} = (x_i^{\text{in}})_{i \in \mathbb{N}} \in (\widehat{X^{\text{in}}})^n$ will be written $\overline{x^{\text{in}}} \in \widehat{X^{\text{in}}}$.

Remark 3.10. Definition 3.8 is a continuation of the definitions of *run maps* and *behaviours* in [AT90], which are functions that assign respectively the resulting state and the last output of the automaton given an input stream. The results we obtain with our notations are similar to those in [AT90].

Definition 3.11 (Equivalence as stream transducers). Let $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ and $G = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0})$ be two discrete systems.

We say that F and G are *equivalent as stream transducers*, and we write $F \equiv G$, when, $\forall \overline{x^{\text{in}}} \in \widehat{X^{\text{in}}}$, $F(\overline{x^{\text{in}}}) = G(\overline{x^{\text{in}}})$.

It is easy to see that:

Proposition 3.12. *The relation \equiv is an equivalence relation on the set $DS(X)$, for any box X .*

3.3 An internal equivalence relation on dynamical systems

The relation \equiv defined above does not give any information on the links between two discrete systems that are equivalent as stream transducers. In this subsection, we define another equivalence relation that provides an internal point of view. We then prove that the two equivalence relations are the same.

In the following, $X = (X^{\text{in}}, X^{\text{out}})$ is any box.

Definition 3.13 (Simulation relation). Suppose given $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ and $G = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0})$ in $DS(X)$.

We say that F *simulates* G , and we write $F \vdash G$, if there exists $\alpha : S_F \rightarrow S_G$ such that $s_{G,0} = \alpha(s_{F,0})$ and such that $\alpha \circ f^{\text{upd}} = g^{\text{upd}} \circ (\text{id}_{\widehat{X^{\text{in}}}, \alpha})$, and $f^{\text{rdt}} = g^{\text{rdt}} \circ \alpha$; that is, preserving the initial state and making the following two diagrams commute:

$$\begin{array}{ccccc}
 F : & \widehat{X^{\text{in}}} \times S_F & \xrightarrow{f^{\text{upd}}} & S_F & \xrightarrow{f^{\text{rdt}}} & \widehat{X^{\text{out}}} \\
 & \downarrow (\text{id}_{\widehat{X^{\text{in}}}, \alpha}) & & \downarrow \alpha & & \uparrow = \\
 G : & \widehat{X^{\text{in}}} \times S_G & \xrightarrow{g^{\text{upd}}} & S_G & \xrightarrow{g^{\text{rdt}}} & \widehat{X^{\text{out}}}
 \end{array} \tag{1}$$

We refer to α as a *simulation function*: it witnesses the simulation $F \vdash G$.

A priori, the simulation relation does not relate the output of the two discrete systems F and G (though this does follow; see Lemma 3.18); it only declares a correspondence between both their state sets and update and readout functions. Both discrete systems can work in parallel; their state sets need not be the same, nor even of the same cardinality, but they somehow *coordinate* via the map α . The function α draws the parallel between the internal machinery of F and that of G .

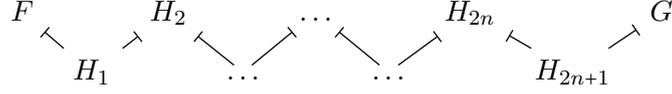
For the rest of the article, we will be more interested in the simulation relation $F \vdash G$ than any particular simulation function witnessing it: any one will do.

Remark 3.14. Definition 3.13 refers to the existence of morphisms between two automata as described in the automata theory litterature [AT90]. The existence of such morphisms suffices for our purposes. We are a bit more restrictive here, as the outputs need to be the same in both automata, while in the usual definition of morphisms, automata can have different output alphabets, as long as there is a function to convert one output into the other.

The simulation relation is not necessarily an equivalence relation and is not enough for our purpose, but we can use it to generate the equivalence relation we actually need.

Definition 3.15 (Internal equivalence relation on $DS(X)$). Let $F, G \in DS(X)$.

We say that F and G are *simulation-equivalent*, and we write $F \sim G$, if there exists a finite sequence $(H_i)_{i \in N} \in DS(X)$ such that:



It is not hard to check that:

Theorem 3.16. *The equivalence relation \sim is the equivalence relation generated by \vdash , that is, \sim is the smallest equivalence relation R such that $\vdash \subseteq R$.*

Finally, we need to show that the equivalence relation \sim actually groups discrete systems that have the same behaviour as a stream transducer, in the sense of Definition 3.11; that is, the external and the internal equivalence relation are the same.

Lemma 3.17. *Let $F, G \in DS(X)$. If $F \equiv G$ then $\exists H \in DS(X)$ such that $H \vdash F$ and $H \vdash G$.*

Proof. Let $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ and $G = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0})$.

Take $H = (S_H, h^{\text{rdt}}, h^{\text{upd}}, s_{H,0})$ such that:

- $S_H = \left\{ (s, s') \in S_F \times S_G \mid \exists \overline{x^{\text{in}}} \text{ such that } F_{\text{res}}(\overline{x^{\text{in}}}) = s \text{ and } G_{\text{res}}(\overline{x^{\text{in}}}) = s' \right\}$
- $h^{\text{upd}}(x, (s, s')) = (f^{\text{upd}}(x, s), g^{\text{upd}}(x, s'))$
- $h^{\text{rdt}}(s, s') = f^{\text{rdt}}(s)$
- $s_{H,0} = (s_{F,0}, s_{G,0})$

Take as simulation functions the respective projections π_{S_F} and π_{S_G} .

It is easy to see that the required diagrams in Definition 3.13 do commute. For all $(s, s') \in S_H$, we have $h^{\text{rdt}} \circ \pi_{S_F}(s, s') = f^{\text{rdt}}(s)$ by definition of h^{rdt} . Also, for all $(s, s') \in S_H$, $h^{\text{rdt}} \circ \pi_{S_G}(s, s') = f^{\text{rdt}}(s) = g^{\text{rdt}}(s')$ because there exists some stream $\overline{x^{\text{in}}}$ such that F results in s and G results in s' ; besides, as $F \equiv G$, we have $F(\overline{x^{\text{in}}}) = G(\overline{x^{\text{in}}})$, which implies $f^{\text{rdt}}(s) = g^{\text{rdt}}(s')$. Consequently, the diagrams commute and H simulates both F and G . □

Lemma 3.18. *Let $F, G \in DS(X)$. If $F \vdash G$ then $F \equiv G$.*

Proof. Follows by induction on the length of an input stream $(x_i^{\text{in}})_{i \in \mathbb{N}} \in \widehat{X^{\text{in}}}$. □

Theorem 3.19. $\forall F, G \in DS(X), F \equiv G \Leftrightarrow F \sim G$; or equivalently: $\equiv = \sim$.

Proof. To prove $\equiv = \sim$, we need $\equiv \subseteq \sim$ and $\sim \subseteq \equiv$.

Suppose first that $F, G \in DS(X)$ are dynamical systems such that $F \equiv G$. According to Lemma 3.17, there exists a $H \in DS(X)$ such that $H \vdash F$ and $H \vdash G$, and hence $F \sim G$ by Definition 3.15. This establishes $\equiv \subseteq \sim$.

We now show that $\sim \subseteq \equiv$. According to Proposition 3.12, \equiv is an equivalence relation, and according to Lemma 3.18, \equiv contains \vdash . Theorem 3.16 states that \sim is the smallest equivalence relation that contains \vdash ; necessarily, we have $\sim \subseteq \equiv$. □

The goal of this article is to show that the behaviour of a general discrete system can be emulated by some specific wiring of some other discrete system, chosen with constraints (for example, on its internal structure). As far as we know, this result cannot be obtained with a pure equality. However, we have a description of what it means to be equivalent, both from an internal and from an external point of view, with the assurance that, seen as a blackbox, the "inhabited" box remains unchanged.

As we are not using real equalities, we need to define relations between sets that correspond to the usual inclusion and equality.

Definition 3.20 (Inclusion/equality up to equivalence). Let $A, B \subseteq \text{DS}(X)$. We consider the equivalence relation \sim from Definition 3.15 (or, equivalently, in Definition 3.11).

We say that A is a subset of B up to equivalence, and we write $A \sqsubseteq B$, when $\forall a \in A, \exists b \in B, a \sim b$.

We say that A is equal to B up to equivalence, or A is equivalent to B , and we write $A \approx B$, when $A \sqsubseteq B$ and $B \sqsubseteq A$.

If $F, G : \mathscr{W}_{\text{Sets}} \rightarrow \text{Sets}$ are functors, then we write $F \sqsubseteq G$ when, for all box X , we have $F(X) \sqsubseteq G(X)$. We write $F \approx G$, when $F \sqsubseteq G$ and $G \sqsubseteq F$.

If $M, N : \mathscr{W}_{\text{Sets}} \rightarrow \text{Sets}$ are mappings (not necessarily functors), then we write $M \sqsubseteq N$ when, for all boxes X and Y , we have $M(X) \sqsubseteq N(X)$ and $\text{Mor}_{\text{Sets}}(M(X), M(Y)) \sqsubseteq \text{Mor}_{\text{Sets}}(N(X), N(Y))$.

4 Main results

Before we introduce the actual results of the paper, we need a few more notions.

4.1 Algebras and closures

Definition 4.1 (Algebra). Given a monoidal category \mathscr{C} , a functor $F : \mathscr{C} \rightarrow \text{Sets}$ is called an *algebra over \mathscr{C}* when it is a lax monoidal functor.

In our case, $\mathscr{C} = \mathscr{W}_{\text{Sets}}$, and DS is an algebra by Theorem 3.7.

Definition 4.2 (Subalgebra). Let $A : \mathscr{W}_{\text{Sets}} \rightarrow \text{Sets}$ be an algebra over $\mathscr{W}_{\text{Sets}}$. Let $\sigma_{X,Y} : A(X) \times A(Y) \rightarrow A(X \boxplus Y)$ denote its first coherence map (we recall that \boxplus is the parallel composition of boxes (cf. Definition 2.11)).

A functor $B : \mathscr{W}_{\text{Sets}} \rightarrow \text{Sets}$ is called a *subalgebra of A* when:

- $\forall X \in \mathscr{W}_{\text{Sets}}, B(X) \subseteq A(X)$
- $\forall X, Y \in \mathscr{W}_{\text{Sets}}, \forall F \in B(X), \forall G \in B(Y), \sigma_{X,Y}(F, G) \in B(X \boxplus Y)$
- $\forall \varphi : X \rightarrow Y \in \mathscr{W}_{\text{Sets}}, \forall F \in B(X), A(\varphi)(F) \in B(Y)$

Here, A and B are functors that transform boxes into sets. In our setting, the conditions can be interpreted as follows:

- (First item) Discrete systems generated by B are included in those generated by A ;
- (Second item) The parallel composition of two discrete systems F and G generated by B is also generated by B .
- (Third item) B is stable through wiring diagrams: wiring a discrete system generated by B gives another discrete system generated by B .

Note that a subalgebra is itself an algebra.

Definition 4.3 (Closure). Let $A : \mathcal{W}_{\mathbf{Sets}} \rightarrow \mathbf{Sets}$ be an algebra over $\mathcal{W}_{\mathbf{Sets}}$.

Let $B : \text{Ob}_{\mathcal{W}_{\mathbf{Sets}}} \rightarrow \text{Ob}_{\mathbf{Sets}}$ be any map such that $\forall X \in \mathcal{W}_{\mathbf{Sets}}, B(X) \subseteq A(X)$. The closure of B , denoted $\text{Clos}(B)$, is the intersection of all subalgebras of A that contain $B(X)$ for all $X \in \mathcal{W}_{\mathbf{Sets}}$. (Any intersection of subalgebras is a subalgebra.)

The closure of a map B can be understood as the minimal lax monoidal functor (or algebra) containing B .

4.2 Memoryless systems

Our first main result concerns the subclass of discrete systems that we call *memoryless*. We show that wiring together memoryless systems can lead to systems that have memory.

Definition 4.4 (Memoryless discrete systems). Let $X = (X^{\text{in}}, X^{\text{out}})$ be a box.

A *memoryless discrete system for the box X* , or *memoryless discrete system for short*, is a discrete system $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X)$ such that f^{upd} immediately discards the previous state and uses only the current input; more precisely, such that f^{upd} factors as

$$f^{\text{upd}} = \widehat{X^{\text{in}}} \times S_F \xrightarrow{\pi_{\widehat{X^{\text{in}}}}} \widehat{X^{\text{in}}} \xrightarrow{f^u} S_F$$

for some $f^u : \widehat{X^{\text{in}}} \rightarrow S_F$.

We denote by $\text{DS}^{\text{ML}}(X)$ the set of all memoryless discrete systems for the box X :
 $\text{DS}^{\text{ML}}(X) = \left\{ (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X) \mid \exists f^u : \widehat{X^{\text{in}}} \rightarrow S_F, f^{\text{upd}} = f^u \circ \pi_{\widehat{X^{\text{in}}}} \right\}$.

We call these discrete systems *memoryless* because we see the states as a kind of memory (as in Example ??). The discrete systems defined above transition from one state to another without checking their current state, i.e. without checking their memory.

The following definition is a natural restriction of memoryless discrete systems; as these systems are memoryless, the only goal of their states is to produce the output via their readout function. The simplest case is when the readout function is the identity.

Definition 4.5 (Direct-output discrete systems). Let $X = (X^{\text{in}}, X^{\text{out}})$ be a box.

A *direct-output memoryless discrete system for the box X* , or *direct-output discrete system for short*, is a discrete system $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X)$ such that:

- $S_F = \widehat{X^{\text{out}}}$
- $f^{\text{rdt}} = \text{id}_{\widehat{X^{\text{out}}}}$
- $f^{\text{upd}} = f^u \circ \pi_{\widehat{X^{\text{in}}}}$ for some $f^u : \widehat{X^{\text{in}}} \rightarrow \widehat{X^{\text{out}}}$

We denote by $\text{DS}_{\text{out}}^{\text{ML}}(X)$ the set of all direct-output discrete systems for the box X :

$$\begin{aligned} \text{DS}_{\text{out}}^{\text{ML}}(X) &= \left\{ (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}^{\text{ML}}(X) \mid S = \widehat{X^{\text{out}}}, f^{\text{rdt}} = \text{id}_{\widehat{X^{\text{out}}}} \right\} \\ &= \left\{ (\widehat{X^{\text{out}}}, \text{id}_{\widehat{X^{\text{out}}}}, f^{\text{upd}}, s_0) \in \text{DS}(X) \mid \exists f^u : \widehat{X^{\text{in}}} \rightarrow \widehat{X^{\text{out}}}, f^{\text{upd}} = f^u \circ \pi_{\widehat{X^{\text{in}}}} \right\} \end{aligned}$$

Remark 4.6. The maps $\text{DS}^{\text{ML}} : \mathcal{W}_{\mathbf{Sets}} \rightarrow \mathbf{Sets}$ and $\text{DS}_{\text{out}}^{\text{ML}} : \mathcal{W}_{\mathbf{Sets}} \rightarrow \mathbf{Sets}$ are not functors, because they are not closed under wiring. Indeed, the whole point is that the result of wiring together memoryless systems is not necessarily memoryless.

We can now prove one of the main results of this paper, which is that every discrete system can be obtained (up to equivalence) by a memoryless system and a feedback loop. The feedback loop is responsible for holding the state that was originally in the discrete system.

Here is the formal statement.

Theorem 4.7. $\text{Clos}(DS_{\text{out}}^{\text{ML}}) \approx DS$.

Proof. We have $DS_{\text{out}}^{\text{ML}} \subseteq DS^{\text{ML}} \subseteq DS$, so $\text{Clos}(DS_{\text{out}}^{\text{ML}}) \subseteq DS$, thus $\text{Clos}(DS_{\text{out}}^{\text{ML}}) \subseteq DS$. We need the opposite inclusion (up to equivalence) $\text{Clos}(DS_{\text{out}}^{\text{ML}}) \supseteq DS$.

Let $Y = (Y^{\text{in}}, Y^{\text{out}}) \in \mathcal{W}_{\text{Sets}}$, and let $G = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0}) \in DS(Y)$. We will find $X \in \mathcal{W}_{\text{Sets}}$, $F \in DS_{\text{out}}^{\text{ML}}(X)$ and $\varphi : X \rightarrow Y$ such that $DS(\varphi)(F) \sim G$.

Let $\delta_{S_G} = \langle S_G \rangle \in \mathbf{TFS}_{\text{Sets}}$ be the list with one element, S_G , and consider the box $(\delta_{S_G}, \delta_{S_G})$ with only that port on the left and the right. We define X as the parallel composition of this box $(\delta_{S_G}, \delta_{S_G})$ and Y , that is:

$$\begin{aligned} X &= (X^{\text{in}}, X^{\text{out}}) \\ &= (\delta_{S_G}, \delta_{S_G}) \boxplus Y \\ &= (\delta_{S_G} + Y^{\text{in}}, \delta_{S_G} + Y^{\text{out}}) \end{aligned}$$

Note that $\widehat{X^{\text{in}}} = S_G \times \widehat{Y^{\text{in}}}$ and $\widehat{X^{\text{out}}} = S_G \times \widehat{Y^{\text{out}}}$. Thus, if $\overline{x^{\text{in}}} \in \widehat{X^{\text{in}}}$, then $\overline{x^{\text{in}}} = (s, \overline{y^{\text{in}}})$. Similarly, if $\overline{x^{\text{out}}} \in \widehat{X^{\text{out}}}$, then $\overline{x^{\text{out}}} = (s, \overline{y^{\text{out}}})$.

We choose $\varphi : X \rightarrow Y$ as the pair $(\varphi^{\text{in}}, \varphi^{\text{out}})$ of coproduct inclusions:

$$\begin{aligned} \bullet \varphi^{\text{in}} : & \begin{cases} \delta_{S_G} + Y^{\text{in}} & \longrightarrow & Y^{\text{in}} + \delta_{S_G} + Y^{\text{out}} \\ x & \longmapsto & x \end{cases} \\ \bullet \varphi^{\text{out}} : & \begin{cases} Y^{\text{out}} & \longrightarrow & \delta_{S_G} + Y^{\text{out}} \\ x & \longmapsto & x \end{cases} \end{aligned}$$

It follows from 2.16 that their dependent products $\widehat{\varphi^{\text{in}}} : \widehat{Y^{\text{in}}} \times S_G \times \widehat{Y^{\text{out}}} \rightarrow S_G \times \widehat{Y^{\text{in}}}$ and $\widehat{\varphi^{\text{out}}} : S_G \times \widehat{Y^{\text{out}}} \rightarrow \widehat{Y^{\text{out}}}$ are projections.

Recall that the goal is to find $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in DS_{\text{out}}^{\text{ML}}(X)$ such that $DS(\varphi)(F) \sim G$. So define F as follows:

$$\begin{aligned} \bullet S_F &= \widehat{X^{\text{out}}} = S_G \times \widehat{Y^{\text{out}}} \\ \bullet f^{\text{rdt}} &= \text{id}_{\widehat{X^{\text{out}}}} \\ \bullet f^{\text{upd}}(\overline{x^{\text{in}}}, \overline{x^{\text{out}}}) &= f^{\text{upd}}(s, \overline{y^{\text{in}}}, s', \overline{y^{\text{out}}}) = (g^{\text{upd}}(\overline{y^{\text{in}}}, s), g^{\text{rdt}}(g^{\text{upd}}(\overline{y^{\text{in}}}, s))) \\ \bullet s_{F,0} &= (s_{G,0}, g^{\text{rdt}}(s_{G,0})) \end{aligned}$$

It is easy to see that F is in $DS_{\text{out}}^{\text{ML}}(X)$ because f^{rdt} and f^{upd} have the correct form. So let $(S_H, h^{\text{rdt}}, h^{\text{upd}}, s_{H,0}) = DS(\varphi)(F)$; we need to show it is equivalent to G . We compute each part of $DS(\varphi)(F)$ according to Definition 3.4.

Its state set is as follows:

$$S_H = S_F = \widehat{X^{\text{out}}} = S_G \times \widehat{Y^{\text{out}}}$$

Its readout function is defined on an arbitrary x^{out} as follows:

$$\begin{aligned} h^{\text{rdt}}(\overline{x^{\text{out}}}) &= h^{\text{rdt}}(s, \overline{y^{\text{out}}}) \\ &= \widehat{\varphi^{\text{out}}}(f^{\text{rdt}}(s, \overline{y^{\text{out}}})) \\ &= \pi_{\widehat{Y^{\text{out}}}}(\widehat{\text{id}_{X^{\text{out}}}}(s, \overline{y^{\text{out}}})) \\ &= \overline{y^{\text{out}}} \end{aligned}$$

Its update function is defined on an arbitrary $(\overline{y^{\text{in}}}, s, \overline{y^{\text{out}}})$ as follows:

$$\begin{aligned} h^{\text{upd}}(\overline{y^{\text{in}}}, s, \overline{y^{\text{out}}}) &= f^{\text{upd}}(\widehat{\varphi^{\text{in}}}(\overline{y^{\text{in}}}, f^{\text{rdt}}(s, \overline{y^{\text{out}}})) , (s, \overline{y^{\text{out}}})) \\ &= f^{\text{upd}}(s, \overline{y^{\text{in}}}, s, \overline{y^{\text{out}}}) \\ &= (g^{\text{upd}}(\overline{y^{\text{in}}}, s), g^{\text{rdt}}(g^{\text{upd}}(\overline{y^{\text{in}}}, s))) \end{aligned}$$

Finally, its start state is as follows:

$$s_{F,0} = (s_{G,0}, g^{\text{rdt}}(s_{G,0}))$$

Consequently, the following diagram commutes:

$$\begin{array}{ccccc} G : & \widehat{Y^{\text{in}}} \times S_G & \xrightarrow{g^{\text{upd}}} & S_G & \xrightarrow{g^{\text{rdt}}} & \widehat{Y^{\text{out}}} \\ & \downarrow \widehat{\text{id}_{X^{\text{in}}} \times \alpha} & & \downarrow \alpha & & \uparrow = \\ \text{DS}(\varphi)(F) : & \widehat{Y^{\text{in}}} \times S_G \times \widehat{Y^{\text{out}}} & \xrightarrow{h^{\text{upd}}} & S_G \times \widehat{Y^{\text{out}}} & \xrightarrow{h^{\text{rdt}}} & \widehat{Y^{\text{out}}} \end{array}$$

where $\alpha = (\text{id}_{S_G}, g^{\text{rdt}})$. This yields $G \vdash \text{DS}(\varphi)(F)$ and hence $\text{DS}(\varphi)(F) \sim G$, which concludes the proof. \square

Corollary 4.8. $\text{Clos}(DS^{\text{ML}}) \approx DS$.

Corollary 4.9. For all $G \in DS(Y)$, if G has finite state set, then there exists $H \in \text{Clos}(DS^{\text{ML}})(Y)$ with finite state set such that $H \sim G$.

Proof. In the proof of Theorem 4.7, take $H = \text{DS}(\varphi)(F)$, but instead of $S_F = S_G \times \widehat{Y^{\text{out}}}$, take $S_F = S_G \times g^{\text{rdt}}(S_G) \subseteq S_G \times \widehat{Y^{\text{out}}}$. If S_G is finite, so is S_F .

In that case, H is no more in $\text{Clos}(DS_{\text{out}}^{\text{ML}})(Y)$ but in $\text{Clos}(DS^{\text{ML}})(Y)$. \square

Corollary 4.10. (Assuming the axiom of choice) For all $G \in DS(Y)$, if G has an infinite state set, then there exists $H \in \text{Clos}(DS^{\text{ML}})(Y)$ with a state set of the same cardinality as G , such that $H \sim G$.

Proof. In the proof of Theorem 4.7, take $H = \text{DS}(\varphi)(F)$, but instead of $S_F = S_G \times \widehat{Y^{\text{out}}}$, take $S_F = S_G \times g^{\text{rdt}}(S_G) \subseteq S_G \times \widehat{Y^{\text{out}}}$. The axiom of choice gives $\text{card}(S_F) = \text{card}(S_G) \times \text{card}(g^{\text{rdt}}(S_G)) = \text{card}(S_G)$.

In that case, H is no more in $\text{Clos}(DS_{\text{out}}^{\text{ML}})(Y)$ but in $\text{Clos}(DS^{\text{ML}})(Y)$. \square

Theorem 4.7 states that systems without memory can be wired together to form systems with memory. In fact, the result is more subtle. It states that for any discrete system, we can find (or build) a memoryless discrete system with the certain wiring such that both systems are equivalent as stream transducers. The internal equivalence relation described in Theorem 3.19 is instrumental to prove Theorem 4.7, while the result is stated with regard to the external equivalence relation.

4.3 Finite-state systems

The second result is a refinement of Theorem 4.7, and is somewhat similar to it. We show that wiring together two-state discrete systems can generate a finite-state system with memory.

We can view the result as the generalisation of transistors being wired together in order to build a computer, or a system of neurons wired together to form a brain with finite memory.

Definition 4.11 (Finite-state systems). Let $X = (X^{\text{in}}, X^{\text{out}})$ be a box.

A *finite-state discrete system for the box X* , or *finite-state system for short*, is a discrete system $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X)$ such that S_F is a finite set.

We denote by $\text{DS}_{\text{Fin}}(X)$ the set of all finite-state discrete systems for the box X : $\text{DS}_{\text{Fin}}(X) = \{(S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}(X) \mid \text{card}(S_F) \in \mathbb{N}\}$. For a wiring diagram ϕ , we set $\text{DS}_{\text{Fin}}(\phi) = \text{DS}(\phi)$.

It is easy to see that:

Proposition 4.12. *The map $\text{DS}_{\text{Fin}} : \mathcal{W}_{\text{Sets}} \rightarrow \text{Sets}$ is a subalgebra of DS .*

Proof. Follows from Definition 4.2. □

Definition 4.13 (Boolean systems). Let $X = (X^{\text{in}}, X^{\text{out}})$ be a box.

A *boolean memoryless discrete system for the box X* , or *boolean system for short*, is a discrete system $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}_{\text{Fin}}(X)$ such that F is memoryless and $S_F = 2^n = \{0, 1\}^n$.

We denote by $\text{DS}_{\text{Bool}}^{\text{ML}}(X)$ the set of all boolean memoryless discrete systems for the box X : $\text{DS}_{\text{Bool}}^{\text{ML}}(X) = \{(S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0}) \in \text{DS}_{\text{Fin}}^{\text{ML}}(X) \mid S_F = \{0, 1\}^n\}$.

Remark 4.14. The map $\text{DS}_{\text{Bool}}^{\text{ML}} : \mathcal{W}_{\text{Sets}} \rightarrow \text{Sets}$ is not a functor, for the same reason as in remark 4.6.

Lemma 4.15. $\text{DS}_{\text{Bool}}^{\text{ML}} \approx \text{DS}_{\text{ML}} \cap \text{DS}_{\text{Fin}}$.

Proof. By construction, $\text{DS}_{\text{Bool}}^{\text{ML}} \subseteq \text{DS}_{\text{ML}} \cap \text{DS}_{\text{Fin}}$, so $\text{DS}_{\text{Bool}}^{\text{ML}} \subseteq \text{DS}_{\text{ML}} \cap \text{DS}_{\text{Fin}}$. We need to show the other inclusion, so let $G = (S_G, g^{\text{rdt}}, g^{\text{upd}}, s_{G,0}) \in \text{DS}_{\text{ML}}(X) \cap \text{DS}_{\text{Fin}}(X)$, and it suffices to show that there is $F \in \text{DS}_{\text{Bool}}^{\text{ML}}(X)$ with $G \sim F$.

We have $g^{\text{upd}} = g^u \circ \pi_{\overline{X^{\text{in}}}}$ and S_G finite. Let $N = \lceil \log_2(\text{card}(S_G)) \rceil$. There exists an injection $i : S_G \rightarrow 2^N$ and a surjection $p : 2^N \rightarrow S_G$ such that $p \circ i = \text{id}_{S_G}$. This is just a binary encoding of S_G .

Define $F = (S_F, f^{\text{rdt}}, f^{\text{upd}}, s_{F,0})$ such that:

- $S_F = 2^N$

- $f^{\text{rdt}} = g^{\text{rdt}} \circ p$
- $f^{\text{upd}} = g^{\text{upd}} \circ p$
- $s_{F,0} = i(s_{G,0})$

Then the following diagram commutes:

$$\begin{array}{ccccc}
G : & \widehat{Y}^{\text{in}} \times S_G & \xrightarrow{g^{\text{upd}}} & S_G & \xrightarrow{g^{\text{rdt}}} & \widehat{X}^{\text{out}} \\
& \text{id}_{\widehat{X}^{\text{in}}} \times i \downarrow & & \downarrow i & & \uparrow = \\
F : & \widehat{X}^{\text{in}} \times 2^N & \xrightarrow{f^{\text{upd}}} & 2^N & \xrightarrow{f^{\text{rdt}}} & \widehat{X}^{\text{out}}
\end{array}$$

We have $F \in \text{DS}_{\text{Bool}}^{\text{ML}}(X)$ and $G \vdash F$ (with i as simulation function), so $F \sim G$, hence the result. \square

Lemma 4.16. $\text{DS}_{\text{Fin}} \approx \text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}}$.

Proof. Observe that $\text{DS}_{\text{Fin}} = \text{DS} \cap \text{DS}_{\text{Fin}}$. By Corollary 4.8, we have $\text{DS} \approx \text{Clos}(\text{DS}^{\text{ML}})$. In particular, $\text{Clos}(\text{DS}^{\text{ML}}) \subseteq \text{DS}$, so $\text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}} \subseteq \text{DS} \cap \text{DS}_{\text{Fin}}$. This gives one inclusion, $\text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}} \subseteq \text{DS} \cap \text{DS}_{\text{Fin}}$.

As for the reverse inclusion (up to equivalence), let X be a box and let $F \in (\text{DS} \cap \text{DS}_{\text{Fin}})(X)$. By Corollary 4.8, there exists $G \in \text{Clos}(\text{DS}^{\text{ML}})$ such that $G \sim F$. By Corollary 4.9, we can choose G so that $G \in \text{DS}_{\text{Fin}}(X)$, hence the result. \square

Theorem 4.17. $\text{Clos}(\text{DS}_{\text{Bool}}^{\text{ML}}) \approx \text{DS}_{\text{Fin}}$.

Proof. Clearly, $\text{Clos}(\text{DS}_{\text{Bool}}^{\text{ML}}) \subseteq \text{DS}_{\text{Fin}}$. By Lemma 4.15, in order to prove $\text{DS}_{\text{Fin}} \subseteq \text{Clos}(\text{DS}_{\text{Bool}}^{\text{ML}})$, it suffices to prove $\text{DS}_{\text{Fin}} \subseteq \text{Clos}(\text{DS}^{\text{ML}} \cap \text{DS}_{\text{Fin}})$. Furthermore, since $\text{DS}_{\text{Fin}} \approx \text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}}$ (Lemma 4.16), we reduce to proving that: $\text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}} \subseteq \text{Clos}(\text{DS}^{\text{ML}} \cap \text{DS}_{\text{Fin}})$.

Let $Y \in \mathscr{W}_{\text{Sets}}$ be a box, and let $G \in (\text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}})(Y)$. We have $G \in \text{Clos}(\text{DS}^{\text{ML}})(Y)$, so according to Corollary 4.8, there exist a box $X \in \mathscr{W}_{\text{Sets}}$, a wiring diagram $\varphi : X \rightarrow Y$, and a $F \in \text{DS}^{\text{ML}}(X)$ such that $\text{DS}(\varphi)(F) \sim G$. Furthermore, according to Corollary 4.9, we can choose F with finite state set. Finally, $F \in (\text{DS}^{\text{ML}} \cap \text{DS}_{\text{Fin}})(X)$, and $\text{DS}(\varphi)(F) \sim G$, so $\text{Clos}(\text{DS}^{\text{ML}}) \cap \text{DS}_{\text{Fin}} \subseteq \text{Clos}(\text{DS}^{\text{ML}} \cap \text{DS}_{\text{Fin}})$, hence the result. \square

5 Conclusion

Boxes are empty frames that condition the inputs and outputs of their content, a generalisation of automata called discrete systems. Such systems come with a state set that represents their memory of previous inputs. In a sense, discrete systems can learn. However, we can define a subclass of discrete systems that do not store any experience of their past. We see these as reactive, in the sense that they still react to any input, but their past experience does not influence that reaction. Unlike typical discrete systems, they do not keep a memory of the previous inputs.

In this paper, we use a category-theoretic framework to give a constructive proof that any discrete system with memory can be simulated by some correctly-wired memoryless

system. This result can be understood as a phenomenon of emergence in a complex system.

This construction opens a number of new questions. A possible question might consist in finding the "best" memoryless system, where "best" could depend on the definition of some valuation function, e.g. the most parsimonious in terms of state set. A similar question could be asked with respect to wiring diagrams, whose number of feedback loops could be bounded by a cost function.

Possible extensions of this work could concern dynamical systems other than DS. For instance, can we establish the same kind of results when considering measurable or continuous dynamical systems?

6 Data availability

No data were used to support this study.

7 Conflict of interest

The authors declare that there is no conflict of interest regarding the publication of this paper.

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