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Characterization of Zero-Bias Microwave Diode Power Detectors at Cryogenic Temperature.

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We present the characterization of commercial tunnel diode low-level microwave power detectors at room and cryogenic temperatures. The sensitivity as well as the output voltage noise of the tunnel diodes are measured as functions of the applied microwave power. We highlight strong variations of the diode characteristics when the applied microwave power is higher than few microwatt. For a diode operating at 4 K, the differential gain increases from 1,000 V/W to about 4,500 V/W when the power passes from -30 dBm to -20 dBm. The diode white noise floor is equivalent to a Noise Equivalent Power of 0.8 pW/ $\sqrt{\text{Hz}}$ and 8 pW/ $\sqrt{\text{Hz}}$ at 4 K and 300 K respectively. Its flicker noise is equivalent to a relative amplitude noise power spectral density $S_{\alpha}(1 \text{ Hz}) = -120$ dB/Hz at 4 K. Flicker noise is 10 dB higher at room temperature.

Keywords: Tunnel diode, backward diode, power measurement, AM noise.

I. INTRODUCTION

Tunnel diodes are known to be very efficient low-level microwave power detectors. They are available off-the-shelf from numerous manufacturers in the form of small packaged coaxial components that can be easily implemented in any microwave system. They are used in power, Amplitude Modulation (AM) noise measurement, control and monitoring systems. Tunnel detectors are actually backward detectors. The backward diode is a tunnel diode in which the negative resistance in the forward-bias region is made negligible by appropriate doping, and used in the reverse-bias region. Most of the work on such detectors dates back to the sixties¹⁻³. Tunnel detectors exhibit fast switching and higher gain than the Schottky counterpart. Tunnel diodes also work in cryogenic environment, provided the package tolerates the mechanical stress of the thermal contraction. Looking at the specifications of commercial power detectors, information about noise is scarce. Some manufacturers give the NEP (Noise Equivalent Power) parameter, i.e., the power at the detector input that produces a video output equal to that of the device noise. In no case is said whether the NEP increases or not in the presence of a strong input signal, which is related to precision. Even worse, no data about flickering is found in the literature or in the data sheets.

In this paper we present a characterization of commercial tunnel diodes (Model Herotek DT8012). Such diodes are implemented in our 10 GHz ultra-stable Cryogenic Sapphire Oscillators (CSO)^{4,5}. The CSO is currently the most stable microwave source achieving better than 1×10^{-15} fractional frequency stability at

short term⁶. The CSO is a Pound-Galani oscillator⁷. In short, the resonator is used in transmission mode in a regular oscillator loop, and in reflection mode as the discriminator of the classical Pound servo⁸. The CSO frequency is affected by the power fluctuation of the microwave signal incident on the sapphire resonator^{9,10}. A power control needs to be implemented to reach the best possible frequency stability. Two tunnel diodes are used as low-level detectors to detect the small changes in the phase and the power of the signal in the oscillating loop. In this way they are key components as their sensitivity conditions the CSO frequency stability. In this application the measurement bandwidth is limited to 100 kHz, we thus do not explore the switching capability of the tunnel diode and restrict our characterisation to the low frequency sensitivity.

Firstly, we introduce the topic of AM noise measurement. The tested diodes specifications and the corresponding electrical model are given in section III. Then we describe the experimental set-up and the power-to-voltage gain measurements (section IV). The principle and the results of the diode intrinsic noise measurements are presented in section V.

II. AM NOISE MEASUREMENT BASICS

Our diode detectors are intended to measure the small power fluctuations affecting the signal of an ultra-stable oscillator. Considering only AM noise the RF voltage is:

$$V_{RF}(t) = V_0 [1 + \alpha(t)] \cos(2\pi\nu_0 t) \quad (1)$$

where V_0 is the RF voltage mean amplitude, ν_0 is the signal frequency and $\alpha(t)$ is the fractional amplitude fluctuation.

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tuation. In low noise conditions, we have $|\alpha(t)| \ll 1$ and the instantaneous signal power is:

$$P(t) = P_0 + \delta P(t) \approx \frac{V_0^2}{2R_0} (1 + 2\alpha(t)) \quad (2)$$

where $R_0 = 50 \Omega$, P_0 is the mean signal power. The amplitude fluctuations are measured through the measurement of the power fluctuation $\delta P(t)$,

$$\alpha(t) = \frac{1}{2} \frac{\delta P(t)}{P_0} \quad (3)$$

and of its Power Spectrum Density (PSD),

$$S_\alpha(f) = \frac{1}{4} S_{\frac{\delta P}{P_0}}(f) = \frac{1}{4P_0^2} S_{\delta P}(f) \quad (4)$$

The basic implementation of a zero-bias microwave power detector is represented in Fig. 1.

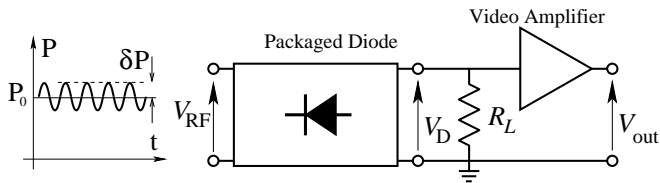


FIG. 1. The basic implementation of a zero-bias microwave power detector.

The RF voltage is applied to the diode input terminal. The video output is loaded by the video amplifier input impedance R_L . A perfect square-law detector will produce a voltage proportional to P :

$$V_D(P) = K_D P \quad (5)$$

K_D is the detector power-to-voltage gain expressed in V/W, also called *sensitivity* in most the data-sheets. Manufacturers generally specify K_D measured at room temperature and for an input signal power of -20 dBm or so. Typically for a X-band diode, $K_D \approx 1,000$ V/W.

It is generally admitted that equation 5 holds for a signal power below -20 dBm and then the detector response turns smoothly from quadratic to linear. At high signal power, typically above 0 dBm, the diode is acting as a peak detector. This traditionally-accepted view of the diode detector leads to expect a sensitivity K_D almost constant below -20 dBm and then saturating smoothly when the signal power is increased like in¹¹. As we will see in the following this simple model is not sufficient to describe the diode behavior on its whole dynamic range. In most cases, the diode output voltage should be written as a non-linear function $\kappa_D(P)$ of the input power. In radiometric measurements the detector transfer function should be known in its whole dynamic range. Various

methods are used to account for the nonlinear detector response^{16–18}. In the case of AM noise measurement or control, we are only interested in small power fluctuations δP around the mean value P_0 , with $\delta P \ll P_0$. Keeping only the first order term in the Taylor development of the function $\kappa_D(P)$:

$$V_D(P) = \kappa_D(P_0) + \frac{d\kappa_D}{dP}(P_0)\delta P + \mathcal{O}(\delta P^2) \quad (6)$$

The diode sensitivity, i.e. the slope of the voltage-to-power curve, should be measured at the actual input power P_0 as it can vary in a large proportion in the whole dynamic range.

Providing that the diode sensitivity is known, $S_\alpha(f)$ is easily determined through the measurement of the detector output voltage PSD. In absence of power fluctuation, the output voltage V_D is affected by the internal diode noise sources and by the input noise of the video amplifier. This intrinsic detector noise sets the noise floor of the detection system and needs to be characterized.

III. DIODE SPECIFICATIONS AND ELECTRICAL MODEL

A simplified electrical model for the video part of the power detector is given in Fig. 2.

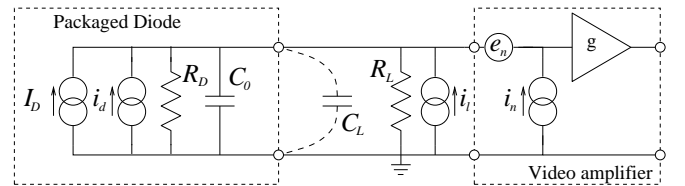


FIG. 2. Electrical model for the detector video output. The capacitance C_L will be temporarily added to evaluate the differential resistance R_D (see section IV C).

R_D is the differential resistance associated with the diode junction. C_0 is the output capacitance required to filter out the RF carrier and harmonics. Here we neglect parasitic resistances and capacitances associated with the bulk semiconductor or connections. They will not change the overall behavior of the diode and can be easily added in a second step if needed. Furthermore we do not seek to deeply understand the physics of the semiconductor but rather to qualify the diode as a low-level power detector. We tested two tunnel diode detectors from Herotek. The diode basic characteristics extracted from the manufacturer data-sheet are given in table I.

The RF voltage applied to the diode junction is rectified by the non-linear current-voltage characteristic of the device. Dropping the ac-components filtered by C_0 , the rectification process is modeled by the low-frequency current generator I_D . The current I_D is a non-linear function of the RF power.

TABLE I. Tested tunnel diode specifications.
(at 25°C and for 10 μW power input)

Model:	HEROTEK DT8012
Serial Number:	SN436409 and SN436410
Frequency Range:	8 – 12 GHz
Minimum sensitivity K_D :	800 V/W
Typical VSWR:	2.5:1
Typical output capacitance C_0 :	10 pF
Typical differential resistance R_D :	125 Ω

The intrinsic fluctuations of the diode current is accounted for by the current noise source i_d . For an ideal detector, owing to the shot effect, the average current I_D flowing in the diode junction is affected by a fluctuation of power spectral density:

$$S_{i_d}^{shot}(f) = 2qI_D \quad (7)$$

For $P_0 = -20$ dBm, the diode voltage on a high-load impedance will be $V_D \approx K_D P_0 = 8$ mV. and the diode current $I_D = V_D/R_D \approx 64$ μA. The shot noise is characterized by a noise current density such as $i_d^{shot} = \sqrt{2qI_D} \approx 5$ pA/√Hz.

The load resistance R_L is associated with the thermal current noise source i_l with a PSD given by:

$$S_{i_l}(f) = \frac{4k_B T}{R_L} \quad (8)$$

where k_B is the Boltzmann constant and T the temperature of the load resistance.

The amplifier voltage and current fluctuations are accounted for by e_n and i_n , according to the popular Rothe-Dahlke model¹². Our custom video amplifier has 36 dB gain. It is based on a AD743 Junction Field Effect Transistor (JFET) operational amplifier characterized by a very low current noise: $i_n = 7$ fA/√Hz. The voltage noise has been measured with a 50 Ω resistance at the amplifier input. The white voltage noise is $e_n = 5$ nV/√Hz. All measurements presented here have been realized with a high load resistance, i.e. $R_L = 1$ MΩ: thus $R_L \gg R_D$. Assuming a noise free incoming RF signal and a shot noise limited detector, the PSD of the voltage fluctuations at the input of the video amplifier will be:

$$S_{\delta V_D}(f) = S_{e_n}(f) + R_D^2 [S_{i_d}^{shot}(f) + S_{i_l}(f) + S_{i_n}(f)] \quad (9)$$

For the simple measurement set-up depicted in Fig. 1 as $R_D \approx 125$ Ω, the contributions of the current noise sources are negligible. The detected noise is mainly limited by the video-amplifier input voltage noise source. For $P_0 = -20$ dBm and $K_D = 800$, the white noise floor is:

$$S_{\alpha}(f) = \frac{S_{e_n}(f)}{(2K_D P_0)^2} = -130 \text{ dB/Hz} \quad (10a)$$

$$S_{\delta P}(f) = 3.9 \times 10^{-23} \text{ W}^2/\text{Hz} \quad (10b)$$

which is equivalent to a NEP of 6 pW/√Hz. These last figures give only a rough estimation of the diode ability to detect small power variations. Indeed they do not report the detector intrinsic noise $S_{i_d}(f)$ and especially its possible flicker (or 1/f) component.

IV. DIODE CHARACTERISTICS MEASUREMENTS

Two identical DT8012 diodes have been tested. The microwave signal is distributed to these diodes by a Wilkinson power divider. This assembly is fastened to the 2nd stage of a pulse-tube cryocooler (model CryoMech PT-403). Three low thermal conductivity coaxial semi-rigid cables connect this set to feedthroughs placed on the cryostat top flange. Once the room temperature measurements realized, the cryocooler is turning on. The 2nd stage temperature stabilizes at 4 K in less than 20 h. No temperature control has been implemented. However during the measurement the mean temperature is stable within ± 0.2 K.

A. Power-to-Voltage gain

The measurement of K_D is straightforward. The video amplifier output voltage V_{out} is recorded as a function of the input RF power. The result is eventually divided by the dc-gain of the video amplifier. The characteristics recorded at room temperature and 4 K are given in Fig. 3

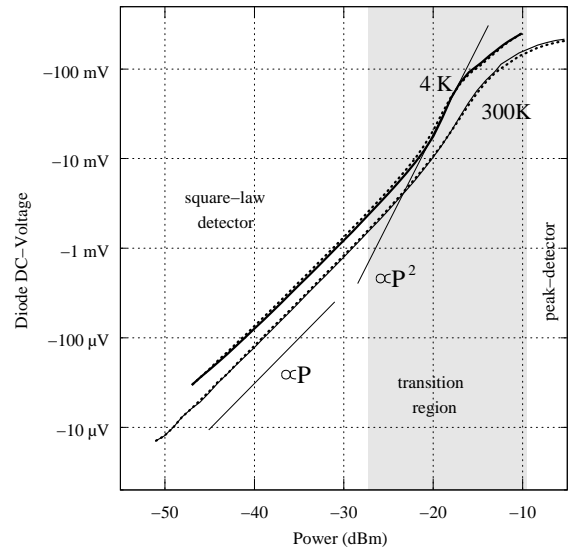


FIG. 3. Power-to-Voltage gain of the tunnel diodes measured at room temperature and 4 K. The signal frequency is $\nu_0 = 10$ GHz, the amplifier input resistance is $R_L = 1$ MΩ. Solid line: SN436409. Dotted line: SN436410.

We can distinguish three different types of operation. At very low input power, i.e. $P_0 \leq -30$ dBm, the diode

behaves like a perfect square-law detector. Its output dc-voltage is proportional to the input power as expected. The sensitivity $K_D \approx 850$ V/W at room temperature and increases up to 1200 V/W at 4 K. At high input power, the diode acts like a peak detector. In between, the voltage-to-power slope indicates that the output voltage depends on the square of the incoming signal power. It is obvious that equation 5 is no valid in this region and its application can induce serious inaccuracy in power measurements. The same behavior has been observed in room temperature Schottky diode detectors¹³. In this case it has been demonstrated that this behavior is inherent to the diode exponential current-voltage characteristic. Tunnel diode characteristic does not follow the Schottky diode equation but an exponential form can be derived as well^{14,15}. In our applications the RF signal power is often in the transition region, and thus we need to clarify the detector sensitivity.

B. Differential gain

We define the first-derivative of the function $\kappa_D(P)$ evaluated at $P = P_0$ as the differential gain of the diode. It can be easily measured by using the set-up depicted in Fig. 4.

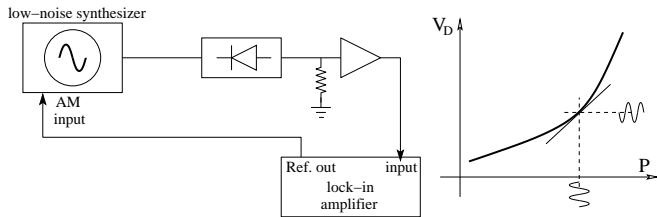


FIG. 4. Differential gain measurement. The synthesizer is a Keysight E-8257D, the lock-in amplifier a Stanford Research SR810.

We intentionally modulate the signal amplitude with a known small deviation and detect with a lockin amplifier the resulting ac-voltage at the detector output. The measured differential gain as a function of incoming power is represented in Fig. 5.

At a very low power the differential gain is almost constant and compatible with the previous measurements. However it shows a dramatic increase in the transition region corresponding to the change in slope in the figure 3. At 4 K and around -17 dBm the diode is approximately four times as sensitive as at low power. Then above ~ -13 dBm the differential gain rapidly drops. The change in sensitivity is less abrupt at room temperature but still exists. This change should be absolutely taken into account in the following to determine with accuracy the noise floor of the AM detection.

The non-linear behavior of the detector is also attested

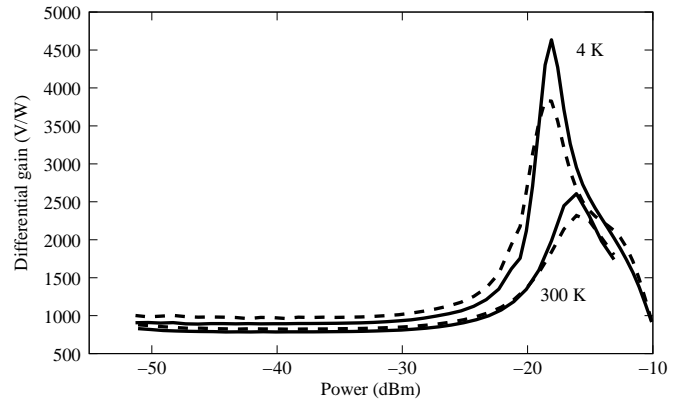


FIG. 5. Tunnel diode differential gain measured at room temperature and 4 K. The signal frequency is $\nu_0 = 10$ GHz, the amplifier input resistance is $R_L = 1$ M Ω . The modulation frequency was 70 kHz, the modulation index was 2×10^{-2} . Solid lines: SN436409. Dashed lines: SN436410.

by the presence of harmonics of the AM frequency at the diode output. These harmonics come from the higher order terms in the development of $\kappa_D(P)$ neglected in equation 6. The figure 6 gives the level of harmonics for a AM frequency of 3 kHz and a modulation index of 0.2.

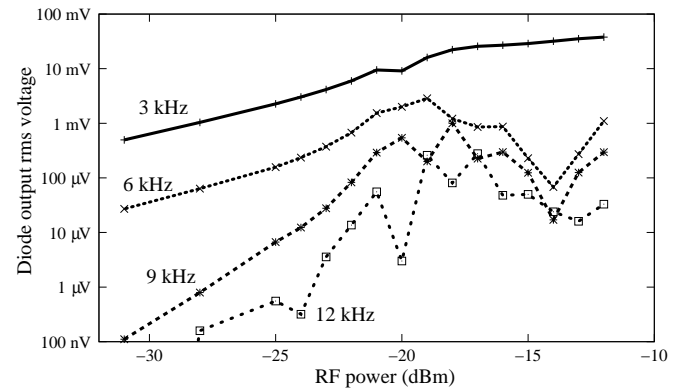


FIG. 6. Harmonics in the diode output voltage. The RF signal frequency is $\nu_0 = 10$ GHz, the amplifier input resistance is $R_L = 1$ M Ω . The AM frequency was 3 kHz, the modulation index was 2×10^{-1} . Diode SN436409.

To facilitate a fast and sensitive measurement of the high order harmonics, we deliberately decreased the frequency and increased the index of the modulation. For a modulation index of 10^{-2} , we only checked that the second harmonic shows the same behavior observed in the Fig. 6.

C. Differential resistance

The diode differential resistance R_D appears in the detector noise budget given by the equation 9. We determine its variation as a function of the RF signal power

with the set-up schematized in the figure 7.

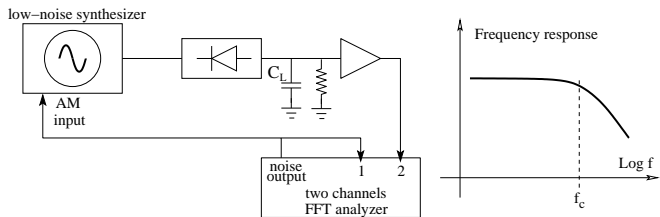


FIG. 7. Video bandwidth measurement. The added capacitance is $C_L = 100$ nF. The synthesizer is a Keysight E-8257D.

In the simplest configuration depicted in the figure 1, the video bandwidth is larger than 100 kHz. Indeed the high load resistance ($R_L = 1$ M Ω) is shorted by the diode differential resistance: $R_L \gg R_D$. By adding a large capacitance $C_L = 100$ nF, the cutoff frequency $f_C \approx (2\pi R_D C_L)^{-1}$ is shifted below 100 kHz. f_C can be easily measured with a baseband Fast Fourier Transform analyzer. Fig. 8 shows R_D as a function of the signal power. We note the transition region is characterized by

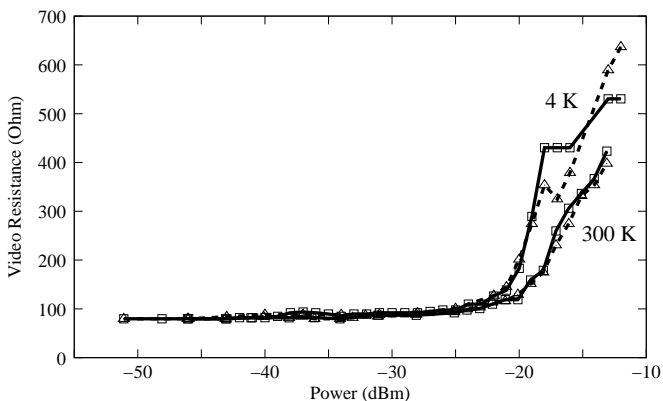


FIG. 8. Diode differential resistance R_D as determined from the measurement of the video bandwidth in presence of the additional capacitor $C_L = 100$ nF. Solid lines: SN436409. Dashed lines: SN436410.

an strong increase of R_D .

V. NOISE MEASUREMENT

A detector alone can be measured only if a reference source is available whose AM noise is lower than the detector noise, and if the amplifier noise can be made negligible. These are unrealistic requirements. A more sophisticated approach is to compare two detectors, A and B as shown in the figure 9.

The first arm (bold line) takes the differential signal $g(P_b - P_a) \approx 0$, which is not affected by the power fluctuation of the source. The voltage PSD measured at the output still contains the contribution of the video amplifiers noise sources. Thus a second arm identical to

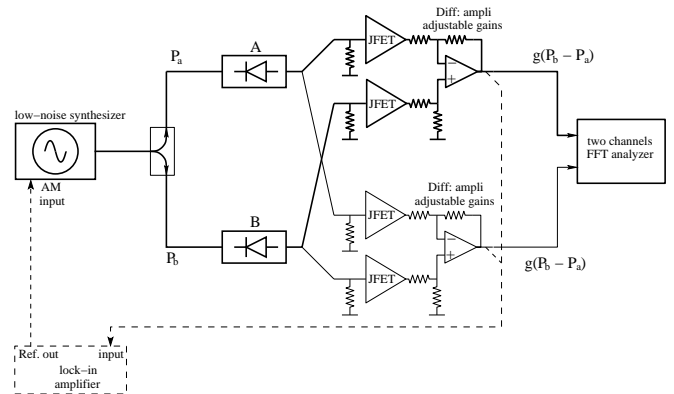


FIG. 9. Two-channels correlation measurement set-up. The lock-in amplifier is used to adjust the gains of the differential amplifiers.

the first one measures also $g(P_b - P_a)$. By measuring the cross spectrum at the output of two arms we eliminate the contribution of the intrinsic voltage noise source e_n of each video amplifier. The noise current i_n of the video-amplifier turns into correlated random voltage fluctuations across each diode video resistance, and thus is not eliminated by correlation. Using a JFET operational amplifier makes the current noise contribution negligible. The figure 10 shows the typical diode voltage PSD obtained with the two diodes at 4 K and receiving -20 dBm. We assume that the two detectors are equivalent: 3 dB have been subtracted from the measured spectrum.

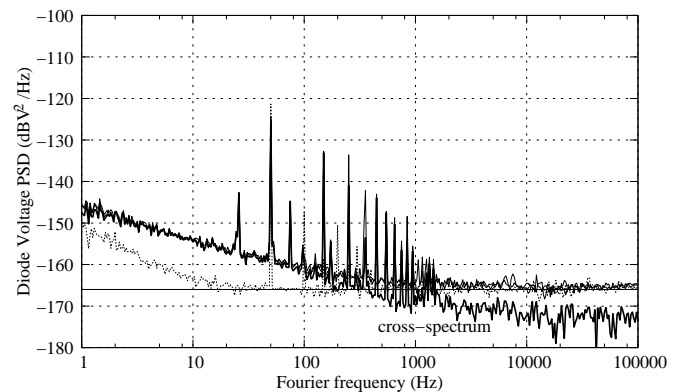


FIG. 10. Diode voltage PSD at 4 K and -20 dBm signal power. Thin lines: PSD of each channel. Bold line: Cross-spectrum. Dashed line: Single channel noise floor.

The video-amplifier voltage noise e_n has been measured with a 50 Ω input load (dashed line): $e_n = 5$ nV/ $\sqrt{\text{Hz}}$. On each channel this noise floor masks the diode intrinsic noise for Fourier frequencies higher than 1 kHz. The later is revealed by the cross-spectrum (bold line). The diode noise is dominated by a flicker ($1/f$ slope) component and a white noise floor reaching ~ -173 dBV²/Hz for $f > 30$ kHz.

It is useful to express the intrinsic diode noise in term of $S_{\delta P}(f)$ (Power fluctuations PSD) or $S_{\alpha}(f)$ (relative amplitude fluctuations PSD). Combining equations 4 and 6, they are related to the experimental measurement of diode voltage PSD as:

$$S_{\delta P}(f) = \left[\frac{1}{\kappa'_D(P_0)} \right]^2 S_{\delta V_d}(f) \quad (11a)$$

$$S_{\alpha}(f) = \left[\frac{1}{2P_0\kappa'_D(P_0)} \right]^2 S_{\delta V_d}(f) \quad (11b)$$

where $\kappa'_D(P_0)$ is the diode differential gain measured at P_0 . $S_{\alpha}(f)$ is presented in Fig. 11.

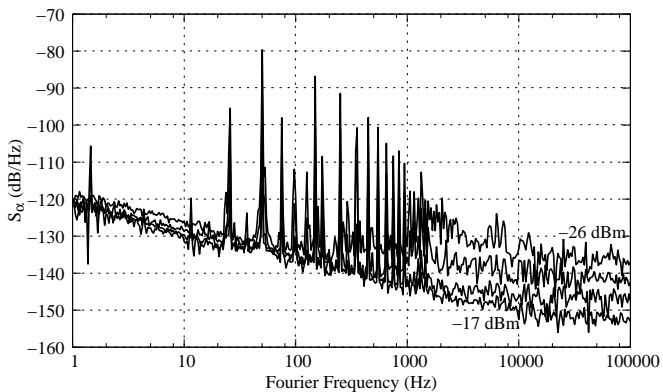


FIG. 11. Diode intrinsic noise expressed as the relative amplitude fluctuations PSD of the incoming RF signal. $S_{\alpha}(f)$ is measured at 4 K for -26,-23, -20 and -17 dBm input signal power.

To keep the figure readable, we only present the PSDs for four signal powers. The same measurements have been done at room temperature. All the recorded crosspectrum show a white noise floor and flicker component, we approximate the two PSDs as:

$$S_{\delta P}(f) = \frac{p_{-1}}{f} + p_0 \quad \text{and} \quad S_{\alpha}(f) = \frac{a_{-1}}{f} + a_0 \quad (12)$$

The figure 12 gives the coefficients p_{-1} , p_0 , a_{-1} and a_0 as function of the signal power at 4 K and 300 K.

In term of power fluctuations PSD, the white noise floor is independant of the signal power. For $f > 10$ kHz, $S_{\delta P} = 6.3 \times 10^{-25}$ W²/Hz, equivalent to a NEP of 0.8 pW/ $\sqrt{\text{Hz}}$. The flicker noise increases proportionally to P^2 . The same dependancy has been reported in¹⁹. When expressed as S_{α} , the flicker noise appears independant of the incoming power. The relative amplitude fluctuations PSD is about -120 dB/Hz at 1 Hz. The room temperature measurements are about 10 dB higher.

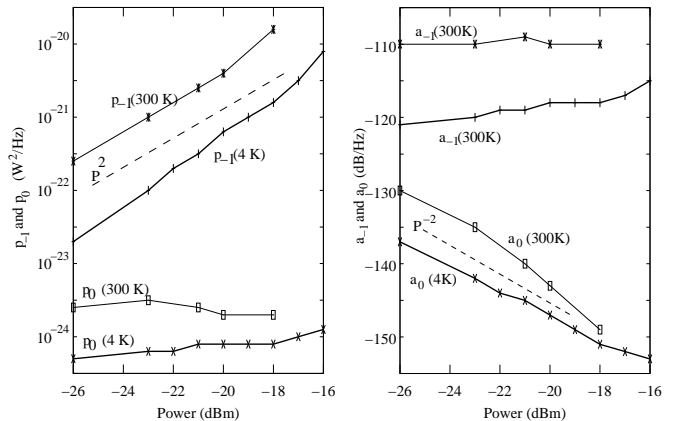


FIG. 12. White and flicker noise components for $S_{\delta P}$ and S_{α} measured at 4 K and 300 K.

VI. CONCLUSION

We characterized commercial tunnel diode low-level microwave power detectors intended to be implemented in a Cryogenic Sapphire Oscillator. At 4 K, these diodes can only be considered as nearly perfect square-law detector for incident microwave power below -30 dBm. Around -20 dBm, the differential gain presents a dramatic increase. This strong non-linearity contradicts the generally admitted model of a square-law detector smoothly saturating with the incident signal power.

A high sensitivity two-channels correlation measurement set-up has been used to measure the noise of the diode detector as a function of the incident power. It is shown that $S_{\alpha}(f)$, the PSD of fractional amplitude fluctuation, presents a flicker noise component almost independant of the incoming power. At 4 K, $S_{\alpha}(f) \approx 10^{-12}/f$. In presence of a such flicker noise, the fractional amplitude stability expressed in term of Allan deviation²⁰ will be 1.2×10^{-6} . For a room temperature diode the limit is 3.7×10^{-6} .

VII. ACKNOWLEDGMENTS

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