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A symbolic approach to voltage stability and power sharing in time-varying DC microgrids

Daniele Zonetti, Adnane Saoud, Antoine Girard and Laurent Fribourg

Abstract— In this paper we address the problem of voltage stability and power sharing in DC microgrids with time-varying power demand. By exploiting the monotonicity property enjoyed by the system, and under the assumption of full observability of the bus voltages, we design a centralized, abstractionbased symbolic controller that, once refined into a controller for the original system, ensures the required specifications. Whereas load voltages cannot be measured, we propose an appropriate decomposition of the system, such that the control problem can be reformulated in terms of assume-guarantee contracts to be satisfied by the observable and unobservable components. A constructive procedure to determine suitable contracts is further investigated and the obtained results are validated with two numerical examples.

I. INTRODUCTION

Direct-current (DC) microgrids have been recognized as a promising choice for the redesign of distribution systems, which are undergoing relevant changes due to the increased penetration of photovoltaic modules, batteries and DC loads [1]. A microgrid is an electrical network gathering a combination of generation units, loads and energy storage elements—in the sequel referred as *power units*. With the exception of passive loads, all units are interfaced to the DC network through power converters, which are internally controlled to guarantee appropriate power profiles under fast current and voltage perturbations. Some of the units are further equipped with an additional layer of control-termed primary control-which operates at a slower time-scale and guarantees robust and safe operation in presence of large disturbances [2]. From the point of view of the grid then, the individual power units exhibit a highly nonlinear behavior, as they are operated to maintain appropriate power profiles irrespectively from perturbations. The control design is usually addressed by splitting the main control problem in two subproblems. First, establish upper and lower bounds on the power profiles of the individual units that ensure existence of an equilibrium point. Second,

design a controller that guarantees that such equilibrium voltage is kept sufficiently near the grid nominal value, while achieving an appropriate distribution of power among the units. Both problems received great attention in recent years, see for example [3]-[5] and [6]-[9]. However, the majority of these works assume that power units behave exactly as constant power devices (CPDs) and that the system stability must be inferred by the existence of an asympotically stable equilibrium point of the closed-loop system. Moreover, the control designs do not provide transient guarantees and are usually realized by prioritizing either the verification of the power sharing [6], [7], either the regulation of the voltages near the nominal value [9]. As a result, the fulfillment of both properties can be only guaranteed by the verification of some extra conditions or by the use of additional controllers.

The use of symbolic control techniques has been an ongoing research area in recent years. In such approaches, a symbolic model-a system with a finite number of states and inputsis constructed starting from the original system. When the concrete (original) and abstract (symbolic) systems are linked by some behavioral relation such as simulation, alternating simulation or their approximate versions, a symbolic controller synthesized for the abstraction can be refined into a controller for the original system [10], [11]. As opposed to traditional control theory, which usually focuses on properties such as stability, controllability and tracking of trajectories, symbolic control leverages the use of automata theoretic techniques for the synthesis of controllers that enforce a wide range of specifications such as safety, reachability or more complex objectives such as those expressed in linear temporal logic [12].

In this work, starting from a *nonlinear, time-varying* model of a DC microgrid, we derive suitable abstractions of the system by exploiting monotonicity and next design symbolic controllers that, once refined into a controller for the original system, ensure the required specifications. We consider two different scenarios. First, we assume that *all* voltages are measurable. In this case we can construct a monolithic abstraction of the whole system, for which we synthesize a centralized symbolic controller that fulfills the control objectives. Then a sampled-data controller preserving the same features is derived. To deal with the partial information case, we employ a contract-based approach: the DC microgrid is decomposed into two subsystems, each subsystem is assigned an assume-guarantee contract, then a compositionality result ensures

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that if both components satisfy their own contract, the global control specification is achieved. A constructive procedure is further proposed for a systematical exploration of feasible contracts. Although the aforementioned control objectives usually require distributed methods, for simplicity we here only focus on a centralized implementation—these to be intended as a first step towards the development of a more general theory that guarantees the fulfillment of the same objectives in a distributed fashion.

The remainder of the paper is structured as follows. The model of the DC microgrid is presented in Section II and is instrumental to establish the control problem, which is illustrated in Section III. In Section IV then, we show the procedure to derive an abstraction and to design a sampled-data controller that guarantees the control specifications in case of full information. To deal with the partial information case, the contract-based approach is developed in Section V. Theoretical results are validated in Section VI on numerical examples of a two-units and four-units DC microgrid. VI. Conclusions and final remarks follows in Section VII.

Notation All vectors are column vectors. We use $\underline{0}$ to denote matrices of zeros, 1 to denote the vector with all ones and I to denote the identity matrix of appropriate dimensions. $x = \operatorname{col}(x_i)$ denotes a vector with entries $x_i \in \mathbb{R}$, diag $\{a_i\}$ denotes a diagonal matrix with entries $a_i \in \mathbb{R}$. T is the set of nonnegative reals. Element-wise (Hadamard) division of matrices is denoted by \oslash . $\mathcal{I}_{\delta}(x)$ denotes the ball centered in $x \in \mathbb{R}^n$ and of radius $\delta > 0$. Given a system Σ described by the continuous-time differential inclusion $\dot{x}(t) \in f(x, u)$, where $x \in \mathcal{X}$ is the state and $u \in \mathcal{U}$ is the input, we will use $\Phi(t; x^0, \mathbf{u})$ to denote the set of points reached at time $t \in \mathbb{T}$ from the initial condition x^0 under the input $\mathbf{u} : [0, t] \to \mathcal{U}$.

II. MODELING OF DC MICROGRIDS

Following the same approach used in [6], we represent a microgrid as a directed graph $\mathcal{G}(\mathcal{N}, \mathcal{E}, \mathcal{B})$, where: \mathcal{N} is the set of nodes, with cardinality n; \mathcal{E} is the set of edges, with cardinality t and $\mathcal{B} \in \mathbb{R}^{n \times t}$ is the incidence matrix capturing the graph topology. The edges correspond to the transmission lines, while the nodes correspond to the buses where the power units are interfaced. We further define \mathcal{N}_S as the subset of nodes associated to controllable power units, *i.e.* the generation and energy storage units, with cardinality m, and \mathcal{N}_L , as the subset of nodes associated to non-controllable power units, i.e. the loads, with cardinality n-m. Accordingly, the collection of voltages and currents at the buses can be partitioned as $V := col(V_S, V_L) \in \mathbb{R}^n$, $I := \operatorname{col}(I_S, I_L) \in \mathbb{R}^n$, while the incidence matrix admits the decomposition $\mathcal{B} = \operatorname{col}(\mathcal{B}_S, \mathcal{B}_L)$, with $\mathcal{B}_S \in \mathbb{R}^{m \times t}$, $\mathcal{B}_L \in \mathbb{R}^{(n-m) \times t}$. Since we focus our attention on dominantly resistive transmission lines, it is assumed that each edge associates a weight modeling the conductance $G_e > 0$ of the line $e \in \mathcal{E}$ and that inductive dynamics are neglected. The interconnection laws between power units are captured

by the generalized Ohm's law:

$$I = \mathcal{L}V \tag{II.1}$$

where $\mathcal{L} \in \mathbb{R}^{n \times n}$ is the Laplacian matrix associated to the graph, which is defined as:

$$\mathcal{L} := \begin{bmatrix} \mathcal{B}_S \\ \mathcal{B}_L \end{bmatrix} G \begin{bmatrix} \mathcal{B}_S \\ \mathcal{B}_L \end{bmatrix}^{\top} = \begin{bmatrix} \mathcal{L}_S & \mathcal{L}_m \\ \mathcal{L}_m^{\top} & \mathcal{L}_L \end{bmatrix} \in \mathbb{R}^{n \times n}, \quad (\text{II.2})$$

with $G := \operatorname{diag}(G_e) \in \mathbb{R}^{t \times t}$.

Using the aforementioned partition, we now characterize the dynamics of the power units. With no loss of generality we make the assumption that controllable power units only inject, and not absorb, power and refer to them as *sources*. Their dynamics is then characterized as follows:

$$C_j \dot{v}_j = -G_j v_j + \frac{u_j}{v_j} - i_j, \qquad j \in \mathcal{N}_S, \tag{II.3}$$

with state $v_j \in \mathbb{R}$, input $i_j \in \mathbb{R}$, constant parameters $C_j, G_j \in \mathbb{R}$ and a continuous signal $u_j : \mathbb{T} \to \mathcal{P}_j$, with $\mathcal{P}_j := [\underline{P}_j, \overline{P}_j] \subset \mathbb{R}$. All parameters are positive. These dynamics consist of a lossy capacitive dynamics, with bounded (positive) power injection, interfaced to the grid via a current sink. The term $u_j(t)$, denoting the power injection, is usually determined by a primary controller u'_j via

$$u_j = P_j + u'_j, \qquad j \in \mathcal{N}_S,$$

with $P_j \ge 0$ a nominal reference value. For simplicity, u_j is interpreted as a control input.

On the other hand, loads can be described by the dynamics:

$$C_k \dot{v}_k = -G_k v_k + \frac{p_k}{v_k} - i_k, \qquad k \in \mathcal{N}_L, \tag{II.4}$$

with state $v_k \in \mathbb{R}$, input $i_k \in \mathbb{R}$, constant parameters C_k, G_k and a continuous signal $p_k : \mathbb{T} \to \mathcal{P}_k$, with $\mathcal{P}_k := [-\underline{P}_k, -\overline{P}_k] \subset \mathbb{R}$. All parameters are positive. These dynamics consist of a lossy capacitive dynamics, with bounded time-varying power demand, interfaced to the grid via a current sink. The term $p_k(t)$, denoting the (negative) power demand is an unknown signal and thus is interpreted as a time-varying perturbation. Using (II.2), the stacked dynamics of (II.3), (II.4) can be rewritten in compact form:

$$\begin{bmatrix} C_S V_S \\ C_L \dot{V}_L \end{bmatrix} = - \begin{bmatrix} G_S & \underline{0} \\ \underline{0} & G_L \end{bmatrix} \begin{bmatrix} V_S \\ V_L \end{bmatrix} + \begin{bmatrix} u \oslash V_S \\ p \oslash V_L \end{bmatrix} - \begin{bmatrix} I_S \\ I_L \end{bmatrix},$$
(II.5)

where

- $V_S := \operatorname{col}(v_j) \in \mathbb{R}^m$, $V_L := \operatorname{col}(v_k) \in \mathbb{R}^{n-m}$ are the state voltages;
- $I_S := \operatorname{col}(i_j) \in \mathbb{R}^m$, $I_L := \operatorname{col}(i_k) \in \mathbb{R}^{n-m}$ are the input currents;
- $u := \operatorname{col}(u_j) \in \mathcal{P}_S$ and $p := \operatorname{col}(P_k) \in \mathcal{P}_L$ are the input and perturbation powers, with

$$\mathcal{P}_S := \Pi \mathcal{P}_j \in \mathbb{R}^m, \quad \mathcal{P}_L := \Pi \mathcal{P}_k \in \mathbb{R}^{n-m};$$

- $C_S := \operatorname{diag}(C_j), G_S := \operatorname{diag}(G_j), C_L := \operatorname{diag}(C_k),$ $G_L := \operatorname{diag}(G_k)$ are matrices of appropriate dimensions. By replacing (II.1)-(II.2) into (II.5) and recalling that $p \in \mathcal{P}_L$, the system dynamics can be equivalently described via the following differential inclusion:

$$\dot{V} \in \mathsf{f}(V, u),\tag{II.6}$$

with

$$f(V,u) := C^{-1} \left(-(\mathcal{L} + G)V + \begin{bmatrix} u \\ \mathcal{P}_L \end{bmatrix} \oslash V \right),$$

where $C := \text{bdiag}\{C_S, C_L\}$, $G := \text{bdiag}\{G_S, G_L\}$ are matrices of appropriate dimensions. In the sequel we will employ the following definition.

Definition 2.1: A DC microgrid is a continuous-time system $\Sigma^{\tau} := (\mathcal{V}, \mathcal{V}^0, \mathcal{U}, \mathcal{Y}, \mathsf{f}, \mathsf{u}, \mathsf{h})$ where

- $\mathcal{V} \subset \mathbb{R}^n$, $\mathcal{V}^0 \subset \mathcal{V}$, $\mathcal{U} \subset \mathbb{R}^p$ and $\mathcal{Y} \subset \mathbb{R}^r$, with $p \leq n$, are the set of state voltages, the set of initial state voltages, controllable input currents and measurable output voltages;
- f: V × U ⇒ V is the continuous-time map associated to the dynamics (II.6);
- u : Y ⇒ U is a sampled-data controller with a clock of period τ > 0.
- h : V → Y is the continuous-time map associated to the measurable output voltages;

A trajectory of the system Σ^{τ} is a signal $V : \mathbb{T} \to \mathcal{V}$ where V is continuous, $V(0) \in \mathcal{V}^0$ and for all t > 0, $\dot{V} \in f(V(t), u_k)$, where $u_k \in u(V(k\tau))$ for $t \in [k\tau, (k+1)\tau)$.

A straightforward property of the DC microgrid is that it is *monotone*—a fact that can be easily verified via the Kamke-Muller conditions for continuously differentiable vector fields, since we have:

$$\frac{\partial \mathbf{f}_i}{\partial v_j} \ge 0, \quad \frac{\partial \mathbf{f}_i}{\partial p_k} \ge 0 \qquad i, j \in \mathcal{N}, \ k \in \mathcal{N}_L, j \neq i.$$

III. PROBLEM FORMULATION

As already mentioned, sources are usually equipped with a primary control layer that ensures the robustness of the grid in presence of sudden change and/or fluctuations in the load profiles. In order to formulate properly the control problem, we find convenient to define the following specification sets:

• the set of admissible state voltages

$$\mathcal{V}_{\delta} := \mathcal{I}_{\delta}(v^{\text{nom}} \mathbf{1}_n) \subset \mathbb{R}^n, \qquad v^{\text{nom}} > 0, \delta > 0;$$

- the set of admissible power injections $\mathcal{U}_S := \mathcal{P}_S$
- the set of power sharing injections

$$\mathcal{U}_{\Gamma} := \{ u \in \mathbb{R}^m : \ \Gamma u = \mathbf{1} lpha \}$$

with $\Gamma = \operatorname{diag}(\gamma_j) \in \mathbb{R}^{m \times m}$, where $\gamma_j > 0$, $\alpha > 0$.

The set \mathcal{V}_{δ} specifies that the system's trajectory must be kept sufficiently near the nominal value $v^{\text{nom}} > 0$. The sets \mathcal{U}_S and \mathcal{U}_{Γ} account for the limited power available at each source and the verification of an appropriate power sharing. We are now ready to define the control problem.

Problem 3.1: Consider a DC microgrid Σ^{τ} , with $\mathcal{V} = \mathcal{V}_{\delta}$, $\mathcal{U} = \mathcal{U}_S \cap \mathcal{U}_{\Gamma}$ and let $\mathcal{Y} \subset \mathbb{R}$ a set of measurable outputs. Determine a sampled-data controller $u : \mathcal{Y} \Rightarrow \mathcal{U}$, with a clock period $\tau > 0$, and a non-empty set $\mathcal{V}^* \subseteq \mathcal{V}$ such that the closed-loop trajectories of the DC microgrid satisfy

$$V(0) \in \mathcal{V}^{\star} \quad \Rightarrow \quad V(t) \in \mathcal{V}^{\star}, \ t > 0.$$

The solution of the problem consists thus in determining an (output-based) *safety* controller that guarantees that the trajectories (V(t), u(t)) originating in the set $\mathcal{V}^* \times \mathcal{U}$ remain there for any future time.

Remark 3.2: An important theoretical question is to determine an optimal (according to some cost function) tuple $(\mathcal{V}^*, \mathcal{P}_S, \mathcal{P}_L)$ so that \mathcal{V}^* is invariant. This formulation scales to the traditional power flow optimization problem whenever we restrict the sets to be singletons.

IV. SYMBOLIC CONTROL

We now illustrate how symbolic control techniques can be used to achieve the control objectives defined in Problem 3.1. For this purpose, we determine a symbolic model—termed *abstraction*—that is linked to the original system Σ^{τ} via an appropriate *alternating simulation* relation [10].

A. Construction of an abstraction of the system

An abstraction Σ^a for the DC microgrid Σ^{τ} is a tuple $\Sigma^a := (\mathcal{V}^a, \mathcal{U}^a, \Delta)$, where \mathcal{V}^a and \mathcal{U}^a are finite (symbolic) sets of states and control inputs respectively, while $\Delta : \mathcal{V}^a \times \mathcal{U}^a \rightrightarrows \mathcal{V}^a$ is a *nondeterministic* transition relation. For the construction of the symbolic sets we proceed as follows. We discretize the state-space \mathcal{V} of the original system into $N \ge 1$ intervals q_i using a finite and uniform partition

$$\mathcal{V}^{\mathbf{a}} := \left\{ q_i | \ i = 1, \dots, N \right\}$$

and define the quantizer $Q: \mathcal{V} \to \mathcal{V}^{a}$, so that Q(V) = q if and only if $V \in q$. Moreover, we discretize the set of inputs \mathcal{U} into $M \geq 2$ values c_h , with the discrete input set given by

$$\mathcal{U}^{\mathbf{a}} := \{ c_h | h = 1, \dots, M \}.$$

In order to characterize the transition relation, we recall that the reachable set of a DC microgrid Σ^{τ} at time t > 0, from a set of states $\mathcal{V}_x \subseteq \mathcal{V}$, with a constant control input $u \in \mathcal{U}$, is given by:

$$\mathfrak{R}_t(\mathcal{V}_x, u) := \left\{ \Phi(t; V_0, u) : | V_0 \in \mathcal{V}_x, u \in \mathcal{U} \right\}$$

Hence, under the same conditions, the reachable set over [0,T] reads $\Re_{[0,T]} := \bigcup_{t \in [0,T]} \Re_t$.

Recalling that the primary controller to be designed is implemented by a microprocessor with a clock of period $\tau > 0$, the transition relation $\Delta : \mathcal{V}^{a} \times \mathcal{U}^{a} \rightrightarrows \mathcal{V}^{a}$ can be defined as follows. For any $q, q' \in \mathcal{V}^{a}, c \in \mathcal{U}^{a}, q' \in \Delta(q, c)$ if and only if :

$$\mathfrak{R}_{[0,\tau]}(q,c) \subseteq \mathcal{V}, \quad q' \cap \mathfrak{R}_{\tau}(q,c) \neq \emptyset.$$
 (IV.1)

The problem of computing reachable sets has been largely investigated in literature—see for example [13], [14] and [15]. For the system under study, the monotonicity property established in Section II can be exploited for the construction of tight systems abstractions [16], [17].

B. Controller synthesis

Using the aforementioned construction, we next show that we can guarantee some suitable equivalence between the abstraction Σ^{a} and the original system Σ^{τ} . We first introduce the following notation. For a state $q \in \mathcal{V}^{a}$ of the abstraction, we denote the set of *enabled* inputs by $enab^{a}(q) = \{c \in \mathcal{U}^{a} | \Delta(q, c) \neq \emptyset\}$ and denote the set of *non-blocking* states by $nb^{a} = \{q \in \mathcal{V}^{a} | enab^{a}(q) \neq \emptyset\}$. We are now ready to present the following proposition. The proof, which is the same reported in [18], is here omitted for sake of brevity.

Proposition 4.1: Consider a continuous-time DC microgrid $\Sigma^{\tau} := (\mathcal{V}, \mathcal{U}, \mathcal{Y}, \mathsf{f}, \mathsf{u}, \mathsf{h})$ and the correspondent abstraction $\Sigma^{\mathrm{a}} := (\mathcal{V}^{\mathrm{a}}, \mathcal{U}^{\mathrm{a}}, \Delta)$ constructed as above. Let $V \in q$, $c \in \operatorname{enab}^{\mathrm{a}}(q)$, with $q \in \operatorname{nb}^{\mathrm{a}}$. Then, for any reached point $x \in \Phi(\tau; V, c)$, there exists $q' \in \Delta(q, c)$ such that $x \in q'$.

The abstraction Σ^{a} is then related to the uncontrolled dynamics of Σ^{τ} (i.e. with $u(y) = \mathcal{U}$ for all $y \in \mathcal{Y}$), via an alternating simulation relation at sampling instants. We are now ready for the synthesis of a controller that solves Problem 3.1. For illustrative purposes, we first consider the centralized full information case (i.e. with $\mathcal{Y} = \mathcal{V}_{\delta}$). The problem of partial information case is instead postponed to Section V.

Using a safety game [10], we thus synthesize a symbolic controller $c: \mathcal{V}^a \rightrightarrows \mathcal{U}^a$ for the abstraction that guarantees that all trajectories of the abstraction originating in some $\mathcal{V}^{a\star} \subseteq \mathcal{V}^{a}$, remain there for any future time. Therefore, the formal relation between the original system Σ and its abstraction Σ^{a} can be used to refine the symbolic controller into a sampled-data controller $u: \mathcal{V}_{\delta} \rightrightarrows \mathcal{U}$ for the original system. More precisely, recalling the definition of the quantizer we have u(V) = c(Q(V)). Since any enabled control input belongs to $\mathcal{U} = \mathcal{U}_S \cap \mathcal{U}_{\Gamma}$ by construction, the controller u(V)ensures that the control objectives defined in Problem 3.1 are achieved at all sampling instants. Guarantees on the intersample behaviour are, on the other hand, provided by the way the abstraction has been constructed. More precisely, by condition (IV.1) that ensures that all trajectories of the system remains in \mathcal{V}_{δ} between consecutive sampling instants.

V. CONTRACT-BASED DESIGN

In general, loads may be not equipped with measurement units that broadcast information on the local voltages to the controller. Hence, we here consider the Problem 3.1 by assuming that the voltages associated to the sources are the only available measurements. Despite this limitation, sources are allowed to communicate and the controller is referred as centralized under partial information.

A. System decomposition

Given this scenario, we find convenient to decompose the DC microgrid Σ^{τ} into two components, a source system Σ_{S}^{τ} and a load system Σ_{L} , which are defined as follows.

A source subsystem of a DC microgrid Σ^{τ} is a system $\Sigma_{S}^{\tau} := (\mathcal{V}_{S}, \mathcal{V}_{S}^{0}, \mathcal{U}, \mathcal{W}_{S}, \mathsf{f}_{S}, \mathsf{u})$ where:

- V_S ⊂ ℝ^m, V_S⁰ ⊂ V_S, U ⊂ ℝ^p and W_S ⊂ ℝ^{n-m} are the set of measurable state voltages, initial state voltages, controllable power inputs and voltage perturbations respectively;
- $f_S: \mathcal{V}_S \times \mathcal{U} \times \mathcal{W}_S \rightrightarrows \mathcal{V}_S$ is the continuous-time map:

$$\mathsf{f}_S := C_S^{-1} \left[-(G_S + \mathcal{L}_S) V_S - \mathcal{L}_m w_S + u \oslash V_S
ight]$$

u : V_S ⇒ U is a sampled-data controller with a clock of period τ > 0 (to be designed).

A trajectory of the subsystem Σ_S^{τ} is a tuple (V_S, w_S) : $\mathbb{T} \to \mathcal{V}_S \times \mathcal{W}_S$ where V_S and w_S are continuous, $V_S(0) \in \mathcal{V}_S^0$ and for all t > 0, $\dot{V}_S = f_S(V_S(t), w_S(t), u_k)$, where $u_k \in u(V_S(k\tau))$ for $t \in [k\tau, (k+1)\tau)$.

A load subsystem of a DC microgrid Σ^{τ} is a system $\Sigma_L := (\mathcal{V}_L, \mathcal{V}_L^0, \mathcal{W}_L, \mathsf{f}_L)$ where:

- $\mathcal{V}_L \subset \mathbb{R}^m$, $\mathcal{V}_L^0 \subset \mathcal{V}_L$ and $\mathcal{W}_L \subset \mathbb{R}^{n-m}$ are the set of state voltages, initial state voltages and voltage perturbations respectively;
- $f_L : \mathcal{V}_L \times \mathcal{W}_L \rightrightarrows \mathcal{V}_L$ is the continuous-time map:

$$\mathsf{f}_L := C_L^{-1} \left[-(G_L + \mathcal{L}_L) V_L - \mathcal{L}_m^\top w_L + \mathcal{P}_L \oslash V_L \right].$$

A trajectory of the subsystem Σ_L is a tuple (V_L, w_L) : $\mathbb{T} \to \mathcal{V}_L \times \mathcal{W}_L$ where V_L and w_L are continuous, $V_L(0) \in \mathcal{V}_L^0$ and for all t > 0, $\dot{V}_L = f_L(V_L(t), w_L(t))$.

Since measurements are available only at the source subsystem, the Problem 3.1 must be now solved for the case $\mathcal{Y} = \mathcal{V}_S$ and abstractions for both subsystems can be constructed as detailed in the previous section.

Remark 5.1: It is immediate to see that the composition of the systems Σ_S , Σ_L via the interconnection laws $w_S = V_L$, $w_L = V_S$ allows to recover the system Σ^{τ} .

B. Compositional reasoning

In order to deal with safety synthesis under partial information, we use the notion of continuous-time assume guarantee contracts [19].

Definition 5.2: Let $\Sigma = (\mathcal{V}, \mathcal{V}^0, \mathcal{U}, \mathcal{W}, \mathsf{f})$, be a continuous-time system where $\mathcal{V} \subset \mathbb{R}^n$ is the set of states, $\mathcal{V}^0 \subseteq \mathcal{V}$ is the set of initial states, $\mathcal{U} \subset \mathbb{R}^m$ is the set of control inputs, $\mathcal{W} \subset \mathbb{R}^p$ is the set of perturbations and f is a locally Lipschitz continuous-time map associated to the dynamics $\dot{V} \in \mathsf{f}(V, \mathcal{W}, u)$. A continuous-time assume-guarantee contract for the system Σ is a pair $\mathcal{C} = (A, G)$ where:

- $A \subseteq W$ is the set of assumptions;
- $G \subseteq \mathcal{V}$, is a nonempty set of guarantees where G is closed.

We say that Σ satisfies a contract C = (A, G), denoted $\Sigma \models C$, if for any initial condition $V(0) \in G$ and any trajectory $(V, w) : \mathbb{T} \to \mathcal{V} \times \mathcal{W}$ of the system Σ originating in V(0), the following logical implication is satisfied:

for all $T \in \mathbb{R}$, such that for all $t \in [0, T]$, $w(t) \in A$, we have the existence of $\epsilon > 0$, such that for all $t \in [0, T + \epsilon]$, $V(t) \in G$.

We can now use the results on compositional reasoning of assume-guarantee contracts of [19, Th. 1-2] to obtain the following proposition.

Proposition 5.3 ([19]): Let $C_i = (A_i, G_i)$ be continuoustime assume-guarantee contracts for the systems Σ_i , $i \in \{S, L\}$ and assume that $\Sigma_S \models C_S$, $\Sigma_L \models C_L$. Then, if

(i) $G_L \times G_S \subseteq A_S \times A_L$;

(ii) $G_S \times G_L \subseteq \mathcal{V}_S \times \mathcal{V}_L$

the control objective defined in Problem 3.1 is achieved with $\mathcal{V}^{\star} = G_S \times G_L$.

To ensure that a component satisfies its contract, we can rely on symbolic methods, similarly to [18]. For systems with no control input, as the load subsystem, the same approach can be used to verify whether the contract is naturally satisfied or not. Nevertheless, the choice of appropriate contracts is not trivial. In fact, Proposition 5.3 merely states that if we are able to construct suitable contracts C_S and C_L for the subsystems Σ_S and Σ_L , then Problem 3.1 is solved for some $\mathcal{V}^* \subseteq \mathcal{V}$. However, no constructive procedure is provided for the derivation of such contracts.

C. Parametric contracts synthesis

In order to explore systematically the space of feasible contracts for a system Σ , we here consider families of contracts $C(\alpha, \gamma)$ as assumption-guarantee pairs parameterized by the parameters $(\alpha, \gamma) \in \mathbb{R}^a \times \mathbb{R}^g$, where a, g are positive integers. The use of parametric assume-guarantee contracts has been already suggested in the context of vehicular traffic networks [20]. For an appropriate formalization, we introduce the following definition.

Definition 5.4: Consider a family of continuous-time assume-guarantee contracts $C(\alpha, \gamma) = (A(\alpha), G(\gamma))$ for the system Σ , parametrized by $A : \mathbb{R}^a \to 2^{\mathcal{W}}, G : \mathbb{R}^g \to 2^{\mathcal{V}}$, with a, g positive integers. Then $C(\alpha, \gamma)$ is said to be satisfied by Σ on $F \subseteq 2^{\mathcal{W}} \times 2^{\mathcal{V}}$ if it is satisfied by Σ for any $(\alpha, \gamma) \in F$. The maximal region where $C(\alpha, \gamma)$ is satisfied by Σ is called the feasibility region of Σ with respect to C.

The set F determines, on the space of parameters, a family of contracts that are satisfied by the system. For the general case the computation of the feasibility region is far from being obvious. However, for the present case, monotonicity of the contracts can be exploited for an efficient calculation of a lower approximation of the feasibility region F. This is illustrated in the following result.

Proposition 5.5: Consider a family of assume-guarantee contracts $C(\alpha, \gamma)$ for the system Σ , where $(\alpha, \gamma) \in 2^{\mathcal{W}} \times 2^{\mathcal{V}}$. If, for any $\alpha, \alpha' \in 2^{\mathcal{W}}, \gamma, \gamma' \in 2^{\mathcal{V}}$, the following logical implications are satisfied:

$$\alpha \leq \alpha' \Rightarrow A(\alpha) \subseteq A(\alpha'), \quad \gamma \leq \gamma' \Rightarrow G(\gamma) \subseteq G(\gamma'),$$
(V.1)

then the following property holds:

$$((\alpha,\gamma)\in \mathbf{F})\wedge(\alpha'\leq\alpha)\wedge(\gamma'\geq\gamma))\Rightarrow((\alpha',\gamma')\in \mathbf{F}).$$

The proposition implies that the boundary of the feasibility region F has the structure of a Pareto front and can therefore

be approximated arbitrarily close, from inside and outside, adapting efficient multidimensional binary search algorithms used in multi-objective optimization [21], [22]. A similar approach for the computation of the feasibility region was applied to timing contracts in [23].

We are now ready to write a corollary of Proposition 5.3 that extends the result to all possible contracts defined on the space of parameters. The result is straightforward and then stated without proof.

Corollary 5.6: Let $C_i(\alpha_i, \gamma_i) = (A_i(\alpha_i), G_i(\gamma_i))$ be a family of continuous-time assume-guarantee contracts for Σ_i , where $(\alpha_i, \gamma_i) \in 2^{\mathcal{W}_i} \times 2^{\mathcal{V}_i}$, $i \in \{S, L\}$ and defined as follows:

$$\mathcal{V}_{S} = \bigcup_{\alpha_{L} \in 2^{\mathcal{W}_{S}}} A_{L}(\alpha_{L}) = \bigcup_{\gamma_{S} \in 2^{\mathcal{V}_{S}}} G_{S}(\gamma_{S})$$
$$\mathcal{V}_{L} = \bigcup_{\alpha_{S} \in 2^{\mathcal{W}_{L}}} A_{S}(\alpha_{S}) = \bigcup_{\gamma_{L} \in 2^{\mathcal{V}_{L}}} G_{L}(\gamma_{L}).$$

Assume that there exist non-empty sets F_S , F_L such that $\Sigma_S \models C_S(\alpha_S, \gamma_S)$ on F_S and $\Sigma_L \models C_L(\alpha_L, \gamma_L)$ on F_L . Then, for any $(\alpha_S, \gamma_S) \in F_S$, $(\alpha_L, \gamma_L) \in F_L$ such that $G_L(\gamma_L) \times G_S(\gamma_S) \subseteq A_S(\alpha_S) \times A_L(\alpha_L)$, the control objective defined in Problem 3.1 is achieved with $\mathcal{V}^*(\gamma_S, \gamma_L) = G_S(\gamma_S) \times G_L(\gamma_L)$.

VI. NUMERICAL EXAMPLES

In this section, we show the practicality of our approach with the following two examples.

A. Two-units case

We consider a two-units source-load DC microgrid with the following parameters: $C_S = 2.2 \text{ mF}$, $C_L = 1.8 \text{ mF}$ the units capacitances; $G_S = G_L = 0.025 \ \Omega^{-1}$ the units conductances; $G_T = 6.65 \ \Omega^{-1}$ the line conductance. The system is supposed to operate within a region \mathcal{V}_{δ} , with grid nominal voltage $v^{\text{nom}} = 450 \text{ V}$ and $\delta = 0.01 \cdot v^{\text{nom}}$, which corresponds to a maximal 1% deviation from the grid nominal value. Following the approach presented in Section V, the DC microgrid is decomposed into a source subsystem Σ_S and a load subsystem Σ_L and two families of assumeguarantee contracts $\mathcal{C}_i(\alpha_i, \gamma_i)$ are considered, $i = \{S, L\}$. Such families are locally constructed over the same space $[-2\delta, 0]\mathbf{1}_2$, in a way that the assumptions and guarantees sets read respectively:

$$A_i(\alpha_i) = [v^{\text{nom}} - \delta - \alpha_{i,1}, v^{\text{nom}} + \delta + \alpha_{i,2}],$$

$$G_i(\gamma_i) = [v^{\text{nom}} - \delta - \gamma_{i,1}, v^{\text{nom}} + \delta + \gamma_{i,2}].$$

Assumptions parameters can be then combined with guarantee parameters via the sources-loads interconnection law, see also Remark 5.1, by taking $\alpha_{S,1} = \gamma_{L,1}$, $\alpha_{S,2} = \gamma_{L,2}$. It can be easily checked then that the proposed families of contract verify conditions (V.1) of Proposition 5.5 and then the feasibility regions F_i have the structure of Pareto fronts. Hence, the global feasible region can be obtained by intersecting such regions. For illustrative purposes, we select $\alpha_{S,2} = \gamma_{S,2} = \alpha_{L,2} = \gamma_{L,2} = 0$, which allows for the representation of the space of parameters on a two-dimensional

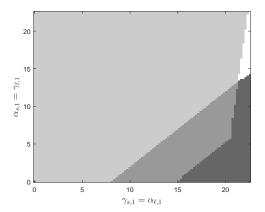


Fig. 1: Global feasibility region (medium gray) obtained as the intersection of the source (light gray) and load (dark gray) feasibility regions.

plane, as shown in Fig. 1. To compute the feasibility regions, we used a symbolic approach, with sampling period for the abstractions $\tau = 0.1 ms$, which corresponds to the clock of the sampled-data controller to be designed. Discretization parameters are N = 10, denoting the dimension of the (symbolic) state-space, and M = 20 denoting the number of discrete inputs. The region F_L associated to the load subsystem can be computed by checking the satisfaction of the correspondent contract $C_L(\alpha_L, \gamma_L)$, while for the source subsystem we take advantage of the additional degree of freedom provided by the control input to construct a sampled-data controller $u : \mathcal{Y} \rightrightarrows \mathcal{U}$ that enforces the satisfaction of the contract $C_S(\alpha_S, \gamma_S)$. For the design, we select thus a feasible assume-guarantee pair that maximize the domain of the controller. Responses to different, bounded, time-varying power demands are illustrated in Fig. 2.

B. Four-units case

We consider a four-terminal DC microgrid as the one depicted in Fig. 3. We assume that two units, namely Unit 2 and 3, are equipped with a primary control layer, while the remaining two units, Unit 1 and Unit 4 correspond to loads with demand varying steadily around a constant power reference. Bus and network parameters are provided in Table I and Table II respectively.

TABLE I: Bus parameters.

	1	2	3	4
$C_i(\mathrm{mF})$	2.2	1.9	1.5	1.7

TABLE II: Network parameters in Ω^{-1} .

G_{12}	5.215	G_{13}	4.615	G_{14}	4.515
G_{24}	6.015	G_{34}	5.615		

The system is supposed to operate within a region V_{δ} , with grid nominal voltage $v^{\text{nom}} = 450 \text{ V}$ and $\delta = 0.005$. The DC microgrid is then decomposed into two-dimensional source

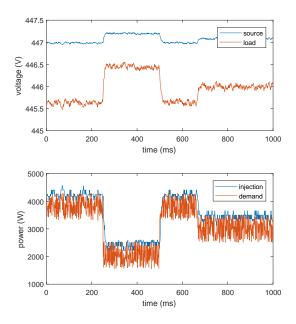


Fig. 2: Voltage and power responses to different power demands, using the proposed sampled-data controller.

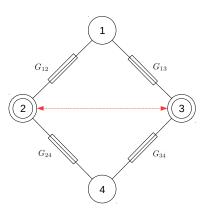


Fig. 3: The four-units architecture used for the simulations. Circles correspond to units, with sources denoted by double circles. Solid lines denote the transmission lines and dashed blue arrows represent the communication between units.

and load subsystems Σ_S and Σ_L . Symbolic abstractions are thus constructed using the same discretization and two families of assume-guarantee contracts $C_i(\alpha_i, \gamma_i)$ are considered, $i = \{S, L\}$. To validate our controller, we assume that the load power demands for Unit 1 and Unit 4 are as follows. Unit 1 is demanding 1 kW from 0 to 250 ms, immediately after stepping up to 5 kW. Unit 4 on the other hand is supposed to be characterized by a constant demand of 0.5 kW from 0 to 750 ms, then stepping up to 4.5 kW. Both demands are affected by small noise. Source power injections are both limited at 12 kW. The controller is implemented via a microprocessor of clock period $\tau = 0.1 ms$ and verifies the power sharing property with ratio $\Gamma = \mathbb{I}_2$ (equal power distribution) by construction. Among the feasible controllers, the one that minimizes the quadratic injection is selected. Power injections and voltage responses are illustrated in Fig. 4. As expected, the controller guarantees that voltages are kept sufficiently near the nominal value and that the power injection is shared in accordance with the assigned ratio.

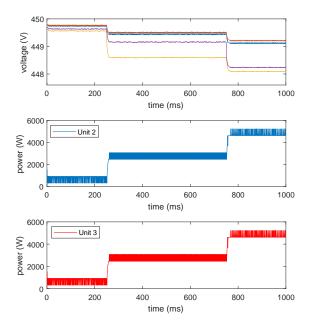


Fig. 4: Voltage and power responses to different power demands, using the proposed sampled-data controller.

VII. CONCLUSIONS

In this work we addressed the problem of designing centralized primary controllers for nonlinear time-varying DC microgrids using symbolic methods, considering both the full information and partial information case. In the former, a traditional approach is employed to construct the system's abstraction, from which a safety controller is easily derived and then refined into a sampled-data controller for the original system. For the partial information case instead, we defined an appropriate system's decomposition, for each of the components an abstraction is derived and a family of locally satisfied, assume-guarantee contracts is constructed. Therefore, locally feasible contracts are combined to establish families of contracts satisfied by the overall system, which are then used for the design of the controllers, similarly to the full information case.

These contributions should be intended as a preliminary step towards the development of a more general theory for contract-based design of symbolic controllers for DC microgrids. Further investigation will focus on the use of parametric contracts in two main directions. First, establish a procedure for optimal selection of performance-guranteed controllers. Second, extend the design to the case of distributed and decentralized communication.

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