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Jayakrishnan Methapettyparambu Purushothama, Arnaud Vena, Brice Sorli, Etienne Perret. Solid-State Conductive-Bridging Reconfigurable RF-Encoding Particle for Chipless RFID Applications. IEEE Microwave and Wireless Components Letters, 2018, 28 (6), pp.506-508. 10.1109/LMWC.2018.2830702 . hal-01992042

HAL Id: hal-01992042

https://hal.science/hal-01992042

Submitted on 13 Jan 2020

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Solid-State Conductive-Bridging Reconfigurable RF-Encoding Particle for Chipless RFID Applications

M.P. Jayakrishnan, Arnaud Vena, Brice Sorli and Etienne Perret, Senior Member, IEEE

Abstract— We report the design and first results of a Re-Configurable RF-encoding particle (REP) for Chipless RFID tag, based on a Nafion-CBRAM Cell. The developed tag consists of a modified shorted dipole as REP, equipped with a Copper-Nafion[®]-Aluminum Metal-Insulator-Metal (MIM) non-volatile Ionic-Bridging switch. The MIM switch is similar to an RC parallel network, which tunes the electrical length of the resonator. The Chipless tags switch between two different resonance frequencies, in the Set and Reset state of the switch. An electrical model is also developed for the REP, to explain the mechanism of switching. The experimental results are in close agreement with the simulated results using commercial EM Simulators. This study is a proof of concept of electronically reconfigurable Chipless tags based on integrated switches which have the potential to be directly printed along with the tag. Keywords — CBRAM, MIM Switch, RF Switch, Chipless **RFID**

I. INTRODUCTION

 ${f R}^{\rm FID}$ is one of the most influential inventions of the 20th century. This technology is now mature enough to be used for a variety of applications like consumer products in supermarkets, health monitoring applications in the medical field, item level tracking for Internet-of -things application etc. [1] - [4]. Yet a hindering factor to the widespread use of this technology is the cost per unit tag. Even though Chipless RFID technology has brought in revolutionary changes in this aspect, this technology is still dominated in application by the optical barcode technology due to its flexibility in use and lower cost. Nevertheless, it is certain that current dedication of researchers all over the world on Chipless RFID, and the advances in disposable printed electronics would uplift it to the technology mainstream in a short period of time, backed by ingenious functionalities like large read range without optical contact, sensor integration functionality,

Manuscript received Nov. 13, 2017; revised Feb. 14, 2018 and accepted Mar. 28, 2018. This work is supported in part by the Grenoble Institute of Technology, Université Grenoble Alpes and the Institut Universitaire de France.

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reconfigurability etc. which cannot be implemented with the current technology of optical barcodes. The reconfigurability of Chipless RFID is a key feature which distinguishes this technology from the optical barcodes and opens the way for new applications. The possibility to reconfigure helps in reducing the cost per unit, by bulk manufacture, and appreciates tag reuse, as the encoding could be done at the time of application by the consumer. Fig. 1 shows the concept of using a single resonator (shorted dipole) with a switch to represent two different spectral signature combinations, which means two different tag's ID. This concept is called Frequency Shift (FS) coding [3] and the coding capacity N per tag is given by (1). Where Δf is the bandwidth per resonator, df is the frequency step per switch position per resonator, and k is the number of resonators on the tag. One should also note that, here, as only one switch is implemented per resonator in Fig. 1, two different frequencies of resonance are obtained, so the Δf could be chosen to be equal 2df.

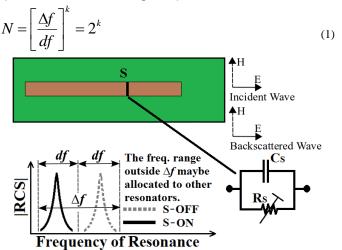


Fig. 1 The concept of encoding data in reconfigurable RFID tags using FS coding technique for one (k=1) resonator, where R_s can switch between two values: low and high resistance so N=2.

The idea of reconfiguring a resonator as shown in Fig. 1 can be implemented in several configurations using multiple switches on resonators to form multi-bit Chipless tags. Jalali *et al.* [5] has reported RFID tags based on capacitively tuned split-dipoles, in which if the capacitance is replaced with a switch as explained here, could evolve to a reconfigurable RF-tag. Similarly, a switch could also be used to reconfigure the electrical length of a resonator, as proposed in this study.

A few articles have reported the concept of modifying the information encoded in the Chipless RFID tag [5] - [7]. This is done by modifying the dimensions of the resonating elements by external methods like conductive inks and lumped elements and are irreversible, making the tags one time programmable. However, a convenient and reliable technology for electronically reconfigurable Chipless RFID tags (write and rewrite feature) is not yet claimed by any authors.

In this article, for the first time, we present the concept and first results of the realization of an electronically reconfigurable solid state REP for Chipless RFID. The presented device is based on a non-volatile memory switching technology called Conductive Bridging Random Access Memory (CBRAM) or Metal-Insulator-Metal (MIM) switch [8] - [11]. Here we use an MIM switch with Copper (active) and Aluminum (passive) electrodes, and Nafion® as electrolyte [11]. This approach would certainly be an excellent breakthrough for Chipless RFID technology [2], as the used switch is directly integrated into the tag and has the potential to be printed along with the tag. This method would be superior in performance in comparison to the integration of classical lumped element switches into tags via soldering, which would tend to degrade the efficiency to some extent.

II. DESIGN AND MECHANISM OF OPERATION

The REP design for RFID tag is inspired from the well-known 'hairpin' or 'C' resonator [4]. The REP resonates similar to a half wavelength shorted dipole, when excited by an electric field polarized along the Y axis as shown in the Fig. 2. Two tags are designed with resonators each of different electrical lengths for validation of the mechanism.

The design is first simulated using the CST Microwave Studio. A lumped resistor similar to the measured filament resistance values of the MIM switch $(0.5\Omega \text{ and } 30\text{M}\Omega)$ is used to change the state of the tag. Response of simulation of both tags, without the switch, is similar to the Set state response.

Topology of the resonators and photograph of the fabricated tags are given respectively in Fig. 2 and 3. The tags are fabricated on a common FR4 substrate using simple fabrication steps in ambient environment without the use of any clean room technologies. The fabrication process has been optimized for the realization of an RF switch [11]. The reported process is compatible even for flexible substrates like paper (real world substrates for chipless RFID tags), with some slight modifications. Where, the electrodes would be formed totally by thermal vapor deposition or inkjet printing, and no process temperature limitation is observed with the technique. The MIM switch is integrated to the resonator as shown in Fig. 2, by tapering the width to reduce effective capacitance of overlapping sections. The switch is formed with Copper as active electrode and Aluminum as inert electrode, both sandwiching a 600nm layer of Nafion as the electrolyte/Ion conductor. On application of a positive voltage with respect to the copper electrode, copper ions migrate through the electrolyte to form a conductive filament to the passive electrode (Aluminum) establishing the set/closed state [11]. Similarly, a voltage pulse of reverse polarity would

reverse the process and breaks this filament, leaving the switch in reset/open state. DC voltage pulses used to operate the switch are generated from a Keithley 2450 source meter. The switch is set using a DC ramp of 12V/0.5mA and Reset instantaneously at -20V/0.15A. The voltage pulses used for the set/reset of the MIM switch are given in Fig. 4.

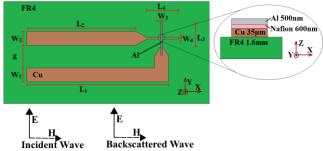


Fig. 2. Topology of the RF-Tag. Tag 1: $W_1 = W_2 = 3mm$, $W_3 = 0.4mm$, $W_4 = 0.16mm$, g = 3mm, $L_1 = 21.5mm$, $L_2 = 14.5mm$, $L_3 = 1.8mm$, $L_4 = 4mm$. Tag 2: $W_1 = W_2 = 3mm$, $W_3 = 0.4mm$, $W_4 = 0.16mm$, g = 3mm, $L_1 = 37mm$, $L_2 = 30mm$, $L_3 = 1.8mm$, $L_4 = 4mm$.

In order to ensure a smooth set of the switch a positive growing current limited voltage ramp waveform is used. The switch is set in the presented case at 10V, as shown by the abrupt fall in measured voltage in Fig. 4. A lumped model of the tag derived from [3] is optimized using the PSpice software and is given in Fig. 5.

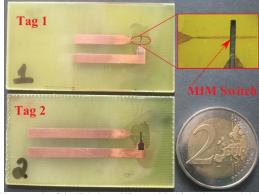


Fig. 3. Photograph of the Fabricated Tags, with microphotograph in the inset showing the MIM switch area.

The MIM switch could be modeled as an RC parallel network [11]. The equivalent electrical model of the 'C' resonator without the switch could be defined as a combination of RLC serial networks each for the antenna mode and the structural mode [3]. In this case, electrical equivalent of the MIM switch is inserted to RLC equivalent for the antenna mode, as it is responsible for the shift in resonance frequency. C_{MIM} is the equivalent parallel plate capacitance of the MIM switch (calculated parallel plate capacitance due to the electrode surface, electrolyte thickness and dielectric constant) and $R_{\mbox{\scriptsize MIM}}$ is the measured DC resistance of the MIM switch, other lumped elements are optimized through fitting, with experimental results. The values of the electrical model for both tags are given in Table 1. Mechanism of operation of the tags could be explained as follows. When the switch is set, filament resistance (R_{MIM}) falls to a very low value (0.5 Ω), which in effect short circuits the C_{MIM}, exhibiting set state resonance. Inversely when the switch is reset, the filament link is broken and R_{MIM} is a large value (30M Ω) and can be neglected from the circuit, thus C_{MIM} adds to the current path

lowering the effective capacitance, thereby shifting the resonance to a higher frequency.

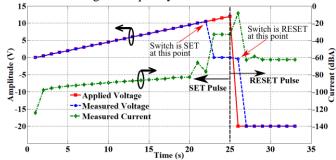


Fig. 4. The DC Voltage and Current waveforms used to operate the switch. The switch is Set at 22s and is maintained between 22s and 26s and Reset after 26s

TABLE I ELECTRICAL MODEL OF THE RECONFIGURABLE TAG

Parameters	Tag1	Tag 2	Parameters	Tag1	Tag2
R_S	20Ω	20Ω	R _A	5Ω	8Ω
L_{S}	1nH	1nH	L_A	14nH	35nH
C_{S}	0.2pF	0.2pF	C_A	0.43pF	0.473pF
V_{S}	$30 \text{mV}/0^{\circ}$	$30 mV/0^{\circ}$	R_{MIM}	0.5Ω	0.5Ω
V_{A}	$1.5 mV/0^{\rm o}$	$1.5 mV/0^{\rm o}$	C_{MIM}	1.98pF	1.98pF

R_{Ant} is a small value resistance for connecting the Current/Voltage probe in PSpice.

III. RESULTS

RF responses of the tags were measured using Agilent N5222A Network Analyzer in a bi-static radar setup [4] using two wideband horn antennas in Co-Polarization. The measured RCS backscatter of the tags along with results of simulation, using the electrical model and full wave simulation are given in Fig. 6. The obtained RCS magnitude is high enough to be recognized with a tag reader in real environment with read range of around 30 cm [4]. Both tags 1 and 2 shows a separation of 200MHz and 140MHz respectively, between set and reset state resonance. The experimental results are in agreement with results of the lumped model and full wave simulation.

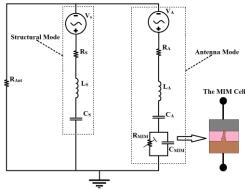


Fig. 5. The electrical circuit model of the REP.

IV. CONCLUSION

CBRAM/MIM switch for solid state reconfigurable tags. Lumped model of the presented tag gives a good physical insight to the operating mechanism of the resonators. The measured best Set/Reset resistances of the switch are 0.5Ω and $30M\Omega$ respectively. The presented resonator represents only one bit, and five such resonators could be integrated to a credit card sized tag. However, more number of MIM switches could be added to a single resonator to increase the number of bits per tag. Both of the tags experimented were switched for more than 50 cycles and are still functional. This is a good number of repetitions for tags in applications like transport tickets, and this is the first time that a chipless RFID tag with non-volatile electronic reconfigurability of its ID is realized, to the best of our knowledge.

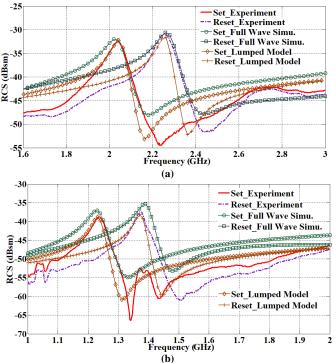


Fig. 6 |RCS| response of Tag1 (a), and Tag2 (b).

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