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# Planar Magnetic Components in More Electric Aircraft: Review of Technology and Key Parameters for DC-DC Power Electronic Converter

Jean Sylvio Ngoua Teu Magambo, Reda Bakri, Xavier Margueron, *Member, IEEE*, Philippe Le Moigne, *Member, IEEE*, Arnaud Mahe, Stephane Guguen and Taoufik Bensalah

Abstract—The More Electric Aircraft (MEA) has motivated aircraft manufacturers since few decades. Indeed, their investigations lead to the increase of electric power in airplanes. The challenge is to decrease the weight of embedded systems and therefore the fuel consumption. This is possible thanks to new efficient power electronic converters made of new components. As magnetic components represent a great proportion of their weight, planar components are an interesting solution to increase the power density of some switching mode power supplies. This paper presents the benefits and drawbacks of high frequency planar transformers in DC/DC converter, different models developed for their design and different issues in MEA context related to planar's specific geometry and technology.

*Index Terms*— MEA; power density; power converters; planar transformer; high frequency losses; parasitic elements.

#### I. INTRODUCTION

Since the beginning of aviation, engineers have investigated solutions to generate and control electrical power aboard airplanes [1]. Currently, with More Electric Aircraft (MEA), this topic mobilizes avionic equipment manufacturers with the aim to increase the available power in new aircraft generations [2]. For the last ten years, the power supplied by electrical systems has been multiplied by a factor of 10 (Fig. 1).

The MEA has ambitious objectives in the coming years such as reducing fuel consumption and the emissions of noise and gas (CO<sub>2</sub>, NO<sub>x</sub>). In Europe, avionics manufacturers mutualize their skills around different projects (Clean Sky 2 [3], Genome [4]) to meet these objectives. Some topics developed in MEA projects deal with the replacement of

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hydraulic and pneumatic systems by electrical ones [5], with a trend toward dc solutions [6][7] or greener taxiing period [8][9] for example. Electrical systems have significant advantages such as gain of mass, energy rationalization, availability and maintenance costs [9].

More generally, electrical systems in aircraft relate to various electrical engineering domains from generation to power distribution, energy storage for emergency power supply or electrical loads. All these devices are connected through power converters: Inverters, rectifiers and choppers. Power electronic systems are needed with specific features due to aeronautic specifications: Harmonics, power factor, electromagnetic compatibility (EMC), efficiency, size, weight, harsh environment and isolation [10]. The latter characteristic is necessary for safety and reduction of EMC current [11]. This isolation function is usually made with high frequency transformer, included in power electronic converter.

Magnetic components, transformers and inductors, present an important issue that limits the integration of power converters. As a comparison, power train of static inverters achieve some 6kW/kg [12], while DC/DC converters with a high frequency transformer are rather below 2kW/kg [13]. However, when comparing a DC-DC converter with an isolated inverter, the results are not the same. In [14], the authors have shown that when using a planar transformer for both a DC-DC converter and a DC-AC inverter, the best power integration and the best efficiency were obtained for the DC-DC converter.

Although those magnetics limit the integration, they are still a key technology to improve power converters performances, in particular in MEA [15]. Indeed, historically, research on lighter transformers began with the first investigation of electrical power in aviation [16].

With the development of planar magnetic components (PMC), high frequency planar self and transformers became an interesting solution in AC/DC or DC/DC power converters to increase power densities [17]. Actually, increasing the frequency allows reduction of the principal inductance value and hence the core size, but true till a certain frequency limit only. Moreover, the low profile of PMC allows them having higher surface/volume ratio as compared to conventional

magnetics, and therefore, have a better thermal behavior [17].

Planar magnetics are largely used in automotive for electric vehicles (EV) and more generally in embedded systems. Aeronautics and aerospace industries also investigate benefits provided by the use of new technology of PMC, especially with new constraints in onboard electrical systems.

This paper presents a review of planar technology and benefits that could be obtained with the use of such component in a MEA application. The aim is to discuss transformer key parameters which can help improving performances of a DC-DC converter and its integration. Based on a MEA power converter application, planar magnetic models available in literature are reviewed and analyzed in compliance with aeronautical specifications. The idea is to give readers a complete view of problematics even if models and analytical methods are not deeply developed in the paper. Finally, a planar transformer is designed in order to highlight different parameters and constraints previously reviewed.

The paper is organized as follows: In Section II, a DC/DC converter example serves as a reference to illustrate the interest of using high frequency planar transformers. Section III presents the issue of planar magnetics in MEA. Then the section IV focuses on the key parameters for efficient planar transformer in an aeronautical environment. In the final section, a planar transformer prototype is designed to illustrate steps and benefits of planar magnetics in MEA.

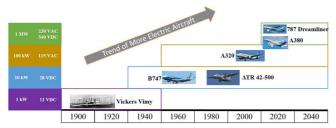


Fig. 1. MEA, throughout the ages

#### II. A MEA APPLICATION: A DC-DC POWER CONVERTER

Voltage and power flow in avionic systems are expanding with the development of new power electronic converters. Fig. 1 shows the increase of active power linked to the rise of voltage level. Till date, the trend is the use of two DC link of  $\pm$  135V or  $\pm$  270V respectively generated from a 115Vac or 230Vac, 400Hz-800Hz through a rectifier unit. The  $\pm$  270V DC voltages enable to obtain the new standard 540Vdc which is known as the high voltage direct current (HVDC) link of new DC aircraft electric network. Fig. 2 shows an example of a power transfer from an AC generator to a 28Vdc storage battery. It could be noted that most of electric loads in avionics are designed for 28Vdc nominal voltage.

In the following, the aim is to present the study of a DC/DC converter. Such a converter can lead to substantial power density enhancement, reducing the size of the transformer and maintaining a good efficiency level.

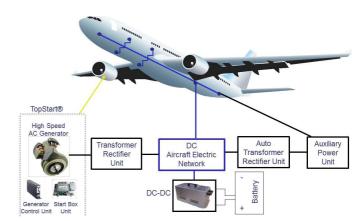


Fig. 2. Example of an Aircraft Electric Network with industrial power converters

## A. Example of Converters' Specifications: Triple Active Bridge Converter

The converter under development is a three port DC/DC converter of few kW. From the new 540V HVDC voltage, the two 28V low voltage direct currents (LVDC) must be generated. One output is regulated to 28Vdc while the other one is connected to a 28V battery. Actually, the converter has different operating modes and must be able to operate in buck, boost and buck-boost depending on external constraints. In case of an interruption of the HVDC bus, the LVDC battery side must be able to restart the input voltage or to generate the other 28V LVDC output. The regeneration of the DC bus is an important aspect and an interesting application when dealing with a DC-DC converter in MEA [18][19][20].

One structure which fits correctly these specifications is the triple active bridge (TAB) [21] as presented in Fig.3. The two LVDC outputs can also be put in parallel to generate the HVDC voltage, becoming a Double Active Bridge (DAB) converter. DAB and TAB architectures are well known in literature [22][23][24][25][26] but for MEA, such LVDC 28V application is a key challenge including the safety compliances.

The TAB is an isolated and bidirectional DC-DC converter which is composed of three full bridges, one input H-bridge and two output H-bridges (Fig. 3). Between input and output bridges, a high frequency transformer not only ensures the different operating mode (buck, boost...) but also the galvanic insulation of the converter. Note that in Fig. 3, the 3-winding high frequency transformer is represented with a leakage transformer [27]. This leakage transformer is useful when representing magnetic coupling between transformer's outputs and useless when this coupling is low.

With such topology and voltage levels, semiconductors are made of SiC MOSFET for the HVDC side and Si MOSFET for the LVDC side. The converter must operate with a switching frequency around few tens of kHz. Table I summarizes the converter's specifications.

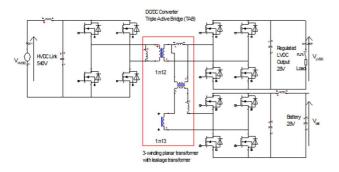


Fig. 3. TAB simplified electric diagram

TABLE I: Converter's electric specifications

TIBLE II Converter a electric apecinications				
$V_{HVDC}$	[450V; 650V]			
$V_{LVDC}$	[26V; 32V]			
$ m V_{bat}$	[16V; 32V]			
Transformer ratio	17:1:1			
Leakage inductance (from primary side) $L_{k1i}$	30µH			
Fsw	[70kHz; 150kHz]			

To reduce the switching and conduction losses, an optimization on the control strategy is made to minimize the rms current through the components [22]. The basic control strategy commonly used is the phase-shift modulation (PSM) which consists in controlling all MOSFET with same duty cycles (D=0.5) delayed with a phase  $\varphi_{1i}$  (i=2, 3 corresponding to the output bridges) between input and output bridges. These two setting parameters ( $\varphi_{12}$  and  $\varphi_{13}$ ) allow to tune the power flow (1). This strategy can lead to good efficiency while operating around nominal conditions. For voltages higher than the nominal, others strategies such as alternative modulations can be privileged [23].

The control strategy which consists in minimizing the rms current is used not only to reduce switching losses but also allows reducing copper losses in the transformer because these losses are linked to the current. The control must also avoid DC current which could quickly saturate the magnetic core.

In order to minimize switching losses at the turn on, all full bridge legs are controlled with zero voltage switching (ZVS), helped by each MOSFET capacitance.

To simplify the study of the TAB, one output H-bridge can be considered as opened. Then, in the equivalent two port, with a PSM strategy on both full bridges, the power flow between the primary and one secondary winding (which is not opened) for example, is expressed as follows [26]:

$$P_{1i} = \frac{\varphi_{1i}(\pi - \varphi_{1i})V_1V_i}{2\pi^2 F_{sw}L_{k1i}}$$
 (1)

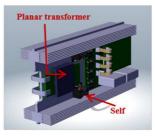
With  $\varphi_{1i}$  the phase between primary winding voltage  $V_1$  and secondary winding i voltage  $V_i$ ,  $F_{sw}$  is the fundamental switching frequency and  $L_{k1i}$  the leakage inductance between the two windings from primary side.

#### B. Discussion on Power Density

Regarding power considerations, this converter must be able to operate in a high temperature environment with a nominal efficiency of 94%. The aim is to reach at least

1.5kW/kg (or 1kW/L) of power density.

The heatsink represents a non-negligible element because of its weight and size, and because MEA power converters are generally in a harsh environment [28]. Thermal performances impact the component's functioning (semiconductors, insulation, core...) and so the reliability of the converter. For example, Fig. 4 shows a 3D view of a prototype of the DC/DC converter, including planar transformer and inductor, with a heatsink placed on the two faces (top and bottom) of the PCB. Note that it is not a final optimized structure. Some adjustments have to be done regarding dimensions and the components' distribution.



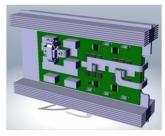


Fig. 4. 3D view of a prototype (a) HVDC side (b) LVDC side

#### C. Toward the integration of magnetics for TAB converter

Research has shown its interest for transformer's technology since the first generation of voltage in airplanes. Initially, transformers were designed for each embedded electric load, operating at the AC frequency of voltage generators and power network. Power density was not yet a topic of interest. Actually, with the development of power transistors and power converters, transformers and more generally magnetics have become key components for the achievement of high power density.

For the TAB converter, as it was introduced previously, the transformer leakage inductance has to be set to a specific value to obtain the nominal power value (1). Leakage inductances of planar transformers are known to be low [29], especially when a good efficiency of the transformer is required. Therefore, the value of the leakage inductance is lower than expected ( $L_{k1i}$  in Table I) at fundamental frequency. The difference could be completed either by adding a resonant self or a bus bar on winding connections. Both solutions increase size and weight of the converter.

Another specification for the TAB converter deals with the fact that both secondary windings can be set in parallel to transfer power to the HVDC side. As a consequence, leakage inductance values, between primary winding and secondary windings  $(L_{k12}, L_{k13})$  must be as close as possible to parallel both windings.

With such converter topology and specifications, the use of an efficient and lightweight high frequency transformer is mandatory. It can be noted that in power electronics, high frequency for PMC means from few tens of kHz to several hundred of kHz. The high frequency range is rarely beyond 1MHz because of skin and proximity effects regarding magnetic core behavior, conductor's size and the conductors' arrangement chosen.

Planar technology presents a lot of benefits for this kind of application but also some drawbacks. The expansion of PMC in MEA is constraint with the perfect control of some component's parameters.

#### III. PLANAR MAGNETICS: ISSUES IN MEA

#### A. Generalities on Planar Components

PMC are interesting alternatives to classic high frequency transformers [17]. Based on the combination of a low profile high frequency magnetic core and printed circuit board (PCB) or copper foil windings technology, PMC enable to obtain significant gain in terms of weight and size but also regarding electrical and thermal performances.

Planar magnetics presents some benefits as compared to traditional high frequency wound magnetic components:

- Low profile component.
- Lightweight.
- High power density.
- Good reproducibility based on winding's precision.
- Easy manufacturing for industrial and cost effective.
- Good thermal behavior [30] due to their surface/volume ratio.
- Low leakage inductance.
- Low AC copper losses due to the rectangular shape of the conductors.

On the opposite, some drawbacks can be listed:

- Restricted to high frequency use.
- Limited core size and shape.
- Limited power range, few kW.
- Too weak leakage inductance value for some resonant converter applications.
- Penalizing parasitic capacitances.
- Longer mean turn length (MTL) and consequently higher DC resistances due to core shape.
- Complexity of wires connections in case of multilayers windings.

Some of these benefits and drawbacks don't have the same impact when dealing with MEA power converters. Let's now detail some topics to investigate how to use efficiently those advantages and how to circumvent those limits in MEA context.

#### B. Planar Magnetic Cores

Due to the industrial context, it is better to begin investigation using existing core on the market, otherwise it is necessary to control the manufacturing techniques and so, the prototyping cost can quickly increase.

The core size of a transformer is often chosen according to the magnetizing inductance desired and the desired active power through the area product of the core [31]. The typical planar core made of ferrite are given in Fig. 5: EE, ER, EQ and PQ [32][33][34].

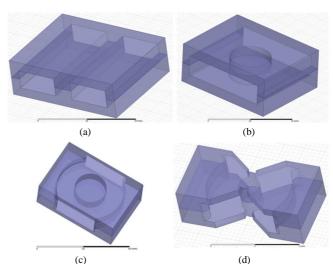


Fig. 5. Available planar core shapes: (a) EE (b) ER (c) EQ and (d) PQ

The EE shape requires straight windings while ER, EQ and PQ match with circular ones. The advantages of ER, EQ and PQ cores are their lower copper losses and parasitic elements thanks to the copper shape which reduce the mean turn length. In [35] the author has shown that a planar transformer with a PQ core leads to the best efficiency for an aircraft application.

Table II presents planar core size and weight for smallest and biggest core in each shape. The core volume represents the external dimensions and not the volume of material. Data are extracted from a manufacturer datasheet [32] but similar shapes and dimensions can be found with other core manufacturers [33][34]. As it can be seen in Table II, EE shape seems to have the best compromise between size/weight and area product for bigger planar cores. The PQ core is a little bit lighter but is penalized by its volume. This is due to its non-low profile core. Others shapes also present interesting characteristics but are limited to smaller core for EQ while the ER one cover the biggest range, from the smallest one (ER9.5) in terms of volume and weight to the biggest one (ER64).

TABLE II: Core size and weight of standard Ferroxcube planar cores – Data from [32]

110111 [32]						
			Core size			Weight
			Dimensions (mm³)	Volume (mm³)	Area product (mm <sup>4</sup> )	(g)
EE	Min	EE14	14x5x7	490	200	1.2
	Max	EE64	64x50.8x20 .4	66324	115000	200
ER	Min	ER9. 5	9.5x5x4.9	232	23.7	0.7
	Max	ER64	64x50.8x25 .4	82580	99616	304
EQ	Min	EQ13	12.8x8.7x5. 7	634	69.6	1.6
ĿŲ	Max	EQ38	38.1x25.4x 16	15483	4334	43
PQ	Min	PQ20 /16	21.3x14x16 .2	4830	1450	13
	Max	PQ50 /50	51x32x50	81600	98400	195

One can note that combination of two E is not the only one solution for planar E and EQ core. For these two kind of cores, they can be combined with PLT to reduce volume and weight of the component.

It can be obvious that PMC are more interesting to achieve higher power integration as compared to conventional magnetic shapes. Nevertheless, an example of comparison is given in Table III and Table IV. Those tables compare smallest and biggest EE planar cores supplied by [32] and their equivalent in conventional cores with approximately the same area product. On one hand, it can be noted that planar cores are lighter and have less volume. On the other hand, they have higher surface/volume ratio. These characteristics make them better candidate not only for cooling but also for increasing the power density for a same given power value.

TABLE III: Planar vs conventional core size of same area product - Smallest

planar core available in [32]

-	Planar core Conven	Conventional
	core	
	E14	EFD15
Area product (mm <sup>4</sup> )	200	222
Effective area (mm²)	14.3	15
Set weight (g)	1.2	2.8
Material volume (mm <sup>3</sup> )	300	510
External volume Vext (mm <sup>3</sup> )	490	1046
External surface for cooling $S_{ext}$ (mm <sup>2</sup> )	70	69.75
Ratio $(S_{ext}/V_{ext})$ (mm <sup>-1</sup> )	0.14	0.066

TABLE IV: Planar vs conventional core size of same area product - Biggest

planar core available in [32]

	Planar core Conventional core	
	E64	E55
Area product (mm <sup>4</sup> )	115000	117000
Effective area (mm²)	519	420
Set weight (g)	200	260
Material volume (mm³)	40700	52000
External volume Vext (mm³)	66324	77275
External surface for cooling Sext (mm²)	3200	1375
Ratio (Sext/Vext) (mm <sup>-1</sup> )	0.048	0.017

If PMC frequency and power level are restricted to specific ranges, temperature and magnetic core can also act as limiting parameters. Indeed, one of the main drawbacks of planar transformers is their limited maximum area product for fixed core shapes supplied by manufacturers.

Magnetic material has an important influence. This parameter is linked to the magnetic performances of the component but also to the thermal characteristic. For a given switching frequency, magnetic material is selected to obtain specific and maximal value of magnetizing inductance. Generally, manufacturers give a tolerance of 20 to 25 % on magnetic characteristics of ferrite cores. An air gap can then be added to control the magnetizing value and to avoid core saturation in case of converter control imbalance. Choosing to add an air-gap leads to problems as fringing flux that can create supplementary losses. Conductors have to be placed far away from air gap [36] to reduce those additional eddy currents generated by this fringing flux.

#### C. Windings' Technology

As previously said, planar windings are either made of PCB or copper foil. Sometimes Litz wire can also be used as in classical high frequency wounded magnetics. Fig. 6 presents two examples of planar transformer. The first one (Fig.6a) is a simple 2-layer PCB planar transformer. The second one (Fig.6b) is an 8-layer copper foil planar transformer.

The choice of conductor's size is linked to several parameters: the desired DC current density, the skin depth (2) calculated at the operating switching frequency including harmonics and the eddy currents in the conductors as known as proximity effect. Usually, copper thickness is quite thin due to technology but also in order to limit those eddy currents in layers. As a consequence, planar windings are generally made of multiple layers connected in parallel to support high current. The copper winding does not fill the core window and generally leads to low filling factor.

$$\delta = \sqrt{\frac{\rho}{\pi \cdot f \cdot \mu}} \quad (2)$$

With f is the operating frequency,  $\mu$  the permeability, and  $\rho$ the copper resistivity.

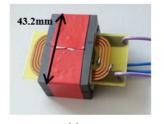




Fig. 6. Planar technology: (a) PCB (b) copper foil

Multilayer PCB fits well with manufacturing process and automated mass production. The copper thicknesses as well as the insulation between conductors are fixed by standard manufacturing techniques. Moreover these thicknesses are the same for primary and secondary windings. Then, these characteristics limit the flexibility to optimize the design of multilayer PCB planar transformer. Transformer can be integrated directly in converter's PCB but this specific case is limited to small power applications. For transformer designed for some hundreds of watts and more, it is preferred to design a specific multilayer PCB for the winding transformer. Multilayer PCB needs vias to connect different layers. This creates additional eddy currents and hotspot inside the windings [36][37]. Finally, multilayer PCB does not allow easy insertion of a magnetic layer to increase potentially the leakage energy and so the leakage inductance [38].

For copper foil planar components, windings' conductors are cut inside foil whose thickness can be chosen differently for primary and secondary. Insulation between layers is made with kapton foil, for example, also cut to be sized for planar magnetic core. Such winding technology usually allows more current for power transformer because of the greater thickness that leads to bigger conductors' across section. As opposed to multilayer PCB, a leakage layer can be added easily between layers. However, a problem still remains, dealing with connections of layers that have to be made one by one.

In both technologies, the maximum number of layers will be limited by the window's core height. Moreover, industrial standards require a minimum space between the conductors and the core and a minimum insulation between conductors of the same layer and conductors of different layers to warranty a level of dielectric rigidity. For every planar component, an attention must also be payed to connections between layers but also between the component and its environment. When they are not well chosen, connections can strongly modify windings and transformer performances.

#### D. Modeling PMC: Analytical vs Numerical

In order to design efficient and optimized planar magnetic components, accurate models are needed. The increase of switching frequency, thanks to new active SiC and GaN semiconductors, can be problematic when dealing with modeling.

In literature, analytical models [39][40][41] are generally based on the 1-D magnetic field hypothesis, known as the Dowell hypothesis [42]. Other derivative models close to Dowell's one have been developed to be more accurate when the conductors do not fill the core window width (porosity factor (3)  $\eta \le 0.7$ ) whereas Dowell's model accuracy is proved when  $0.7 \le \eta \le 1$  [43]. This porosity factor is usually higher for traditional high frequency transformer than for planar ones. Note also that porosity factor is different from the filling factor which is the ratio of the total area of conductors on the core window surface. This filling factor is generally lower for PMC (< 0.4) as compared to traditional transformer or self (> 0.75). Such approach is also limited with the frequency evolution because of the field hypothesis that becomes inacurate.

$$\eta = \frac{n \cdot a}{h} \tag{3}$$

With n the number of turns in a layer, a the conductor width and b the core window width

Some other semi-analytical computational models based on the resolution of Poisson's equation such as PEEC formulation [44] or Roth's method [45] can be used for 2-D transformer's winding cross section. These methods are efficient only when material in rectangular region is homogeneous. Moreover, they require more computational time than analytical ones.

Regarding numerical modeling, 2-D finite element analysis (FEA) are often performed [46][47][48]. 3-D FEA are more complex to achieve and require important computational ressources and time computing. Simplifications are sometimes done, based on the use of 2-D methodology [49] or model-order reduction (MOR) [50] to shorten resolution time.

Main advantages of 3-D FEA are the possible coupling between electromagnetic and thermal solvers (Maxwell 3D, CFD...) [51]. With such coupling, impact of losses in temperature's distribution is facilitated and can lead to the identification of potential hotspots inside windings, which can be destructive for transformer. This thermal aspect is also an indicator of the PMC's lifetime, focusing on the insulation's aging.

#### E. Planar self for EMC

Apart from the application of filtering inductors in power converter, planar self can also be used to design EMI filters [52][53]. This is possible only when the stress current through the filter does not saturate the core. As a consequence, planar self are more dedicated to common mode (CM) suppression filters than differential one, except adding air gaps through the magnetic core. One can note that CM filters which are coupled, allow compensating the main flux generated by the power current. Some configurations of planar transformers are also interesting while they allow reduction of the EMI conducted noise without adding EMI filters but electrostatic screen between conductive layers [54]. For MEA, these Electromagnetic compatibility (EMC) aspects must be taken into account by designers to meet aeronautical standards such as DO-160 and it begins with qualification tests [55].

#### IV. PLANAR MAGNETICS: KEY PARAMETERS

The design of planar magnetic transformer as well as planar self, must consider some key parameters that need to be controlled or tuned to reach good performances for the component and the power converter.

#### A. Multiphysics Component

Modeling and analysis phenomena inside PMC are based on multiple domains of physics, as for traditional high frequency transformer. The main fields involved in the component are: Electrical, electromagnetic, electrostatic and thermal. Fig. 7 presents complete high frequency equivalent circuit for a 2winding transformer, including elements (resistances for losses and inductances) that vary with frequency as well as capacitors for the electrostatic behavior [56][57]. Generally, engineers design transformers to have low losses and low leakage inductance, but capacitances are rarely considered. However, the reduction of the leakage inductance will be made at the expense of parasitic capacitance which will increase. Both aspects are coupled through the thickness of the dielectric material. Different methods can be used to reduce winding capacitances such as using thicker dielectric or inserting a grounded electrostatic screen, but, presently, no method allow reducing both leakage inductance and winding capacitances [17]. In some designs, these parasitic aspects are desired and even enhanced, especially for LLC power

These electromagnetic and electrostatic aspects are effective to represent the equivalent electric circuit of planar transformer, but this one does not include thermal behavior which is linked to transformer's geometry and boundary conditions. Same equivalent circuit (Fig. 7) can be obtained for 3-winding transformer and more generally for n-winding transformers. Such circuit is a really interesting tool that can be included in circuit software.

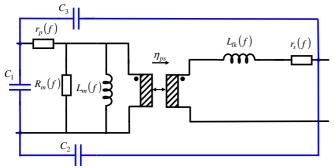


Fig. 7. High frequency equivalent circuit of a planar transformer

The focus is now on some design rules and modeling aspects which must be considered when dealing with planar transformers.

#### B. Losses and Thermal performances

Copper losses are a major topic in high frequency power electronic transformer. As mentioned before, copper losses can be evaluated based on analytical formulae or using 2-D and 3-D FEA. Skin and proximity effects increase with frequency and thus create more losses and heat to dissipate.

In high frequency, current densities are concentrated in the corner of the rectangular conductors (i.e PCB or copper foil). Fig.8 presents current density repartition along a 7x0.5 mm<sup>2</sup> conductor for different frequencies and for a sinusoidal excitation. Moreover, planar rectangular conductors are sized thin to limit eddy current effects. In order to withstand enough current, conductors have to be connected in parallel. It has been shown in [58] that even with such connections, high frequency currents are not distributed uniformly between layers. One solution to reduce these high frequency effects, and thus the AC equivalent resistance of transformer, is to interleave copper layers.

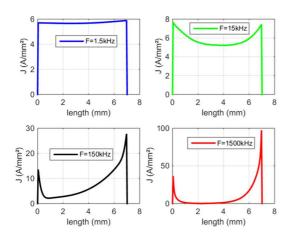


Fig. 8. Example of Eddy current effects on planar rectangular conductors

Regarding copper tracks, it has been shown in [59] that the shape of the windings also have an impact on copper losses in conductors located outside the magnetic core. Mixing different copper shapes (square/round) for primary and secondary windings can improve the reduction of high frequency copper losses.

The other losses to evaluate are the core losses and the dielectric losses, but, the latter are generally considered null regarding the two other types of losses. Core losses can be calculated using data of magnetic material [32], in particular complex permeability. For more precision, magnetic core losses can be calculated with Steinmetz's equation (4) [60] but only when the excitation is sinusoidal. For non-sinusoidal power electronic waveforms, Improved General Steinmetz Equation (iGSE) [61][62] is preferred.

$$P_{core} = K_c * F_{sw}^{\alpha} * B_{peak}^{\beta} * V_c \tag{4}$$

Where  $K_c$ ,  $\alpha$  and  $\beta$  are parameters given by manufacturer's datasheet and  $V_c$  is the core volume.

As total losses have been determined, the thermal behavior of the planar transformer can be estimated considering these losses as input. The increase of transformer's temperature due to total transformer losses has to be minimized with the use of cooling technology. The thermal behavior can be obtained by performing FEA with numerical tools, but under some assumptions, such as the transfer mode considered, boundary conditions and the exchange coefficient values.

For PMC design, analytical models and tools were recently developed to help designer with the thermal modeling of transformer. In [30], the equivalent thermal resistance variation with ambient temperature and power losses was established analytically. The simplest method to estimate the global temperature rise is based on the following expression:

$$\Delta T = T_{\text{max}} - T_{amb} = P_{losses} \times R_{th}$$
 (5)

With  $R_{th}$  the thermal resistance depending on the transformer's geometry and the thermal coefficient as following for example:

$$R_{th}' = \frac{1}{hS_{ext}} \tag{6}$$

$$R_{th} = \frac{l}{K_{\lambda} S_c} \tag{7}$$

With h the thermal coefficient by convection,  $K_{\lambda}$  the thermal conductivity, l the characteristic length,  $S_{ext}$  and  $S_c$  the orthogonal surface of the losses flux.

The equivalent thermal resistance  $R_{th}$  is calculated considering parallel and series associations of different  $R_{th}$  and  $R_{th}$ .

One can note that radiative effect with thermal emissivity is not taken into account, but if so, it can contribute to reduce relatively the temperature rise.

In order to be more precise and to locate potential hotspots inside the component, some computational methods [63] as well as 3D FEA can be performed. In [64][65], temperature inside planar transformer is calculated based on the segmentation of transformer in thermal resistances. The

authors in [66] proposed an analytical coupled electro-thermal model (1-D) to estimate the transformer's temperature with a good accuracy, but only in transient.

#### C. Leakage inductance

Leakage inductance has a major role in power electronics. Most of applications require low values of leakage inductances because they can induce supplementary losses, stress on active components as well as electromagnetic interference (EMI) due to high frequency voltage oscillations [38][54]. For some applications [67], an important leakage inductance value is useful as in TAB converter in ZVS conditions.

Analytically, the leakage inductance is often calculated using leakage energy (8), based on the 1-D Dowell's assumptions [42] and with windings excited as in (9) so that the flux generated is only linked to the leakage energy.

$$W_{lk} = \frac{1}{2} L_{lk} I^2$$
(8)

With  $L_{lk}$  is the leakage inductance which depends on core and conductors dimensions and arrangement and I the current through the considered winding.

$$I_p + \eta \cdot I_s = 0 \tag{9}$$

Where  $I_p$  and  $I_s$  are respectively primary and secondary rms currents, and  $\eta$  is the transformer ratio.

Other calculations are performed assuming that the current is uniform across the conductor's section [68]. This hypothesis leads to low frequency inductance values. For higher frequencies, leakage inductance can decrease up to 50% [69][70] depending on the frequency range chosen. To take into account 3-D effects, FEA can also be performed and are generally more accurate to compute leakage energy.

Interleaving windings (Fig. 9) is the best solution for reducing high frequency copper losses but it also leads to small leakage inductance values.

Some recent studies [71][72] have involved in finding the way to increase the magnetic energy in a core window adding a magnetic layer such as C350 [73] ferrite polymer composite (FPC) for example. Models were developed and results seems pomising since they could improve significantly the leakage inductance but they must also maintain the efficiency. However, this last solution is only valid for planar transformers made of copper foil or several 2-layer PCB assembled together.

#### D. Parasitic capacitances

Parasitic capacitance is also a topic of huge interest in high frequency power electronic converters. Transformers' capacitances exist between different winding's layers. Different electrostatic representations exist but each one is linked to the electrostatic energy stored in the component [57] depending on the number of independent voltage across the component: 3 for a 2-winding transformer (Fig. 7) and 6 for a

3-winding transformer. These capacitances cause resonance, vibration and additional losses in the transformer. That effect could be desired in some resonant converters such as LLC converters [73] but it could also create additional losses while coupling to a consequent leakage inductance [74]. Finally, it could be dangerous for the reliability of the converter. Parasitic capacitances are also paths for common mode currents inducing conducted EMI noises. This phenomenon is more important in circuit where the voltage dynamics are important as in the application presented in Fig. 3 where HVDC SiC MOSFETs can switch up to 650V (Table I) within tens of nanoseconds.

Some capacitances between two layers can be determined with the expression of a plan capacitor assuming there is no voltage drop in a same layer of conductors. With a complete component, including multiple layers and magnetic core, the analytical calculations of parasitic capacitances becomes quickly complex. Even if some models have been developed based on Electric Field Decomposition Method [75] or Green's functions [76], such computation is cumbersome and FEA will be preferred to extract capacitance matrix. Moreover, in transformers with distributed turns between windings, conductors of the same layer should be spaced as far as possible to enhance the fringing effect of the electric field, which reduces the stray capacitance [77] by reducing the electrostatic energy between two parallel plate conductors.

### V. APPLICATION DESIGN: FROM AREA PRODUCT TO THERMAL BEHAVIOR

In order to illustrate previous topics, an example of planar transformer design is developed. In the following, a design for the TAB 3-winding transformer (Fig.3) is thus detailed. This transformer has a maximum power of  $P_{\rm max} = 4.5kW$  and a switching frequency of  $F_{sw} = 70kHz$ . Using expression of area product [78], the size of the core can be determined with the expression (10).

$$A_p = A_w \times A_c = \frac{P_{\text{max}}}{K_f K_u B_{peak} F_{sw} J_w} \quad (10)$$

With  $A_w$  the winding window area,  $A_c$  the core window area,  $K_f = 4$  for a non-sinusoidal excitation,  $K_u$  is the utilization factor of the window by conductors,  $B_m$  and  $J_w$  are the maximum magnetic induction and the primary current density, respectively.

The factor  $K_u$  has a maximum value of 0.2 for planar conductors [79]. In our design,  $K_u$  is set to 0.14. The peak induction is assumed to  $B_{peak} = 130mT$  in order to avoid the core saturation and the DC current density is fixed  $J_w = 8A/mm^2$ . It comes  $A_p \approx 110000mm^4$ , according to Table II, this area product matches with the biggest sizes of planar cores. To achieve the best power density, the core chosen is the EE64.

With this core size, conductors' dimensions can be set up

taking into account a minimal distance between conductors and core to ensure dielectric insulation. In order to have a good transformer's efficiency, full interleaved windings (Fig. 9) are fixed to reduce the resistive factor  $F_r$  (11):

$$R_{AC}(f) = F_r(f) \cdot R_{DC} \tag{11}$$

One of the possible transformer's configurations (Fig. 9) is made of 18-layer PCB with a copper thickness of 0.21mm and a width of 4mm for primary winding to achieve  $J_w$  fixed in (10). Primary and secondary windings have the same thickness because of PCB manufacturing constraints. Each winding is made of six layers; the secondary windings are large enough to limit their DC current density under 20A/mm², which is the limit we assumed for a better thermal dissipation. With the conductors' arrangement chosen, the utilization factor is  $K_u = 0.137$ , which is closed to the assumed value.

All layers are separated by FR-4 epoxy with a thickness of 0.25mm. Primary conductors on the same layer have an insulation of 0.5mm.

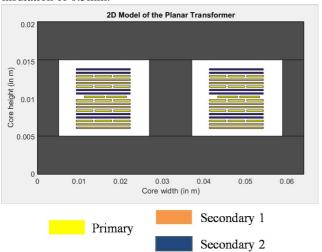


Fig. 9. 2D representation of the planar transformer

To evaluate high frequency copper losses with the arrangement in Fig. 9, 1-D analytical modeling is used on a wide frequency range [10kHz; 1MHz]. Equation (12) is a simplification of the resistive factor, in literature [42], for plate conductors. Skin effect (first term in (12)) and proximity effect (second term in (12)) can be expressed as following:

$$F_r = X \left[ \frac{e^{2X} - e^{-2X} + 2\sin(2X)}{e^{2X} + e^{-2X} - 2\cos(2X)} + \frac{2}{3} (m^2 - 1) \frac{e^X - e^{-X} - 2\sin(X)}{e^{2X} + e^{-2X} + 2\cos(2X)} \right]$$
 (12)

With X defined in (13) and m the number of layers in a winding portion.

$$X = \frac{h}{\delta} \tag{13}$$

With h the thickness of the conductor and  $\delta$  the skin depth expressed in (2).

A similar expression is used to model the decrease of the leakage inductance from low frequency to high frequency [42].

The DC resistances can easily be expressed knowing the windings' dimensions (14). Finally, the windings' AC resistances can be calculated based on (11), (12) and (14). Fig. 10 presents AC resistances for the primary and one secondary winding.

$$R_{DC} = \frac{\rho}{S} * MTL*N \qquad (14)$$

Where N is the turn's number, MTL is the mean turn length and S the current cross-section.

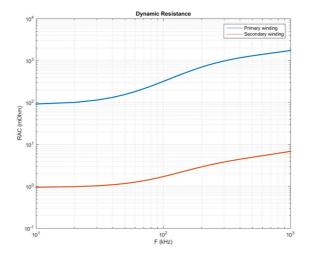


Fig. 10. Analytical winding AC resistances

Resistive factor  $F_r$  (11) (i.e. ratio of  $R_{AC}$  to  $R_{DC}$ ) for primary winding in series is between 2 and 3 for a fully interleaved transformer with frequency between [50kHz; 150kHz] but could be higher than 10 for non-interleaved windings. It can be noted that for secondary winding, made of one large conductor per layer, resistive factor is generally higher than 4, especially when secondary layers are put in parallel. Then, depending on the cases, the AC copper losses could be well distributed between layers.

To estimate the temperature rise of the transformer, total losses are considered for a sinusoidal excitation. Therefore, in the following example, losses are under-estimated because TAB's waveforms contain harmonics. In the design example, losses due to the sinusoidal voltage are equal to 31W and 9W for the copper losses and the ferrite core losses, respectively. Fig. 11 presents the resulting temperature's distribution in the planar transformer designed as well as the one obtained for and equivalent conventional transformer. In both cases, the area product is approximately the same (Table IV). For this simulation, a natural convection by air was considered with a coefficient h=14W/m².K [79], in steady-state and with an ambient temperature of 25°C. As boundary condition, the bottoms of transformers are considered to be put on a cooling

heatsink at 70°C. Same total losses (i.e. 40W) were considered in both cases. To perform 3D-FEA, an equivalent thermal conductivity was calculated according to the transverse direction (15) and according to the normal direction (16). The calculated coefficients are reported in Table V. This table shows that most of thermal exchange made by conduction are in the X-Y plane.

$$K_{XY} = \frac{\sum_{i=1}^{M} h_i \times k_i}{\sum_{i=1}^{M} h_i} = \frac{N_{cu} \times h_{cu} \times k_{cu} + N_{ins} \times h_{ins} \times k_{ins}}{N_{cu} \times h_{cu} + N_{ins} \times h_{ins}}$$
(15)

$$K_{Z} = \frac{\sum_{i=1}^{M} h_{i}}{\sum_{i=1}^{M} \frac{h_{i}}{k_{i}}} = \frac{N_{cu} \times h_{cu} + N_{ins} \times h_{ins}}{N_{cu} \times \frac{h_{cu}}{k_{cu}} + N_{ins} \times \frac{h_{ins}}{k_{ins}}}$$
(16)

With  $N_{cu}$  and  $h_{cu}$  the number of copper layers and their thickness,  $N_{ins}$  and  $h_{ins}$  are the number of insulating layers and their thickness, respectively.

TABLE V: Equivalent thermal conductivity

	Thermal conductivity (W.m <sup>-1</sup> .K <sup>-1</sup> )
Copper	380
FR-4	0.25
Core	5
Transverse $K_{XY}$	174
Normal $K_Z$	0.46

As it can be seen in Fig. 11, the maximum temperature in the planar transformer is lower than the conventional one. It is principally due to the surface/volume ratio which is higher for the planar technology (Table IV) and hence, its equivalent thermal resistance is lower. This is an important result because it allows weight savings on heatsink and so a better power integration.

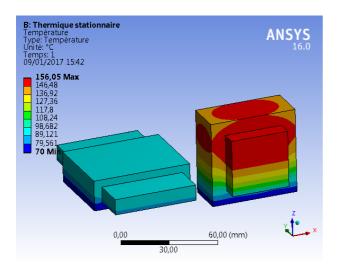


Fig. 11. Temperature distribution in planar vs conventional transformer

Regarding leakage inductances, their analytical values are plotted in Fig. 12 for both secondary windings. Both leakage inductances are close. This can easily be explained by the symmetrical configuration of the arrangement (Fig. 9). Their equivalent inductance in primary winding is about 0.5μH in low frequency, which is very low, and lower again in high frequency (especially 70kHz which is the switching frequency). This value is not enough according to the desired value 30μH (Table I). Then, this value should be enhanced without largely decreasing transformer's performances. As mentionned in section *II.C*, an external self (for example from [80]) can be added in series with the windings. Another solution is to add bus bar on secondary windings' connections considering about *10nH/cm* loop inductance.

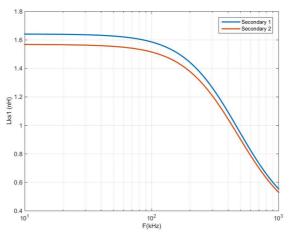


Fig. 12. Analytical leakage inductances from secondary windings

This short example has helped to highlight the link between some of planar transformer's parameters: Design, conductors' layout, losses and leakage inductances. Of course, this design is not complete and could be extended and deepened in order to optimize a planar transformer prototype.

#### VI. CONCLUSION

In this paper, challenges and issues on planar magnetic components for MEA have been presented, based on the example of a specific DC/DC converter. Potential benefits of PMC in aircrafts have been highlighted, as well as drawbacks that have to be limited. PMC's key parameters to be optimized have been presented to improve the power converter performances.

Some models were reviewed and discussed with the objective of helping designers to tune or, at least, to control parameters of planar transformers. An example of a high frequency planar transformer was designed and compared to a high frequency conventional transformer, highlighting the planar technology's interest.

Finally, because they can reach higher power densities with better thermal behavior, these planar magnetic components are going to be increasingly used for the MEA in aeronautics as well as for the EV in automotive.

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