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Model Predictive Power Control based on Virtual Flux for Grid connected Three-level Neutral-Point Clamped Inverter

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Keywords

«Multilevel converters», «Finite Control Set Model Predictive Control», «Direct power control», «Converter control», «Control methods for electrical systems».

Abstract

The paper presents a detailed description of the prediction model for Predictive Power Control based on Virtual Flux (MPPC-VF) scheme for control grid connected three-level neutral-point clamped inverter (3L-NPC). The controller uses a model to calculate predictions of the future values of the virtual flux, grid currents and DC-link capacitor voltages for all possible voltage vectors. The active and reactive power can be estimated based on the virtual flux and the current. The cost function is defined to minimize the error between the active, reactive power and their references, balance the DC-link capacitor voltage and reduce the switching frequency. The optimal switching state that minimizes the cost function is selected and applied to the inverter. Simulation results under different conditions are presented and compared with conventional Direct Power Control and Space Vector Modulation (DPC-SVM). The obtained results show the better performances of the proposed control method.

Introduction

The multilevel converters represent an attractive solution for high power applications due to its ability to meet the increasing capacity requirements and power quality associated with reduced harmonic distortion and have been shown in the recent years to offer high performance compared with the two-level converters. In particular, the three-level neutral point clamped (3L-NPC) inverter structure is considered to be a good solution due to the following advantages: capability to reduce the output voltage and current harmonic content, common mode voltage and increasing the capacity of the converter thanks to a decreased voltage applied to each power semiconductor [1], [2].

Grid connected power inverters plays an important role in industrial applications such as new energy systems, high voltage direct current transmission, flexible AC transmission systems (FACTS), especially with the development of high power generators using wind power. Most of the existing control method use the voltage-oriented control (VOC) which guarantees high dynamics and static performance via internal current control loops [2]. However, it has a low dynamic performance and its performances depend on the quality of the current control loop, which is strongly influenced by unbalanced grid voltages. Better performances are obtained with the look up table direct power control (LUT-DPC). In this technique, the optimal switching status is selected by a switching table based on the instantaneous error between the reference values and estimated values of the active and reactive power and voltage or virtual flux position vector [3], [4]. The drawback of LUT-DPC is that the switching frequency would not remain constant. However, this problem can be solved by using pulse width modulation (PWM), space vector modulation

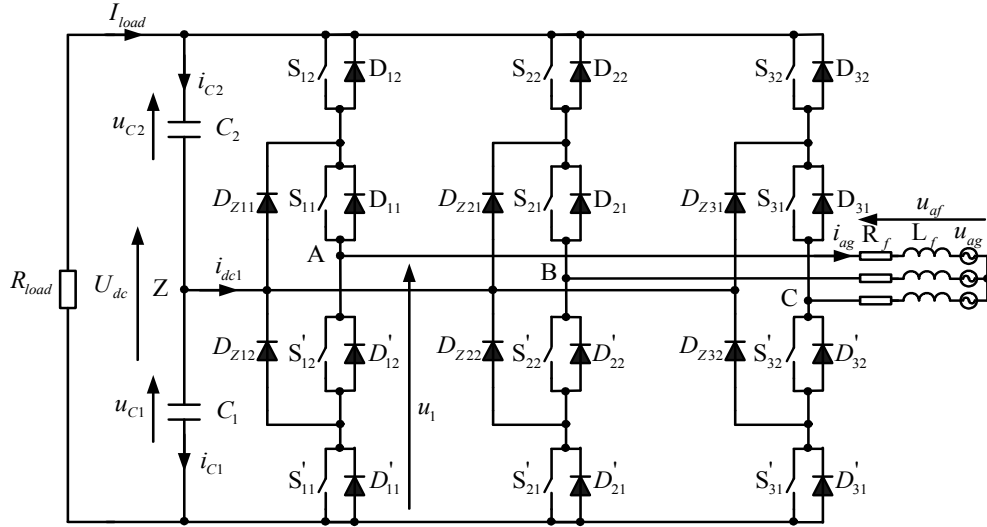


Fig. 1: The configuration of the grid connected three-level neutral point clamped inverter

(SVM) [5] or predictive control strategy, [6]. Model predictive control (MPC) has been successfully used in industrial applications due to its advantages, such as easy inclusion of non-linearities in the model, constraint and delay compensation [1], [7], [8].

The present paper presents the model MPPC-VF to control the active and reactive power for grid connected 3L-NPC while maintaining the balance between the DC link capacitor voltage and reducing the switching frequency. These objectives are accomplished through the cost function in a predictive control strategy. No current loop is considered and the inverter switches are directly obtained from cost function minimization. This control allows to improve the quality of the power regulation and to minimize the switching losses. A control horizon of two is used for the prediction, where only combination of inputs having a difference of one switch in the inverter is considered.

This paper is organized as follow: section 2 presents the mathematical prediction model for direct power control for grid connected three-level inverter. Section 3 details the proposed control method. In section 4, simulation results are represented and analyzed and finally section 5 draws the conclusion.

Model predictive power control based on virtual flux for grid connected 3L-NPC inverter

Topology

A simplified diagram of the grid connected three-level neutral point clamped inverter is presented in Fig. 1. The operating status of 3L-NPC switches can be represented by three switching states [P], [O] and [N]. The switching state [P] or [1] signifies both S_{11} and S_{12} switches in branch A are states "ON" and the inverter terminal voltage U_{AZ} has the value $+U_{dc}/2$, while [N] or [-1] indicates two switches S_{11} and S_{12} are conditions "OFF", leading to $U_{AZ} = -U_{dc}/2$. The switching state [O] or [0] means that the two internal switch S_{11} and S'_{12} are states "ON" and U_{AZ} is clamped to zero through the clamping diodes. Taking into account the three phases of the inverter, there are in total 27 possible combinations of switching states corresponding to 19 voltage vectors.

Mathematical model

The neutral-point current i_{dc1} can be calculated from three-phase grid currents and the switching states as follows [9]:

$$i_{dc1} = i_{c2} - i_{c1} = (1 - |S_a|)i_{ag} + (1 - |S_b|)i_{bg} + (1 - |S_c|)i_{cg} \quad (1)$$

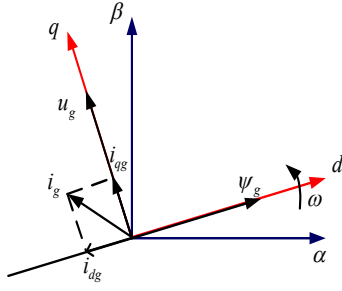


Fig. 2: Reference coordinates and vectors

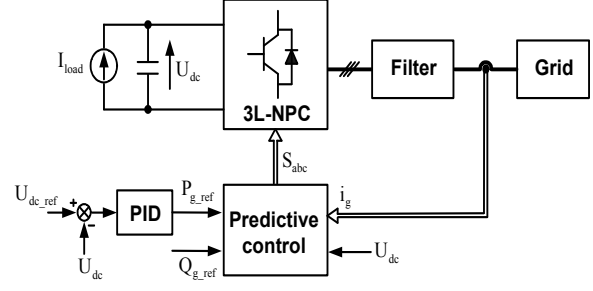


Fig. 3: Block diagram of MPPC-VF scheme

where S_x represents the state of a leg and has three possible values: $[-1, 0, 1]$.

Assuming that the DC bus voltage U_{dc} is constant and $C_1 = C_2 = C$, the neutral-point voltage can be expressed as follows:

$$\frac{du_z}{dt} = -\frac{1}{2C}(i_{c2} - i_{c1}) = -\frac{1}{2C} \left((1 - |S_a|)i_{ag} + (1 - |S_b|)i_{bg} + (1 - |S_c|)i_{cg} \right) \quad (2)$$

By using the Clarke transformation, the dynamic of neutral-point voltage u_z can be written as follows:

$$\frac{du_z}{dt} = \frac{1}{4C} \left((2|S_a| - |S_b| - |S_c|)i_{g\alpha} + \sqrt{3}(|S_b| - |S_c|)i_{g\beta} \right) \quad (3)$$

Furthermore, the system configured by the grid side inverter, filter and grid voltage can be represented as follows:

$$u_{inv} = u_g + R_f i_g + L_f \frac{di_g}{dt} \quad (4)$$

where u_g, i_g : are the grid voltage and current and R_f, L_f : are the resistance and inductance filter

The inverter output voltage u_{inv} can be estimated by the measured DC link voltage and exploiting the knowledge of the current switching state. After the $\alpha\beta$ transformation this can be expressed as:

$$u_{inv} = u_{inv\alpha} + j u_{inv\beta} = \frac{U_{dc}}{3} \left(S_a - \frac{S_b}{2} - \frac{S_c}{2} + j \left(\frac{\sqrt{3}}{2} S_b - \frac{\sqrt{3}}{2} S_c \right) \right) \quad (5)$$

On the other hand, from the Fig. 2, we can calculate the grid voltage from the virtual flux of the grid ψ_g as:

$$\begin{aligned} u_{g\alpha} &= \frac{d\psi_{g\alpha}}{dt} = -|\psi_g| \omega \sin \omega t = -\omega \psi_{g\beta} \\ u_{g\beta} &= \frac{d\psi_{g\beta}}{dt} = |\psi_g| \omega \cos \omega t = \omega \psi_{g\alpha} \end{aligned} \quad (6)$$

Based on equation (4) and (6) we can express the grid current as follows:

$$\begin{aligned} \frac{di_{g\alpha}}{dt} &= \frac{1}{L_f} \left(u_{inv\alpha} + \omega \psi_{g\beta} - R_f i_{g\alpha} \right) \\ \frac{di_{g\beta}}{dt} &= \frac{1}{L_f} \left(u_{inv\beta} - \omega \psi_{g\alpha} - R_f i_{g\beta} \right) \end{aligned} \quad (7)$$

Consequently, we have the continuous-time dynamics of the system based on the equation (3), (5), (6)

and (7):

$$\frac{di_{g\alpha}}{dt} = \frac{1}{L_f} \left(\frac{U_{dc}}{6} (2S_a - S_b - S_c) + \omega \Psi_{g\beta} - R_f i_{g\alpha} \right) \quad (8)$$

$$\frac{di_{g\beta}}{dt} = \frac{1}{L_f} \left(\frac{U_{dc}}{2\sqrt{3}} (S_b - S_c) - \omega \Psi_{g\alpha} - R_f i_{g\beta} \right)$$

$$\frac{d\Psi_{g\alpha}}{dt} = -\omega \Psi_{g\beta}$$

$$\frac{d\Psi_{g\beta}}{dt} = \omega \Psi_{g\alpha}$$

$$\frac{du_z}{dt} = \frac{1}{4C} \left((2|S_a| - |S_b| - |S_c|) i_{g\alpha} + \sqrt{3} (|S_b| - |S_c|) i_{g\beta} \right)$$

The instantaneous active and reactive powers can be calculated as follows [3]:

$$P_g = \frac{3}{2} (u_{g\alpha} i_{g\alpha} + u_{g\beta} i_{g\beta}) = \frac{3}{2} \omega (\Psi_{g\alpha} i_{g\beta} - \Psi_{g\beta} i_{g\alpha}) \quad (9)$$

$$Q_g = \frac{3}{2} (u_{g\beta} i_{g\alpha} - u_{g\alpha} i_{g\beta}) = \frac{3}{2} \omega (\Psi_{g\alpha} i_{g\alpha} + \Psi_{g\beta} i_{g\beta})$$

where ω is the angular frequency of the grid

The application of MPPC-VF for grid connected to 3L-NPC inverter

The aim of the predictive direct power control scheme is to minimize the error between the predicted active and reactive powers and their reference values, to maintain voltage balance of the capacitor and to reduce the switching frequency (Fig. 3). In order to achieve these objectives, the cost function for 3L-NPC will use the following ingredients suggested in [1], [7], [8]:

$$g = |P_g^* - P_g^p| + |Q_g^* - Q_g^p| + \lambda_{dc} |u_z^p| + \lambda_n n_c \quad (10)$$

Where P_g^p and Q_g^p are the predicted active and reactive powers; P_g^* and Q_g^* are the reference values for active and reactive powers.

In the cost function (10) n_c penalises the number of switching change when the switching state $S(k)$ is applied compared with previous state $S(k-1)$. It can be expressed as follows:

$$n_c = |S_a(k) - S_a(k-1)| + |S_b(k) - S_b(k-1)| + |S_c(k) - S_c(k-1)| \quad (11)$$

And λ_{dc}, λ_n are the weighting factor of the capacitor voltage balancing and the reduction of commutation.

In the real time implementation, because of calculation time of the control law, there will be a sample time delay in the actuation [7], [8]. A simple solution to compensate the computational delay is at time instant k to compute the cost function corresponding to time instant $k+1$ using an estimation of the state at time $k+1$ and then the optimal switching state is applied at time $k+1$. As previously mentioned, there are 27 voltage vectors which have to be evaluated within one step prediction. When two steps are considered for prediction, a discrete set of 27^2 possible trajectories of the voltage vector have to be considered. Consequently, their evaluation leads to large number of cost computations and make difficult to implement the algorithm in practice. In order to reduce the numbers of real time evaluations only combinations of inputs having one switching variation are considered, as illustrated in Fig. 4. For example if $u(k+1) = [1 \ -1 \ -1]$ or $[0 \ 1 \ 1]$, for $u(k+2)$ the set $[1 \ -1 \ -1]$, $[1 \ 0 \ -1]$, $[0 \ -1 \ -1]$ and $[1 \ -1 \ 0]$ or $[1 \ 1 \ 1]$, $[0 \ 1 \ 0]$, $[0 \ 0 \ 1]$, $[-1 \ 1 \ 1]$ and $[0 \ 1 \ 1]$ are considered. This leads to 135 combinations. Therefore, the computations required are limited with respect to the total of $27^2 = 729$ trajectories.

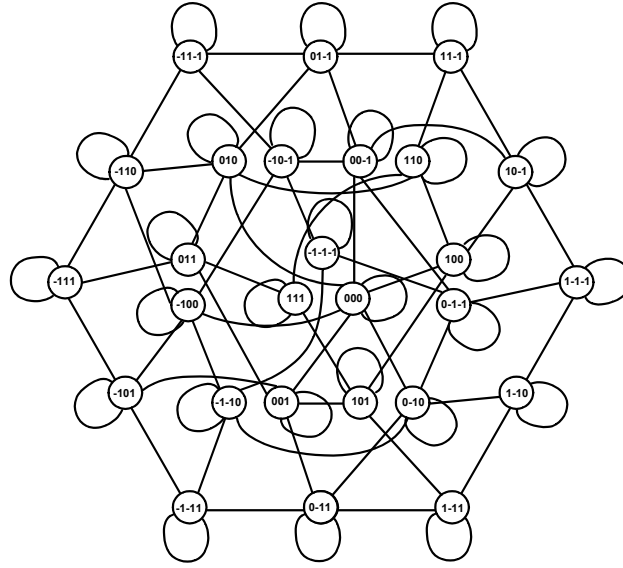


Fig. 4: The switching transitions of 3L-NPC inverter

Where the resistance is neglected, the virtual grid flux Ψ_g can be estimated by the virtual flux of the inverter Ψ_{inv} as:

$$\Psi_g = \Psi_{inv} - L_f i_g \quad (12)$$

According to equation (8), the grid currents are represented in discrete-time by using the first-order forward Euler approximation as follows:

$$\begin{aligned} i_{g\alpha}^p(k+1) &= \left(1 - \frac{R_f T_s}{L_f}\right) i_{g\alpha}(k) + \frac{T_s}{L_f} \omega \Psi_{g\beta}(k) + \frac{T_s}{6L_f} U_{dc} (2S_a(k) - S_b(k) - S_c(k)) \\ i_{g\beta}^p(k+1) &= \left(1 - \frac{R_f T_s}{L_f}\right) i_{g\beta}(k) - \frac{T_s}{L_f} \omega \Psi_{g\alpha}(k) + \frac{T_s}{2\sqrt{3}L_f} U_{dc} (S_b(k) - S_c(k)) \end{aligned} \quad (13)$$

Thus, the discrete-time forms for grid currents are expressed at instant $k+2$ as follows:

$$\begin{aligned} i_{g\alpha}^p(k+2) &= \left(1 - \frac{R_f T_s}{L_f}\right) i_{g\alpha}^p(k+1) + \frac{T_s}{L_f} \omega \Psi_{g\beta}^p(k+1) + \frac{T_s}{6L_f} U_{dc} (2S_a(k+1) - S_b(k+1) - S_c(k+1)) \\ i_{g\beta}^p(k+2) &= \left(1 - \frac{R_f T_s}{L_f}\right) i_{g\beta}^p(k+1) - \frac{T_s}{L_f} \omega \Psi_{g\alpha}^p(k+1) + \frac{T_s}{2\sqrt{3}L_f} U_{dc} (S_b(k+1) - S_c(k+1)) \end{aligned} \quad (14)$$

where the grid virtual flux $\Psi_{g\alpha}(k)$, $\Psi_{g\beta}(k)$ are estimated from the equations (5) and (12).

In the same way, the grid virtual fluxes are expressed based on the equation (8) and (12) as:

$$\begin{aligned} \Psi_{g\alpha}^p(k+1) &= \Psi_{inv\alpha}^p(k+1) - L_f i_{g\alpha}^p(k+1) \\ \Psi_{g\beta}^p(k+1) &= \Psi_{inv\beta}^p(k+1) - L_f i_{g\beta}^p(k+1) \\ \Psi_{g\alpha}^p(k+2) &= \Psi_{g\alpha}^p(k+1) - T_s \omega \Psi_{g\beta}^p(k+1) \\ \Psi_{g\beta}^p(k+2) &= \Psi_{g\beta}^p(k+1) + T_s \omega \Psi_{g\alpha}^p(k+1) \end{aligned} \quad (15)$$

The evolution of neutral-point voltage for two-step prediction is obtained by using equation (8). Then its

expression is given by:

$$\begin{aligned}
u_z^p(k+1) &= u_z(k) + \frac{\sqrt{3}T_s}{4C} (|S_b(k)| - |S_c(k)|) i_{g\beta}(k) + \frac{T_s}{4C} (2|S_a(k)| - |S_b(k)| - |S_c(k)|) i_{g\alpha}(k) \\
u_z^p(k+2) &= u_z^p(k+1) + \frac{\sqrt{3}T_s}{4C} (|S_b(k+1)| - |S_c(k+1)|) i_{g\beta}^p(k+1) \\
&\quad + \frac{T_s}{4C} (2|S_a(k+1)| - |S_b(k+1)| - |S_c(k+1)|) i_{g\alpha}^p(k+1)
\end{aligned} \tag{16}$$

As a result, the cost function of MPPC-VF for the grid connected 3L-NPC inverter with two-step prediction can be expressed as follows:

$$g = |P_g^*(k+2) - P_g^p(k+2)| + |Q_g^*(k+2) - Q_g^p(k+2)| + \lambda_{dc} |u_z^p(k+2)| + \lambda_n n_c \tag{17}$$

The future powers $P_g^*(k+2)$ and $Q_g^*(k+2)$ are estimated by extrapolation using Lagrange method based on the present and passed values. This process can be expressed as follows [1]:

$$\begin{aligned}
P_g^*(k+2) &= 6P_g^*(k) - 8P_g^*(k-1) + 3P_g^*(k-2) \\
Q_g^*(k+2) &= 6Q_g^*(k) - 8Q_g^*(k-1) + 3Q_g^*(k-2)
\end{aligned} \tag{18}$$

Finally, the objective of proposed predictive control can be obtained by evaluation of the cost function for all trajectories (see algorithm 1).

Algorithm 1 Algorithm of model predictive power control based on virtual flux for grid connected 3L-NPC

```

Measure  $i_g(k)$ ,  $u_z(k)$ ,  $u_g(k)$  and  $U_{dc}$ ; Read the reference values  $U_{dc-ref}(k)$  and  $Q_g^*(k)$ 
Estimation of inverter flux  $\psi_{inv}(k)$ ; Extrapolation of references  $P_g^*(k+2)$  and  $Q_g^*(k+2)$ 
%comment:  $x_{opt}$ ,  $g_{opt}$  are the optimal values of the switching states and cost function%
Initialize optimal values of the switching state and cost function:  $x_{opt}$ ,  $g_{opt}$ 
for  $i = 1$  to 27 do
  Compute predictions:  $i_g^p(k+1)$ ,  $\psi_g^p(k+1)$  and  $u_z^p(k+1)$ 
  Predict switching transitions corresponding
  for  $j = 1$  to  $m$  do %comment:  $m = \text{length}(\text{switching transitions corresponding})$ 
    Predict next state of variables:  $i_g^p(k+2)$ ,  $\psi_g^p(k+2)$  and  $u_z^p(k+2)$ 
    Calculate the number of switching change  $n_c$ 
    Estimation of power:  $P_g^p(k+2)$  and  $Q_g^p(k+2)$ 
    Compute the cost function  $g$ 
    if  $g < g_{opt}$  then
       $g_{opt} = g$ ;  $x_{opt} = i$ 
    end if
  end for
end for
Store the present value of  $x_{opt}$ 
Apply optimal switching states:  $S_a, S_b, S_c$ 

```

Simulation results

To analysis the operation of the MPPC-VF system under different conditions, the whole control scheme has been simulated using Matlab software with the parameters as indicated in Table I.

In order to evaluate the steady state performance, one can use the mean absolute percentage error (MAPE)

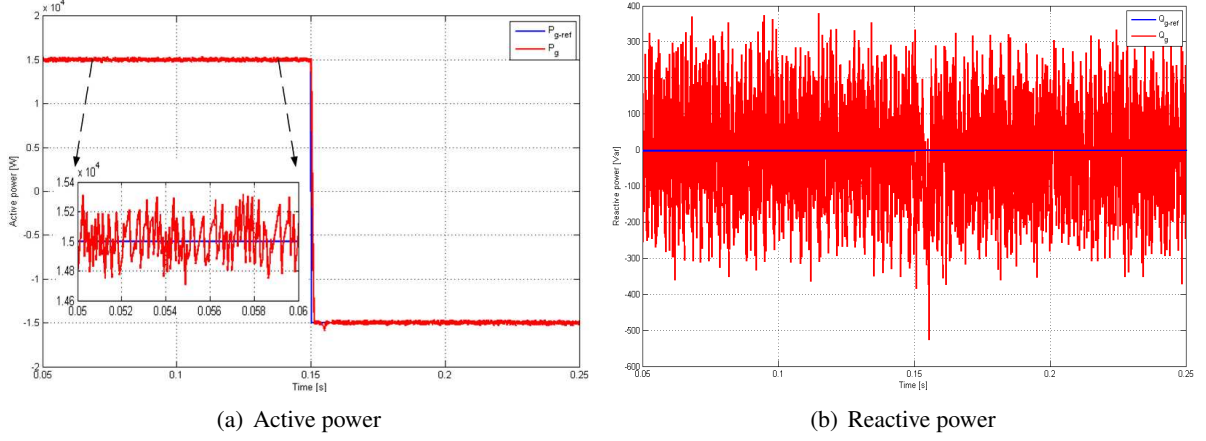


Fig. 5: The transient power responses for a step in the active power reference from 15 to -15 kW

which can be express as follows:

$$MAPE = \frac{1}{n} \sum_{i=1}^n \left| \frac{y_i^* - y_i}{y_i^*} \right| \quad (19)$$

where y_i^* is the reference vector and y_i is the vector measured.

On other hand, in an attempt to estimate the average switching frequency per semiconductor ($\overline{f_{sw}}$) of MPPC-VF, the following expression proposed in [1] can serve as appropriate indicator:

$$\overline{f_{sw}} = \sum_{x=a,b,c} \frac{f_{sw1x} + f_{sw2x}}{6} \quad (20)$$

where f_{sw1x} , f_{sw2x} are the switching frequency of each upper switch and are calculated by measuring the number of switching changes in the gating signals for 15 fundamental cycles.

Table I: Electrical and control parameters for simulation

Parameter	Value	Description
U_{dc}	600 [V]	DC-link voltage
C	940 [μ F]	DC-link capacitor
R_f	80 [$m\Omega$]	Grid-side filter resistance
L_f	10 [mH]	Grid-side filter inductance
f_s	20 [kHz]	Sampling frequency for MPPC-VF
f_{svm}	5 [kHz]	Sampling frequency for DPC-SVM
f	50 [Hz]	Frequency of the grid
U	220 [V]	Rated phase grid voltage
P_{dm}	15 [kW]	Rated active power
I_{dm}	32 [A]	Rated current

The performance of the proposed method for power control was tested by applying a step in the active power reference P_g^* without DC bus voltage control. The step change in P_g^* from 15 to -15 kW is applied at $t = 0.15$ s, with a unity power factor reactive power set point set to zero $Q_g^* = 0$ (Fig. 5). The active power tracks the assigned reference with very good performance in what it may concern during the transient and steady-state conditions. The phase shift between the voltage and current is also shown for this change in the power factor (Fig.6).

With the purpose of verify the dynamic performances, we perform a comparison between MPPC-VF and

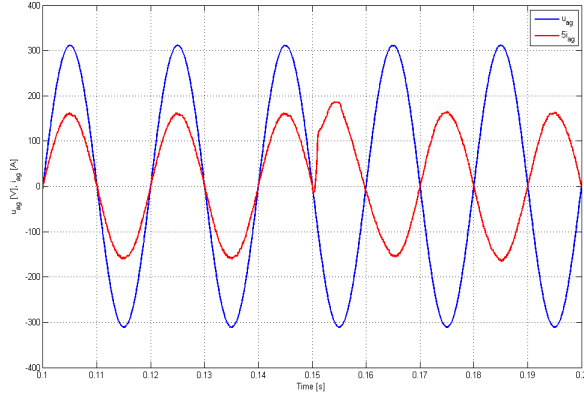


Fig. 6: The responses of voltage and current

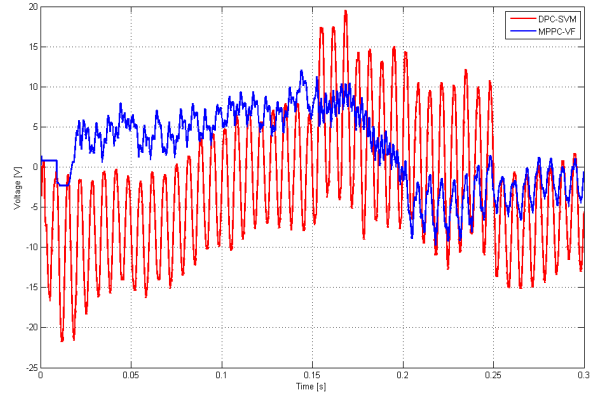
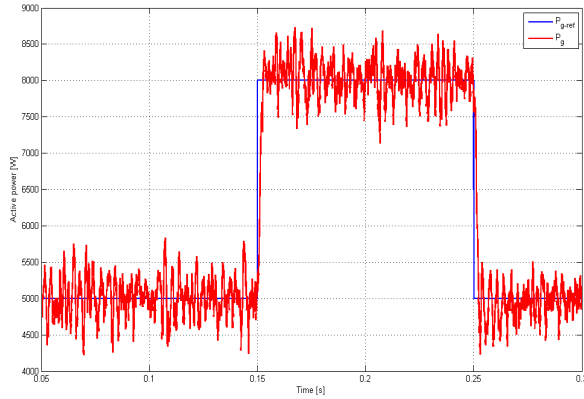
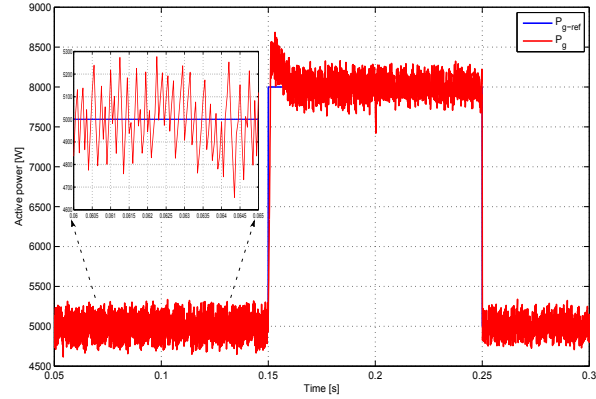


Fig. 7: Performance of the u_z



(a) DPC-SVM



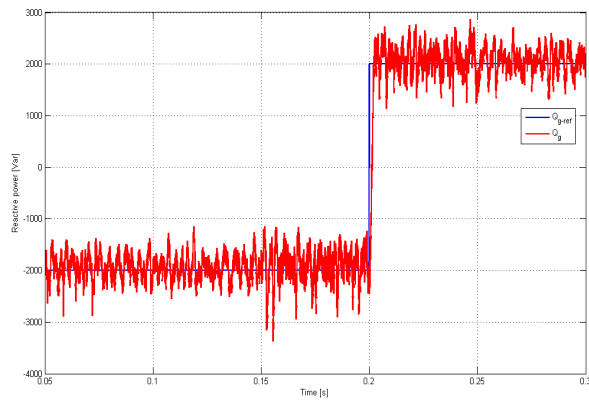
(b) MPPC-VF

Fig. 8: The dynamic response of active power

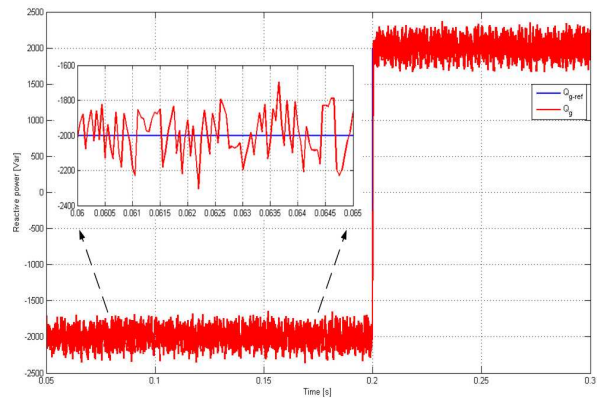
DPC-SVM [2] for different conditions of references and the same parameters in Table I. To generate the same average switching frequency per semiconductor, the sampling frequency of the MPPC-VF is considered $f_s = 20$ kHz and the sampling frequency of the DPC-SVM is 5 kHz. For the SVM at 5 kHz, $\overline{f_{sw}} = 2.5$ kHz and the measured value of $\overline{f_{sw}}$ for MPPC-VF is about 2.5 kHz. In order to observe the dynamics ability to track the active and reactive power reference, several power steps has been applied to its references in the grid side. The active power reference presents a steps from 5 kW to 8 kW at $t = 0.15$ s, then steps from 8 kW to 5 kW at $t = 0.25$ s (Fig. 8), while the reactive power reference is changed from -2 kVar to 2 kVar at $t = 0.2$ s (Fig. 9). The results shown in Figs. 8 and 9 indicate that the active power is tracking the reference with fast dynamics and without affecting the reactive power (the average computation time of the algorithm is 0.17 ms in a 3.7 GHz, i7 CPU, permitting a real time implementation). The proposed method presents better performance compared to the DPC-SVM. The mean absolute percentage error of active and reactive power for DPC-SVM is 4.15% and 12.15%, whereas, for the proposed method they are 2.07%, and 5.43%, respectively.

The steady state behavior of the inverter phase voltage is performed with the total harmonic distortion (THD) for the proposed method of 15.25% in comparison with a classical method distortion of 21.92%. The transient responses of the grid current, i_{ag} and its Fast Fourier Transform (FFT) are illustrated in Figs. 10 and 11, where the THD is found to be lower for the proposed method in contrast to the classical controller. The THD of grid current is under the 5% limit required by the IEEE 519 standards.

At the instant $t = 0.15$ s, a resistance load change from $R_{load} = 72 \Omega$ to $R_{load} = 45 \Omega$ is produced. The effect of DC side load on the operation of system is shown in Fig 12. As shown in this figure the change in load doesn't affect DC link voltage and only change the amplitude of the grid current and the grid side active power. One of the important issues of 3L-NPC structure is the balancing voltage of the DC-link

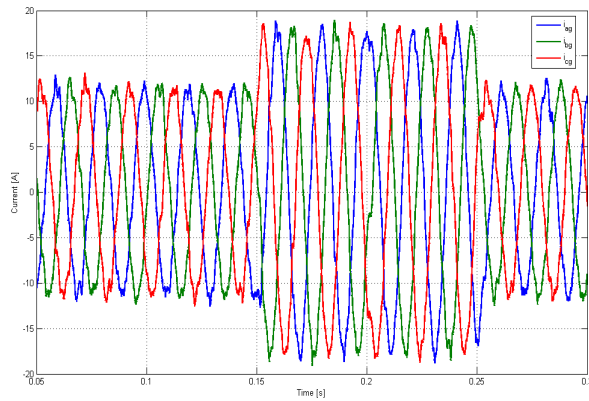


(a) DPC-SVM

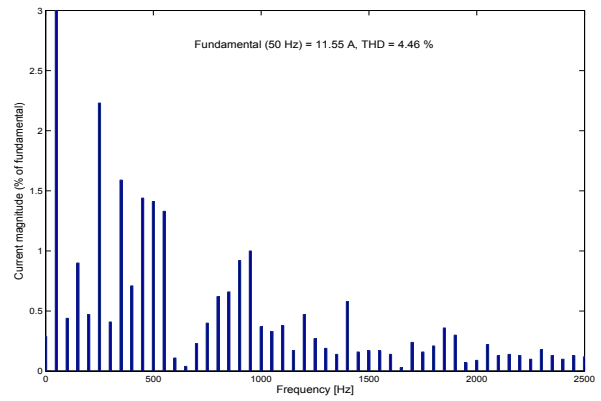


(b) MPPC-VF

Fig. 9: The dynamic response of reactive power

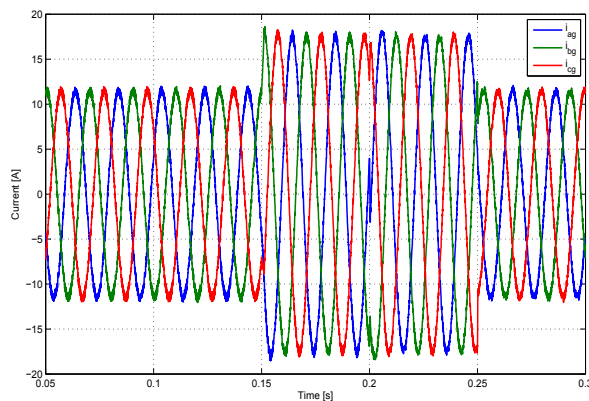


(a) The steady state and transient response of the grid current

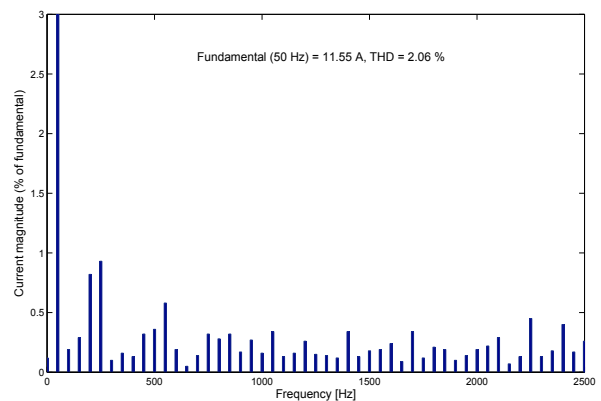


(b) Fast Fourier Transform of the grid current

Fig. 10: The dynamic response and FFT of the grid current for DPC-SVM



(a) The steady state and transient response of the grid current



(b) Fast Fourier Transform of the grid current

Fig. 11: The dynamic response and FFT of the grid current for MPPC-VF

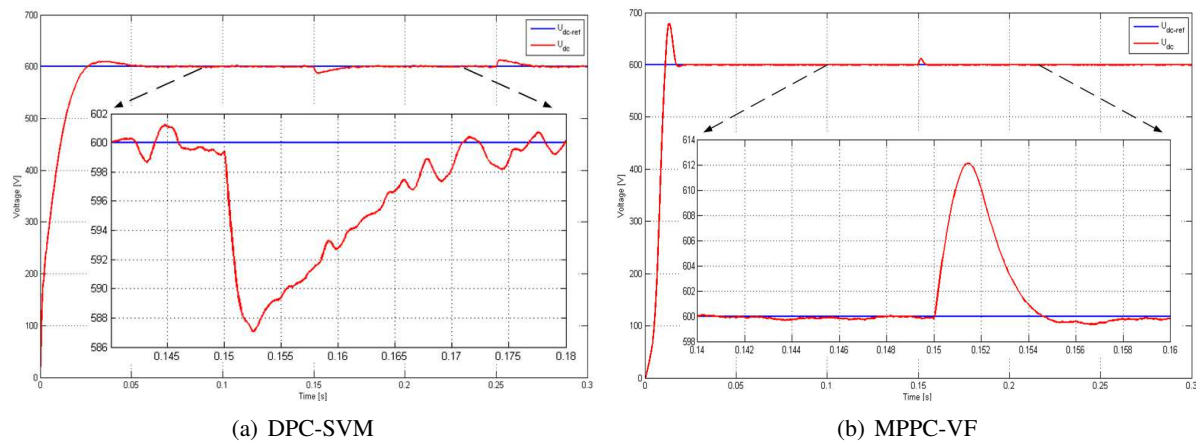


Fig. 12: The DC-link voltage response with load change at $t = 0.15$ [s]

capacitor. It is obvious that the voltage of DC-link capacitor remains balanced with MAPE of DC-link capacitor voltages deviation for MPPC-VF 0.51% and for DPC-SVM 1.1% in spite of the transition of the power references (Fig. 7).

Conclusion

This study proposes the MPPC-VF strategy with two steps prediction for grid connected 3L-NPC inverter. First, the mathematical model of the system is established, and then the cost function, which contains the power error, the capacitor voltage balancing and the reduction of the switching frequency, is established. In this paper, there is a comparison between DPC-SVM and MPPC-VF for 3L-NPC inverter. The simulation results show that the algorithm can successfully track the active and reactive powers, maintain their balanced capacitor voltages and reduce the switching frequency. In addition, by using proposed method we can reduce the switching losses and high number of calculations while maintaining an acceptable quality of current and power. The linear PI controllers for the power, current loops and the modulation block are further eliminated. Therefore, the proposed method is an interesting alternative to control the grid connected 3L-NPC inverter.

References

- [1] Jose Rodriguez and Patricio Cortes.: Predictive Control of Power Converters and Electrical Drives, John Wiley, 2012
- [2] Mohamed Abbes and Jamel Belhadji.: New control method of a robust NPC converter for renewable energy source grid connection, Electric Power Systems Research, Vol 88, pp. 52-63, 2012
- [3] Mariusz Malinowski, Marian P. Kazmierkowski, Steffan Hansen, Frede Blaabjerg and G. D. Marques.: Virtual-Flux-Based Direct Power Control of Three-Phase PWM Rectifiers, IEEE Transaction On Industrial Electronics, Vol 37 no 4, pp. 1019-1027, 2001
- [4] L. A. Serpa and J. W. Kolar.: Virtual-Flux Direct Power Control for Mains Connected Three-Level NPC Inverter Systems, Proceeding of Power Conversion Conference PCC '07, Nagoya, pp. 130-136, 2007
- [5] M. Malinowski, M. Jasinski and M. P. Kazmierkowski.: Simple direct power control of three phase pwm rectifier using space vector modulation (dpc-svm), IEEE Transaction On Industrial Electronics, Vol 51 no 2, pp. 447-454, 2004
- [6] James Scoltock, Tobias Geyer and Udaya K. Madawala.: Model Predictive Direct Power Control for Grid-Connected Neutral-Point-Clamped Converters, IEEE Transaction On Industrial Electronics, Vol 62 no 9, pp. 5319-5328, 2015
- [7] Jose Rodriguez, Marian P. Kazmierkowski, Jose R. Espinoza, Pericle Zanchetta, Haitham Abu-Rub, Hector A. Young and Christian A. Rojas.: State of the Art of Finite Control Set Model Predictive Control in Power Electronics, IEEE Transaction on Industrial Informatics, Vol 9 no 2, pp. 1003-1016, 2013
- [8] P. Cortes, J. Rodriguez, P. Antoniewicz and M. Kazmierkowski.: Direct Power Control of an AFE Using Predictive Control, IEEE Transactions on Power Electronics, Vol 23 no 5, pp. 2516-2523, 2008
- [9] B. Q. V. Ngo, P. R. Ayerbe and S. Olaru.: Model Predictive Control with Two-step horizon for Three-level Neutral-Point Clamped inverter, Proceeding of 20th International Conference on Process Control, Slovakia, pp. 215-220, 2015