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# Reasoning about Data Repetitions with Counter Systems

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**Abstract**—We study linear-time temporal logics interpreted over data words with multiple attributes. We restrict the atomic formulas to equalities of attribute values in successive positions and to repetitions of attribute values in the future or past. We demonstrate correspondences between satisfiability problems for logics and reachability-like decision problems for counter systems. We show that allowing/disallowing atomic formulas expressing repetitions of values in the past corresponds to the reachability/coverability problem in Petri nets. This gives us  $2\text{EXPSPACE}$  upper bounds for several satisfiability problems. We prove matching lower bounds by reduction from a reachability problem for a newly introduced class of counter systems. This new class is a succinct version of vector addition systems with states in which counters are accessed via pointers, a potentially useful feature in other contexts. We strengthen further the correspondences between data logics and counter systems by characterizing the complexity of fragments, extensions and variants of the logic. For instance, we precisely characterize the relationship between the number of attributes allowed in the logic and the number of counters needed in the counter system.

## I. INTRODUCTION

*Words with multiple data:* Finite data words [4] are ubiquitous structures that include timed words, runs of counter automata or runs of concurrent programs with an unbounded number of processes. These are finite words in which every position carries a label from a finite alphabet and a data value from some infinite alphabet. More generally, structures over an infinite alphabet provide an adequate abstraction for objects from several domains: for example, infinite runs of counter automata can be viewed as infinite data words, finite data trees model XML documents with attribute values [9] and so on. A wealth of specification formalisms for data words (or slight variants) has been introduced stemming from automata, see e.g. [26], [29], to adequate logical languages such as first-order logic [1], [5] or temporal logics [23], [19], [9], [17], [10] (see also a related formalism in [12]). Depending on the type of structures, other formalisms have been considered such as XPath [9] or monadic second-order logic [3]. In full generality, most formalisms lead to undecidable decision problems and a well-known research trend consists of finding a good trade-off between expressiveness and decidability. Restrictions to regain decidability are protean: bounding the models (from trees to words for instance), restricting the number of variables, see e.g. [1], limiting the set of the temporal operators or the use

of the data manipulating operator, see e.g. [11], [6]. Moreover, interesting and surprising results have been exhibited about relationships between logics for data words and counter automata [1], [7], [2], leading to a first classification of automata on data words [2]. This is why logics for data words are not only interesting for their own sake but also for their deep relationships with data automata or with counter automata. Herein, we pursue further this line of work and we work with words in which every position contains a *vector* of data values.

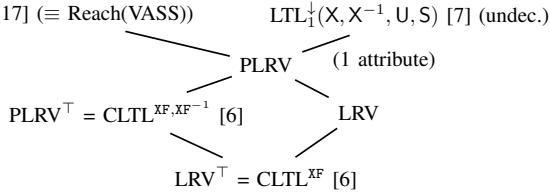
*Motivations:* In [6], a decidable logic interpreted over (finite or infinite) sequences of variable valuations (understood as words with multiple data) is introduced in which the atomic formulae are of the form either  $x \approx X^i y$  or  $x \approx \langle \top? \rangle y$ . The formula  $x \approx X^i y$  states that the current value of variable  $x$  is the same as the value of  $y$   $i$  steps ahead (local constraint) whereas  $x \approx \langle \top? \rangle y$  states that the current value of  $x$  is repeated in a future value of  $y$  (future obligation). Such atomic properties can be naturally expressed with a *freeze* operator that stores a data value for later comparison, and in [6], it is shown that the satisfiability problem is decidable with the temporal operators in  $\{X, X^{-1}, U, S\}$ . The freeze operator allows to store a data value in a register and then to test later equality between the value in the register and a data value at some other position. This is a powerful mechanism but the logic in [6] uses it in a limited way: only repetitions of data values can be expressed and it restricts very naturally the use of the freeze operator. The decidability result is robust since, for instance, it holds with the addition of atomic formulas of the form  $x \approx \langle \top? \rangle^{-1} y$  stating that the current value of  $x$  is repeated in a past value of  $y$  (past obligation). Decidability can be shown either by reduction into  $\text{FO}^2(\sim, <, +\omega)$ , a first-order logic over data words introduced in [1] or by reduction into the verification of fairness properties in Petri nets, shown decidable in [16]. In both cases, an essential use of the decidability of the reachability problem for Petri nets is made, for which no primitive recursive algorithm is known, see e.g. [21]. Hence, even though the logics shown decidable in [6] poorly use the freeze operator (the only properties about data are related to controlled repetitions), the complexity of their satisfiability problems is unknown. Moreover, it is unclear whether the reductions into the reachability problem for Petri nets are really needed; this would be the case if reductions in the other direction exist. Note that in [17], a richer logic has been introduced and it has been shown that satisfiability

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is equivalent to the reachability problem for Petri nets.

Our main motivation is to investigate logics that express repetitions of values, revealing the correspondence between expressivity of the logic and reachability problems for counter machines, including well-known problems for Petri nets. This work can be seen as a study of the precision with which counting needs to be done as a consequence of having a mechanism for demanding “*the current data value is repeated in the future/past*” in a logic. Hence, this is not the study of yet another logic, but of a natural feature shared by most studied logics on data words [6], [1], [7], [11], [17], [9], [10]: the property of demanding that a data value be repeated. We consider different ways in which one can demand the repetition of a value, and study the repercussion in terms of the “precision” with which we need to count in order to solve the satisfiability problem. Our measurement of precision here distinguishes the reachability versus the coverability problem for Petri nets and the number of counters needed as a function of the number of variables used in the logic.

*Our contribution:* We introduce the logic LRV (“Logic of Repeating Values”) interpreted over finite words with multiple data, equipped with atomic formulas of the form either  $x \approx X^i y$  or  $x \approx \langle \phi? \rangle y$  [resp.  $x \not\approx \langle \phi? \rangle y$ ], where  $x \approx \langle \phi? \rangle y$  [resp.  $x \not\approx \langle \phi? \rangle y$ ] states that the current value of  $x$  is repeated [resp. is not repeated] in some future value of  $y$  in a position where  $\phi$  holds true. When we impose  $\phi = \top$ , the logic introduced in [6] is obtained and it is denoted by  $\text{LRV}^\top$ . The syntax for future obligations is freely inspired from PDL with its test operator ‘?’ . Even though LRV contains the past-time temporal operators  $X^{-1}$  and  $S$ , it has no past obligations. We write PLRV to denote the extension of LRV with past obligations of the form  $x \approx \langle \phi? \rangle^{-1} y$  or  $x \not\approx \langle \phi? \rangle^{-1} y$ . Below, we illustrate how LRV and variants are compared to existing data logics.



Our main results are listed below.

1. We begin where [6] stopped: the reachability problem for Petri nets is reduced to the satisfiability problem of PLRV (i.e., the logic with past obligations).
2. Without past obligations, the satisfiability problem is much easier: we reduce the satisfiability problem of  $\text{LRV}^\top$  and LRV to the control-state reachability problem for VASS, via a detour to a reachability problem on gainy VASS. But the number of counters in the VASS is exponential in the number of variables used in the formula. This gives us a 2EXSPACE upper bound.
3. The exponential blow up mentioned above is unavoidable: we show a polynomial-time reduction in the converse direction, starting from a linear sized counter machine (without zero tests) that can access exponentially many counters.

This gives us a matching 2EXSPACE lower bound.

4. Several augmentations to the logic do not alter the complexity: we show that complexity is preserved when MSO-definable temporal operators are added or when infinite words with multiple data are considered.
5. The power of nested testing formulas: we show that the complexity of the satisfiability problem for  $\text{LRV}^\top$  reduces to PSPACE-complete when the number of variables in the logic is bounded by a constant, while the complexity of the satisfiability of LRV does not reduce even when only one variable is allowed. Recall that the difference between  $\text{LRV}^\top$  and LRV is that the later allows any  $\phi$  in  $x \approx \langle \phi? \rangle y$  while the former restricts  $\phi$  to just  $\top$ .
6. The power of pairs of repeating values: we show that the satisfiability problem of  $\text{LRV}^\top$  augmented with  $\langle x, y \rangle \approx \langle \top? \rangle \langle x', y' \rangle$  (repetitions of pairs of data values) is undecidable, even when  $\langle x, y \rangle \approx \langle \top? \rangle^{-1} \langle x', y' \rangle$  is not allowed (i.e., even when past obligations are not allowed).
7. Implications for classical logics: we show a 3EXSPACE upper bound for the satisfiability problem for *forward-EMSO*<sup>2</sup>(+1, <, ~) over data words, using results on LRV.

For proving the result mentioned in point 3 above, we introduce a new class of counter machines that we call *chain systems* and show a key hardness result for them. This class is interesting for its own sake and could be used in situations where the power of binary encoding needs to be used. We prove the  $(k+1)$ EXSPACE-completeness of the control state reachability problem for chain systems of level  $k$  (we only use  $k = 1$  in this paper but the proof for arbitrary  $k$  is no more complex than the proof for the particular case of  $k = 1$ ). In chain systems, the number of counters is equal to an exponential tower of height  $k$  but we cannot access the counters directly in the transitions. Instead, we have a pointer that we can move along a chain of counters. The  $(k+1)$ EXSPACE lower bound is obtained by a non-trivial extension of the EXSPACE-hardness result from [22], [8]. Then we show that the control state reachability problem for the class of chain systems with  $k = 1$  can be reduced to the satisfiability problem for LRV (see Section V). It was known that data logics are strongly related to classes of counter automata, see e.g. [1], [7], [2] but herein, we show how varying the expressive power of logics leads to correspondence with different reachability problems for counter machines.

## II. PRELIMINARIES

We write  $\mathbb{N}$  [resp.  $\mathbb{Z}$ ] to denote the set of non-negative integers [resp. integers] and  $[i, j]$  to denote  $\{k \in \mathbb{Z} : i \leq k \text{ and } k \leq j\}$ . For  $\mathbf{v} \in \mathbb{Z}^n$ ,  $\mathbf{v}(i)$  denotes the  $i^{\text{th}}$  element of  $\mathbf{v}$  for every  $i \in [1, n]$ . We write  $\mathbf{v} \preceq \mathbf{v}'$  whenever for every  $i \in [1, n]$ , we have  $\mathbf{v}(i) \leq \mathbf{v}'(i)$ . For a (possibly infinite) alphabet  $\Sigma$ ,  $\Sigma^*$  represents the set of finite words over  $\Sigma$ ,  $\Sigma^+$  the set of finite non-empty words over  $\Sigma$ . For a finite word or sequence  $u = a_1 \dots a_k$  over  $\Sigma$ , we write  $|u|$  to denote its length  $k$ . For  $0 \leq i < |u|$ ,  $u(i)$  represents the  $(i+1)$ -th letter of the word, here  $a_{i+1}$ .

*Logics of Repeating Values:* Let  $\text{VAR} = \{x_1, x_2, \dots\}$  be a countably infinite set of *variables*. We denote by LRV the logic whose formulas are defined as follows, where  $x, y \in \text{VAR}$ ,  $i \in \mathbb{N}$ .

$$\begin{aligned} \phi ::= & x \approx X^i y \mid x \approx \langle \phi? \rangle y \mid x \not\approx \langle \phi? \rangle y \mid \phi \wedge \phi \mid \neg \phi \mid \\ & X\phi \mid \phi U \phi \mid X^{-1}\phi \mid \phi S \phi \end{aligned}$$

A *valuation* is a map from  $\text{VAR}$  to  $\mathbb{N}$ , and a *model* is a finite non-empty sequence  $\sigma$  of valuations. For every model  $\sigma$  and  $0 \leq i < |\sigma|$ , the satisfaction relation  $\models$  is defined:

- $\sigma, i \models x \approx X^j y$  iff  $i+j < |\sigma|$  and  $\sigma(i)(x) = \sigma(i+j)(y)$ ,
- $\sigma, i \models x \approx \langle \phi? \rangle y$  iff there exists  $j$  such that  $i < j < |\sigma|$ ,  $\sigma(i)(x) = \sigma(j)(y)$  and  $\sigma, j \models \phi$ .

The semantics  $x \not\approx \langle \phi? \rangle y$  is defined similarly but asking for a different data value. The temporal operators next ( $X$ ), previous ( $X^{-1}$ ), until ( $U$ ) and since ( $S$ ) and Boolean connectives are defined in the usual way. We also use the standard derived temporal operators ( $G, F, F^{-1}, \dots$ ) and constants  $\top, \perp$ . We write  $\sigma \models \phi$  if  $\sigma, 0 \models \phi$ . Given a set of temporal operators  $\mathcal{O}$ , we write  $\text{LRV}(\mathcal{O})$  to denote the fragment of LRV restricted to formulas with operators from  $\mathcal{O}$ . The *satisfiability problem* for LRV (written  $\text{SAT}(\text{LRV})$ ) is to check for an LRV formula  $\phi$ , whether there is  $\sigma$  such that  $\sigma \models \phi$ . Let  $\text{PLRV}$  be the extension of LRV with additional atomic formulas of the form  $x \approx \langle \phi? \rangle^{-1} y$  and  $x \not\approx \langle \phi? \rangle^{-1} y$ . The satisfaction relation is extended as expected:  $\sigma, i \models x \approx \langle \phi? \rangle^{-1} y$  iff there is  $0 \leq j < i$  such that  $\sigma(i)(x) = \sigma(j)(y)$  and  $\sigma, j \models \phi$ , and similarly for  $x \not\approx \langle \phi? \rangle^{-1} y$ . We write  $\text{LRV}^\top$  [resp.  $\text{PLRV}^\top$ ] to denote the fragment of LRV [resp.  $\text{PLRV}$ ] in which atomic formulas are restricted to  $x \approx X^i y$  and  $x \approx \langle \top? \rangle y$  [resp. to  $x \approx X^i y$ ,  $x \approx \langle \top? \rangle y$  and  $x \approx \langle \top? \rangle^{-1} y$ ]. In [6],  $\text{LRV}^\top$  and  $\text{PLRV}^\top$  were shown to be decidable by reduction into the reachability problem for Petri nets. However, the characterization of their complexity remained open.

*Properties:* In the table below, we justify our choices for atomic formulae by presenting several abbreviations (with their obvious semantics). By contrast, we include in LRV both  $x \approx \langle \phi? \rangle y$  and  $x \not\approx \langle \phi? \rangle y$  when  $\phi$  is an arbitrary formula since there is no obvious way to express one with the other.

Abbreviation	Definition
$x \not\approx X^i y$	$\neg(x \approx X^i y) \wedge \overbrace{X \dots X}^{i \text{ times}} \top$
$x \approx X^{-i} y$	$\overbrace{X^{-1} \dots X^{-1}}^{i \text{ times}} (X^i x \approx y)$
$x \not\approx X^{-i} y$	$\neg(x \approx X^{-i} y) \wedge \overbrace{X^{-1} \dots X^{-1}}^{i \text{ times}} \top$
$x \not\approx \langle \top? \rangle y$	$(x \not\approx X y) \vee X((y \approx X y) U (y \not\approx X y))$
$x \not\approx \langle \top? \rangle^{-1} y$	$(x \not\approx X^{-1} y) \vee X^{-1}((y \approx X^{-1} y) S (y \not\approx X^{-1} y))$

Models for LRV can be viewed as finite data words in  $(\Sigma \times \mathbb{D})^*$ , where  $\Sigma$  is a finite alphabet and  $\mathbb{D}$  is an infinite domain. E.g., equalities between dedicated variables can simulate that a position is labelled by a letter from  $\Sigma$ ; moreover, we may assume that the data values are encoded with the variable  $x$ . Let us express that whenever there are  $i < j$  s.t.  $i$  and  $j$  [resp.  $i+1$  and  $j+1$ ,  $i+2$  and  $j+2$ ] are labelled by  $a$  [resp.  $a'$ ,

$a''$ ],  $\sigma(i+1)(x) \neq \sigma(j+1)(x)$ . This can be stated in LRV by:  $G(a \wedge X(a' \wedge X a'')) \Rightarrow X \neg(x \approx (X^{-1} a \wedge a' \wedge X a''))(x)$ . This is an example of key constraints, see e.g. [27, Definition 2.1] and the paper contains also numerous examples of properties that can be captured by LRV.

*Basics on VASS:* A *vector addition system with states* (VASS) is a tuple  $\mathcal{A} = \langle Q, C, \delta \rangle$  where  $Q$  is a finite set of *control states*,  $C$  is a finite set of *counters* and  $\delta$  is a finite set of *transitions* in  $Q \times \mathbb{Z}^C \times Q$ . A *configuration* of  $\mathcal{A}$  is a pair  $\langle q, \mathbf{v} \rangle \in Q \times \mathbb{N}^C$ . We write  $\langle q, \mathbf{v} \rangle \rightarrow \langle q', \mathbf{v}' \rangle$  if there is  $(q, \mathbf{u}, q') \in \delta$  such that  $\mathbf{v}' = \mathbf{v} + \mathbf{u}$ . The *reachability problem* for VASS (written  $\text{Reach}(\text{VASS})$ ) consists in checking whether  $\langle q_0, \mathbf{v}_0 \rangle \xrightarrow{*} \langle q_f, \mathbf{v}_f \rangle$ , given two configurations  $\langle q_0, \mathbf{v}_0 \rangle$  and  $\langle q_f, \mathbf{v}_f \rangle$ . The reachability problem for VASS is decidable with non-primitive recursive complexity algorithms [25], [18], [21], but the best known lower bound is only EXPSPACE [22], [8]. The *control state reachability problem* is the following EXPSPACE-complete problem [22], [28]: given a configuration  $\langle q_0, \mathbf{v}_0 \rangle$  and a control state  $q_f$ , check whether  $\langle q_0, \mathbf{v}_0 \rangle \xrightarrow{*} \langle q_f, \mathbf{v} \rangle$  for some  $\mathbf{v} \in \mathbb{N}^C$ . We also use two other kinds of computations: with gains ( $\rightarrow_{\text{gainy}}$ ) or losses ( $\rightarrow_{\text{lossy}}$ ). We write  $\langle q, \mathbf{v} \rangle \rightarrow_{\text{gainy}} \langle q', \mathbf{v}' \rangle$  [resp.  $\langle q, \mathbf{v} \rangle \rightarrow_{\text{lossy}} \langle q', \mathbf{v}' \rangle$ ] if there is a transition  $(q, \mathbf{u}, q') \in \delta$  and  $\mathbf{w} \in \mathbb{N}^C$  such that  $\mathbf{v} \preceq \mathbf{w}$  and  $\mathbf{w} + \mathbf{u} \preceq \mathbf{v}'$  [resp.  $\mathbf{w} \preceq \mathbf{v}$  and  $\mathbf{v}' \preceq \mathbf{w} + \mathbf{u}$ ].

### III. THE POWER OF PAST: FROM REACH(VASS) TO SAT(PLRV)

While [6] concentrated on decidability results, here we begin with a hardness result. When past obligations are allowed as in PLRV,  $\text{SAT}(\text{PLRV})$  is equivalent to the very difficult problem of reachability in VASS. Combined with the result of the next section where we prove that removing past obligations leads to a reduction into the control state reachability problem for VASS, this means that reasoning with past obligations is much more complicated.

**Theorem 1.** There is a polynomial-space reduction from  $\text{Reach}(\text{VASS})$  into  $\text{SAT}(\text{PLRV})$ .

The proof of Theorem 1 is analogous to the proof of [1, Theorem 16] except that properties are expressed in PLRV instead of being expressed in  $\text{FO}^2(\sim, <, +1)$ . In Section IV-A, we show how to eliminate test formulas  $\phi$  from  $x \approx \langle \phi? \rangle y$  and  $x \approx \langle \phi? \rangle^{-1} y$ . Combining this with the decidability proof for  $\text{PLRV}^\top$  satisfiability from [6], we get that  $\text{SAT}(\text{PLRV})$  [resp. for  $\text{SAT}(\text{PLRV}^\top)$ ] is equivalent to  $\text{Reach}(\text{VASS})$  modulo polynomial-space reductions.

### IV. LEAVING THE PAST BEHIND SIMPLIFIES THINGS: FROM SAT(LRV) TO CONTROL STATE REACHABILITY

In this section, we show the reduction from  $\text{SAT}(\text{LRV})$  to the control state reachability problem in VASS. We obtain a  $2\text{EXPSPACE}$  upper bound for  $\text{SAT}(\text{LRV})$  as a consequence. This is done in two steps: (1) simplifying formulas of the form  $x \approx \langle \phi? \rangle y$  to remove the test formula  $\phi$  (i.e., a reduction from  $\text{SAT}(\text{LRV})$  into  $\text{SAT}(\text{LRV}^\top)$ ); and (2) reducing from  $\text{SAT}(\text{LRV}^\top)$  into the control state reachability pb. in VASS.

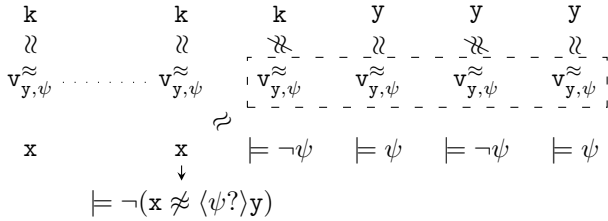
### A. Elimination of Test Formulas

We give a polynomial-time algorithm that for every  $\varphi \in \text{LRV}$  computes a formula  $\varphi' \in \text{LRV}^\top$  that preserves satisfiability: there is a model  $\sigma$  s.t.  $\sigma \models \varphi$  iff there is a model  $\sigma'$  s.t.  $\sigma' \models \varphi'$ . We give the reduction in two steps. First, we eliminate formulas with inequality tests of the form  $x \not\approx \langle \varphi? \rangle y$  using only positive tests of the form  $x \approx \langle \varphi? \rangle y$ . We then eliminate formulas of the form  $x \approx \langle \varphi? \rangle y$ , using only formulas of the form  $x \approx \langle \top? \rangle y$ . Let  $\text{LRV}^\approx$  be the logic LRV where there are no appearances of formulas of the form  $x \not\approx \langle \varphi? \rangle y$ ; and let  $\text{PLRV}^\approx$  be PLRV without  $x \not\approx \langle \varphi? \rangle y$  or  $x \not\approx \langle \varphi? \rangle^{-1} y$ .

**Proposition 2** (from LRV to  $\text{LRV}^\approx$ ). There is a polynomial-time reduction from  $\text{SAT}(\text{LRV})$  into  $\text{SAT}(\text{LRV}^\approx)$ ; and from  $\text{SAT}(\text{PLRV})$  into  $\text{SAT}(\text{PLRV}^\approx)$ .

*Proof sketch:* For every  $\varphi \in \text{LRV}$ , we compute  $\varphi' \in \text{LRV}^\approx$  in polynomial time, which preserves satisfiability. Besides all the variables from  $\varphi$ ,  $\varphi'$  uses a distinguished variable  $k$  (which will have a constant value, different from all the values of the variables of  $\varphi$ ) and variables  $v_{y,\psi}^\approx, v_{x \not\approx \langle \psi? \rangle y}$  for every subformula  $\psi$  of  $\varphi$  and variables  $x, y$  of  $\varphi$ .

Each subformula  $x \not\approx \langle \psi? \rangle y$  is replaced by  $x \not\approx v_{x \not\approx \langle \psi? \rangle y} \wedge v_{x \not\approx \langle \psi? \rangle y} \approx \langle \psi? \rangle y$ , where  $v_{x \not\approx \langle \psi? \rangle y}$  is a fresh variable. On the other hand, for each subformula  $\neg(x \not\approx \langle \psi? \rangle y)$ , we use the variable  $v_{y,\psi}^\approx$  in conjunction with  $k$  as shown below.



In the beginning,  $v_{y,\psi}^\approx \approx k$ , which is broken at the first position where all future positions where  $\psi$  holds have the same value for  $y$ . At this position (say  $i+1$ ),  $v_{y,\psi}^\approx \not\approx k$  and  $v_{y,\psi}^\approx$  maintains the same value (say  $n$ ) at all future positions, illustrated as a dashed box above. In all positions after  $i$  that satisfy  $\psi$ ,  $y \approx v_{y,\psi}^\approx$ . These conditions can be enforced without using  $\neg(x \not\approx \langle \psi? \rangle y)$ . Suppose  $x$  also has value  $n$  at position  $i$  as shown above. Now in any position after  $i$  that satisfies  $\psi$ ,  $y$  has the value  $n$ , which is the value of  $x$  in position  $i$ . This is exactly the semantics of  $\neg(x \not\approx \langle \psi? \rangle y)$ . Hence,  $\neg(x \not\approx \langle \psi? \rangle y)$  can be replaced by  $\neg X F \psi \vee x \approx X v_{y,\psi}^\approx$ . Past obligations are treated in a symmetrical way. ■

**Proposition 3** (from  $\text{LRV}^\approx$  to  $\text{LRV}^\top$ ). There is a polynomial-time reduction from  $\text{SAT}(\text{LRV}^\approx)$  into  $\text{SAT}(\text{LRV}^\top)$ ; and from  $\text{SAT}(\text{PLRV}^\approx)$  into  $\text{SAT}(\text{PLRV}^\top)$ .

*Proof sketch:* For every  $\varphi \in \text{LRV}^\approx$ , we compute in polynomial time  $\varphi' \in \text{LRV}^\top$  that preserves satisfiability. Besides all the variables from  $\varphi$ ,  $\varphi'$  uses a new distinguished variable  $k$ , and a variable  $v_{y,\psi}$  for every subformula  $\psi$  of  $\varphi$  and every variable  $y$  of  $\varphi$ . We enforce  $k$  to have a constant value different from all values of variables of  $\varphi$ . At every

$k$	$k$	$k$	$k$
$v_{y,\psi}$	$\approx$	$v_{y,\psi}$	$\approx$
$\approx$	$v_{y,\psi}$	$\approx$	$v_{y,\psi}$
$y$	$y$	$y$	$y$
$\psi$	$\neg\psi$	$\psi$	$\neg\psi$

position, we enforce  $\psi$  to hold if  $v_{y,\psi} \approx y$ , and  $\psi$  not to hold if  $v_{y,\psi} \approx k$  as shown above. Then  $x \approx \langle \psi? \rangle y$  is replaced by  $x \approx \langle \top? \rangle v_{y,\psi}$ . ■

**Corollary 4.** There is a polynomial-time reduction from  $\text{SAT}(\text{LRV})$  into  $\text{SAT}(\text{LRV}^\top)$ .

Since  $\text{SAT}(\text{PLRV}^\top)$  is decidable [6], we obtain the decidability of  $\text{SAT}(\text{PLRV})$ .

**Corollary 5.**  $\text{SAT}(\text{PLRV})$  is decidable.

### B. From $\text{LRV}^\top$ Satisfiability to Control State Reachability

In [6],  $\text{SAT}(\text{LRV}^\top)$  is reduced to the reachability problem for a subclass of VASS. Herein, this is refined by introducing *incremental errors* in order to improve the complexity.

In [6], the standard concept of atoms from the Vardi-Wolper construction of automaton for LTL is used. Refer to the diagram at the top of Figure 1. The formula  $x \approx \langle \top? \rangle y$  in the left atom creates an obligation for the current value of  $x$

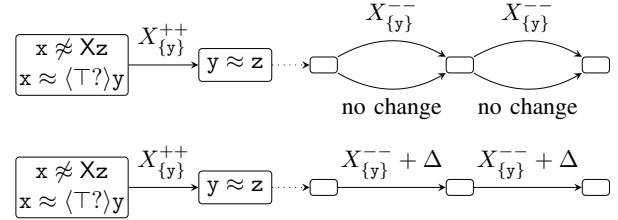


Fig. 1. Automaton constructions from [6] (top) and from this paper (bottom).

to appear some time in the future in  $y$ . This obligation cannot be satisfied in the second atom, since  $y$  has to satisfy some other constraint there ( $y \approx z$ ). To remember this unsatisfied obligation about  $y$  while taking the transition from the first atom to the second, the counter  $X_{\{y\}}$  is incremented. The counter can be decremented later in transitions that allow the repetition in  $y$ . If several transitions allow such a repetition, only one of them needs to decrement the counter (since there was only one obligation at the beginning). The other transitions which should not decrement the counter can take the alternative labelled “no change” in the right part of Figure 1.

The idea here is to replace the combination of the decrementing transition and the “no change” transition in the top of Figure 1 with a single transition with incremental errors as shown in the bottom. After Lemma 6 below that formalises ideas from [6, Section 7], we prove that the transition with incremental errors is sufficient.

**Lemma 6** ([6]). For a  $\text{LRV}^\top$  formula  $\phi$  that uses the variables  $\{x_1, \dots, x_k\}$ , a VASS  $\mathcal{A}_\phi = \langle Q, C, \delta \rangle$  can be defined, along with sets  $Q_0, Q_f \subseteq Q$  of *initial* and *final* states resp., s.t.

- the set of counters  $C$  consists of all nonempty subsets of  $\{\mathbf{x}_1, \dots, \mathbf{x}_k\}$ .
- For all  $q, q' \in Q$ , either  $\{\mathbf{u} \mid (q, \mathbf{u}, q') \in \delta\} = [n_1, m_1] \times \dots \times [n_{|C|}, m_{|C|}]$  for some  $n_1, m_1, \dots, n_{|C|}, m_{|C|} \in [-k, k]$ , or  $\{\mathbf{u} \mid (q, \mathbf{u}, q') \in \delta\} = \emptyset$ . (We call this property *closure under component-wise interpolation*.)
- If  $\delta \cap (\{q\} \times [-k, k]^C \times \{q'\})$  is not empty, then for every  $X \in C$  there is  $(q, \mathbf{u}, q') \in \delta$  so that  $\mathbf{u}(X) \geq 0$ . (We call this property *optional decrement*.)
- Let  $\mathbf{0}$  be the counter valuation that assigns 0 to all counters. Then  $\phi$  is satisfiable iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  for some  $q_0 \in Q_0$  and  $q_f \in Q_f$ .

At the top of Figure 1, only one counter  $X_{\{y\}}$  is shown and is decremented by 1 for simplicity. In general, multiple counters can be changed and they can be incremented/decremented by any number up to  $k$ , depending on the initial and target atoms of the transition. If a counter can be incremented by  $k_1$  and can be decremented by  $k_2$ , then there will also be transitions between the same pair of atoms allowing changes of  $k_1 - 1, \dots, 1, 0, -1, \dots, -(k_2 - 1)$ . This corresponds to the closure under component-wise interpolation mentioned in the lemma above. The optional decrement property corresponds to the fact that there will always be a “no change” transition that does not decrement any counter.

Now, we show that a single transition that decrements all counters by the maximal possible number can simulate the set of all transitions between two atoms, using incremental errors. Let  $\mathcal{A}_{\text{inc}} = \langle Q, C, \delta^{\text{min}} \rangle$  and  $Q_0, Q_f \subseteq Q$ , where  $Q, Q_0, Q_f$  and  $C$  are same as those of  $\mathcal{A}_\phi$  and  $\delta^{\text{min}}$  is defined as follows:  $(q, \text{minup}_{q,q'}, q') \in \delta^{\text{min}}$  iff  $\delta \cap (\{q\} \times [-k, k]^C \times \{q'\})$  is not empty and  $\text{minup}_{q,q'}(X) = \min_{\mathbf{u}: (q, \mathbf{u}, q') \in \delta} \{\mathbf{u}(X)\}$  for all  $X \in C$ .

**Lemma 7.** If  $\langle q, \mathbf{v} \rangle \rightarrow \langle q', \mathbf{v}' \rangle$  in  $\mathcal{A}_\phi$ , then  $\langle q, \mathbf{v} \rangle \rightarrow_{\text{gainy}} \langle q', \mathbf{v}' \rangle$  in  $\mathcal{A}_{\text{inc}}$ .

*Proof sketch:* If  $\mathcal{A}_\phi$  takes one of the transitions in the top of Figure 1,  $\mathcal{A}_{\text{inc}}$  takes the corresponding transition at the bottom, adjusting the incremental error  $\Delta$  accordingly. ■

**Lemma 8.** If  $\langle q_1, \mathbf{v}_1 \rangle \xrightarrow{*}_{\text{gainy}} \langle q_2, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{inc}}$  and  $\mathbf{v}'_1 \preceq \mathbf{v}_1$ , then  $\langle q_1, \mathbf{v}'_1 \rangle \xrightarrow{*} \langle q_2, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi$ .

*Proof sketch:* If  $\mathcal{A}_{\text{inc}}$  takes a transition at the bottom of Figure 1,  $\mathcal{A}_\phi$  takes the corresponding decrementing transition at the top, ignoring any incremental errors. This may result in  $\mathcal{A}_\phi$  reaching  $\mathbf{0}$  earlier in the run, in which case “no change” transitions are used in the rest of the run. ■

**Theorem 9.**  $\text{SAT}(\text{LRV}^\top)$  is in  $2\text{EXPSpace}$ .

*Proof sketch:* The proof is in four steps (standard arguments are used for 3. and 4.).

- 1 From [6], a  $\text{LRV}^\top$  formula  $\phi$  is satisfiable iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi$  for some  $q_0 \in Q_0$  and  $q_f \in Q_f$ .
- 2 This is the step that requires new insight. From Lemmas 7 and 8,  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi$  iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*}_{\text{gainy}} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{inc}}$ .

- 3 Let  $\mathcal{A}_{\text{dec}}$  be the VASS obtained from  $\mathcal{A}_{\text{inc}}$  by “reversing” each transition. By replacing each gainy transition of  $\mathcal{A}_{\text{inc}}$  by the reverse lossy transition of  $\mathcal{A}_{\text{dec}}$ , we infer that  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*}_{\text{gainy}} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{inc}}$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*}_{\text{lossy}} \langle q_0, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{dec}}$ .
- 4  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*}_{\text{lossy}} \langle q_0, \mathbf{0} \rangle$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some  $\mathbf{v}$ . Checking the latter condition is precisely the control state reachability problem for VASS.

The number of control states and counters in  $\mathcal{A}_\phi$  and in  $\mathcal{A}_{\text{dec}}$  is exponential in  $|\phi|$  (size of  $\phi$ ). Each control state and transition function of  $\mathcal{A}_{\text{dec}}$  can be represented in space polynomial in  $|\phi|$ . Given a control state, testing if it is an initial or a final state can be done in linear time in the size of the state. The automaton  $\mathcal{A}_{\text{dec}}$  can be constructed in exponential time in  $|\phi|$ . Hence, the  $\text{EXPSpace}$  upper bound for the control state reachability problem in VASS [28] gives the  $2\text{EXPSpace}$  upper bound for  $\text{SAT}(\text{LRV}^\top)$ . ■

**Corollary 10.**  $\text{SAT}(\text{LRV})$  is in  $2\text{EXPSpace}$ .

## V. SIMULATING EXPONENTIALLY MANY COUNTERS

In Section IV, the reduction from  $\text{SAT}(\text{LRV})$  to control state reachability involves an exponential blow up, since we use one counter for each subset of variables. The question of whether this can be avoided depends on whether LRV is powerful enough to reason about subsets of variables or whether there is a smarter reduction without a blow-up. Similar questions are open in other related areas [24].

Here we prove that LRV is indeed powerful enough to reason about subsets of variables. We establish a  $2\text{EXPSpace}$  lower bound, which leverages the power of LRV to access exponentially many counters through binary encoding. Consider the variables and their values shown in the table below. The conditions in the third row are about the equality of variable values in a single position.

Variable	$x_1$	$x_2$	$x_3$	$z$	inc	dec
Value	20	30	20	20	10	20
Condition	$x_1 \approx z$	$x_2 \not\approx z$	$x_3 \approx z$		$\text{inc} \not\approx z$	$\text{dec} \approx z$
Encodes	1	0	1			decrement

The combination of all the conditions can be thought of as encoding “decrement 101<sup>th</sup> counter”, considering 101 as the binary encoding of 5, where the  $i$ -th bit is 1 if  $x_i \approx z$ . A total of 8 counters can be manipulated with  $x_1, x_2$  and  $x_3$ . Using the power of LRV to reason about values that repeat in the future, we can encode the condition “for every counter, an increment is followed by a decrement in the future”, which then ensures control state reachability.

The rest of this section is divided into three parts. The first part defines chain systems, which are like VASS where transition rules like “increment counter  $X$ ” are replaced by “increment 101<sup>th</sup> counter”. The second part shows lower bounds for the control state reachability problem for chain systems. The third part shows that LRV can reason about chain systems of level 1.

## A. Chain Systems

We introduce a new class of counter systems that is instrumental to show that  $\text{SAT}(\text{LRV}^\top)$  is  $2\text{EXPSPACE}$ -hard. This is an intermediate formalism between counter automata with zero-tests with counters bounded by triple exponential values (having  $2\text{EXPSPACE}$ -hard control state reachability problem) and properties expressed in  $\text{LRV}^\top$ . Systems with chained counters have no zero-tests and the only updates are increments and decrements. However, the systems are equipped with a finite family of counters, each family having an exponential number of counters. Let  $\text{exp}(0, n) \stackrel{\text{def}}{=} n$  and  $\text{exp}(k+1, n) \stackrel{\text{def}}{=} 2^{\text{exp}(k, n)}$  for every  $k \geq 0$ .

**Definition 11.** A *counter system with chained counters* (herein called a *chain system*) is a tuple  $\mathcal{A} = \langle Q, f, k, Q_0, Q_F, \delta \rangle$  where (1)  $f : [1, n] \rightarrow \mathbb{N}$  where  $n \geq 1$  is the *number of chains* and  $\text{exp}(k, f(\alpha))$  is the number of counters for the chain  $\alpha$  where  $k \geq 0$ , (2)  $Q$  is a non-empty finite set of *states*, (3)  $Q_0 \subseteq Q$  is the set of *initial states* and  $Q_F \subseteq Q$  is the set of *final states*, (4)  $\delta$  is the set of *transitions* in  $Q \times I \times Q$  where

$$I = \{\text{inc}(\alpha), \text{dec}(\alpha), \text{next}(\alpha), \text{prev}(\alpha), \text{first}(\alpha)?, \overline{\text{first}(\alpha)?}, \text{last}(\alpha)?, \overline{\text{last}(\alpha)?} : \alpha \in [1, n]\}.$$

The system  $\mathcal{A} = \langle Q, f, k, Q_0, Q_F, \delta \rangle$  is said to be at *level*  $k$ . In order to encode the natural numbers from  $f$  and the value  $k$ , we use a unary representation. We say that a transition containing  $\text{inc}(\alpha)$  is  $\alpha$ -*incrementing*, and a transition containing  $\text{dec}(\alpha)$  is  $\alpha$ -*decrementing*. The idea is that for each chain  $\alpha \in [1, n]$ , we have  $\text{exp}(k, f(\alpha))$  counters, but we cannot access them directly in the transitions like we do in VASS. Instead, we have a pointer to a counter that we can move. We can ask if we are pointing to the first counter ( $\text{first}(\alpha)?$ ) or not ( $\overline{\text{first}(\alpha)?}$ ), or the last counter ( $\text{last}(\alpha)?$ ) or not ( $\overline{\text{last}(\alpha)?}$ ), and we can change the pointer to the next ( $\text{next}(\alpha)$ ) or previous ( $\text{prev}(\alpha)$ ) counter.

A *run* is a finite sequence  $\rho$  in  $\delta^*$  such that

- 1) for every two  $\rho(i) = q \xrightarrow{\text{instr}} r$  and  $\rho(i+1) = q' \xrightarrow{\text{instr}'}$   $r'$  we have  $r = q'$ ,
- 2) for every chain  $\alpha \in [1, n]$ , for every  $i \in [1, |\rho|]$ , we have  $0 \leq c_i^\alpha < \text{exp}(k, f(\alpha))$  where

$$c_i^\alpha = \text{card}(\{i' < i \mid \rho(i') = q \xrightarrow{\text{next}(\alpha)} r\}) - \text{card}(\{i' < i \mid \rho(i') = q \xrightarrow{\text{prev}(\alpha)} r\}), \quad (1)$$

- 3) for every  $i \in [1, |\rho|]$  and for every chain  $\alpha \in [1, n]$ ,
  - (a) if  $\rho(i) = q \xrightarrow{\text{first}(\alpha)?} q'$ , then  $c_i^\alpha = 0$ ;
  - (b) if  $\rho(i) = q \xrightarrow{\overline{\text{first}(\alpha)?}} q'$ , then  $c_i^\alpha \neq 0$ ;
  - (c) if  $\rho(i) = q \xrightarrow{\text{last}(\alpha)?} q'$ , then  $c_i^\alpha = \text{exp}(k, f(\alpha)) - 1$ ;
  - (d) if  $\rho(i) = q \xrightarrow{\overline{\text{last}(\alpha)?}} q'$ , then  $c_i^\alpha \neq \text{exp}(k, f(\alpha)) - 1$ .

A run is *accepting* whenever  $\rho(1)$  starts with an initial state from  $Q_0$  and  $\rho(|\rho|)$  ends with a final state from  $Q_F$ . A run is *perfect* iff for every  $\alpha \in [1, n]$ , there is some injective function

$$\gamma : \{i \mid \rho(i) \text{ is } \alpha\text{-decrementing}\} \rightarrow \{i \mid \rho(i) \text{ is } \alpha\text{-incrementing}\}$$

such that for every  $\gamma(i) = j$  we have that  $j < i$  and  $c_i^\alpha = c_j^\alpha$ . A run is *gainy and ends at zero* iff for every chain  $\alpha \in [1, n]$ , there is some injective function

$$\gamma : \{i \mid \rho(i) \text{ is } \alpha\text{-incrementing}\} \rightarrow \{i \mid \rho(i) \text{ is } \alpha\text{-decrementing}\}$$

such that for every  $\gamma(i) = j$  we have that  $j > i$  and  $c_i^\alpha = c_j^\alpha$ . In the sequel, we shall simply say that the run is *gainy*. Below, we define two problems for which we shall characterize the computational complexity.

<b>PROBLEM:</b> Existence of a perfect accepting run of level $k \geq 0$ ( $\text{Per}(k)$ )
<b>INPUT:</b> A chain system $\mathcal{A}$ of level $k$ .
<b>QUESTION:</b> Does $\mathcal{A}$ have a <i>perfect</i> accepting run?

<b>PROBLEM:</b> Existence of a gainy accepting run of level $k \geq 0$ ( $\text{Gainy}(k)$ )
<b>INPUT:</b> A chain system $\mathcal{A}$ of level $k$ .
<b>QUESTION:</b> Does $\mathcal{A}$ have a <i>gainy</i> accepting run?

$\text{Per}(k)$  is actually a control state reachability problem in VASS where the counters are encoded succinctly.  $\text{Gainy}(k)$  is a reachability problem in VASS with incrementing errors and the reached counter values are equal to zero. Here, the counters are encoded succinctly too.

**Lemma 12.** For every  $k \geq 0$ ,  $\text{Per}(k)$  and  $\text{Gainy}(k)$  are interreducible in logarithmic space.

**Lemma 13.**  $\text{Per}(k)$  is in  $(k+1)\text{EXPSPACE}$ .

The proof of Lemma 13 consists of simulating perfect runs by the runs of a VASS in which the control states record the positions of the pointers in the chains.

## B. Hardness Results for Chain Systems

We show that  $\text{Per}(k)$  is  $(k+1)\text{EXPSPACE}$ -hard. The implication is that replacing direct access to counters by a pointer that can move along a chain of counters does not decrease the power of VASS, while providing access to more counters. To demonstrate this, we extend Lipton's  $\text{EXPSPACE}$ -hardness proof for the control state reachability problem in VASS [22] (see also its exposition in [8]). Since our pointers can be moved only one step at a time, this extension involves new insights into the control flow of algorithms used in Lipton's proof, allowing us to implement it even with the limitations imposed by step-wise pointer movements.

Lipton's proof starts from the standard result in computability theory that a Turing Machine using space  $2^{n^\gamma}$  can be simulated by a counter automaton equipped with 4 counters whose values are bounded by  $2^{2^{n^\gamma}}$ . Lipton's proof shows that such a counter automaton can be simulated with a VASS. Unlike counter automata, VASS does not have zero-test transitions. Each counter  $c$  of the automaton is complemented with an extra counter  $\bar{c}$  in the VASS. The VASS is designed such that the sum of the values in  $c$  and  $\bar{c}$  is  $2^{2^{n^\gamma}}$  in any reachable configuration. Now testing  $c$  for zero is equivalent to testing that  $\bar{c}$  is  $2^{2^{n^\gamma}}$ . This later test is implemented in a VASS as explained next.

Figure 2 illustrates how a counter  $s_i$  is decremented  $2^{2^i}$  times. There are two nested loops indexed by  $y_{i-1}$  and  $z_{i-1}$ , initialised to  $2^{2^{i-1}}$ . When all the iterations are finished, we would have decremented  $s_i$  exactly  $2^{2^{i-1}} \times 2^{2^{i-1}} = 2^{2^i}$  times. Testing  $y_{i-1}$  and  $z_{i-1}$  for zero is done by a similar gadget, where  $i, i-1$  are replaced by  $i-1, i-2$ . The resulting VASS is composed of  $n^\gamma$  constant sized VASS, each one implementing the nested loops for an  $i$  between 1 and  $n^\gamma$ . The idea behind our  $(k+1)$ EXPSPACE lower bound is to replace the VASS with a chain system and  $n^\gamma$  with  $\exp(k, n^\gamma)$ . This will not work as it is, since a chain system composed of  $\exp(k, n^\gamma)$  constant sized portions will not give a polynomial-time reduction. We reduce the size of the chain system by observing that the decrementing algorithm for  $i-1$  is the same as the one for  $i$ , except that  $i, i-1$  are replaced by  $i-1, i-2$  respectively. We can write a single sequence of instructions for the decrementing algorithm and invoke it for any  $i$  by placing the pointers at the appropriate counters. Roughly speaking,  $\exp(k, n^\gamma)$  copies of the code for large decrements is replaced by one copy of the code plus a chain of implicit length  $\exp(k, n^\gamma)$  equipped with a pointer to refer to a specific copy.

The main difficulty in the implementation is to ensure that the multiple invocations of the algorithm return to the correct point. Unlike Lipton’s proof that uses VASS, we have to work with chain systems and this introduces some more constructions, the technical details of which are rather tedious.

**Theorem 14.**  $\text{Per}(k)$  is  $(k+1)$ EXPSPACE-hard.

### C. Reasoning About Chain Systems with LRV

Given a chain system  $\mathcal{A}$  of level 1, we construct a formula in LRV that is satisfiable iff  $\mathcal{A}$  has a gainy accepting run. Hence, we get a 2EXPSPACE lower bound for SAT(LRV). The main idea is to encode runs of chain systems with LRV formulas that access the counters using binary encoding, so that the formulas can handle exponentially many counters.

**Lemma 15.** There is a polynomial-time reduction from Gainy(1) into SAT(LRV(X, F)).

*Proof sketch:* Let  $\mathcal{A} = \langle Q, f, 1, Q_0, Q_F, \delta \rangle$  be a chain system of level 1 with  $f : [1, n] \rightarrow \mathbb{N}$ , having thus  $n$  chains of counters, of respective size  $2^{f(1)}, \dots, 2^{f(n)}$ .

We encode a word  $\rho \in \delta^*$  that represents an accepting run. For this, we use the alphabet  $\delta$  of transitions. We can simulate

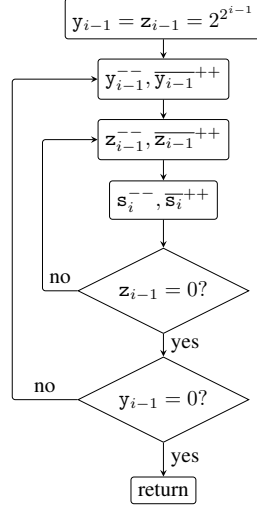


Fig. 2. Control flow of algorithm that decrements  $s_i$   $2^{2^i}$  times.

the labels  $\delta = \{t_1, \dots, t_m\}$  with variables  $\tau_0, \dots, \tau_m$ , where a node has an encoding of the label  $t_i$  iff the formula  $\langle t_i \rangle = \tau_0 \approx \tau_i$  holds true. We build an LRV formula  $\varphi$  so that there is an accepting gainy run  $\rho \in \delta^*$  of  $\mathcal{A}$  if, and only if, there is a model  $\sigma$  so that  $\sigma \models \varphi$  and  $\sigma$  encodes  $\rho$ .

The counter-blind conditions to check are: (a) Every position satisfies  $\langle t \rangle$  for some  $t \in \delta$ ; (b) the first position satisfies  $\langle (q_0, \text{instr}, q) \rangle$  for some  $q_0 \in Q_0, \text{instr} \in I, q \in Q$ ; (c) the last position satisfies  $\langle (q, \text{instr}, q') \rangle$  for some  $q \in Q, \text{instr} \in I, q' \in Q_F$ ; (d) no two consecutive positions  $i$  and  $i+1$  satisfy  $\langle (q, u, q') \rangle$  and  $\langle (p, u', p') \rangle$  respectively, with  $q' \neq p'$ . The difficulty is then in checking that the values of the counters encode indeed a correct gainy run.

We say that a position  $i$  is  $\alpha$ -incrementing [resp.  $\alpha$ -decrementing] if it satisfies  $\langle (q, u, q') \rangle$  for some  $q, q' \in Q$  and  $u = \text{inc}(\alpha)$  [resp.  $u = \text{dec}(\alpha)$ ]. We use a label  $(\alpha, i)$  and variables  $x_{inc}^\alpha, x_{dec}^\alpha$  for every  $\alpha \in [1, n]$  and  $i \in [1, f(\alpha)]$ . We say that a position  $i$  operates on the  $\alpha$ -counter  $c$ , if  $\langle (\alpha, j) \rangle$  holds (i.e., position  $i$  encodes the label  $(\alpha, j)$ ) for every position  $j$  of the representation of  $c$  in base 2 containing a ‘1’, and  $\neg \langle (\alpha, j) \rangle$  for every position  $j$  containing a ‘0’. Note that we can encode every value  $0 \leq c < 2^{f(\alpha)}$ .

For every chain  $\alpha$ , let us consider the following properties:

- Every two positions of  $\sigma$  have different values of  $x_{inc}^\alpha$  [resp. of  $x_{dec}^\alpha$ ].
- For every position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$  with an instruction ‘first( $\alpha$ )?’ [resp. ‘first( $\alpha$ )?’, ‘last( $\alpha$ )?’, ‘last( $\alpha$ )?’], we have  $c = 0$  [resp.  $c \neq 0, c = 2^{f(\alpha)} - 1, c \neq 2^{f(\alpha)} - 1$ ].
- For every position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$ , if the position contains an instruction ‘next( $\alpha$ )’ [resp. ‘prev( $\alpha$ )’], then the next position  $i+1$  operates on the  $\alpha$ -counter  $c+1$  [resp.  $c-1$ ]; otherwise, the position  $i+1$  operates on the  $\alpha$ -counter  $c$ .
- For every  $\alpha$ -incrementing position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$  there is a future  $\alpha$ -decrementing position  $j > i$  on the same  $\alpha$ -counter, so that  $\sigma(i)(x_{inc}^\alpha) = \sigma(j)(x_{dec}^\alpha)$ .

In fact, these properties together with (a)–(d) are sufficient and necessary to encode a gainy and accepting run of  $\mathcal{A}$ , and they can be all expressed in LRV. Then, we obtain a polynomial-time reduction from Gainy(1) into the satisfiability problem for LRV(X, F). ■

## VI. A ROBUST EQUIVALENCE

We have seen that the satisfiability problem for LRV is equivalent to the control state reachability problem in an exponentially larger VASS. In this section we evaluate how robust is this result with LRV variants or fragments.

### A. Infinite Words with Multiple Data

So far, we have considered only finite words with multiple data. It is also natural to consider the variant with infinite words, but it is known that this sometimes leads to undecidability. However, in this case the decidability and complexity results are preserved. Let  $\text{LRV}_\omega$  be the variant of LRV in which infinite models of length  $\omega$  are taken into account



instead of finite ones. The logics  $\text{PLRV}_\omega$ ,  $\text{LRV}_\omega^\top$ , etc. are defined accordingly.

**Proposition 16.** (I)  $\text{SAT}(\text{PLRV}_\omega)$  is decidable.  
 (II)  $\text{SAT}(\text{LRV}_\omega)$  is  $2\text{EXPSPACE}$ -complete.

### B. Adding MSO-Definable Temporal Operators

It is standard to extend LTL with MSO-definable temporal operators (see e.g. [30], [13]), and the same can be done with LRV. A temporal operator  $\oplus$  of arity  $n$  is *MSO-definable* whenever there is a formula  $\phi(\mathbf{x}, P_1, \dots, P_n)$  from monadic second-order logic with a unique free position variable  $\mathbf{x}$  and with  $n$  free unary predicates such that  $\sigma, i \models \oplus(\phi_1, \dots, \phi_n)$  iff  $\sigma \models \phi(i, X_1, \dots, X_n)$  in MSO where each set  $X_j$  is equal to the set of positions from  $\sigma$  satisfying the formula  $\phi_j$ , see e.g. [13]. The  $2\text{EXPSPACE}$  upper bound is preserved with a fixed finite set of MSO-definable operators.

**Theorem 17.** Let  $\{\oplus_1, \dots, \oplus_N\}$  be a finite set of MSO-definable temporal operators. Satisfiability problem for LRV extended with  $\{\oplus_1, \dots, \oplus_N\}$  is  $2\text{EXPSPACE}$ -complete.

Note that PLRV augmented with MSO-definable temporal operators is decidable too.

### C. The Now Operator or the Effects of Moving the Origin

The satisfiability problem for Past LTL with the temporal operator *Now* is known to be  $\text{EXPSPACE}$ -complete [20]. The satisfaction relation is parameterized by the current position of the origin and past-time temporal operators use that position. For instance,  $\sigma, i \models_o \text{Now } \phi \stackrel{\text{def}}{\iff} \sigma, i \models_i \phi$  and  $\sigma, i \models_o \phi_1 \text{S} \phi_2 \stackrel{\text{def}}{\iff}$  there is  $j \in [o, i]$  such that  $\sigma, j \models_o \phi_2$  and for all  $j' \in [j-1, i]$ , we have  $\sigma, j' \models_o \phi_1$ . The powerful operator *Now* can be obviously defined in MSO but not with the above definition since it requires *two* free position variables, one of which refers to the current position of the origin.

**Theorem 18.**  $\text{SAT}(\text{LRV} + \text{Now})$  is  $2\text{EXPSPACE}$ -complete.

This contrasts with the undecidability results from [17, Theorem 5] in presence of the operator *Now*.

### D. Bounding the Number of Variables

Given the relationship between the number of variables in a formula and the number of counters needed in the corresponding VASS, we investigate the consequences of fixing the number of variables. Interestingly, this classical restriction has an effect only for  $\text{LRV}^\top$ , i.e., when test formulas  $\phi$  are restricted to  $\top$  in  $x \approx \langle \phi? \rangle y$ . Let  $\text{LRV}_k$  [resp.  $\text{LRV}_k^\top$ ,  $\text{PLRV}_k^\top$ ] be the restriction to formulas with at most  $k$  variables. In [6, Theorem 5], it is shown that  $\text{SAT}(\text{LRV}_1^\top)$  is  $\text{PSPACE}$ -complete by establishing a reduction into the reachability problem for VASS when counter values are linearly bounded. Below, we generalize this result for any  $k \geq 1$  by using the proof of Theorem 9 and the fact that the control state reachability problem for VASS with at most  $k$  counters is in  $\text{PSPACE}$ .

**Proposition 19.** For every  $k \geq 1$ ,  $\text{SAT}(\text{LRV}_k^\top)$  is  $\text{PSPACE}$ -complete.

This does not imply that  $\text{LRV}_k$  is in  $\text{PSPACE}$ , since the reduction from LRV into  $\text{LRV}^\top$  in Section IV-A introduces new variables. In fact, it introduces a number of variables that depends on the size of the formula. It turns out that this is unavoidable, and that its satisfiability problem is  $2\text{EXPSPACE}$ -hard, by the following reduction.

**Lemma 20.** There is a polynomial-time reduction from  $\text{SAT}(\text{LRV}^\top)$  into  $\text{SAT}(\text{LRV}_1)$  [resp.  $\text{SAT}(\text{PLRV}^\top)$  and  $\text{SAT}(\text{PLRV}_1)$ ].

*Proof sketch:* Let  $\varphi \in \text{LRV}^\top$  using  $k$  variables  $x_1, \dots, x_k$  so that  $\sigma \models \varphi$ . We will encode  $\sigma$  restricted to  $x_1, \dots, x_k$  inside a model  $\sigma_\varphi$  with only one variable, say  $x$ . To this end,  $\sigma_\varphi$  is divided into  $N$  segments  $s_1 \cdots s_N$  of equal length, where  $N = |\sigma|$ . A special data value  $d$  not used in  $\sigma$  plays the role of *delimiter* between segments and between positions that code values from  $\sigma$ . Suppose that  $d$  is a data value that is not in  $\sigma$ . Then, each segment  $s_i$  has length  $k' = 2k + 1$ , and is defined as the data values “ $d d_1 d d_2 \dots d d_k d$ ”, where  $d_j = \sigma(i)(x_j)$ . Figure 3 contains an example. We can force that the model has this shape with  $\text{LRV}_1$ . With this coding, we can tell that we are between two segments if there are two consecutive equal data values. In fact, we are at a position corresponding to  $x_i$  (for  $i \in [1, k]$ ) inside a segment if we are standing at the  $2i$ -th element of a segment, and we can test this with the formula  $\gamma_i = \mathbf{X}^{k'-2i} x \approx \mathbf{X}^{k'-2i+1} x \vee (\mathbf{X}^{k'-2i} \top \wedge \neg \mathbf{X}^{k'-2i+1} \top)$ . Using the  $\gamma_i$ 's, we translate  $x_i \approx \langle \top? \rangle x_j$  in  $\varphi$  into a formula that: moves to the position  $2i$  of the segment (the one corresponding to  $x_i$ ), and tests  $x \approx \langle \gamma_j? \rangle x$ . We can do this similarly with all formulas. ■

**Corollary 21.** For all  $k \geq 1$ ,  $\text{SAT}(\text{LRV}_k)$  is  $2\text{EXPSPACE}$ -complete.

**Corollary 22.** For all  $k \geq 1$ ,  $\text{SAT}(\text{PLRV}_k)$  is as hard as  $\text{Reach}(\text{VASS})$ .

### E. The Power of Pairs of Repeating Values

Let us consider an LRV variant so that repetition of tuples of values is possible: we add formulas of the form  $(x_1, \dots, x_k) \approx \langle \phi? \rangle (y_1, \dots, y_k)$  where  $x_1, \dots, x_k, y_1, \dots, y_k \in \text{VAR}$ . This extends  $x \approx \langle \varphi? \rangle y$  by testing whether the vector of data values from the variables  $(x_1, \dots, x_k)$  of the current position coincides with that of  $(y_1, \dots, y_k)$  in a future position.

We call this extension  $\text{LRV}_{\text{vec}}$ . Unfortunately, we can show that  $\text{SAT}(\text{LRV}_{\text{vec}})$  is undecidable, even when only tuples of dimension 2 are allowed. This is proved by reduction from a variant of Post's Correspondence Problem (PCP). In order to code solutions of PCP instances, we adapt a proof technique used in [1] for first-order logic with two variables and two equivalence relations on words. However, our proof uses only *future* modalities (unlike the proof of [1, Proposition 27]) and no past obligations (unlike the proof of [17, Theorem 4]). To prove this result, we work with a variant of the PCP problem in which solutions  $u_{i_1} \cdots u_{i_n} = v_{i_1} \cdots v_{i_n}$  have to satisfy  $|u_{i_1} \cdots u_{i_j}| \leq |v_{i_1} \cdots v_{i_j}|$  for every  $j$ .

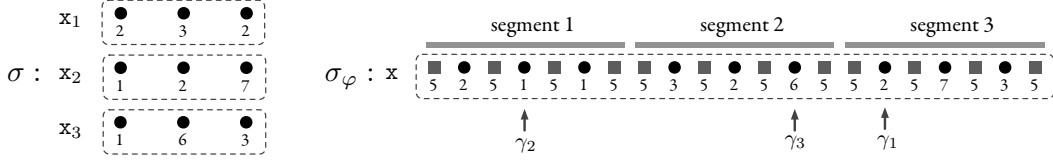


Fig. 3. Example of the reduction from  $\text{SAT}(\text{LRV}^\top)$  into  $\text{SAT}(\text{LRV}_1)$ , for  $k = 3$ ,  $N = 3$  and  $d = 5$ .

**Theorem 23.**  $\text{SAT}(\text{LRV}_{vec}(X, U))$  is undecidable.

Furthermore, our proof can be adapted to show that  $\text{SAT}(\text{LRV}_{vec}^\top)$  is also undecidable.

## VII. IMPLICATIONS FOR LOGICS ON DATA WORDS

A data word is an element of  $(\Sigma \times \mathbb{D})^*$ , where  $\Sigma$  is a finite alphabet and  $\mathbb{D}$  is an infinite domain. We focus here on first-order logic with two variables, and on a temporal logic.

*Two-variable Logics:* We study a fragment of  $\text{EMSO}^2(+1, <, \sim)$  on data words, and we show that it has a satisfiability problem in  $3\text{EXPSpace}$ , as a consequence of our results on the satisfiability for LRV. The satisfiability problem for  $\text{EMSO}^2(+1, <, \sim)$  is known to be decidable, equivalent to the reachability problem for VASS [1], with no known primitive-recursive algorithm. Here we show a large fragment with elementary complexity.

Consider the fragment of  $\text{EMSO}^2(+1, <, \sim)$ —that is, first-order logic with two variables, with a prefix of existential quantification over monadic relations— where all formulas are of the form  $\exists X_1, \dots, X_n \varphi$  with

$$\begin{aligned} \varphi ::= & \text{atom} \mid \neg\varphi \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \\ & \exists x \exists y \varphi \mid \forall x \forall y \varphi \mid \forall x \exists y (x \leq y \wedge \varphi), \text{ where} \\ \text{atom} ::= & \zeta = \zeta' \mid \zeta \neq \zeta' \mid \zeta \sim \zeta' \mid \zeta < \zeta' \mid \zeta \leq \zeta' \mid \\ & +1(\zeta, \zeta') \mid X_i(\zeta) \mid a(\zeta) \end{aligned}$$

for any  $a \in \Sigma$ ,  $i \in [1, n]$ , and  $\zeta, \zeta' \in \{x, y\}$ . The relation  $x < y$  tests that the position  $y$  appears after  $x$  in the word;  $+1(x, y)$  tests that  $y$  is the next position to  $x$ ; and  $x \sim y$  tests that positions  $x$  and  $y$  have the same data value. We call this fragment *forward-EMSO*<sup>2</sup>(+1, <, ~). In fact, *forward-EMSO*<sup>2</sup>(+1, <, ~) captures  $\text{EMSO}^2(+1, <)$  (i.e., all regular languages on the finite labeling of the data word).<sup>1</sup> However, it seems to be strictly less expressive than  $\text{EMSO}^2(+1, <, \sim)$ , since it does not appear to be able to express, for example, *there are exactly two occurrences of every data value*. Yet, it can express *there are at most two occurrences of every data value* (with  $\exists X \forall x \forall y. x \sim y \wedge x < y \rightarrow X(x) \wedge \neg X(y)$ ), and *there is exactly one occurrence of every data value*. For the same reason, it would neither capture  $\text{EMSO}^2(+1, \sim)$ .

By an exponential reduction into  $\text{SAT}(\text{LRV})$ , we obtain that *forward-EMSO*<sup>2</sup>(+1, <, ~) is decidable in elementary time.

<sup>1</sup>Indeed, note that one can easily test whether a word is accepted by a finite automaton with the help of a monadic relation  $X_{fst}$  that holds only at the first position. Further, the property of  $X_{fst}$  can be expressed in *forward-EMSO*<sup>2</sup>(+1, <, ~) as  $(\exists y. X_{fst}(y)) \wedge (\forall x \forall y. x < y \rightarrow \neg X_{fst}(y))$ .

**Proposition 24.** The satisfiability problem for *forward-EMSO*<sup>2</sup>(+1, <, ~) is in  $3\text{EXPSpace}$ .

*Proof sketch:* Through a standard translation we can bring any formula of *forward-EMSO*<sup>2</sup>(+1, <, ~) into a formula of the form

$$\varphi = \exists X_1, \dots, X_n \left( \forall x \forall y \chi \wedge \bigwedge_k \forall x \exists y (x \leq y \wedge \psi_k) \right)$$

that preserves satisfiability, where  $\chi$  and all  $\psi_k$ 's are quantifier-free formulas, and there are no tests for labels. Furthermore, this is a polynomial-time translation. This translation is just the Scott normal form of  $\text{EMSO}^2(+1, <, \sim)$  [1] adapted to *forward-EMSO*<sup>2</sup>(+1, <, ~), and can be done in the same way.

We now give an exponential-time translation  $tr : \text{forward-EMSO}^2(+1, <, \sim) \rightarrow \text{LRV}$ . For any formula  $\varphi$  of *forward-EMSO*<sup>2</sup>(+1, <, ~),  $tr(\varphi)$  is an equivalent (in the sense of satisfiability) LRV formula.

The translation makes use of: a distinguished variable  $x$  that encodes the data values of any data word satisfying  $\varphi$ ; variables  $x_0, \dots, x_n$  that are used to encode the monadic relations  $X_1, \dots, X_n$ ; and a variable  $x_{prev}$  whose purpose will be explained later on. We give now the translation. To translate  $\forall x \forall y \chi$ , we first bring the formula to a form

$$\bigwedge_{m \in M} \neg \exists x \exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y), \quad (*)$$

where every  $\chi_m^x$  (resp.  $\chi_m^y$ ) is a conjunction of (negations of) atoms of monadic relations on  $x$  (resp.  $y$ ); and  $\chi_m = \mu \wedge \nu$  where  $\mu \in \{x=y, +1(x, y), \neg(+1(x, y) \vee x=y)\}$  and  $\nu \in \{x \sim y, \neg(x \sim y)\}$ . As an example, if  $\chi = \neg(x \sim y) \vee X(x) \vee X(y)$ , then the corresponding formula would be

$$\bigwedge_{\mu, \chi^x, \chi^y} ((\neg \exists x \exists y x \leq y \wedge \mu \wedge \neg X(x) \wedge \chi^y) \wedge (\neg \exists x \exists y x \leq y \wedge \mu \wedge \chi^x \wedge \neg X(y)))$$

for all  $\mu \in \{x=y, +1(x, y), \neg(+1(x, y) \vee x=y)\}$ ,  $\chi^x \in \{X(x), \neg X(x)\}$ ,  $\chi^y \in \{X(y), \neg X(y)\}$ . We can bring the formula into this normal form in exponential time. We define  $tr(\chi_m^x)$  as the conjunction of all the formulas  $x_0 \approx x_i$  so that  $X_i(x)$  is a conjunct of  $\chi_m^x$ , and all the formulas  $\neg(x_0 \approx x_i)$  so that  $\neg X_i(x)$  is a conjunct of  $\chi_m^x$ ; we do similarly for  $tr(\chi_m^y)$ . If  $\mu = +1(x, y)$  and  $\nu = x \sim y$  we define  $tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y))$  as  $x \approx Xx \wedge tr(\chi_m^x) \wedge Xtr(\chi_m^y)$ .

If  $\mu = (x = y)$  and  $\nu = x \sim y$  we translate

$$tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)) = tr(\chi_m^x) \wedge tr(\chi_m^y).$$

We proceed similarly for  $\mu = +1(x, y)$ ,  $\nu = \neg(x \sim y)$ ; and the translation is of course  $\perp$  (false) if  $\mu = (x=y)$ ,  $\nu = \neg(x \sim y)$ . The difficult cases are the remaining ones. Suppose  $\mu = \neg(+1(x, y) \vee x=y)$ ,  $\nu = x \sim y$ . In other words,  $x$  is at least two positions before  $y$ , and they have the same data value. Observe that the formula  $tr(\chi_m^x) \wedge \mathbf{x} \approx \langle tr(\chi_m^y)? \rangle \mathbf{x}$  does not encode precisely this case, as it would correspond to a weaker condition  $x < y \wedge x \sim y$ . In order to properly translate this case we make use of the variable  $\mathbf{x}_{prev}$ , ensuring that it always has the data value of the variable  $\mathbf{x}$  in the previous position

$$prev = G( X\top \Rightarrow \mathbf{x} \approx X\mathbf{x}_{prev} ).$$

We then define  $tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y))$  as

$$tr(\chi_m^x) \wedge \mathbf{x} \approx \langle \mathbf{x}_{prev} \approx \langle tr(\chi_m^y)? \rangle \mathbf{x}? \rangle \mathbf{x}_{prev}.$$

Note that by nesting twice the future obligation we ensure that the target position where  $tr(\chi_m^y)$  must hold is at a distance of at least two positions. For  $\nu = \neg(x \sim y)$  we produce a similar formula, replacing the innermost appearance of  $\approx$  with  $\not\approx$  in the formula above. We then define  $tr(\forall x \forall y \chi)$  as

$$prev \wedge \bigwedge_{m \in M} \neg F tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)).$$

To translate  $\forall x \exists y (x \leq y \wedge \psi_k)$  we proceed in a similar way. One can show that  $tr$  preserves satisfiability. By Corollary 10 we can decide the satisfiability of the translation in 2EXPSpace, and since the translation is exponential, this gives us a 3EXPSpace bound for the satisfiability of *forward-EMSO*<sup>2</sup>(+1, <, ~). ■

*Temporal Logics:* Our results have also implications for a fragment of the logic LTL extended with one register for storing and comparing data values (called the *freeze* operator), noted  $LTL_1^\downarrow$ . Its satisfiability problem was shown to be decidable, but with non-primitive-recursive complexity [7].

Our results yield a fragment of  $LTL_1^\downarrow$  with elementary 2EXPSpace upper bound.

## VIII. CONCLUSION

We introduced the logic LRV and variants by allowing data value repetitions thanks to formulas of the form  $\mathbf{x} \approx \langle \phi? \rangle \mathbf{y}$ . LRV extends the main logic from [6] but it is also a fragment of BD-LTL from [17] whose satisfiability is equivalent to Reach(VASS). We showed that SAT(LRV) is 2EXPSpace-complete by reduction into the control-state reachability problem for VASS (via a detour to gainy VASS) and by introducing a new class of counter machines (the chain systems) in order to get the complexity lower bound. This new class of counter machines is interesting for its own sake and could be used to establish other hardness results thanks to our results that extend non-trivially the proof from [22], [8]. Correspondences between extensions of LRV (such as PLRV,  $PLRV^\top$  and  $PLRV_1$ ) and reachability problem for VASS are also established, strengthening further the relationships between data

logics and reachability problems for counter machines. Other results for variants are presented in the paper and a summary can be found below.

$$\begin{aligned} LRV_k^\top &: \text{PSPACE-complete} \\ LRV &\equiv LRV^\top \equiv LRV_1 \equiv LRV + \{\oplus_1, \dots, \oplus_k\} : 2\text{EXPSpace-complete} \\ PLRV &\equiv PLRV^\top \equiv PLRV_1 \equiv \text{Reach(VASS)} \\ LRV_{vec}^\top &: \text{undecidable} \end{aligned}$$

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### A. Proof of Theorem 1

*Proof:* First, the reachability problem for VASS can be reduced in polynomial-space to its restriction such that the initial and final configurations have all the counters equal to zero and each transition can only increment or decrement a unique counter. In the sequel, we consider an instance of this subproblem:  $\mathcal{A} = \langle Q, C, \delta \rangle$  is a VASS, the initial configuration is  $\langle q_i, \vec{0} \rangle$  and the final configuration is  $\langle q_f, \vec{0} \rangle$ .

Now, we build a formula  $\phi$  in PLRV such that  $\langle q_f, \vec{0} \rangle$  is reachable from  $\langle q_i, \vec{0} \rangle$  iff  $\phi$  is satisfiable. To do so, we encode runs of  $\mathcal{A}$  by data words following exactly the proof of [1, Theorem 16] except that the properties are expressed in PLRV instead of FO2( $\sim, <, +1$ ).

The objective is to encode a word  $\rho \in \delta^*$  that represents an accepting run from  $\langle q_i, \vec{0} \rangle$  to  $\langle q_f, \vec{0} \rangle$ . We use the alphabet  $\delta$  of transitions, that we code using a logarithmic number of variables. One can simulate  $m$  different labels in PLRV, by using  $\lceil \log(m) + 1 \rceil$  variables and its equivalence classes. In order to simulate the alphabet  $\delta$ , we use the variables  $x_0, \dots, x_N$ , with  $N = \lceil \log(\text{card}(\delta)) \rceil$ . For any  $t \in \delta$ , let  $\langle t \rangle \in \text{PLRV}$  be the formula that tests for label  $t$  at the current position. More precisely, for any fixed injective function  $\lambda : \delta \rightarrow 2^{\{1, \dots, N\}}$  we define

$$\langle t \rangle = \bigwedge_{i \in \lambda(t)} x_0 = x_i \quad \wedge \quad \bigwedge_{1 \leq i \leq N, i \notin \lambda(t)} x_0 \neq x_i.$$

Note that  $\langle t \rangle$  uses exclusively variables  $x_0, \dots, x_N$ , that is of size logarithmic in  $\text{card}(\delta)$ , and that  $\langle t \rangle$  holds at a position for at most one  $t \in \delta$ . We build a PLRV formula  $\phi$  so that any word from  $\delta^*$  corresponding to a model of  $\phi$  is an accepting run for  $\mathcal{A}$  from  $\langle q_i, \vec{0} \rangle$  to  $\langle q_f, \vec{0} \rangle$ . And conversely, for any accepting run of  $\mathcal{A}$  from  $\langle q_i, \vec{0} \rangle$  to  $\langle q_f, \vec{0} \rangle$  there is a model of  $\phi$  corresponding to the run. The following are standard counter-blind conditions to check.

- 1) Every position satisfies  $\langle t \rangle$  for some  $t \in \delta$ .
- 2) The first position satisfies  $\langle \langle q_i, \text{instr}, q \rangle \rangle$  for some  $q \in Q$ .
- 3) The last position satisfies  $\langle \langle q, \text{instr}, q_f \rangle \rangle$  for some  $q \in Q$ .
- 4) There are no two consecutive positions  $i$  and  $i + 1$  satisfying  $\langle \langle q_1, \text{instr}, q_2 \rangle \rangle$  and  $\langle \langle q'_1, \text{instr}', q'_2 \rangle \rangle$  respectively, with  $q_2 \neq q'_1$ .

In the formula  $\phi$ , we use the variable  $x$  to relate increments and decrements. Here are the main properties to satisfy.

- 1) For every counter  $c \in C$ , there are no two positions labelled by a transition with instruction  $\text{inc}(c)$  having the same value for  $x$ :

$$G(\text{inc}(c) \Rightarrow \neg(x \approx \langle \text{inc}(c) \rangle x))$$

where  $\text{inc}(c)$  is a shortcut for  $\bigvee_{\langle q, \text{inc}(c), q' \rangle \in \delta} \langle t \rangle$ . A similar constraint can be expressed with  $\text{dec}(c)$ .

- 2) For every counter  $c \in C$ , for every position labelled by a transition with instruction  $\text{inc}(c)$ , there is a future position labelled by a transition with instruction  $\text{dec}(c)$  with the same value for  $x$ :

$$G(\text{inc}(c) \Rightarrow x \approx \langle \text{dec}(c) \rangle x)$$

where  $\text{dec}(c)$  is a shortcut for  $\bigvee_{\langle q, \text{dec}(c), q' \rangle \in \delta} \langle t \rangle$ . This will guarantee that the final configuration ends with all counters equal to zero.

- 3) Similarly, for every counter  $c \in C$ , for every position labelled by a transition with instruction  $\text{dec}(c)$ , there is a past position labelled by a transition with instruction  $\text{inc}(c)$  with the same value for  $x$ :

$$G(\text{dec}(c) \Rightarrow x \approx \langle \text{inc}(c) \rangle^{-1} x)$$

This will guarantee that every decrement follows a corresponding increment, satisfying that counter values are never negative.

Let  $\phi$  be the conjunction of all the formulas defined above. Since all the properties considered herein are those used in the proof of [1, Theorem 16] (but herein they are expressed in PLRV instead of FO2( $\sim, <, +1$ )), it follows that  $\phi$  is satisfiable in PLRV iff there is an accepting run of  $\mathcal{A}$  from  $\langle q_i, \vec{0} \rangle$  to  $\langle q_f, \vec{0} \rangle$ .  $\blacksquare$

## B. Proofs of Propositions 2 and 3

Henceforward,  $\text{vars}(\varphi)$  denotes the set of all variables in  $\varphi$ , and  $\text{sub}_{\langle \rangle}(\varphi)$  the set of all subformulas  $\psi$  such that  $x \approx \langle \psi? \rangle y$  or  $x \not\approx \langle \psi? \rangle y$  appears in  $\varphi$  for some  $x, y \in \text{vars}(\varphi)$ .

In both reductions we make use of the following easy lemma.

**Lemma 25.** There is a polynomial-time satisfiability-preserving translation  $t : \text{LRV} \rightarrow \text{LRV}$  [resp.  $t : \text{LRV}^{\approx} \rightarrow \text{LRV}^{\approx}$ ] such that for every  $\varphi$  and  $\psi \in \text{sub}_{\langle \rangle}(t(\varphi))$ , we have  $\text{sub}_{\langle \rangle}(\psi) = \emptyset$ .

*Proof:* This is a standard reduction. Indeed, given a formula  $\varphi$  with subformula  $x \approx \langle \psi? \rangle y$ ,  $\varphi$  is satisfiable iff  $G(\psi \Leftrightarrow x_{\text{new}} \approx y_{\text{new}}) \wedge \varphi[x \approx \langle \psi? \rangle y \leftarrow x \approx \langle x_{\text{new}} \approx y_{\text{new}}? \rangle y]$  is satisfiable, where  $x_{\text{new}}, y_{\text{new}} \notin \text{vars}(\varphi)$ , and  $\varphi[x \approx \langle \psi? \rangle y \leftarrow x \approx \langle x_{\text{new}} \approx y_{\text{new}}? \rangle y]$  is the result of replacing every occurrence of  $x \approx \langle \psi? \rangle y$  by  $x \approx \langle x_{\text{new}} \approx y_{\text{new}}? \rangle y$  in  $\varphi$ . ■

*Proof of Proposition 2:* We show how to compute in polynomial time, for every  $\varphi \in \text{LRV}$ , a formula  $\varphi' \in \text{LRV}^{\approx}$  that preserves satisfiability. The formula  $\varphi'$  uses, besides all the variables from  $\varphi$ , a distinguished variable  $k \notin \text{vars}(\varphi)$ , and variables  $v_{x,\psi}^{\approx}, v_{x \not\approx \langle \psi? \rangle y}$  for every subformula  $\psi$  of  $\varphi$  and variables  $x, y \in \text{vars}(\varphi)$ .

The idea behind the coding is the following. Any positive test  $x \approx \langle \psi? \rangle y$  can be always converted to a test  $x \not\approx v_{x \not\approx \langle \psi? \rangle y} \wedge v_{x \not\approx \langle \psi? \rangle y} \approx \langle \psi? \rangle y$ , where  $v_{x \not\approx \langle \psi? \rangle y}$  is a fresh variable. At any given position, we only need at most as many fresh variables as there are subformulas  $\psi$  and source and target variables  $x, y \in \text{vars}(\varphi)$ , and we hence index these fresh variables by  $x, y$  and  $\psi$ . On the other hand, when a negative test  $\neg(x \not\approx \langle \psi? \rangle y)$  holds at a position  $i$ , it means that all positions satisfying  $\psi$  to the right of  $i$  have the same data value in the variable  $y$ . Since this data value is determined for every  $\psi, y$ , there is only boundedly many data values to remember, one for every subformula and variable. We use separate variables  $v_{x,\psi}^{\approx}$  to remember this data value for every  $x, \psi$ . This variable is used in conjunction with  $k$ . Whenever  $v_{x,\psi}^{\approx} \not\approx k$  at a position  $i$ , it means that  $\neg(v_{x,\psi}^{\approx} \approx \langle \psi? \rangle x)$ , and we can make sure of this without using  $\not\approx$ . Indeed, we can do this by letting  $v_{x,\psi}^{\approx}$  maintain the same value from position  $i$  onwards —until the last element verifying  $\psi$ —, we can test that whenever  $\psi$  holds, we have  $x \approx v_{x,\psi}^{\approx}$ .

For any model  $\sigma$ , let  $\sigma_{\varphi}$  be so that

- (a)  $|\sigma| = |\sigma_{\varphi}|$ ;
- (b) for every  $0 \leq i < |\sigma|$  and  $x \in \text{vars}(\varphi)$ ,  $\sigma(i)(x) = \sigma_{\varphi}(i)(x)$ ;
- (c) there is some data value  $d \notin \{\sigma(i)(x) \mid x \in \text{vars}(\varphi), 0 \leq i < |\sigma|\}$  such that for every  $0 \leq i < |\sigma_{\varphi}|$ ,  $\sigma_{\varphi}(i)(k) = d$ ;
- (d) for every  $0 \leq i < |\sigma|$ ,  $x \in \text{vars}(\varphi)$ , and  $\psi \in \text{sub}_{\langle \rangle}(\varphi)$ ,
  - if for some  $j \geq i$ ,  $\sigma, j \models \psi$  and for every  $i \leq j' < |\sigma|$  such that  $\sigma, j' \models \psi$  we have  $\sigma(j)(x) = \sigma(j')(x)$ , then  $\sigma_{\varphi}(i)(v_{x,\psi}^{\approx}) = \sigma(j)(x)$ ,
  - otherwise,  $\sigma_{\varphi}(i)(v_{x,\psi}^{\approx}) = \sigma_{\varphi}(i)(k)$ ;
- (e) for every  $0 \leq i < |\sigma|$ ,  $x, y \in \text{vars}(\varphi)$ , and  $\psi \in \text{sub}_{\langle \rangle}(\varphi)$ ,
  - if for some  $j > i$ ,  $\sigma(j)(y) \neq \sigma(i)(x)$  and  $\sigma, j \models \psi$ , then let  $j_0$  be the first such  $j$ , and  $\sigma_{\varphi}(i)(v_{x \not\approx \langle \psi? \rangle y}) = \sigma(j_0)(y)$ ,
  - otherwise,  $\sigma_{\varphi}(i)(v_{x \not\approx \langle \psi? \rangle y}) = \sigma_{\varphi}(i)(k)$ .

For every other unmentioned variable,  $\sigma$  and  $\sigma_{\varphi}$  coincide. It is evident that for every  $\varphi$  and  $\sigma$ , a model  $\sigma_{\varphi}$  with the aforementioned properties exists. Next, we define  $\varphi' \in \text{LRV}^{\approx}$  so that

$$\sigma \models \varphi \text{ if, and only if, } \sigma_{\varphi} \models \varphi'. \quad (\dagger)$$

We assume that for every  $\psi \in \text{sub}_{\langle \rangle}(\varphi)$ , we have that  $\text{sub}_{\langle \rangle}(\psi) = \emptyset$ ; this is without any loss of generality due to Lemma 25. We will also assume that  $\varphi$  is in negation normal form, that is, negation appears only in subformulas of the type  $\neg(x \approx X^{\ell}y)$  or  $\neg(x \approx \langle \psi? \rangle y)$  [resp.  $\not\approx$ ]. In particular, this means that we introduce a dual operator for each temporal operator.

- First, the variable  $k$  will act as a constant; it will always have the same data value at any position of the model, which must be different from those of all variables of  $\varphi$ .

$$\text{const} = G \left( (k \approx Xk \vee \neg X\top) \wedge \bigwedge_{x \in \text{vars}(\varphi)} (k \not\approx x) \right)$$

- Second, for every position we ensure that if  $v_{x,\psi}^{\approx}$  is different from  $k$ , then it preserves its value until the last element verifying  $\psi$ ; and if  $\psi$  holds at any of these positions then  $v_{x,\psi}^{\approx}$  contains the value of  $x$ .

$$val-v_{x,\psi}^{\approx} = G \left( \begin{array}{l} k \not\approx v_{x,\psi}^{\approx} \Rightarrow v_{x,\psi}^{\approx} \approx Xv_{x,\psi}^{\approx} \vee \neg XF\psi \quad \wedge \\ k \not\approx v_{x,\psi}^{\approx} \wedge \psi \Rightarrow v_{x,\psi}^{\approx} \approx x \end{array} \right)$$

- Finally, let  $\varphi^{\approx}$  be the result of replacing
  - (a) every appearance of  $\neg(x \not\approx \langle \psi? \rangle y)$  by  $\neg XF\psi \vee x \approx Xv_{y,\psi}^{\approx}$ , and
  - (b) every positive appearance of  $x \not\approx \langle \psi? \rangle y$  by  $x \not\approx v_{x \not\approx \langle \psi? \rangle y} \wedge v_{x \not\approx \langle \psi? \rangle y} \approx \langle \psi? \rangle y$
 in  $\varphi$ .

We then define the translation  $\varphi'$  as follows.

$$\varphi' = \varphi^{\approx} \wedge const \wedge \bigwedge_{\substack{\psi \in sub_{\langle \rangle}(\varphi), \\ x \in vars(\varphi)}} val-v_{x,\psi}^{\approx}$$

Notice that  $\varphi'$  can be computed from  $\varphi$  in polynomial time.

**Claim 26.**  $\varphi$  is satisfiable if, and only if,  $\varphi'$  is satisfiable.

*Proof:*

[ $\Rightarrow$ ] Note that one direction would follow from ( $\dagger$ ): if  $\varphi$  is satisfiable by some  $\sigma$ , then  $\varphi'$  is satisfiable in  $\sigma_{\varphi}$ . In order to establish ( $\dagger$ ), we show that

$$\text{for every subformula } \gamma \text{ of } \varphi \text{ and } 0 \leq i < |\sigma_{\varphi}|, \text{ we have } \sigma, i \models \gamma \text{ iff } \sigma_{\varphi}, i \models \gamma^{\approx}. \quad (\ddagger)$$

Since  $\sigma_{\varphi} \models const$  by condition (c), and  $\sigma_{\varphi} \models val-v_{x,\psi}^{\approx}$  for every  $\psi$  due to (d), this is sufficient to conclude that  $\sigma \models \varphi$  iff  $\sigma_{\varphi} \models \varphi'$ .

We show ( $\ddagger$ ) by structural induction. If  $\gamma = x \not\approx \langle \psi? \rangle y$ , then  $\sigma, i \models \gamma$  if there is some  $j > i$  where  $\sigma(j)(y) \neq \sigma(i)(x)$  and  $\sigma, j \models \psi$ . Let  $j_0$  be the first such  $j$ . Then, by condition (e),  $\sigma(i)(x) \neq \sigma_{\varphi}(i)(v_{x \not\approx \langle \psi? \rangle y}) = \sigma(j_0)(y)$ . Hence,  $\sigma_{\varphi}, i \models v_{x \not\approx \langle \psi? \rangle y} \approx \langle \psi? \rangle y$  and  $\sigma_{\varphi}, i \models x \not\approx v_{x \not\approx \langle \psi? \rangle y}$  and thus  $\sigma_{\varphi}, i \models \gamma^{\approx}$ .

If, on the other hand,  $\gamma = \neg(x \not\approx \langle \psi? \rangle y)$  then either

- there is no  $j > i$  so that  $\sigma, j \models \psi$ , or, otherwise,
- for every  $j' \geq i + 1$  so that  $\sigma, j' \models \psi$  we have  $\sigma(j')(y) = \sigma(i)(x)$ .

In the first case we have that  $\sigma_{\varphi}, i \models \neg XF\psi$ , and in the second case we have that, by (d),  $\sigma_{\varphi}(i')(v_{y,\psi}^{\approx}) = \sigma_{\varphi}(i)(x)$  for every  $i' > i$ , and in particular for  $i' = i + 1$ . Hence,  $\sigma_{\varphi}, i \models \neg XF\psi \vee x \approx Xv_{y,\psi}^{\approx}$  and thus  $\sigma_{\varphi}, i \models \gamma^{\approx}$ .

Finally, the proof for the base case of the form  $\gamma = x \approx X^{\ell}y$  [resp.  $\not\approx$ ] and for all boolean and temporal operators are by an easy verification, since  $(\cdot)^{\approx}$  is homomorphic for these. Hence, ( $\ddagger$ ) holds.

[ $\Leftarrow$ ] Now suppose that  $\sigma \models \varphi'$ . Since  $\sigma \models const$ , we have that  $\sigma$  verifies condition (c). And since  $\sigma \models val-v_{x,\psi}^{\approx}$  for every  $\psi \in sub_{\langle \rangle}(\varphi)$ ,  $x \in vars(\varphi)$ , we have that  $\sigma$  verifies (d). We prove by structural induction that for every subformula  $\gamma$  of  $\varphi$ , if  $\sigma, i \models \gamma^{\approx}$  then  $\sigma, i \models \gamma$ .

- If  $\gamma = x \approx y$  [resp.  $x \not\approx y$ ,  $x \approx X^{\ell}y$ ,  $x \not\approx X^{\ell}y$ ] it is immediate since  $\gamma^{\approx} = \gamma$ .
- If  $\gamma = x \not\approx \langle \psi? \rangle y$  and thus  $\gamma^{\approx} = x \not\approx v_{x \not\approx \langle \psi? \rangle y} \wedge v_{x \not\approx \langle \psi? \rangle y} \approx \langle \psi? \rangle y$ . Then,  $\sigma(i)(x) \neq \sigma(i)(v_{x \not\approx \langle \psi? \rangle y}) = \sigma(j)(y)$  for some  $j > i$  where  $\sigma, j \models \psi$ . Hence,  $\sigma, i \models x \not\approx \langle \psi? \rangle y$ .
- If  $\gamma = \neg(x \not\approx \langle \psi? \rangle y)$ , and thus  $\gamma^{\approx} = \neg XF\psi \vee x \approx Xv_{y,\psi}^{\approx}$ . This means that either
  - there is no  $j > i$  so that  $\sigma, j \models \psi$  and hence  $\sigma, i \models \neg(x \not\approx \langle \psi? \rangle y)$ , or
  - $\sigma, j \models \psi$  for some  $j > i$  and  $\sigma(i)(x) = \sigma(i+1)(v_{y,\psi}^{\approx})$ . By condition (d), we have that for all  $i < j'$ , so that  $\sigma, j' \models \psi$ , we have  $\sigma(j')(v_{y,\psi}^{\approx}) = \sigma(j')(y)$ . Then,  $\sigma, i \models \neg(x \not\approx \langle \psi? \rangle y)$ .
- If  $\gamma = F\psi$  and  $\gamma^{\approx} = F\psi^{\approx}$ , there must be some position  $i' \geq i$  so that  $\sigma, i' \models \psi^{\approx}$ . By inductive hypothesis,  $\sigma, i' \models \psi$  and hence  $\sigma, i \models F\psi$ . We proceed similarly for all temporal operators and their dual, as well as for the boolean operators  $\wedge$ ,  $\vee$ . This is because  $(\cdot)^{\approx}$  is homomorphic for all temporal and positive boolean operators. ■

We can easily extend this coding allowing for past obligations. We only need to use some extra variables  $v_{x,\psi}^{-1,\approx}$ ,  $v_{x \not\approx \langle \psi? \rangle^{-1}y}^{-1}$  that behave in the same way as the previously defined, but with past obligations. That

is, we also define a  $val\text{-}v_{x,\psi}^{-1,\approx}$  as  $val\text{-}v_{x,\psi}^{\approx}$ , but making use of  $v_{x,\psi}^{-1,\approx}$ ,  $X^{-1}$  and  $F^{-1}$ . And finally, we have to further replace

- (c) every appearance of  $\neg(x \not\approx \langle\psi?\rangle^{-1}y)$  by  $\neg X^{-1}F^{-1}\psi \vee x \approx Xv_{y,\psi}^{-1,\approx}$ , and
  - (d) every positive appearance of  $x \not\approx \langle\psi?\rangle^{-1}y$  by  $x \not\approx v_{x \not\approx \langle\psi?\rangle^{-1}y}^{-1} \wedge v_{x \not\approx \langle\psi?\rangle^{-1}y}^{-1} \approx \langle\psi?\rangle^{-1}y$
- in  $\varphi$  to obtain  $\varphi^{\approx}$ . ■

*Proof of Proposition 3:* We show how to compute in polynomial time, for every  $\varphi \in \text{LRV}^{\approx}$ , a formula  $\varphi' \in \text{LRV}^{\top}$  that preserves satisfiability. The formula  $\varphi'$  uses, besides all the variables from  $\varphi$ , a distinguished variable  $k$  that does not appear in  $\varphi$ , and a variable  $v_{x,\psi}$  for every subformula  $\psi$  of  $\varphi$  and every variable  $x$  appearing in  $\varphi$ .

The idea is that  $k$  acts as a constant, it preserves the same data value throughout the model, and we enforce, for every subformula  $\psi$  and variable  $x$  of  $\varphi$ , that:  $\psi$  holds at a position if  $v_{x,\psi} \approx x$ , and  $\psi$  does not hold at a position if  $v_{x,\psi} \approx k$ . Now, instead of testing  $x \approx \langle\psi?\rangle y$ , one can simply test  $x \approx \langle\top?\rangle v_{y,\psi}$ .

Let  $\varphi \in \text{LRV}^{\approx}$ . We define  $\text{vars}(\varphi)$  as the set of all variables in  $\varphi$ , and  $\text{sub}_{\langle?\rangle}(\varphi)$  as the set of all subformulas  $\psi$  such that  $x \approx \langle\psi?\rangle y$  appears in  $\varphi$  for some  $x, y$ . We show how to build a formula  $\varphi' \in \text{LRV}^{\top}$  which preserves satisfiability. For any model  $\sigma$ , let  $\sigma_{\varphi}$  be so that

- (a)  $|\sigma| = |\sigma_{\varphi}|$ ,
- (b) for every  $0 \leq i < |\sigma|$  and  $x \in \text{vars}(\varphi)$ ,  $\sigma(i)(x) = \sigma_{\varphi}(i)(x)$ ,
- (c) there is some data value  $d \notin \{\sigma_{\varphi}(i)(x) \mid x \in \text{vars}(\varphi), 0 \leq i < |\sigma|\}$  such that for every  $0 \leq i < |\sigma|$ ,  $\sigma_{\varphi}(i)(k) = d$ , and
- (d) for every  $0 \leq i < |\sigma|$ ,  $\sigma_{\varphi}(i)(v_{x,\psi}) = \sigma_{\varphi}(i)(x)$  if  $\sigma, i \models \psi$ , and  $\sigma_{\varphi}(i)(v_{x,\psi}) = \sigma_{\varphi}(i)(k)$  otherwise.

For every other unmentioned variable,  $\sigma$  and  $\sigma_{\varphi}$  coincide. It is evident that for every  $\varphi$  and  $\sigma$ , a model  $\sigma_{\varphi}$  with the aforementioned properties exists. Next, we define  $\varphi' \in \text{LRV}^{\top}$  so that

$$\sigma \models \varphi \text{ if, and only if, } \sigma_{\varphi} \models \varphi'. \quad (\dagger)$$

We assume that for every  $\psi \in \text{sub}_{\langle?\rangle}(\varphi)$ , we have that  $\text{sub}_{\langle?\rangle}(\psi) = \emptyset$ . This is without any loss of generality by Lemma 25.

- First, the variable  $k$  will act as a constant; it will always have the same data value at any position of the model, which must be different from those of all variables of  $\varphi$ .

$$\text{const} = G \left( (k \approx Xk \vee \neg X\top) \wedge \bigwedge_{x \in \text{vars}(\varphi)} (k \not\approx x) \right)$$

- Second, any variable  $v_{x,\psi}$  has either the value of  $k$  or that of  $x$ . Further, the latter holds if, and only if,  $\psi$  is true.

$$\text{val-}v_{x,\psi} = G(v_{x,\psi} \approx k \vee v_{x,\psi} \approx x) \wedge G(v_{x,\psi} \approx x \Leftrightarrow \psi)$$

- Finally, let  $\varphi^{\top}$  be the result of replacing every appearance of  $x \approx \langle\psi?\rangle y$  by  $x \approx \langle\top?\rangle v_{y,\psi}$  in  $\varphi$ . We then define  $\varphi'$  as follows.

$$\varphi' = \varphi^{\top} \wedge \text{const} \wedge \bigwedge_{\psi \in \text{sub}_{\langle?\rangle}(\varphi)} \text{val-}v_{x,\psi}$$

Notice that  $\varphi'$  can be computed from  $\varphi$  in polynomial time.

**Claim 27.**  $\varphi$  is satisfiable if, and only if,  $\varphi'$  is satisfiable.

*Proof:*

[ $\Rightarrow$ ] Note that one direction would follow from ( $\dagger$ ): if  $\varphi$  is satisfiable by some  $\sigma$ , then  $\varphi'$  is satisfiable in  $\sigma_{\varphi}$ . In order to establish ( $\dagger$ ), we show that

$$\text{for every subformula } \gamma \text{ of } \varphi \text{ and } 0 \leq i < |\sigma_{\varphi}|, \text{ we have } \sigma, i \models \gamma \text{ iff } \sigma_{\varphi}, i \models \gamma^{\approx}. \quad (\ddagger)$$

Since  $\sigma_{\varphi} \models \text{const}$  by condition (c) and  $\sigma_{\varphi} \models \text{val-}v_{x,\psi}$  for every  $\psi$  due to (d), this is sufficient to conclude then  $\sigma \models \varphi$  iff  $\sigma_{\varphi} \models \varphi'$ . We show it by structural induction. If  $\sigma, i \models x \approx \langle\psi?\rangle y$  then there is some  $j > i$  where  $\sigma(j)(y) = \sigma(i)(x)$  and  $\sigma, j \models \psi$ . Then, by condition (d),  $\sigma_{\varphi}(j)(v_{y,\psi}) = \sigma_{\varphi}(j)(y)$  and thus  $\sigma_{\varphi}, i \models x \approx \langle\top?\rangle v_{y,\psi}$ . Conversely, if  $\sigma_{\varphi}, i \models x \approx \langle\top?\rangle v_{y,\psi}$  this means that there is some  $j > i$  such that  $\sigma_{\varphi}(j)(x) = \sigma_{\varphi}(j)(v_{y,\psi})$ . By (c), we have that  $\sigma_{\varphi}(j)(v_{y,\psi}) \neq \sigma_{\varphi}(j)(k)$ , and by (d) this means that  $\sigma_{\varphi}(j)(v_{y,\psi}) = \sigma_{\varphi}(j)(y)$  and  $\sigma_{\varphi}, j \models \psi$ . Therefore,  $\sigma, i \models x \approx \langle\psi?\rangle y$ . The proof for the base case of



the form  $\psi = x \approx X^i y$  and for all the cases of the induction step are by an easy verification since  $(\cdot)^\top$  is homomorphic. Hence,  $(\ddagger)$  holds.

[ $\Leftarrow$ ] Suppose that  $\sigma \models \varphi'$ . Note that due to *const* condition (c) holds in  $\sigma$ , and due to *val-v<sub>x,ψ</sub>*, condition (d) holds. We show that for every position  $i$  and subformula  $\gamma$  of  $\varphi$ , if  $\sigma, i \models \gamma^\top$ , then  $\sigma, i \models \gamma$ .

The only interesting case is when  $\gamma = x \approx \langle \psi? \rangle y$ , and  $\gamma^\top = x \approx \langle \top? \rangle v_{y,\psi}$ , since for all boolean and temporal operators  $(\cdot)^\top$  is homomorphic. If  $\sigma, i \models x \approx \langle \top? \rangle v_{y,\psi}$  there must be some  $j > i$  so that  $\sigma(j)(v_{y,\psi}) = \sigma(i)(x)$ . By condition (c),  $\sigma(j)(k) \neq \sigma(j)(v_{y,\psi}) = \sigma(i)(x)$ , and by condition (d), this means that  $\sigma, j \models \psi$  and  $\sigma(j)(v_{y,\psi}) = \sigma(j)(y)$ . Hence,  $\sigma, i \models x \approx \langle \psi? \rangle y$ . The remaining cases are straightforward since  $(\cdot)^\top$  is homomorphic on temporal and boolean operators. ■

Finally, note that we can extend this coding to treat past obligations in the obvious way. ■

### C. Proofs of Lemmas 7, 8 and Theorem 9

We define the VASS  $\mathcal{A}_{\text{inc}}$  as  $\langle Q, C, \delta^{\text{min}} \rangle$  and  $Q_0, Q_f \subseteq Q$ , where  $Q, Q_0, Q_f$  and  $C$  are same as those of  $\mathcal{A}_\phi$  and  $\delta^{\text{min}}$  is defined as follows:  $(q, \text{minup}_{q,q'}, q') \in \delta^{\text{min}}$  iff  $\delta \cap (\{q\} \times [-k, k]^C \times \{q'\})$  is not empty and

$$\text{minup}_{q,q'}(X) = \min_{\mathbf{u}: (q, \mathbf{u}, q') \in \delta} \{\mathbf{u}(X)\} \text{ for all } X \in C.$$

We also use the “maximal” transition in some lemmas:

$$\text{maxup}_{q,q'}(X) = \max_{\mathbf{u}: (q, \mathbf{u}, q') \in \delta} \{\mathbf{u}(X)\}.$$

*Proof of Lemma 7:* Since  $\langle q, \mathbf{v} \rangle \rightarrow \langle q', \mathbf{v}' \rangle$ , there is a transition  $(q, \mathbf{u}, q') \in \delta$  such that  $\mathbf{u} + \mathbf{v} = \mathbf{v}'$ . By definition of  $\mathcal{A}_{\text{inc}}$ , there is a transition  $(q, \text{minup}_{q,q'}, q') \in \delta^{\text{min}}$  where  $\mathbf{u} - \text{minup}_{q,q'} \succeq \mathbf{0}$ . Let  $\text{incer} = \mathbf{u} - \text{minup}_{q,q'}$  (this will be the incremental error used in  $\mathcal{A}_{\text{inc}}$ ). Now we have  $\langle q, \mathbf{v} + \text{incer} \rangle \xrightarrow{\text{minup}_{q,q'}} \langle q', \mathbf{v} + \mathbf{u} \rangle = \langle q', \mathbf{v}' \rangle$  in  $\mathcal{A}_{\text{inc}}$ . Hence,  $\langle q, \mathbf{v} \rangle \rightarrow_{\text{gainy}} \langle q', \mathbf{v}' \rangle$  in  $\mathcal{A}_{\text{inc}}$ . ■

*Proof of Lemma 8:* By induction on the length  $n$  of the run  $\langle q_1, \mathbf{v}_1 \rangle \xrightarrow{*}_{\text{gainy}} \langle q_2, \mathbf{0} \rangle$ . The base case  $n = 0$  is trivial since there is no change in the configuration.

Induction step: the idea is to simulate the first gainy transition by a normal transition that decreases each counter as much as possible while ensuring that (1) the resulting value is non-negative and (2) we can apply the induction hypothesis to the resulting valuation. We calculate the update required for each counter individually and by closure under component-wise interpolation, there will always be a transition with the required update function. Let  $\langle q_1, \mathbf{v}_1 \rangle \rightarrow_{\text{gainy}} \langle q_3, \mathbf{v}_3 \rangle \xrightarrow{*}_{\text{gainy}} \langle q_2, \mathbf{0} \rangle$  and  $\mathbf{v}'_1 \preceq \mathbf{v}_1$ . We will define an update function  $\mathbf{u}'$  such that  $\langle q_1, \mathbf{v}'_1 \rangle \rightarrow \langle q_3, \mathbf{v}'_3 \rangle$ ,  $\mathbf{v}'_3(X) = \mathbf{v}'_1(X) + \mathbf{u}'(X)$  for each  $X \in C$  and  $\mathbf{v}'_3 \preceq \mathbf{v}_3$ . For each counter  $X \in C$ ,  $\mathbf{u}'(X)$  is defined as follows:

*Case 1:*  $\mathbf{v}'_1(X) + \text{minup}_{q_1,q_3}(X) \geq 0$ . Let  $\mathbf{u}'(X) = \text{minup}_{q_1,q_3}(X)$ .

$$\begin{aligned} \mathbf{v}_3(X) &\geq \mathbf{v}_1(X) + \text{minup}_{q_1,q_3}(X) && \text{[From the semantics of } \mathcal{A}_{\text{inc}}\text{]} \\ &\geq \mathbf{v}'_1(X) + \text{minup}_{q_1,q_3}(X) && \text{[Since } \mathbf{v}'_1 \preceq \mathbf{v}_1\text{]} \\ &= \mathbf{v}'_1(X) + \mathbf{u}'(X) && \text{[By definition of } \mathbf{u}'(X)\text{]} \\ &= \mathbf{v}'_3(X) \end{aligned}$$

*Case 2:*  $\mathbf{v}'_1(X) + \text{minup}_{q_1,q_3}(X) < 0$ . Therefore,  $\text{minup}_{q_1,q_3}(X) < -\mathbf{v}'_1(X)$ . Moreover, since  $\text{maxup}_{q_1,q_3}(X) \geq 0$  (due to the optional decrement property) and  $\mathbf{v}'_1(X) \geq 0$ ,  $-\mathbf{v}'_1(X) \leq \text{maxup}_{q_1,q_3}(X)$ . Let  $\mathbf{u}'(X) = -\mathbf{v}'_1(X)$ . Now,  $\mathbf{v}'_3(X) = \mathbf{v}'_1(X) + \mathbf{u}'(X) = 0 \preceq \mathbf{v}_3(X)$ .

By definition of  $\text{minup}_{q_1,q_3}$  and the closure of the set of transitions  $\delta$  of  $\mathcal{A}_\phi$  under component-wise interpolation, we have  $(q_1, \mathbf{u}', q_3) \in \delta$  and hence  $\langle q_1, \mathbf{v}'_1 \rangle \rightarrow \langle q_3, \mathbf{v}'_3 \rangle$ . Since  $\mathbf{v}'_3 \preceq \mathbf{v}_3$  and  $\langle q_3, \mathbf{v}_3 \rangle \xrightarrow{*}_{\text{gainy}} \langle q_2, \mathbf{0} \rangle$ , we can use the induction hypothesis to conclude that  $\langle q_3, \mathbf{v}'_3 \rangle \xrightarrow{*} \langle q_2, \mathbf{0} \rangle$ . So we conclude that  $\langle q_1, \mathbf{v}'_1 \rangle \rightarrow \langle q_3, \mathbf{v}'_3 \rangle \xrightarrow{*} \langle q_2, \mathbf{0} \rangle$ . ■

*Proof of Theorem 9:* The proof is in four steps.

*Step 1:* From [6], a  $\text{LRV}^\top$  formula  $\phi$  is satisfiable iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi$  for some  $q_0 \in Q_0$  and  $q_f \in Q_f$ .

*Step 2:* This is the step that requires new insight. From Lemmas 7 and 8,  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi$  iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*}_{\text{gainy}} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{inc}}$ .

*Step 3:* This is a standard trick. Let  $\mathcal{A}_{\text{dec}} = \langle Q, C, \delta^{\text{rev}} \rangle$  be a VASS such that for every transition  $(q, \mathbf{u}, q') \in \delta^{\text{min}}$  of  $\mathcal{A}_{\text{inc}}$ ,  $\mathcal{A}_{\text{dec}}$  has a transition  $(q', -\mathbf{u}, q) \in \delta^{\text{rev}}$ , where  $-\mathbf{u} : C \rightarrow \mathbb{Z}$  is the function such that  $-\mathbf{u}(X) = -1 \times \mathbf{u}(X)$  for all  $X \in C$ . We infer that  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*}_{\text{gainy}} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{inc}}$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*}_{\text{lossy}} \langle q_0, \mathbf{0} \rangle$  in  $\mathcal{A}_{\text{dec}}$  (we can simply reverse every transition in the run of  $\mathcal{A}_{\text{inc}}$  to get a run of  $\mathcal{A}_{\text{dec}}$  and vice-versa).

*Step 4:* This is another standard trick. Since  $\mathcal{A}_{\text{dec}}$  does not have zero-tests, we can remove all decrementing errors from a run of  $\mathcal{A}_{\text{dec}}$  from  $\langle q_f, \mathbf{0} \rangle$  to  $\langle q_0, \mathbf{0} \rangle$ , to get another run from  $\langle q_f, \mathbf{0} \rangle$  to  $\langle q_0, \mathbf{v} \rangle$ , where  $\mathbf{v}$  is some counter valuation (possibly different from  $\mathbf{0}$ ). Using decremental errors at the last configuration,  $\mathcal{A}_{\text{dec}}$  can then reach the configuration  $\langle q_0, \mathbf{0} \rangle$ . In other words,  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*}_{\text{lossy}} \langle q_0, \mathbf{0} \rangle$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some counter valuation  $\mathbf{v}$ . Checking the latter condition is precisely the control state reachability problem for VASS.

If the control state in the above instance is reachable, then Rackoff gives a bound on the length of a shortest run reaching it [28]. The bound is doubly exponential in the size of the VASS. Since in our case, the size of the VASS is exponential in the size of the  $\text{LRV}^\top$  formula  $\phi$ , the bound is triply exponential. A non-deterministic Turing machine can maintain a binary counter to count up to this bound, using doubly exponential space. The machine can start by guessing some initial state  $q_0$  and a counter valuation set to  $\mathbf{0}$ . (this can be done in polynomial space). In one step, the machine guesses a transition to be applied next and updates the current configuration accordingly, while incrementing the binary counter. At any step, the space required to store the current configuration is at most doubly exponential. By the time the binary counter reaches its triply exponential bound, if a final control state is not reached, the machine rejects its input. Otherwise, it accepts. Since this non-deterministic machine operates in doubly exponential space, an application of Savitch's Theorem gives us the required  $2\text{EXPSPACE}$  upper bound for the satisfiability problem of  $\text{LRV}^\top$ . ■

#### D. Proofs of Lemmas 12 and 13

Let  $\mathcal{A} = \langle Q, f, k, Q_0, Q_F, \delta \rangle$  be a chain automaton of level  $k$ . A run  $\rho$  ends in zero whenever for every chain  $\alpha \in [1, n]$ ,  $c_L^\alpha = 0$  with  $L = |\rho|$ . We write  $\text{Per}^{\text{zero}}(k)$  and  $\text{Gainy}^{\text{zero}}(k)$  to denote the variant problems of  $\text{Per}(k)$  and  $\text{Gainy}(k)$ , respectively, in which runs that end in zero are considered. First, note that  $\text{Per}^{\text{zero}}(k)$  and  $\text{Per}(k)$  are interreducible in logarithmic space since it is always possible to add adequately self-loops when a final state is reached in order to guarantee that  $c_L^\alpha = 0$  for every chain  $\alpha \in [1, n]$ . Similarly,  $\text{Gainy}^{\text{zero}}(k)$  and  $\text{Gainy}(k)$  are interreducible in logarithmic space.

*Proof of Lemma 12:* Below, we show that  $\text{Per}^{\text{zero}}(k)$  and  $\text{Gainy}^{\text{zero}}(k)$  are interreducible in logarithmic space, which allows us to get the proof of the lemma since logarithmic-space reductions are closed under composition. From  $\mathcal{A}$ , let us define a reverse counter automaton  $\tilde{\mathcal{A}}$  of level  $k$ , where the reverse operation  $\tilde{\cdot}$  is defined on instructions, transitions, sets of transitions and automata as follows:

- $\widetilde{\text{inc}}(\alpha) \stackrel{\text{def}}{=} \text{dec}(\alpha)$ ;  $\widetilde{\text{dec}}(\alpha) \stackrel{\text{def}}{=} \text{inc}(\alpha)$ ;  $\widetilde{\text{next}}(\alpha) \stackrel{\text{def}}{=} \text{prev}(\alpha)$ ;  $\widetilde{\text{prev}}(\alpha) \stackrel{\text{def}}{=} \text{next}(\alpha)$ ,
- $\widetilde{\text{first}}(\alpha)? \stackrel{\text{def}}{=} \text{last}(\alpha)?$ ;  $\widetilde{\text{last}}(\alpha)? \stackrel{\text{def}}{=} \text{first}(\alpha)?$ ;  $\widetilde{\text{first}}(\alpha)? \stackrel{\text{def}}{=} \text{last}(\alpha)?$ ;  $\widetilde{\text{last}}(\alpha)? \stackrel{\text{def}}{=} \text{first}(\alpha)?$ ,
- $q \xrightarrow{\text{instr}} q' \stackrel{\text{def}}{=} q' \xrightarrow{\widetilde{\text{instr}}} q$ ,
- $\tilde{\mathcal{A}} \stackrel{\text{def}}{=} \langle Q, f, k, Q_F, Q_0, \tilde{\delta} \rangle$  with  $\tilde{\delta} \stackrel{\text{def}}{=} \{q \xrightarrow{\widetilde{\text{instr}}} q' : q \xrightarrow{\text{instr}} q' \in \delta\}$ . Note that  $Q_0$  and  $Q_F$  have been swapped.

The reverse operation can be extended to sequences of transitions as follows:  $\tilde{\varepsilon} \stackrel{\text{def}}{=} \varepsilon$  and  $\widetilde{t \cdot u} \stackrel{\text{def}}{=} \tilde{u} \cdot \tilde{t}$ . Note that  $\tilde{\cdot}$  extends the reverse operation defined in the proof of Theorem 9 (step 3).

One can show the following implications, for any run  $\rho$  for  $\mathcal{A}$  that ends in zero:

- 1)  $\rho$  is perfect and accepting for  $\mathcal{A}$  implies  $\tilde{\rho}$  is gainy and accepting for  $\tilde{\mathcal{A}}$ .
- 2)  $\rho$  is gainy and accepting for  $\tilde{\mathcal{A}}$  implies  $\tilde{\rho}$  is perfect and accepting for  $\mathcal{A}$ .
- 3)  $\rho$  is gainy and accepting for  $\mathcal{A}$  implies  $\tilde{\rho}$  is perfect and accepting for  $\tilde{\mathcal{A}}$ .
- 4)  $\rho$  is perfect and accepting for  $\tilde{\mathcal{A}}$  implies  $\tilde{\rho}$  is gainy and accepting for  $\mathcal{A}$ .

(1) and (4) [resp. (2) and (3)] have very similar proofs because  $\widetilde{\cdot}^{-1}$  is actually equal to  $\widetilde{\cdot}$ . (1)–(4) are sufficient to establish that  $\text{Per}^{\text{zero}}(k)$  and  $\text{Gainy}^{\text{zero}}(k)$  are interreducible in logarithmic space.  $\blacksquare$

*Proof of Lemma 13:* It is sufficient to show that  $\text{Per}^{\text{zero}}(k)$  is in  $(k+1)\text{EXPSPACE}$ . Let  $\mathcal{A} = \langle Q, f, k, Q_0, Q_F, \delta \rangle$  be a chain automaton with  $f : [1, n] \rightarrow \mathbb{N}$ . We reduce this instance of  $\text{Per}^{\text{zero}}(k)$  into several instances of the control state reachability problem for VASS such that the number of instances is bounded by  $\mathcal{O}(|\mathcal{A}|^2)$  and the size of each instance is in  $\mathcal{O}(\exp(k, |\mathcal{A}|)^{|\mathcal{A}|})$ , which provides the  $(k+1)\text{EXPSPACE}$  upper bound by [28]. The only instructions in the VASS  $\mathcal{A}'$  defined below are: increment a counter, decrement a counter or the `skip` action, which just changes the control state without modifying the counter values (which can be obviously simulated by an increment followed by a decrement).

Let us define a VASS  $\mathcal{A}' = \langle Q', C', \delta' \rangle$  with

$$C' = \{c_0^1, \dots, c_{\exp(k, f(1))-1}^1, \dots, c_0^n, \dots, c_{\exp(k, f(n))-1}^n\}$$

In  $\mathcal{A}'$  it will be possible to access the counters directly by encoding the positions of the pointers in the states. Let  $Q' = Q \times [0, \exp(k, f(1)) - 1] \times \dots \times [0, \exp(k, f(n)) - 1]$ . It remains to define the transition relation  $\delta'$ :

- Whenever  $q \xrightarrow{\text{inc}(\alpha)} q' \in \delta$ , we have  $\langle q, \beta_1, \dots, \beta_n \rangle \xrightarrow{\text{inc}(c_{\beta_\alpha}^\alpha)} \langle q', \beta_1, \dots, \beta_n \rangle \in \delta'$  (and similarly with decrements),
- Whenever  $q \xrightarrow{\text{next}(\alpha)} q' \in \delta$ , we have

$$\langle q, \beta_1, \dots, \beta_\alpha, \dots, \beta_n \rangle \xrightarrow{\text{skip}} \langle q', \beta_1, \dots, \beta_\alpha + 1, \dots, \beta_n \rangle \in \delta'$$

if  $\beta_\alpha + 1 < \exp(k, f(\alpha))$  (and similarly with  $\text{prev}(\alpha)$ ),

- When  $q \xrightarrow{\text{first}(\alpha)?} q' \in \delta$ ,  $\langle q, \beta_1, \dots, \beta_n \rangle \xrightarrow{\text{skip}} \langle q', \beta_1, \dots, \beta_n \rangle \in \delta'$  if  $\beta_\alpha = 0$  (and similarly with  $\text{first}(\alpha)?$ ,  $\text{last}(\alpha)?$  and  $\text{last}(\alpha)?$ ).

It is easy to show that there is a perfect accepting run that ends in zero iff there are  $\mathbf{q}_0 \in Q_0 \times \{0\}^n$  and  $\mathbf{q}_F \in Q_F \times \{0\}^n$  such that there is a run from the configuration  $\langle \mathbf{q}_0, \mathbf{0} \rangle$  to the configuration  $\langle \mathbf{q}_F, \mathbf{x} \rangle$  for some  $\mathbf{x}$ .

In order to prove the above equivalence, we can use the transformations (I) and (II) stated below.

(I) Let  $\rho = t_1 \dots t_L$  be a run of  $\mathcal{A}$  such that for every  $i \in [1, L-1]$ ,  $t_i = q_{i-1} \xrightarrow{\text{instr}_i} q_i$  and the values  $c_i^\alpha$  are defined as before (1). One can show that  $\rho' = t'_1 \dots t'_L$  with  $t'_i = \langle q_{i-1}, \mathbf{x}_{i-1} \rangle \xrightarrow{\text{instr}'_i} \langle q_i, \mathbf{x}_i \rangle$  for every  $i$ , is a run of  $\mathcal{A}'$  where

- $\mathbf{x}_0 = \mathbf{0}$  and for every  $i \in [1, L]$ ,  $\mathbf{x}_i = \langle c_i^1, \dots, c_i^n \rangle$ ,
- for every  $i \in [1, L]$ ,
  - if  $\text{instr}_i = \text{inc}(\alpha)$  then  $\text{instr}'_i = \text{inc}(c_{c_i^\alpha}^\alpha)$  (a similar clause holds for decrements),
  - otherwise (i.e., if  $\text{instr}_i$  is not  $\text{inc}(\alpha)$  nor  $\text{dec}(\alpha)$  for any  $\alpha$ ),  $\text{instr}'_i = \text{skip}$ .

(II) Similarly, let  $\rho' = t'_1 \dots t'_L$  be a run of  $\mathcal{A}'$  where  $t'_i = \langle q_{i-1}, \mathbf{x}_{i-1} \rangle \xrightarrow{\text{instr}'_i} \langle q_i, \mathbf{x}_i \rangle$  for every  $i$ , with  $\mathbf{x}_0 = \mathbf{0}$ . One can show that  $\rho = t_1 \dots t_L$  with  $t_i = q_{i-1} \xrightarrow{\text{instr}_i} q_i$  for every  $i$ , is a perfect accepting run of  $\mathcal{A}$  such that for every  $i \in [1, L]$ ,

- if  $\text{instr}'_i = \text{inc}(c_j^\alpha)$  then  $\text{instr}_i = \text{inc}(\alpha)$  (a similar clause holds for decrements),
- otherwise, if  $\mathbf{x}_{i+1}(\alpha) = \mathbf{x}_i(\alpha) + 1$  for some  $\alpha$ , then  $\text{instr}_i = \text{next}(\alpha)$  (a similar clause holds for previous),
- otherwise, if  $\mathbf{x}_i(\alpha) = 0$  and  $q_i \xrightarrow{\text{first}(\alpha)?} q_{i+1} \in \delta$  for some  $\alpha$ , then  $\text{instr}_i = \text{first}(\alpha)?$  (a similar clause holds for  $\text{first}(\alpha)?$ ),
- otherwise, if  $\mathbf{x}_i(\alpha) = \exp(k, f(\alpha)) - 1$  and  $q_i \xrightarrow{\text{last}(\alpha)?} q_{i+1} \in \delta$  for some  $\alpha$ , then  $\text{instr}_i = \text{last}(\alpha)?$  (a similar clause holds for  $\text{last}(\alpha)?$ ).

$\blacksquare$

## E. Details of the Hardness Result for Chain Automata

This section is dedicated to prove Theorem 14 by extending Lipton's proof [22] (see also its presentation in [8]) that shows that control state reachability problem for VASS is  $\text{EXPSPACE}$ -hard. In Sub-section E1, we recall principles of Lipton's proof without entering into the implementation details; indeed when dealing with chain automata the coding has to be refined. In Sub-section E2, we explain

how large decrements can be encoded with chain automata (a key point in the proof). Sub-section E3 is dedicated to the initialization of the counter values (another key point in the proof). In Lipton's proof, initialization amounts to performing a series of large increments using principles analogous to those for encoding decrements. When chain automata are used as a low level programming language, we need to maintain additional data structures (in chain automata, this means chains of counters). Unlike [22], [8], we do not use a return mechanism and we simulate directly the call/return stack. Hence, our construction is more general but at the cost of a more careful analysis. Sub-section E4 contains the statements and proofs of the main lemmas to conclude the correctness of our simulation. Finally, Sub-section E5 summarizes the different steps of the proof and concludes this section.

The proof below is complicated and sometimes we may repeat arguments for the sake of clarity and to present arguments from different viewpoints. We believe this might help the reader to grasp the present simulation.

1) *Principles of Lipton's Proof:* In this section, we present the principles behind the proof showing that the control state reachability problem for VASS is EXPSPACE-hard [22], [8].

Lipton's proof starts from the standard result in computability theory that a Turing Machine using space  $2^{n^\gamma}$  can be simulated by a counter automaton equipped with 4 counters whose values are bounded by  $2^{2^{n^\gamma}}$ . Lipton's proof shows that such a counter automaton can be simulated with a VASS. Unlike counter automata, VASS does not have zero-test transitions. Each counter  $c$  of the automaton is complemented with an extra counter  $\bar{c}$  in the VASS. The VASS is designed such that the sum of the values in  $c$  and  $\bar{c}$  is  $2^{2^{n^\gamma}}$  in any reachable configuration. Now testing  $c$  for zero is equivalent to testing that  $\bar{c}$  is  $2^{2^{n^\gamma}}$ . This later test is performed as explained next.

Testing that  $\bar{c}$  is  $2^{2^{n^\gamma}}$  is done by decrementing  $\bar{c}$  by  $2^{2^{n^\gamma}}$ . To do so, the value of  $\bar{c}$  is transferred to another counter  $s$  and then  $s$  is decremented by  $2^{2^{n^\gamma}}$ . This whole operation results in the values of  $c$  and  $\bar{c}$  getting swapped, while all other counters retain their original values. To rectify the swapping between  $c$  and  $\bar{c}$ , we repeat the operation except that the value of  $c$  is transferred to  $s$  and then  $s$  is decremented by  $2^{2^{n^\gamma}}$ .

Decrementing  $s$  by  $2^{2^{n^\gamma}}$  is done by having a series of counters  $s_i, \bar{s}_i$  for each  $i$  between 1 and  $n^\gamma$ . For each  $i$ , the sum of values in  $s_i$  and  $\bar{s}_i$  is maintained at  $2^{2^i}$ . Below, we illustrate how a counter  $s_i$  is decremented  $2^{2^i}$  times.

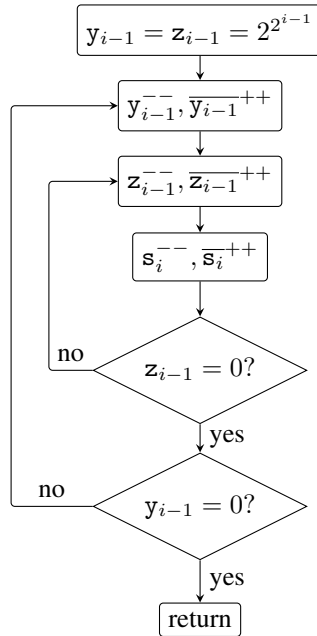


Fig. 4. Algorithm for decrementing  $s_i$  by  $2^{2^i}$

There are two nested loops indexed by  $y_{i-1}$  and  $z_{i-1}$ , initialised to  $2^{2^{i-1}}$ . When all the iterations are finished, we would have decremented  $s_i$  exactly  $2^{2^{i-1}} \times 2^{2^{i-1}} = 2^{2^i}$  times. Testing  $y_{i-1}$  and  $z_{i-1}$  for zero is done by a similar gadget, where  $i, i-1$  are replaced by  $i-1, i-2$ . The resulting VASS is

composed of  $n^\gamma$  constant sized VASS, each one implementing the nested loops for an  $i$  between 1 and  $n^\gamma$ .

In the above explanation of the decrementing algorithm, we have assumed that the counters  $y_{i-1}$  and  $z_{i-1}$  have the exact value  $2^{2^{i-1}}$  in the beginning. This can be achieved if we first inductively initialize all counters  $y_j$  and  $z_j$  to have the value  $2^{2^j}$  for each  $j$  between 1 and  $i-2$ . Assuming this initialization has been done, the algorithm shown in Fig. 5 initializes  $y_{i-1}$  and  $z_{i-1}$  to  $2^{2^{i-1}}$ . The algorithm shown in

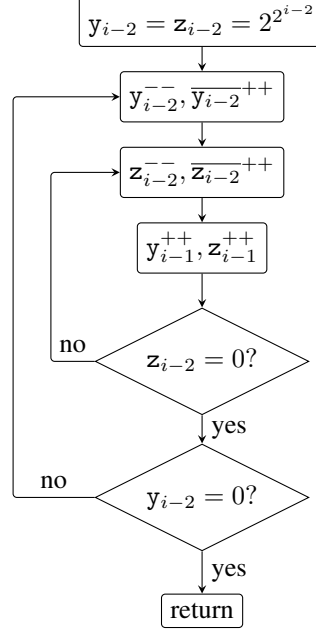


Fig. 5. Algorithm for initializing  $y_{i-1}, z_{i-1}$

Fig. 5 is based on the same principle as the one in Fig. 4, except that  $i, i-1$  are replaced by  $i-1, i-2$  and that instead of decrementing  $s_i$  inside the nested loops,  $y_{i-1}, z_{i-1}$  are incremented.

Now that we know how zero-tests are implemented in a VASS, let us see how a VASS simulates counter automata with bounded counter values. For every transition  $\langle q, c \leftarrow c + 1, q' \rangle$  of the counter automaton, the VASS will have the transition  $\langle q, (\text{inc}(c); \text{dec}(\bar{c})), q' \rangle$ . Thus, for every incrementing transition of the counter automaton, the VASS can execute a corresponding incrementing transition. Conversely, for every incrementing transition executed by the VASS, the counter automaton can execute the corresponding incrementing transition.

For every transition  $\langle q : \text{if } c = 0 \text{ goto } q_1 \text{ else } c \leftarrow c - 1 \text{ goto } q_2 \rangle$  of the counter automaton, the VASS will have transitions corresponding to the following program.

```

q: goto nonzero or zero
nonzero: dec(c); inc(c); goto q2
zero: transfer( $\bar{c}$ , s); goto Deczerorep
zerorep: transfer(c, s); goto Decq1
  
```

The macro  $\text{transfer}(\bar{c}, s)$  is intended to be a shortcut for “ $s := s + \bar{c}; \bar{c} := 0$ ” but its actual implementation is described in the next section. Its code guarantees that at least one of its executions performs the sequence “ $s := s + \bar{c}; \bar{c} := 0$ ”. The macro  $\text{transfer}(c, s)$  is defined analogously.

In the above, the control state  $\text{Dec}_{\text{zerorep}}$  launches a program that decrements  $s$  by  $2^{2^{n^\gamma}}$  and then transfers the control to **zerorep**. Similarly,  $\text{Dec}_{q_1}$  launches a program that decrements  $s$  by  $2^{2^{n^\gamma}}$  and then transfers the control to  $q_1$ .

The idea behind our  $(k+1)\text{EXPSPACE}$  lower bound is that a Turing Machine using space  $\exp(k+1, n^\gamma)$  can be simulated by a counter automaton equipped with 4 counters whose values are bounded by  $\exp(k+2, n^\gamma)$ . We shall show that such a counter automaton can be simulated with a chain automaton of level  $k$  with a map  $f$  such that for each chain  $\alpha$ ,  $f(\alpha) = n^\gamma$ . Each chain  $\alpha$  of the chain automaton will be complemented with another chain  $\bar{\alpha}$ . We will denote the counters in these chains by  $\alpha_1, \bar{\alpha}_1, \dots, \alpha_{\exp(k, n^\gamma)}, \bar{\alpha}_{\exp(k, n^\gamma)}$ . The chain automaton will be designed such that the sum of the

values in  $\alpha_i$  and  $\bar{\alpha}_i$  is  $2^{2^i}$  in any reachable configuration and for any  $i$  between 1 and  $\exp(k, n^\gamma)$ . Now testing  $\alpha_i$  for zero is equivalent to testing that  $\bar{\alpha}_i$  is  $2^{2^i}$ . This later test will not work directly as explained above, since a chain automaton composed of  $\exp(k, n^\gamma)$  constant sized portions will not give a polynomial-time reduction. We reduce the size of the chain automaton by observing that the decrementing algorithm for  $i - 1$  is the same as the one for  $i$ , except that  $i, i - 1$  are replaced by  $i - 1, i - 2$  respectively. We can write a single sequence of instructions for the decrementing algorithm and invoke it for any  $i$  by placing the pointers at the appropriate counters. The main difficulty in the implementation is to ensure that the multiple invocations of the algorithm return to the correct point, which is handled in the next section.

2) *Encoding Large Decrements with Chain Automata:* In the ideas explained in the previous section, we assumed that a call to a procedure will return to the correct program point. In Lipton's proof [22] and its exposition contained in [8], the decrementing algorithms are described as subroutines, and a return mechanism (which involves remembering return addresses) is implemented in a VASS. Since we do not have direct access to counters in chain automata, it is cumbersome to implement a general purpose return mechanism. In this section, we explain how decrements can be implemented with a chain automaton without using a return mechanism.

Recall the following high-level algorithm used in Lipton's proof.

**outerloop:** Decrement  $y_{i-1}$ .  
**innerloop:** Decrement  $z_{i-1}$ .  
**decrement:** Decrement  $s_i$ .  
**innerexit:** If  $z_{i-1} \neq 0$ , go to innerloop. Otherwise, set  $z_{i-1}$  back to  $2^{2^{i-1}}$  and go to outerexit.  
**outerexit:** If  $y_{i-1} \neq 0$ , go to outerloop. Otherwise, set  $y_{i-1}$  back to  $2^{2^{i-1}}$  and return.

Whenever we have to return from the decrementing algorithm for stage  $i - 1$ , we have to return to the step labeled either innerexit or outerexit of stage  $i$ , in the high-level algorithm described above. To remember which one, we maintain a pair of chains  $\text{stack}$  and  $\overline{\text{stack}}$ , each having  $\exp(k, n^\gamma)$  counters, so that the sum of the values of  $\text{stack}_i$  and  $\overline{\text{stack}}_i$  is 1. For convenience, we will use  $N = \exp(k, n^\gamma)$  from here onwards. We follow the convention that if at stage  $i$ , the counter in the chain  $\text{stack}$  has value 1, then it is the step labeled innerexit that invoked the decrementing algorithm for stage  $i - 1$ . A  $\text{stack}$  value of 0 at stage  $i$  implies that the step labeled outerexit invoked the decrementing algorithm for stage  $i - 1$ . In addition, we have six chains  $y, \bar{y}, z, \bar{z}, s$  and  $\bar{s}$ , each with  $N$  counters. Recall that for the decrementing algorithm to work properly at stage  $i$ ,  $y_{i-1}$  and  $z_{i-1}$  both should have the value  $2^{2^{i-1}}$  and  $s_{i-1}$  should have the value 0 (so  $\bar{s}_{i-1}$  should have the value  $2^{2^{i-1}}$ ). We also need to have  $\text{stack}_{i-1}$  to have the value 0 (so  $\overline{\text{stack}}_{i-1}$  should have the value 1).

To understand our implementation of the decrementing algorithm, it will be helpful to think of the counters in the chains arranged as follows.

Chain\Stage	$N$	$N - 1$	$N - 2$	...	$i$	$i - 1$	...	1
$y$						$2^{2^{i-1}}$		
$z$						$2^{2^{i-1}}$		
$s$					$2^{2^i}$	0		
$\text{stack}$	0	0	1	...	1		...	0
Pointer				Nextstage $\leftarrow$	$\uparrow$	$\rightarrow$ Prevstage		

The fact that all chains have their pointers at stage  $i$  is shown by the arrow  $\uparrow$  in the last row of the above table. We will frequently move the pointers. The macro **Nextstage** is short for the sequence of transitions  $\text{next}(y); \text{next}(\bar{y}); \dots; \text{next}(\text{stack}); \text{next}(\overline{\text{stack}})$ , which moves all the pointers one stage up as shown above. The macro **Prevstage** similarly moves all the pointers one stage down. The macro **transfer**( $\bar{z}, s$ ) is short for the following sequence of transitions.

**transfer**( $\bar{z}, s$ ) (Next control state)  
{  
    **zero:**  $\text{inc}(z); \text{dec}(\bar{z}); \text{inc}(s); \text{dec}(\bar{s}); \text{goto zero or fin}$   
    **fin:** goto (Next control state)  
}

In the above description, (Next control state) represents the control state that immediately follows the macro **transfer**( $\bar{z}, s$ ) in a listing. The macro **transfer**( $\bar{y}, s$ ) is similar to the above, with  $y$  and  $\bar{y}$  replacing  $z$  and  $\bar{z}$  respectively. The macro **inc(next(z))** moves the pointer of the chain  $z$  one stage up, increments  $z$

once and moves the pointer back one stage down:  $\text{next}(z)$ ;  $\text{inc}(z)$ ;  $\text{prev}(z)$ . The macros **inc(next(y))** and **inc(next( $\bar{s}$ ))** are similar to **inc(next(z))** with  $y$  and  $\bar{s}$  replacing  $z$  respectively. The macro **dec(next(s))** is similarly defined to decrement the next counter in the chain  $s$ . The macro **stack == 1** tests if the counter in the current stage in the chain **stack** is greater than 0:  $\text{dec}(\text{stack})$ ;  $\text{inc}(\text{stack})$ .

Our returning mechanism is specific to the decrementing algorithm. There will be one copy for every zero testing transition in the counter automaton that we simulate (the size of the chain automaton will be linear in the size of the automaton even after creating these copies). The listing below is written in the form of a program in a low level programming language for readability. It can be easily translated into a set of transitions of a chain automaton. There is a control state between every two consecutive instructions below, but only the important ones are given names like “outerloop2”. A line such as “innernonzero2:  $\text{dec}(z)$ ;  $\text{inc}(z)$ ; goto innerloop2” actually represents the set of transitions  $\{\langle \text{innernonzero2}, \text{dec}(z), q \rangle, \langle q, \text{inc}(z), \text{innerloop2} \rangle\}$ . The instruction “ $q$ : if  $\text{first}(\alpha)$ ? then {program 1} else {program 2}” represents the set of transitions  $\{\langle q, \text{first}(\alpha)?, q_1 \rangle, \langle q, \text{first}(\bar{\alpha})?, q_2 \rangle\} \cup \{\text{transitions for program 1 from } q_1\} \cup \{\text{transitions for program 2 from } q_2\}$ . An instruction of the form “ $q$  :  $\text{inc}(\bar{\text{stack}})$ ; goto outernonzero2 or outerzero2” represents the set of transitions  $\{\langle q, \text{inc}(\bar{\text{stack}}), \text{outernonzero2} \rangle, \langle q, \text{inc}(\bar{\text{stack}}), \text{outerzero2} \rangle\}$ . Depending on the non-deterministic choices made at control states that have multiple transitions enabled, there will be several different runs. We prove later that there is one run which has the intended effect and the other runs will never reach the final state. To understand the flow of the algorithm below, whenever the pointer is at stage  $i$  and the instruction “goto Dec” is executed, the reader can assume that  $s$  is decremented  $2^{2^i}$  times and the control is magically transferred to the instruction immediately following the “goto Dec”. We later prove formally that this actually happens. Referring to the tabular arrangement of the counters shown above would also help understand the following implementation and proofs of correctness.

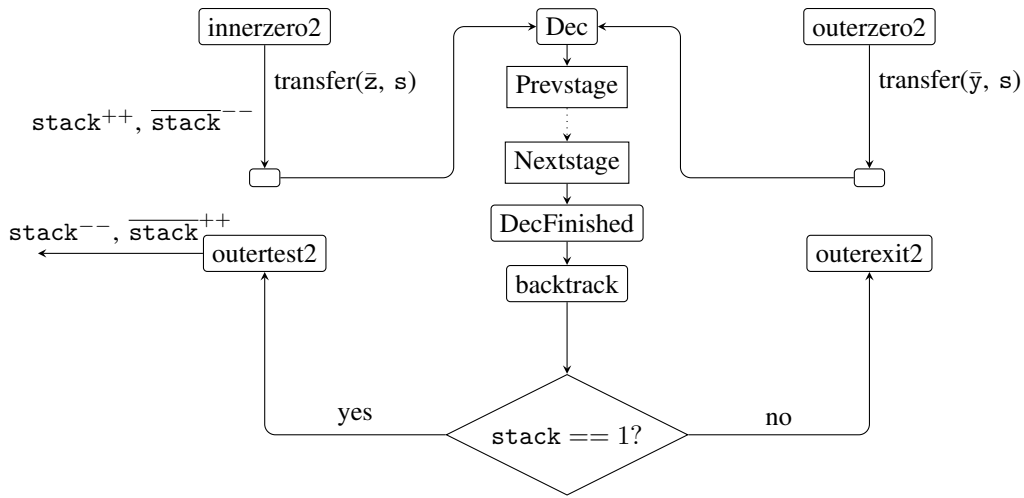
```

Dec:
if first(stack)? then
    (dec(s))4; (inc( $\bar{s}$ ))4; goto DecFinished
else
    Prevstage
    outerloop2: dec(y); inc( $\bar{y}$ ) /* y is the index for outer loop */
    innerloop2: dec(z); inc( $\bar{z}$ ) /* z is the index for inner loop */
    dec(next(s)); inc(next( $\bar{s}$ ))
    innertest2: goto innernonzero2 or innerzero2
    innernonzero2: dec(z); inc(z); goto innerloop2 /* inner loop not yet complete */
    innerzero2: transfer( $\bar{z}$ , s); inc(stack); dec( $\bar{\text{stack}}$ ); goto Dec /* inner loop complete. Assume control
    magically goes to outertest2 */
    outertest2: dec(stack); inc( $\bar{\text{stack}}$ ); goto outernonzero2 or outerzero2
    outernonzero2: dec(y); inc(y); goto outerloop2 /* outer loop not yet complete */
    outerzero2: transfer( $\bar{y}$ , s); goto Dec /* outer loop complete. Assume control magically goes to outerexit2 */
    outerexit2: Nextstage; goto DecFinished
fi
DecFinished: goto backtrack

backtrack:
if (last( $\bar{\text{stack}}$ )?) then
    if (stack == 1) then goto outertest2
    else goto outerexit2
else
    goto (Next control state)
fi

```

In the diagram below, we explain how the “magical transfers” happen after recursive calls to “Dec”.



As can be seen from the listing, there are two recursive calls to “Dec” from inside “Dec”. The first one is from “innerzero2”, which should return to “outertest2”. The second one is from “outerzero2”, which should return to “outerexit2”. As seen in the listing and also in the diagram above, the call from “innerzero2” is made just after incrementing `stack`. When the recursive call to “Dec” finishes (after possibly moving the pointers to previous stage and moving them back to current stage multiple times) and comes to `backtrack`, it will check if `stack == 1`. Since this is the case, the control goes to “outertest2”, where the `stack` is set back to 0 and rest of the computation continues.

On the other hand, when a recursive call to “Dec” is made from “outerzero2”, `stack` will be set to 0. Hence, when the recursive call to “Dec” returns and checks whether `stack == 1`, the answer will be no and the control goes to “outerexit2”.

3) *Initialisation Phase:* In this section, we explain how to get counters to their required initial values (at the beginning of the runs all the values are equal to zero). We briefly recall what are these values after initialization:

- 1) each counter  $c$  has value zero,
- 2) for every  $i \in [1, N]$ ,  $\bar{y}_i$ ,  $\bar{z}_i$  and  $s_i$  are equal to zero,
- 3) for every  $i \in [1, N]$ , `stacki` is equal to zero and  $\overline{\text{stack}}_i$  is equal to one,
- 4) each complement counter  $\bar{c}$  has value  $2^{2^N}$ ,
- 5) for every  $i \in [1, N]$ ,  $y_i$ ,  $z_i$  and  $\bar{s}_i$  have the value  $2^{2^i}$ .

Initialization for the points 1)–3) is easy to perform and below we focus on the initialization for the point 5). Initialization of the complement counter in point 4) will be dealt with in Section E5 by simply adjusting what is done below.

To help achieve these initial values for 5), we have another chain `init`, with  $N$  counters. We follow the convention that if at stage  $i$ , the counter in the chain `init` has value 1, then all the counters in all the chains at stage  $i$  or below have been properly initialized. By convention, the condition `last(init)?` is true if the pointer in the chain `init` is at stage  $N$ .

To understand our implementation of the decrementing algorithm, again, it will be helpful to think of the counters in the chains arranged as follows (this time we added the chain `init`)

Chain\Level	$N$	$N - 1$	$N - 2$	$\dots$	$i$	$i - 1$	$\dots$	1
<code>y</code>						$2^{2^{i-1}}$		
<code>z</code>						$2^{2^{i-1}}$		
<code>s</code>					$2^{2^i}$	0		
<code>stack</code>	0	0	1	$\dots$	1		$\dots$	0
<code>init</code>	0	0	1	$\dots$	1		$\dots$	1
Pointer					Nextstage $\leftarrow$	$\uparrow$	$\rightarrow$ Prevstage	

The macro **Nextstage** is short for the sequence of transitions `next(y); next( $\bar{y}$ ); ...; next(init); next( $\bar{\text{init}}$ )`, which moves all the pointers one stage up as shown above. The macro **Prevstage** similarly moves all the pointers one stage down. The macro `next(init) == 1` tests if the value of the counter one stage up in the chain `init` is greater than 0: `next(init); dec(init); inc(init); prev(init)`.



We give below the code used to initialize  $y$ ,  $z$ ,  $s$  and  $stack$  to the required values, assuming that all counters are initially set to 0 and all the pointers in all the chains are pointing to stage 0. To ease the reading, we have reproduced the code for Dec from Section E2.

As explained in Section E1, the principle for initialization is again nested loops. Below, the outer loop is implemented between the states “outerinit” and “outernonzero1”, while the inner loop is between “innerinit” and “innernonzero1”. Inside these loops, we have “inc(next( $y$ )); inc(next( $z$ )); inc(next( $\bar{s}$ ))”, which will increment counters in the next stage. This will work properly provided the counters in the current stage and below have already been initialized. We will prove later that initialization of counters at some stage will start only after all stages below have been initialized. We will also need to test counters at lower stages for zero, for which we will use the Dec algorithm from the previous section. Apart from the recursive calls to Dec from inside Dec itself, now there are also calls to Dec from “innerzero1” and “outerzero1” below. To handle these additional return locations, “backtrack” part of the code below has been updated that also checks if the call has been made from initialization code.

```

begininit: (inc( $y$ ))4; (inc( $z$ ))4; (inc( $\bar{s}$ ))4; inc( $init$ ); inc( $\overline{stack}$ )
initialise: If last( $init$ )? goto beginsim else goto outerinit
outerinit: dec( $y$ ); inc( $\bar{y}$ ) /*  $y$  is the index for outer loop */
innerinit: dec( $z$ ); inc( $\bar{z}$ ) /*  $z$  is the index for inner loop */
INC: inc(next( $y$ )); inc(next( $z$ )); inc(next( $\bar{s}$ ))
innertest1: goto innernonzero1 or innerzero1
innernonzero1: dec( $z$ ); inc( $z$ ); goto innerinit /* inner loop not yet complete */
innerzero1: transfer( $\bar{z}$ ,  $s$ ); inc( $stack$ ); dec( $\overline{stack}$ ); goto Dec /* inner loop complete. Assume control
magically goes to outertest1 */
outertest1: dec( $stack$ ); inc( $\overline{stack}$ ); goto outernonzero1 or outerzero1
outernonzero1: dec( $y$ ); inc( $y$ ); goto outerinit /* outer loop not yet complete */
outerzero1: transfer( $\bar{y}$ ,  $s$ ); goto Dec /* outer loop complete. Assume control magically goes to outerexit1 */
outerexit1: Nextstage; inc( $init$ ); inc( $\overline{stack}$ ); goto initialise
beginsim: start simulating the counter machine (details follow but this is standard)

```

```

Dec:
if first( $init$ )? then
    (dec( $s$ ))4; (inc( $\bar{s}$ ))4; goto DecFinished
else
    Prevstage
    outerloop2: dec( $y$ ); inc( $\bar{y}$ ) /*  $y$  is the index for outer loop */
    innerloop2: dec( $z$ ); inc( $\bar{z}$ ) /*  $z$  is the index for inner loop */
    dec(next( $s$ )); inc(next( $\bar{s}$ ))
    innertest2: goto innernonzero2 or innerzero2
    innernonzero2: dec( $z$ ); inc( $z$ ); goto innerloop2 /* inner loop not yet complete */
    innerzero2: transfer( $\bar{z}$ ,  $s$ ); inc( $stack$ ); dec( $\overline{stack}$ ); goto Dec /* inner loop complete. Assume control
magically goes to outertest2 */
    outertest2: dec( $stack$ ); inc( $\overline{stack}$ ); goto outernonzero2 or outerzero2
    outernonzero2: dec( $y$ ); inc( $y$ ); goto outerloop2 /* outer loop not yet complete */
    outerzero2: transfer( $\bar{y}$ ,  $s$ ); goto Dec /* outer loop complete. Assume control magically goes to outerexit2 */
    outerexit2: Nextstage; goto DecFinished
fi
DecFinished: goto backtrack
backtrack:
if (next( $init$ ) == 1) then /* we are not at the end of recursion */
    if (stack == 1) then goto outertest2
    else goto outerexit2
else /* we are at the end of recursion */
    if (stack == 1) then goto outertest1
    else goto outerexit1
fi

```

The control flow of the Dec procedure is presented in Figure 6.

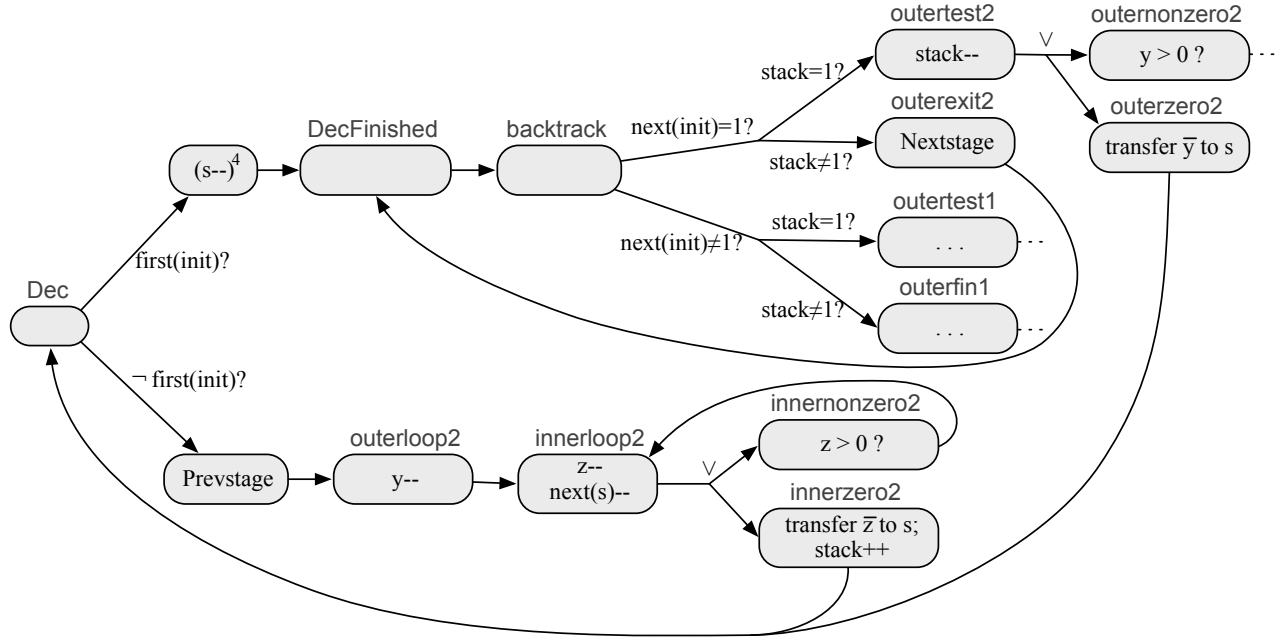


Fig. 6. Depiction of the control flow of the Dec procedure. In the figure we simplify some of the instructions, for example instead of  $\text{dec}(s); \text{inc}(\bar{s})$  we just put  $s--$ , instead of  $\text{dec}(z); \text{inc}(z)$  we just put  $z > 0?$ , etc.

4) *Proof of Correctness:* In the following lemmas, we refer to “a configuration of the chain automaton”, by which we mean a configuration of the VASS  $\mathcal{A}'$  defined in the proof of Lemma 13. A configuration has the pointer in chain  $\alpha$  at stage  $i$  if the control state of  $\mathcal{A}'$  is  $\langle q, \beta_1, \dots, \beta_\alpha, \dots \rangle$  for some  $q \in Q$  and  $\beta_\alpha = i$ . A configuration has the value  $x$  in the counter in the chain  $\alpha$  at stage  $i$  if the counter  $c_i^\alpha$  has the value  $x$ .

**Lemma 28.** Suppose in a configuration of the chain automaton, all the pointers in all the chains are at stage  $i$ , the counters in the chains  $\text{init}$  and  $s$  at stage  $i$  have values 1 and  $2^{2^i}$  respectively and all the counters in all the chains at or below stage  $i - 1$  have been initialised. If the control state is at “Dec”, then there is a run  $\rho_i$  ending at the control state “DecFinished” (just before “backtrack” in the above listing) with the pointers at stage  $i$  in all the chains and no change to any counter at any stage, except that the counter at stage  $i$  in the chain  $s$  has the value 0 (intuitively, there is a run which decreases  $s$  by  $2^{2^i}$  and does nothing else).

*Proof:* By induction on  $i$ . For the base case  $i = 1$ , we note that the test  $\text{first}(\text{init})?$  is true. So  $\rho_1$  is the run that decrements  $s$  four times, increments  $\bar{s}$  four times and goes to “DecFinished”.

Assuming the lemma is true up to value  $i$ , we prove it for  $i + 1$ . The run  $\rho_{i+1}$  does not perform the test  $\text{first}(\text{init})?$  at the beginning of “Dec” but instead goes to the “else” branch. Following is the run  $\rho_{i+1}$ .

$$\begin{aligned} & \text{Prevstage}; (\text{outerloop2}; (\text{innerloop2}; \text{dec}(\text{next}(s)); \text{inc}(\text{next}(\bar{s})); \text{innertest2})^{2^{2^i}} \\ & \quad \text{innerzero2}; \text{transfer}(\bar{z}, s); \rho_i; \text{DecFinished}; \text{backtrack}; \text{outertest2})^{2^{2^i}} \\ & \text{outerzero2}; \text{transfer}(\bar{y}, s); \rho_i; \text{DecFinished}; \text{backtrack}; \text{outerexit2}; \text{Nextstage}; \text{DecFinished} \end{aligned}$$

Note that in the above run, we have omitted some instructions and program points but we have kept those that are the most essential for our reasoning.

The first transition is to move all pointers one stage down to  $i$ . Since all counters at stage  $i$  or below have been initialised,  $y$  and  $z$  both have the value  $2^{2^i}$ . The first execution of  $\text{outerloop2}$  results in  $y$  decreasing and  $\bar{y}$  increasing. The first execution of  $\text{innerloop2}$  results in  $z$  decreasing and  $\bar{z}$  increasing. Then  $\text{dec}(\text{next}(s)); \text{inc}(\text{next}(\bar{s}))$  results in the counter at stage  $i + 1$  in the chain  $s$  decreasing and  $\bar{s}$  increasing (recall that the pointer is now at stage  $i$ ). Then our run chooses to go to  $\text{innernonzero2}$ , decreases and increases  $z$  and goes back to  $\text{innerloop2}$ . This inner loop is repeated  $2^{2^i}$  times, at the end of which the counter at stage  $i$  in the chain  $z$  has value 0,  $\bar{z}$  has value  $2^{2^i}$  and the counter at stage

$i + 1$  in the chain  $\mathbf{s}$  has been decremented  $2^{2^i}$  times. Now our run chooses to go to `innerzero2`, where the first macro transfers the value of  $\bar{z}$  into  $\mathbf{s}$  at stage  $i$ . So, the counters at stage  $i$  in the chains  $\mathbf{s}$  and  $\mathbf{z}$  both have the value  $2^{2^i}$ . Then `inc(stack); dec(stack)`; sets `stack` to 1 at stage  $i$  and the control is transferred to “Dec”. Since all the counters at or below stage  $i - 1$  have been initialised (they were initialised in the beginning and we did not touch any counter at or below stage  $i - 1$  so far) and the counter at stage  $i$  in the chain `init` is 1 (recall that all counters at stage  $i$  were initialised at the beginning and we have not touched `init` so far), we can apply the induction hypothesis. So our run now goes to “DecFinished” with  $\mathbf{s}$  at stage  $i$  set to 0 and no other changes, and all the pointers in all the chains at stage  $i$ . At DecFinished, the control is transferred to “backtrack”. Here the test `(next(init) == 1)` passes (since the counter at stage  $i + 1$  in the chain `init` has the value 1) and so does `(stack == 1)` (since we increased `stack` at stage  $i$  before going to Dec) and so the control is transferred to `outertest2`. At `outertest2`, our run sets `stack` back to 0 and chooses to go to `outernonzero2`. Here,  $\mathbf{y}$  is increased, then decreased and the control is transferred back to `outerloop2`. Here  $\mathbf{y}$  is decreased and the whole innerloop is again executed to decrease  $\mathbf{s}$  at stage  $i + 1$   $2^{2^i}$  times. This way, the inner loop is executed  $2^{2^i}$  times, each time decreasing  $\mathbf{y}$  at stage  $i$  once and decreasing  $\mathbf{s}$  at stage  $i + 1$ ,  $2^{2^i}$  times. At the end,  $\mathbf{s}$  at stage  $i + 1$  has been decremented  $2^{2^i} \times 2^{2^i} = 2^{2^{i+1}}$  times and now our run chooses to go to `outerzero2`.

Now, the first macro transfers the value of  $\bar{y}$  into  $\mathbf{s}$  at stage  $i$ . So, the counters at stage  $i$  in the chains  $\mathbf{s}$  and  $\mathbf{y}$  both have the value  $2^{2^i}$ . Then the control is transferred to “Dec”. Since all counters at or below stage  $i - 1$  have been initialised (they were initialised in the beginning and our calls to Dec till now left them intact by induction hypothesis) and the counter at stage  $i$  in the chain `init` is 1 (recall that all counters at stage  $i$  were initialised at the beginning and we have not changed `init` so far), we can apply the induction hypothesis. So our run now goes to “DecFinished” with  $\mathbf{s}$  at stage  $i$  set to 0 and no other changes, and all the pointers in all the chains at stage  $i$ . At DecFinished, the control is transferred to “backtrack”. Here the test `(next(init) == 1)` passes (since the counter at stage  $i + 1$  in the chain `init` has the value 1) but the test `(stack == 1)` does not go through. So the control is transferred to `outerexit2`. Here, all the pointers are moved to stage  $i + 1$  and the control is transferred to `backtrack`, exactly as stated in the lemma. Recall that we have reset the values in  $\mathbf{z}$  and  $\mathbf{y}$  back to  $2^{2^i}$ , so we have not made any changes to any counter except decreasing the counter in the chain  $\mathbf{s}$  at stage  $i + 1$  by  $2^{2^{i+1}}$ . This proves the induction step and hence the lemma. ■

**Lemma 29.** Suppose the control state is “begininit”, all the pointers in all the chains are at stage 0 and all the counters at all stages have the value 0. For any  $i$  between 1 and  $N$ , there is a run  $\rho'_i$  ending at “initialise”, such that the pointer is at stage  $i$  in all the chains and all the counters at or below stage  $i$  have been initialised.

*Proof:* By induction on  $i$ . For the base case  $i = 1$ ,  $\rho'_1$  is the run that executes the instructions immediately after “begininit” and ends at “initialise”.

Now we assume the lemma is true up to  $i$  and prove it for  $i + 1$ . By induction hypothesis, there is a run  $\rho'_i$  ending at “initialise”, with all the pointers in all the chains at stage  $i$  and all the counters at or below stage  $i$  initialised. The run  $\rho'_{i+1}$  is obtained by appending the following sequence to  $\rho'_i$ . It uses the runs  $\rho_i$  constructed from the previous lemma.

$$\begin{aligned} &(\text{outerinit}; (\text{innerinit}; \text{inc}(\text{next}(\mathbf{y})); \text{inc}(\text{next}(\mathbf{z})); \text{inc}(\text{next}(\bar{\mathbf{s}})); \text{innertest1})^{2^{2^i}}; \\ &\quad \text{innerzero1}; \text{transfer}(\bar{\mathbf{z}}, \mathbf{s}); \rho_i; \text{DecFinished}; \text{backtrack}; \text{outertest1})^{2^{2^i}}; \\ &\quad \text{outerzero1}; \text{transfer}(\bar{\mathbf{y}}, \mathbf{s}); \rho_i; \text{DecFinished}; \text{backtrack}; \text{outerexit1}; \text{initialise} \end{aligned}$$

Note that in the above run, we have omitted some instructions and program points but we have kept those that are the most essential for our reasoning.

The run  $\rho'_i$  is continued by testing if  $i$  is the last stage. If not, we go to “outerinit”, decrement  $\mathbf{y}$ , then go to “innerinit”, decrement  $\mathbf{z}$ , then increment the counters in the chains  $\mathbf{y}$ ,  $\mathbf{z}$  and  $\bar{\mathbf{s}}$  at stage  $i + 1$ . Then our run chooses “innernonzero1”, decrements and increments  $\mathbf{z}$  and goes back to “innerinit”. This inner loop is executed  $2^{2^i}$  times, at the end of which the counter at stage  $i$  in the chain  $\mathbf{z}$  has value 0,  $\bar{\mathbf{z}}$  has value  $2^{2^i}$  and the counters at stage  $i + 1$  in the chains  $\mathbf{y}$ ,  $\mathbf{z}$  and  $\bar{\mathbf{s}}$  have been incremented  $2^{2^i}$  times. Now our run chooses to go to `innerzero1`, where the first macro transfers the value of  $\bar{\mathbf{z}}$  into  $\mathbf{s}$  at stage  $i$ . So, the counters at stage  $i$  in the chains  $\mathbf{s}$  and  $\mathbf{z}$  both have the value  $2^{2^i}$ . Then `inc(stack); dec(stack)`; sets `stack` to 1 at stage  $i$  and the control is transferred to “Dec”. Since all the counters at or below stage  $i - 1$  have been initialised and the counter at stage  $i$  in the chain `init` is 1 (recall our convention that counters at stage  $i$  or below initialised implies that the counter in the chain `init` at stage  $i$  has

the value 1), we can apply Lemma 28. So our run now goes to “DecFinished” with  $s$  at stage  $i$  set to 0 and no other changes, and all the pointers in all the chains at stage  $i$ . At DecFinished, the control is transferred to “backtrack”. Here the test  $(\text{next}(\text{init}) == 1)$  does not go through and the test  $(\text{stack} == 1)$  passes, so control is transferred to “outertest1”. At outertest1, our run sets  $\text{stack}$  back to 0 and chooses to go to outernonzero1. Here,  $y$  is increased, then decreased and the control is transferred back to outerloop1. Here  $y$  is decreased and the whole innerloop is again executed to increment  $y, z$  and  $\bar{s}$  at stage  $i + 1$   $2^{2^i}$  times. This way, the inner loop is executed  $2^{2^i}$  times, each time decreasing  $y$  at stage  $i$  once and incrementing  $y, z$  and  $\bar{s}$  stage  $i + 1$   $2^{2^i}$  times. At the end,  $y, z$  and  $\bar{s}$  at stage  $i + 1$  have been incremented  $2^{2^i} \times 2^{2^i} = 2^{2^{i+1}}$  times and now our run chooses to go to outerzero1.

Now, the first macro at “outerzero1” transfers the value of  $\bar{y}$  into  $s$  at stage  $i$ . So, the counters at stage  $i$  in the chains  $s$  and  $y$  both have the value  $2^{2^i}$ . Then the control is transferred to “Dec”. Since all counters at or below stage  $i - 1$  have been initialised (they were initialised in the beginning and our calls to Dec till now left them intact by Lemma 28) and the counter at stage  $i$  in the chain  $\text{init}$  is 1, we can apply Lemma 28. So our run now goes to “DecFinished” with  $s$  at stage  $i$  set to 0 and no other changes, and all the pointers in all the chains at stage  $i$ . At DecFinished, the control is transferred to “backtrack”. Here the test  $(\text{next}(\text{init}) == 1)$  does not go through and neither does the test  $(\text{stack} == 1)$ . So the control is transferred to “outerexit1”. Here, all the pointers are moved to stage  $i + 1$ ,  $\text{init}$  and  $\text{stack}$  at stage  $i + 1$  are set to 1 and the control is transferred to “initialise”, exactly as in the lemma. This proves the induction step and hence the lemma. ■

The macros Nextstage and Prevstage move all the pointers in all the chains synchronously. Hence, in any run of the chain automaton, all the pointers are at the same stage. This condition can be temporarily violated while executing macros like  $\text{inc}(\text{next}(y))$ , but if all the pointers are at the same stage before the macro is executed, then any run that executes the macro is forced to regain the condition after the macro is executed (there is no non-deterministic choice inside the macro).

**Lemma 30.** Suppose that in a configuration of the chain automaton, all the pointers in all the chains are at stage  $i$ , all the counters at or below stage  $i - 1$  have been initialised, the counter in the chain  $\text{init}$  at stage  $i$  has the value 1 and the control state is at “Dec”. If any run starts from this configuration (say  $\text{conf}_1$ ) and ends at a configuration  $\text{conf}_2$  with control state at “DecFinished” and all pointers at stage  $i$  (with no other intermediate configurations satisfying these two properties), then the run decreases the counter in the chain  $s$  at stage  $i$  by  $2^{2^i}$ .

*Proof:* By induction on  $i$ . For the base case  $i = 1$ , if a run chooses the “else” branch of Dec, it gets stuck since the next instruction is to move all the pointers one stage down, which is not possible. So the run has to choose the only other option — the “if” branch, where it is forced to decrease  $s$  at stage 1 by 4 to reach “DecFinished”.

Now we assume the lemma is true up to  $i$  and prove it for  $i + 1$ . So suppose that in the configuration  $\text{conf}_1$ , all the pointers are at stage  $i + 1$ , all the counters at or below stage  $i$  are initialised, the counter in the chain  $\text{init}$  at stage  $i + 1$  is 1 and the control state is at Dec. Suppose a run starts at  $\text{conf}_1$  and reaches  $\text{conf}_2$  with the control state at “DecFinished” and all pointers at stage  $i + 1$ . First observe that if between  $\text{conf}_1$  and  $\text{conf}_2$ , the pointers are moved below stage  $i + 1$ , then the only way to get the pointers back to stage  $i + 1$  is through the macro Nextstage at control state “outerexit2”, which can only be executed if the control state was at “backtrack”, which means before  $\text{conf}_2$ , there is a configuration in which the control state is “DecFinished” and pointers are at stage  $i$ .

Now let us track the instructions executed by this run. In the first transition after  $\text{conf}_1$ , this run cannot choose the “if” branch of Dec since the test  $\text{first}(\text{init})?$  does not go through. So the run goes to the “else” branch. It is forced to move all the pointers one stage down to  $i$ , decrement  $y$  and  $z$  at stage  $i$ , decrement  $s$  at stage  $i + 1$  and make a choice between “innernonzero2” and “innerzero2”. If “innerzero2” is chosen at this stage, then in  $\text{transfer}(\bar{z}, s)$ , the value of  $s$  can be incremented at most once and then the control is forced to Dec. Then by induction hypothesis, no run can reach “DecFinished”. So the only way to continue is to execute the innerloop2  $2^{2^i}$  times, which requires decrementing  $s$  at stage  $i + 1$   $2^{2^i}$  times. At the end of this, when the control goes to Dec (after setting  $\text{stack}$  at stage  $i$  to 1), we know it has to reach “DecFinished” with pointers at stage  $i$  before reaching  $\text{conf}_2$ . By induction hypothesis, we know that when the run first comes to “DecFinished” with pointers at stage  $i$ , it would have decreased  $s$  at stage  $i$  by  $2^{2^i}$ . Now the control is forced to “backtrack”, and then to “outertest2” (since  $\text{init}$  at stage  $i + 1$  is 1 and we had set  $\text{stack}$  at stage  $i$  to 1). Here, the run is forced to set  $\text{stack}$  back to 0 and make a choice between outernonzero2 or outerzero2. As before, the run cannot continue if it chooses outerzero2 at this stage, so it is forced to choose outernonzero2. In fact, the run is forced to choose

outernonzero2  $2^{2^i}$  times, which forces decreasing  $s$  at stage  $i$  by  $2^{2^{i+1}}$ . At the end of this, the run is forced to choose outerzero2 (choosing outernonzero2 now will make the run get stuck since  $\text{dec}(y)$  no longer possible with  $y$  having the value 0), increment  $s$  at stage  $i$   $2^{2^i}$  times (to avoid getting stuck in the Dec that follows), go to Dec, then by induction hypothesis forced to go to DecFinished with  $s$  at stage  $i$  decreased by  $2^{2^i}$  and all the pointers at stage  $i$ . Now the control is forced to backtrack and then to outerexit2 (since  $\text{init}$  at stage  $i+1$  has the value 1 and  $\text{stack}$  at stage  $i$  has the value 0). Here, the pointers are moved one stage up to  $i+1$  and the control goes to DecFinished. ■

5) *Concluding the Proof of Theorem 14:* We give a reduction from the control state reachability problem for counter automata with zero tests with counters bounded by  $2^{2^N}$ , where  $N = \exp(k, n^\gamma)$  and  $n$  is the size of counter automaton. Without any loss of generality, we can assume that the initial counter values are equal zero (then these values will be updated during the initialization phase). Given such a counter automaton, we construct a chain automaton, with each chain having  $N$  counters. It will have the chains  $s, \bar{s}, y, \bar{y}, z, \bar{z}, \text{stack}, \overline{\text{stack}}, \text{init}$  and  $\overline{\text{init}}$  required for zero tests as explained previously. There will be two more chains  $\text{counters}$  and  $\overline{\text{counters}}$  to simulate the counters of the counter automaton. The right number of counters can be obtained by imposing that for every chain  $\alpha$ ,  $f(\alpha)$  is set to  $n^\gamma$ .

The initial state of the chain automaton is “begininit”, the control state that launches the program for initialising the zero testing counters. The chain automaton will have all the control states of the counter automaton (in addition to others). The final state of the chain automaton is equal to the accepting state of the counter automaton. At the control state “beginsim”, the chain automaton launches a program to initialise all the counters in the chain  $\overline{\text{counters}}$  to  $2^{2^N}$ , similar to the program for initialising the zero testing counters.

In order to initialize the first  $\delta \leq n$  counters from  $\text{counters}$  and  $\overline{\text{counters}}$  that correspond to the counters of the original counter automaton, we replace the code for **INC** from the code for the initialization phase by the following one:

```

INC: inc(next(y)); inc(next(z)); inc(next( $\bar{s}$ ));
next(init);
if last(init)? then
    inc( $\overline{\text{counters}}$ );
    (next(counters); inc( $\overline{\text{counters}}$ )) $^{\delta-1}$ ;
    (prev(counters)) $^{\delta-1}$ ;
prev(init);

```

After initialization, the next control state will be the initial state of the counter automaton and the pointers of all the chains  $s, \bar{s}, y, \bar{y}, z, \bar{z}, \text{stack}, \overline{\text{stack}}, \text{init}$  will be at stage  $N = \exp(k, n^\gamma)$ . The pointers of the chains  $\text{counters}$  and  $\overline{\text{counters}}$  will be at stage 0. From Lemma 29, there is a run that does all initialisations correctly and reaches the initial state of the counter automaton. From Lemma 30, any run that does not properly initialise all the counters gets stuck and hence will not reach the final state.

We denote the counters in the chains  $\text{counters}, \overline{\text{counters}}$  by  $c_0, \bar{c}_0, \dots, c_{\delta-1}, \bar{c}_{\delta-1}$ . For every transition  $\langle q, c_j \leftarrow c_j + 1, q' \rangle$  of the counter automaton, the chain automaton will have the transition

$$\langle q, (\text{next}(\text{counters}); \text{next}(\overline{\text{counters}}))^j; \text{inc}(\text{counters}); \text{dec}(\overline{\text{counters}}); (\text{prev}(\text{counters}); \text{prev}(\overline{\text{counters}}))^j, q' \rangle$$

Thus, for every incrementing transition of the counter automaton, the chain automaton can execute a corresponding incrementing transition. Conversely, for every incrementing transition executed by the chain automaton, the counter automaton can execute the corresponding incrementing transition.

For every transition  $\langle q : \text{if } c_j = 0 \text{ goto } q_1 \text{ else } c_j \leftarrow c_j - 1 \text{ goto } q_2 \rangle$  of the counter automaton, the chain automaton will have transitions corresponding to the following program. If the  $j$ th counter is equal to zero, then the content of the complement counter is equal to  $2^{2^N}$ , which we transfer to  $s$  and then invoke Dec. We consider a slight variant of Dec so that the return address is  $q_1$  after passing the zero-test successfully. In addition, since we need to restore the value of the complement counter, we perform a second transfer from the counter to  $s$  so that the total effect is null.

```

q: goto nonzero or zero
nonzero: (next(counters); next( $\overline{\text{counters}}$ )) $^j$ ; dec(counters); inc( $\overline{\text{counters}}$ );
(prev(counters); prev( $\overline{\text{counters}}$ )) $^j$ ; goto q2

```

**zero:** (next(counters); next( $\overline{\text{counters}}$ ))<sup>j</sup>; transfer( $\overline{\text{counters}}$ , s); goto Dec<sub>zerorep</sub>  
**zerorep:** transfer(counters, s); goto Dec<sub>zeropass</sub>  
**zeropass:** (prev(counters); prev( $\overline{\text{counters}}$ ))<sup>j</sup>; goto q<sub>1</sub>

In the above, the control state Dec<sub>zerorep</sub> launches a program very similar to Dec explained previously, except that instead of backtrack, the following backtrack<sub>zerorep</sub> is called.

**backtrack<sub>zerorep</sub>:**  
 if (last(stack)?) then /\* we are not at the end of recursion \*/  
     if (stack == 1) then goto outertest2  
     else goto outerexit2  
 else /\* we are at the end of recursion \*/  
     goto zerorep  
 fi

The difference between backtrack and backtrack<sub>zerorep</sub> is that the later transfers control to zerorep after completion. The control state Dec<sub>zeropass</sub> launches a program very similar to Dec explained previously, except that instead of backtrack, backtrack<sub>zeropass</sub> is called, which transfers control to zeropass after completion.

Now we explain the working of the above program in detail, starting from the state **zero**. If the value in the  $j^{\text{th}}$  counter of the counter automaton is 0 when the control state is  $q$ , then the value in the counter at stage  $j$  of the chain  $\overline{\text{counters}}$  in the chain automaton will have the value  $2^{2^N}$ . The macro transfer( $\overline{\text{counters}}$ , s) will set the  $j^{\text{th}}$  counter in the chain  $\overline{\text{counters}}$  to 0 and set the  $j^{\text{th}}$  counter in the chain counters to  $2^{2^N}$ , while swapping the values of s and  $\bar{s}$  at stage  $N = \exp(k, n^\gamma)$ . Then by Lemma 28, there is a run that swaps back the values of s and  $\bar{s}$  at stage  $N$  and ends in the control state zerorep. Now the macro transfer(counters, s) will set the  $j^{\text{th}}$  counter in the chain  $\overline{\text{counters}}$  back to  $2^{2^N}$  and set the  $j^{\text{th}}$  counter in the chain counters back to 0 while swapping the values of s and  $\bar{s}$  at stage  $N$ . Now the call to the decrementing algorithm at Dec<sub>zeropass</sub> swaps back the values of s and  $\bar{s}$  at stage  $N$  and the control is at zeropass, with no changes in any of the counters. Hence, for any zero testing transition executed by the counter automaton, the chain automaton can execute the corresponding sequence of transitions. Conversely, by Lemma 30, for any run of the chain automaton that begins at  $q$  and ends at  $q_1$ , the value of the  $j^{\text{th}}$  counter in the chain  $\overline{\text{counters}}$  was  $2^{2^N}$  and hence the value of the  $j^{\text{th}}$  counter in the chain counters was 0. Therefore, for any zero testing sequence of transitions executed by the chain automaton, the counter automaton can execute the corresponding zero testing transition.

We conclude that the chain automaton has a perfect run ending at its final state iff the counter automaton has an accepting run.

## F. Proof of Lemma 15

*Proof:* Let  $\mathcal{A} = \langle Q, f, 1, Q_0, Q_F, \delta \rangle$  be a chain system of level 1 with  $f : [1, n] \rightarrow \mathbb{N}$ , having thus  $n$  chains of counters, of respective size  $2^{f(1)}, \dots, 2^{f(n)}$ .

The objective is to encode a word  $\rho \in \delta^*$  that represents an accepting run. For this, we use the alphabet  $\delta$  of transitions, that we code using a logarithmic number of variables.

We encode a word  $\rho \in \delta^*$  that represents an accepting run. For this, we use the alphabet  $\delta$  of transitions. Note that we can easily simulate the labels  $\delta = \{t_1, \dots, t_m\}$  with the variables  $\tau_0, \dots, \tau_m$ , where a node has an encoding of the label  $t_i$  iff the formula  $\langle t_i \rangle = \tau_0 \approx \tau_i$  holds true. We build a LRV formula  $\varphi$  so that there is an accepting gainy run  $\rho \in \delta^*$  of  $\mathcal{A}$  if, and only if, there is a model  $\sigma$  so that  $\sigma \models \varphi$  and  $\sigma$  encodes the run  $\rho$ .

The following are standard counter-blind conditions to check.

- Every position satisfies  $\langle a \rangle$  for some  $a \in \delta$ .
- The first position satisfies  $\langle (q_0, \text{instr}, q) \rangle$  for some  $q_0 \in Q_0$ ,  $\text{instr} \in I$ ,  $q \in Q$ .
- The last position satisfies  $\langle (q, \text{instr}, q') \rangle$  for some  $q \in Q$ ,  $\text{instr} \in I$ ,  $q' \in Q_F$ .
- There are no two consecutive positions  $i$  and  $i+1$  satisfying  $\langle (q, u, q') \rangle$  and  $\langle (p, u', p') \rangle$  respectively, with  $q' \neq p$ .

We use a variable  $x$ , and variables  $x_{inc}^\alpha, x_{dec}^\alpha, x_i^\alpha$  for every chain  $\alpha$  and  $i \in [1, f(\alpha)]$ . Let us fix the bijections  $\chi^\alpha : [0, 2^{f(\alpha)} - 1] \rightarrow 2^{[1, f(\alpha)]}$  for every  $\alpha \in [1, n]$ , that assign to each number  $m$  the set of 1-bit positions of the representation of  $m$  in base 2. We say that a position  $i$  is  $\alpha$ -*incrementing* [resp.  $\alpha$ -*decrementing*] if it satisfies  $\langle (q, u, q') \rangle$  for some  $q, q' \in Q$  and  $u = \text{inc}(\alpha)$  [resp.  $u = \text{dec}(\alpha)$ ].

In the context of a model  $\sigma$  with the properties (a)–(d), we say that a position  $i$  operates on the  $\alpha$ -counter  $c$ , if  $\chi^\alpha(c) = X_i$ , where

$$X_i = \{b \in [1, f(\alpha)] \mid \sigma(i)(x) = \sigma(i)(x_b^\alpha)\}. \quad (\dagger)$$

Note that thus  $0 \leq c < 2^{f(\alpha)}$ .

For every chain  $\alpha$ , let us consider the following properties:

- (1) Any two positions of  $\sigma$  have different values of  $x_{inc}^\alpha$  [resp. of  $x_{dec}^\alpha$ ].
- (2) For every position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$  containing an instruction ‘first( $\alpha$ )?’ [resp. ‘first( $\alpha$ )?’, ‘last( $\alpha$ )?’, ‘last( $\alpha$ )?’], we have  $c = 0$  [resp.  $c \neq 0$ ,  $c = 2^{f(\alpha)} - 1$ ,  $c \neq 2^{f(\alpha)} - 1$ ].
- (3) For every position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$ ,
  - if the position contains an instruction ‘next( $\alpha$ )’ [resp. ‘prev( $\alpha$ )’], then the next position  $i + 1$  operates on the  $\alpha$ -counter  $c + 1$  [resp.  $c - 1$ ],
  - otherwise, the position  $i + 1$  operates on the  $\alpha$ -counter  $c$ .
- (4) For every  $\alpha$ -incrementing position  $i$  of  $\sigma$  operating on an  $\alpha$ -counter  $c$  there is a future  $\alpha$ -decrementing position  $j > i$  operating on the same  $\alpha$ -counter  $c$ , such that  $\sigma(i)(x_{inc}^\alpha) = \sigma(j)(x_{dec}^\alpha)$ .

**Claim 31.** There is a model satisfying the conditions above if, and only if,  $\mathcal{A}$  has a gainy and accepting run.

*Proof: (Only if)* Suppose we have a model  $\sigma$  that verifies all the properties above. Since by (1), all the positions have different data values for  $x_{dec}^\alpha$ , it then follows that the position  $j$  to which property (4) makes reference is unique. Since, also by (1), all the positions have different data values for  $x_{inc}^\alpha$ , we have that the  $\alpha$ -decrement corresponds to at most one  $\alpha$ -increment position in condition (4). Moreover, it is an  $\alpha$ -decrement of the same counter, that is,  $X_i = X_j$  (cf.  $(\dagger)$ ). For these reasons, for every  $\alpha$ -increment there is a future  $\alpha$ -decrement of the same counter which corresponds in a unique way to that increment. More precisely, for every chain  $\alpha$ , the function

$$\gamma_\sigma^\alpha : \{0 \leq i < |\sigma| : i \text{ is } \alpha\text{-incrementing}\} \rightarrow \{0 \leq i < |\sigma| : i \text{ is } \alpha\text{-decrementing}\}$$

where  $\gamma_\sigma^\alpha(i) = j$  iff  $\sigma(i)(x_{inc}^\alpha) = \sigma(j)(x_{dec}^\alpha)$  is well-defined and injective, and for every  $\gamma_\sigma^\alpha(i) = j$  we have that  $j > i$  and  $X_i = X_j$ .

Consider  $\rho \in \delta^*$  as having the same length as  $\sigma$  and such that  $\rho(i) = (q, \text{instr}, q')$  whenever  $\sigma, i \models \langle (q, \text{instr}, q') \rangle$ . From conditions (2) and (3), it follows that the counter  $c_i^\alpha$  corresponding to position  $i$  from  $\rho$ , is equal to  $\chi^{-1}(X_i)$ . Therefore, the functions  $\{\gamma_\sigma^\alpha\}_{\alpha \in [1, n]}$  witness the fact that  $\rho$  is a gainy and accepting run of  $\mathcal{A}$ .

**(If)** Let us now focus on the converse, by exhibiting a model satisfying the above properties, assuming that  $\mathcal{A}$  has an accepting gainy run  $\rho \in \delta^*$ . We therefore have, for every  $\alpha \in [1, n]$ , an injective function

$$\gamma^\alpha : \{i \mid \rho(i) \text{ is } \alpha\text{-incrementing}\} \rightarrow \{i \mid \rho(i) \text{ is } \alpha\text{-decrementing}\}$$

where for every  $\gamma(i) = j$  we have that  $j > i$  and  $c_i^\alpha = c_j^\alpha$ .

We build a model  $\sigma$  of the same length of  $\rho$ , and we now describe its data values. Let us fix two distinct data values  $d, e$ . For any position  $i$ , we have  $\sigma(i)(l_0) = d$ ; and  $\sigma(i)(l_j) = d$  if  $j \in \lambda(\rho[i])$ , and  $\sigma(i)(l_j) = e$  otherwise. In this way we make sure that the properties (a)–(d) hold.

We use distinct data values  $d_0, \dots, d_{|\sigma|-1}$  and  $d'_0, \dots, d'_{|\sigma|-1}$  (all different from  $d, e$ ). We define the data values of the remaining variables for any position  $i$ :

- For every  $\alpha$ ,  $\sigma(i)(x_{inc}^\alpha) = d_i$ .
- For every  $\alpha$ ,
  - if  $i$  is  $\alpha$ -decrementing, we define  $\sigma(i)(x_{dec}^\alpha) = \sigma(i')(x_{inc}^\alpha)$  where  $i' = (\gamma^\alpha)^{-1}(i)$ ; if such  $(\gamma^\alpha)^{-1}(i)$  does not exist, then  $\sigma(i)(x_{dec}^\alpha) = d'_i$ ;
  - otherwise, if  $i$  is not  $\alpha$ -decrementing,  $\sigma(i)(x_{dec}^\alpha) = d'_i$ .
- $\sigma(i)(x) = d$ .
- For every  $\alpha$ ,  $\sigma(i)(x_l^\alpha) = \sigma(i)(x) = d$  if  $l \in \chi(c_i^\alpha)$ , otherwise  $\sigma(i)(x_l^\alpha) = e$ .

Observe that these last two items ensure that the properties (2) and (3) hold.

By definition, every position of  $\sigma$  has a different data value for  $x_{inc}^\alpha$ . Let us show that the same holds for  $x_{dec}^\alpha$ . Note that at any position  $i$ ,  $x_{dec}^\alpha$  has: the data value of  $\sigma(i')(x_{inc}^\alpha) = d_{i'}$  for some  $i' < i$ ; or the data value  $d'_i$ . If there were two positions  $i < j$  with  $\sigma(i)(x_{dec}^\alpha) = \sigma(j)(x_{dec}^\alpha)$  it would then be because:

- (i)  $d'_i = d'_j$ ; (ii)  $d_{i'} = d_{j'}$  for some  $i' < i$ ; (iii)  $d'_i = d_{j'}$  for some  $j' < j$ ; or (iv)  $d_{i'} = d_{j'}$  for some

$i' < i, j' < j$ . It is evident that none of (i), (ii), (iii) can hold since all  $d_0, \dots, d_{|\sigma|-1}, d'_0, \dots, d'_{|\sigma|-1}$  are distinct. For this reason, if (iv) holds, it means that  $i' = j'$ , and hence that  $(\gamma^\alpha)^{-1}(i) = (\gamma^\alpha)^{-1}(j)$ , implying that  $\gamma^\alpha$  is not injective, which is a contradiction. Hence, all the positions have different data values for  $\mathbf{x}_{dec}^\alpha$ . Therefore,  $\sigma$  has the property (1).

To show that  $\sigma$  has property (4), let  $i$  be a  $\alpha$ -incrementing position of  $\sigma$ , remember that  $\sigma(i)(\mathbf{x}_{inc}^\alpha) = d_i$ . Note that position  $\gamma^\alpha(i) = j$  must be  $\alpha$ -decrementing on the same counter. By definition of the value of  $\mathbf{x}_{dec}^\alpha$ , we have that  $\sigma(j)(\mathbf{x}_{dec}^\alpha)$  must be equal to  $\sigma((\gamma^\alpha(j))^{-1})(\mathbf{x}_{inc}^\alpha) = \sigma(i)(\mathbf{x}_{inc}^\alpha) = d_i$ . Thus, property (4) holds. ■

We complete the reduction by showing that all the properties expressed before can be efficiently encoded in our logic.

**Claim 32.** Properties (a)–(d) and (1)–(4) can be expressed by formulas of LRV, that can be constructed in polynomial time in the size of  $\mathcal{A}$ .

*Proof:* Along the proof, we use the following formulas, for any  $\alpha \in [1, n]$  and  $i \in [1, f(\alpha)]$ ,

$$bit_i^\alpha \stackrel{\text{def}}{=} \mathbf{x} \approx \mathbf{x}_i^\alpha.$$

We now show how to code each of the properties.

- Properties (a)–(d) are easy to express in LRV and present no complications.
- For expressing (1), we force  $\mathbf{x}_{inc}^\alpha$  and  $\mathbf{x}_{dec}^\alpha$  to have a different data value for every position of the model with the formula

$$\neg F \mathbf{x}_{inc}^\alpha \approx \langle \top? \rangle \mathbf{x}_{inc}^\alpha \quad \wedge \quad \neg F \mathbf{x}_{dec}^\alpha \approx \langle \top? \rangle \mathbf{x}_{dec}^\alpha.$$

- Property (2) is straightforward to express in LRV.
- We express property (3) by first determining which is the bit  $i$  that must be flipped for the increment of the counter pointer of the chain  $\alpha$ .

$$flip_i^\alpha = \neg bit_i^\alpha \wedge \bigwedge_{j>i} bit_j^\alpha$$

Then by making sure that all the bits before  $i$  are preserved.

$$copy_i^\alpha = \bigwedge_{j<i} (bit_j^\alpha \Leftrightarrow X(bit_j^\alpha))$$

And by making every bit greater or equal to  $i$  be a zero.

$$zero_i^\alpha = \bigwedge_{j>i} X(\neg bit_j^\alpha)$$

And finally by swapping bit  $i$ .

$$swap_i^\alpha = X bit_i^\alpha$$

Hence, the property to check is, for every  $\alpha \in [1, n]$ ,

$$\bigvee_{(q, \text{next}(\alpha), q') \in \delta} \langle (q, \text{next}(\alpha), q') \rangle \Rightarrow \bigwedge_{i \in [1, n]} (flip_i^\alpha \Rightarrow copy_i^\alpha \wedge zero_i^\alpha \wedge swap_i^\alpha).$$

The formula expressing the property for decrements of counter pointers ( $\text{prev}(\alpha)$ ) is analogous.

- Finally, we express property (4) by testing, for every  $\alpha$ -incrementing position,

$$\mathbf{x}_{inc}^\alpha \approx \langle \alpha\text{-dec?} \rangle \mathbf{x}_{dec}^\alpha \quad \text{where} \quad \alpha\text{-dec} = \bigvee_{(q, \text{dec}(\alpha), q') \in \delta} \langle (q, \text{dec}(\alpha), q') \rangle$$

and for every  $i \in [1, f(\alpha)]$ ,

$$bit_i^\alpha \Rightarrow \mathbf{x}_{inc}^\alpha \approx \langle bit_i^{\alpha?} \rangle \mathbf{x}_{dec}^\alpha \quad \wedge \quad \neg bit_i^\alpha \Rightarrow \mathbf{x}_{inc}^\alpha \approx \langle \neg bit_i^{\alpha?} \rangle \mathbf{x}_{dec}^\alpha.$$

If the  $\alpha$ -increment has some data value  $d$  at variable  $\mathbf{x}_{inc}^\alpha$ , there must be only one future position  $j$  where  $\mathbf{x}_{dec}^\alpha$  carries the data value  $d$ —since every  $\mathbf{x}_{dec}^\alpha$  has a different value, by (1). For this reason, both positions (the  $\alpha$ -increment and the  $\alpha$ -decrement) operate on the same counter, and thus the formula faithfully expresses property (4). ■

As a corollary of Claims 31 and 32 we obtain a polynomial-time reduction from Gainy(1) into the satisfiability problem for LRV( $X, F$ ). ■



## G. Proof of Proposition 16

*Proof:* (I) The developments from Section IV-A apply to the infinite case and since  $\text{PLRV}_\omega^\top$  is shown decidable in [6], we get decidability of the satisfiability problem for  $\text{PLRV}_\omega$ .

(II) First, note that  $\text{LRV}_\omega$  is  $2\text{EXPSpace}$ -hard. Indeed, there is a simple logarithmic-space reduction from the satisfiability problem for LRV into the satisfiability problem for  $\text{LRV}_\omega$ , which can be performed as for standard LTL. Indeed, it is sufficient to introduce two new variables  $x_{new}$  and  $y_{new}$ , to state that  $x_{new} \approx y_{new}$  is true at a finite prefix of the model (herein  $x_{new} \approx y_{new}$  plays the role of a new propositional variable) and to relativize all the temporal operators and obligations to positions on which  $x_{new} \approx y_{new}$  holds true.

Concerning the complexity upper bound, from Section IV-A, we can conclude that there is a polynomial-time reduction from  $\text{LRV}_\omega$  to  $\text{LRV}_\omega^\top$ . The satisfiability problem for  $\text{LRV}_\omega^\top$  is shown decidable in [6] and we can adapt developments from Section IV-B to get also an  $2\text{EXPSpace}$  upper bound for  $\text{LRV}_\omega^\top$ . This is the purpose of the rest of the proof.

By analyzing the constructions from [6, Section 7], one can show that  $\phi$  built over the variable  $\{x_1, \dots, x_k\}$  is  $\text{LRV}_\omega^\top$  satisfiable iff there are  $Z \subseteq \mathcal{P}^+(\{x_1, \dots, x_k\})$ , a VASS  $\mathcal{A}_\phi^Z = \langle Q, Z, \delta \rangle$  along with sets  $Q_0, Q_f \subseteq Q$  of *initial* and *final* states and a Büchi automaton  $\mathcal{B}^Z$  such that:

- 1)  $\mathcal{A}_\phi^Z$  is the restriction of  $\mathcal{A}_\phi$  defined in Section IV-B and  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  in  $\mathcal{A}_\phi^Z$  for some  $q_0 \in Q_0$  and  $q_f \in Q_f$ .
- 2)  $\mathcal{B}^Z$  accepts a non-empty language.

By similar arguments from Section IV-B, existence of a run  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  can be checked in  $2\text{EXPSpace}$ . Observe that in the construction in [6, Section 7], counters in  $\mathcal{P}^+(\{x_1, \dots, x_k\}) \setminus Z$  are also updated in  $\mathcal{A}_\phi^Z$  (providing the VASS  $\mathcal{A}_\phi$ ) but one can show that this is not needed because of *optional decrement* condition and because  $\mathcal{B}^Z$  is precisely designed to take care of the future obligations in the infinite related to the counters in  $\mathcal{P}^+(\{x_1, \dots, x_k\}) \setminus Z$ .  $\mathcal{B}^Z$  can be built in exponential time and it is of exponential size in the size of  $\phi$ . Hence, non-emptiness of  $\mathcal{B}^Z$  can be checked in  $\text{EXPSpace}$ . Finally, the number of possible  $Z$  is only at most double exponential in the size of  $\phi$ , which allows to get a nondeterministic algorithm in  $2\text{EXPSpace}$  and provides a  $2\text{EXPSpace}$  upper bound by Savitch's Theorem. ■

## H. Proof of Theorem 17

*Proof:*  $2\text{EXPSpace}$ -hardness is inherited from LRV. In order to establish the complexity upper bound, first note that LTL extended with a fixed finite set of MSO-definable temporal operators preserves the nice properties about LTL (see e.g. [13]):

- Model-checking and satisfiability problems are  $\text{PSPACE}$ -complete.
- Given a formula  $\phi$  from such an extension, one can build a Büchi automaton  $\mathcal{A}_\phi$  accepting exactly the models for  $\phi$  and the size of  $\mathcal{A}_\phi$  is in  $\mathcal{O}(2^{p(|\phi|)})$  for some polynomial  $p(\cdot)$  (depending on the finite set of MSO-definable operators).

All the results hold because the set of MSO-definable operators is finite and fixed, otherwise the complexity for satisfiability and the size of the Büchi automata are of non-elementary magnitude in the worst case. Moreover, this holds for finite and infinite models.

In order to obtain the  $2\text{EXPSpace}$ , the following properties are now sufficient:

- 1) Following developments from Section IV-A, it is straightforward to show that there is a logarithmic-space reduction from the satisfiability problem for  $\text{LRV} + \{\oplus_1, \dots, \oplus_N\}$  into the satisfiability problem for  $\text{LRV}^\top + \{\oplus_1, \dots, \oplus_N\}$ .
- 2) By combining [13] and [6, Theorem 4], a formula  $\phi$  in  $\text{LRV}^\top + \{\oplus_1, \dots, \oplus_N\}$  is satisfiable iff  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  for some  $q_0 \in Q_0$  and  $q_f \in Q_f$  in some VASS  $\mathcal{A}_\phi$  such that the number of states in exponential in  $|\phi|$ . The relatively small size for  $\mathcal{A}_\phi$  is due to the fact that  $\mathcal{A}_\phi$  is built as the product of a VASS checking obligations (of exponential size in the number of variables and in the size of the local equalities) and of a finite-state automaton accepting the symbolic models of  $\phi$  of exponential size thanks to [13] (see also more details in Section IV-B).

By using arguments from Section IV-B, we can then reduce existence of a run  $\langle q_0, \mathbf{0} \rangle \xrightarrow{*} \langle q_f, \mathbf{0} \rangle$  to an instance of the control state reachability problem in some VASS of linear size in the size of  $\mathcal{A}_\phi$ , whence the  $2\text{EXPSpace}$  upper bound. ■

## I. Proof of Theorem 18

*Proof:* Again, 2EXPSpace-hardness is inherited from LRV. In order to get the 2EXPSpace, we use arguments from Proposition 19. Indeed, the decidability proof from [6, Theorem 4] can be adapted to LRV + Now. The only difference is that the finite-state automaton is of double exponential size, see details below. Despite this exponential blow-up, the 2EXPSpace upper bound can be preserved.

Let  $\phi$  be a formula with  $k$  variables. There exist a VASS  $\mathcal{A}_{\text{dec}} = \langle Q, C, \delta \rangle$  and  $Q_0, Q_f \subseteq Q$  such that  $\phi$  is satisfiable iff there are  $q_f \in Q_f$  and  $q_0 \in Q_0$  such that  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some counter valuation  $\mathbf{v}$ . Note that the number of counters in  $\mathcal{A}_{\text{dec}}$  is bounded by  $2^k$ ,  $\text{card}(Q)$  is double exponential in  $|\phi|$  and the maximal value for an update in a transition of  $\mathcal{A}_{\text{dec}}$  is  $k$ . Indeed, a formula from Past LTL+Now is equivalent to a Büchi automaton of double exponential size in its size [20]. Moreover, deciding whether a state in  $Q$  belongs to  $Q_0$  [resp.  $Q_f$ ] can be checked in exponential space in  $|\phi|$  and  $\delta$  can be decided in exponential space too. By using [28] (see also <sup>2</sup>), we can easily conclude that  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some counter valuation  $\mathbf{v}$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v}' \rangle$  for some  $\mathbf{v}'$  such that the length of the run is bounded by  $p(|\mathcal{A}_{\text{dec}}| + \max(\mathcal{A}_{\text{dec}}))^{\alpha(k)}$  where  $p(\cdot)$  is a polynomial,  $\alpha$  is a map of double exponential growth and  $\max(\mathcal{A}_{\text{dec}})$  denotes the maximal absolute value in an update (bounded by  $k$  presently). In order to take advantage of the results on VAS, we use the translation from VASS to VAS introduced in [15]: if a VASS has  $N_1$  control states, the maximal absolute value in an update is  $N_2$  and it has  $N_3$  counters, then we can build a VAS (being able to preserve coverability properties) such that it has  $N_3 + 3$  counters, the maximal absolute value in an update is  $\max(N_2, N_1^2)$ . From  $\mathcal{A}_{\text{dec}}$ , we can indeed build an equivalent VAS with a number of counters bounded by  $2^k + 3$  and with a maximal absolute value in an update at most double exponential in the size of  $|\phi|$ . So, the length of the run is at most triple exponential in  $|\phi|$ . Consequently, the satisfiability problem for LRV + Now is in 2EXPSpace. ■

## J. Proof of Proposition 19

*Proof of Proposition 19:* PSPACE-hardness is due the fact that LTL with a single propositional variable is PSPACE-hard, which can be easily simulated with the atomic formula  $x \approx y$ . Let  $k \geq 1$  be some fixed value and  $\phi \in \text{LRV}_k^\top$ . In the proof of Theorem 9, we have seen that there exist a VASS  $\mathcal{A}_{\text{dec}} = \langle Q, C, \delta \rangle$  and  $Q_0, Q_f \subseteq Q$  such that  $\phi$  is satisfiable iff there are  $q_f \in Q_f$  and  $q_0 \in Q_0$  such that  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some counter valuation  $\mathbf{v}$ . Note that the number of counters in  $\mathcal{A}_{\text{dec}}$  is bounded by  $2^k$ ,  $\text{card}(Q)$  is exponential in  $|\phi|$  and the maximal value for an update in a transition of  $\mathcal{A}_{\text{dec}}$  is  $k$ . Moreover, deciding whether a state in  $Q$  belongs to  $Q_0$  [resp.  $Q_f$ ] can be checked in polynomial space in  $|\phi|$  and  $\delta$  can be decided in polynomial space too. By using [28] (see also <sup>3</sup>), we can easily conclude that  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v} \rangle$  for some counter valuation  $\mathbf{v}$  iff  $\langle q_f, \mathbf{0} \rangle \xrightarrow{*} \langle q_0, \mathbf{v}' \rangle$  for some  $\mathbf{v}'$  such that the length of the run is bounded by  $p(|\mathcal{A}_{\text{dec}}| + \max(\mathcal{A}_{\text{dec}}))^{\alpha(k)}$  where  $p(\cdot)$  is a polynomial,  $\alpha$  is a map of double exponential growth and  $\max(\mathcal{A}_{\text{dec}})$  denotes the maximal absolute value in an update (bounded by  $k$  presently). Since  $k$  is fixed, the length of the run is at most exponential in  $|\phi|$ . Consequently, the following polynomial-space nondeterministic algorithm allows to check whether  $\phi$  is satisfiable. Guess  $q_f \in Q_f$ ,  $q_0 \in Q_0$  and guess on-the-fly a run of length at most  $p(|\mathcal{A}_{\text{dec}}| + \max(\mathcal{A}_{\text{dec}}))^{\alpha(k)}$  from  $\langle q_f, \mathbf{0} \rangle$  to some  $\langle q_0, \mathbf{v}' \rangle$ . Note that counter valuations can be represented in polynomial space too. By Savitch's Theorem, we conclude that the satisfiability problem for  $\text{LRV}_k^\top$  is in PSPACE. ■

## K. Proof of Lemma 20

*Proof of Lemma 20:* The idea of the coding is the following. Suppose we have a formula  $\varphi \in \text{LRV}^\top$  using  $k$  variables  $x_1, \dots, x_k$  so that  $\sigma \models \varphi$ .

We will encode  $\sigma$  in a model  $\sigma_\varphi$  that encodes in only one variable, say  $x$  the whole model of  $\sigma$  restricted to  $x_1, \dots, x_k$ . To this end,  $\sigma_\varphi$  is divided into  $N$  segments  $s_1 \cdots s_N$  of equal length, where  $N = |\sigma|$ . A special fresh data value is used as a special constant. Suppose that  $d$  is a data value that is not in  $\sigma$ . Then, each segment  $s_i$  has length  $k' = 2k + 1$ , and is defined as the data values “ $d \ d_1 \ d \ d_2 \ \dots \ d \ d_k \ d$ ”, where  $d_j = \sigma(i)(x_j)$ . Figure 7 contains an example for  $k = 3$  and  $N = 3$ . In fact, we can force what the model has this shape with  $\text{LRV}_1$ . Note that with this coding, we can tell that we are between two segments if there are two consecutive equal data values. In fact, we are at a position corresponding to  $x_i$  (for  $i \in [1, k]$ ) inside a segment if we are standing at the  $2i$ -th element of

<sup>2</sup>S. Demri, M. Jurdziński, O. Lachish, and R. Lazić. The covering and boundedness problems for branching VAS. *JCSS*, 79(1):23–38, 2013.

<sup>3</sup>S. Demri, M. Jurdziński, O. Lachish, and R. Lazić. The covering and boundedness problems for branching VAS. *JCSS*, 79(1):23–38, 2013.

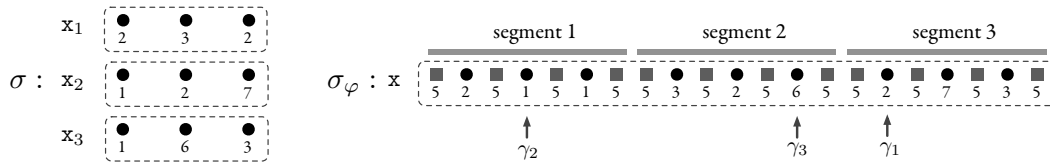


Fig. 7. Example of the encoding for  $k = 3$  and  $N = 3$ .

a segment, and we can test this with the formula

$$\gamma_i = \mathbf{X}^{k'-2i}\mathbf{x} \approx \mathbf{X}^{k'-2i+1}\mathbf{x} \vee (\mathbf{X}^{k'-2i}\top \wedge \neg\mathbf{X}^{k'-2i+1}\top).$$

Using these formulas  $\gamma_i$ , we can translate any test  $\mathbf{x}_i \approx \langle \top? \rangle \mathbf{x}_j$  into a formula that

- 1) moves to the position  $2i$  of the segment (the one corresponding to the  $\mathbf{x}_i$  data value),
- 2) tests  $\mathbf{x} \approx \langle \gamma_j? \rangle \mathbf{x}$ .

We can do this similarly with all formulas.

Let us now explain in more detail how to do the translation, and why it works.

Consider the following property of a given position  $i$  multiple of  $k'$  of a model  $\sigma_\varphi$ :

- for every  $0 \leq j \leq k' - 1$ ,  $\sigma_\varphi(i + j)(\mathbf{x}) = \sigma_\varphi(i)(\mathbf{x})$  if, and only if,  $j$  is even, and
- either  $i + k' \geq |\sigma_\varphi|$  or  $\sigma_\varphi(i + k') = \sigma_\varphi(i)$ .

This property can be easily expressed with a formula

$$\text{segment-}k' = \bigwedge_{\substack{0 \leq j \leq k'-1, \\ j \text{ is even}}} \mathbf{x} \approx \mathbf{X}^j \mathbf{x} \wedge \bigwedge_{\substack{0 \leq j \leq k'-1, \\ j \text{ is odd}}} \mathbf{x} \not\approx \mathbf{X}^j \mathbf{x} \wedge (\neg\mathbf{X}^{k'}\top \vee \mathbf{x} \approx \mathbf{X}^{k'}\mathbf{x}).$$

Note that this formula tests that we are standing at the beginning of a segment in particular. It is now straightforward to produce a  $\text{LRV}_1$  formula that tests

- $\sigma_\varphi, 0 \models \text{segment-}k'$
- for every position  $i$  so that  $\sigma_\varphi(i)(\mathbf{x}) = \sigma_\varphi(i + 1)(\mathbf{x})$ , we have  $\sigma, i + 1 \models \text{segment-}k'$ .

Let us call *many-segments* the formula expressing such a property. Note that the property implies that  $\sigma_\varphi$  is a succession of segments, as the one of Figure 7.

We give now the translation of a formula  $\varphi$  of  $\text{LRV}^\top$  with  $k$  variables into a formula  $\varphi'$  of  $\text{LRV}_1$  with 1 variable.

$$\begin{aligned} \text{tr}(\mathbf{X}\psi) &= \overbrace{\mathbf{X} \cdots \mathbf{X}}^{k' \text{ times}} \text{tr}(\psi) \\ \text{tr}(\mathbf{X}^{-1}\psi) &= \overbrace{\mathbf{X}^{-1} \cdots \mathbf{X}^{-1}}^{k' \text{ times}} \text{tr}(\psi) \\ \text{tr}(\mathbf{F}\psi) &= \mathbf{F}(\text{segment-}k' \wedge \text{tr}(\psi)) \\ \text{tr}(\psi \mathbf{U} \gamma) &= (\text{segment-}k' \Rightarrow \text{tr}(\psi)) \mathbf{U} (\text{segment-}k' \Rightarrow \text{tr}(\gamma)) \\ \text{tr}(\psi \mathbf{S} \gamma) &= (\text{segment-}k' \Rightarrow \text{tr}(\psi)) \mathbf{S} (\text{segment-}k' \Rightarrow \text{tr}(\gamma)) \\ \text{tr}(\mathbf{x}_i \approx \mathbf{X}^\ell \mathbf{x}_j) &= \mathbf{X}^{2i-1} \mathbf{x} \approx \mathbf{X}^{\ell \cdot k' + 2j-1} \mathbf{x} \quad (\text{and similarly for } \not\approx) \end{aligned}$$

Now we have to translate  $\mathbf{x}_i \approx \langle \top? \rangle \mathbf{x}_j$ . This would be translated in our encoding by saying that the  $i$ -th position of the current segment is equal to the  $j$ -th position of a future segment. Note that the following formula

$$\xi_{i,j} = \mathbf{X}^{2i-1}(\mathbf{x} \approx \langle \gamma_j? \rangle \mathbf{x})$$

does not exactly encode this property. For example, consider that we would like to test  $\mathbf{x}_2 \approx \langle \top? \rangle \mathbf{x}_3$  at the first element of the model  $\sigma_\varphi$  depicted in Figure 7. Although  $\xi$  holds, the property is not true, there is no *future* segment with the data value 1 in the position encoding  $\mathbf{x}_3$ . In fact, the formula  $\xi$  encodes correctly the property only when  $\mathbf{x}_i \not\approx \mathbf{x}_j$ . However, this is not a problem since when  $\mathbf{x}_i \approx \mathbf{x}_j$  the formula  $\mathbf{x}_i \approx \langle \top? \rangle \mathbf{x}_j$  is equivalent to  $\mathbf{x}_j \approx \langle \top? \rangle \mathbf{x}_i$ . We can then translate the formula as follows.

$$\text{tr}(\mathbf{x}_i \approx \langle \top? \rangle \mathbf{x}_j) = (\text{tr}(\mathbf{x}_i \approx \mathbf{x}_j) \wedge \xi_{i,j}) \vee (\text{tr}(\mathbf{x}_i \not\approx \mathbf{x}_j) \wedge \xi_{i,j})$$

We then define  $\varphi' = \text{many-segments} \wedge \text{tr}(\varphi)$ .

**Claim 33.**  $\varphi$  is satisfiable if, and only if,  $\varphi'$  is satisfiable.

*Proof:* In fact, if  $\sigma \models \varphi$ , then by the discussion above,  $\sigma_\varphi \models \varphi'$ . If, on the other hand,  $\sigma' \models \varphi'$  for some  $\sigma'$ , then since  $\sigma' \models \text{many-segments}$  it has to be a succession of segments of size  $2k + 1$ , and we can recover a model  $\sigma$  of size  $|\sigma'|/2k + 1$  where  $\sigma(i)(x_j)$  is the data value of the  $2j$ -th position of the  $i$ -th segment of  $\sigma'$ . In this model, we have that  $\sigma \models \varphi$ . ■

This coding can also be extended with past obligations in a straightforward way,

$$\begin{aligned} \text{tr}(\mathbf{x}_i \approx \langle \phi? \rangle^{-1} \mathbf{x}_j) &= (\text{tr}(\mathbf{x}_i \approx \mathbf{x}_j) \wedge \xi_{i,j}^{-1}) \vee (\text{tr}(\mathbf{x}_i \not\approx \mathbf{x}_j) \wedge \xi_{i,j}^{-1}) \quad \text{where} \\ \xi_{i,j}^{-1} &= X^{2i-1}(\mathbf{x} \approx \langle \gamma_j? \rangle^{-1} \mathbf{x}). \end{aligned}$$

Therefore, there is also a reduction from  $\text{PLRV}^\top$  into  $\text{PLRV}_1$ . ■

#### L. Proof of Theorem 23

Let us introduce the problem below as a variant of PCP:

PROBLEM:	Modified Directed Post's Correspondence Problem (MPCP <sup>dir</sup> )
INPUT:	A finite alphabet $\Sigma$ , $n \in \mathbb{N}$ , $u_1, \dots, u_n, v_1, \dots, v_n \in \Sigma^+$ , $ u_1 ,  u_2  \leq 2$ and $ v_1 ,  v_2  \geq 3$ .
QUESTION:	Are there indices $1 \leq i_1, \dots, i_m \leq n$ so that $u_{i_1} \cdots u_{i_m} = v_{i_1} \cdots v_{i_m}$ , $i_1 \in \{1, 2\}$ , $ u_{i_1} \cdots u_{i_m} $ is even and for every $ u_{i_1}  < j <  u_{i_1} \cdots u_{i_m} $ , if the $j^{\text{th}}$ position of $v_{i_1} \cdots v_{i_m}$ occurs in $v_{i_k}$ for some $k$ , then the $j^{\text{th}}$ position of $u_{i_1} \cdots u_{i_m}$ occurs in $u_{i_{k'}}$ for some $k' > k$ ?

**Lemma 34.** The MPCP<sup>dir</sup> problem is undecidable.

This is a corollary of the undecidability proof for PCP as done in [14]. By reducing MPCP<sup>dir</sup> to satisfiability for  $\text{LRV}_{\text{vec}}$ , we get undecidability.

*Proof:* In [14], the halting problem for Turing machines is first reduced to a variation of PCP called modified PCP. In modified PCP, a requirement is that the solution should begin with the pair  $u_1, v_1$  (this requirement can be easily eliminated by encoding it into the standard PCP, but here we find it convenient to work in the presence of a generalization of this requirement). To ensure that there is a solution of even length whenever there is a solution, we let  $u_2 = \$u_1$  and  $v_2 = \$v_1$ , where  $\$$  is a new symbol. Now  $u_1 u_{i_2} \cdots u_{i_m} = v_1 v_{i_2} \cdots v_{i_m}$  is a solution iff  $u_2 u_{i_2} \cdots u_{i_m} = v_2 v_{i_2} \cdots v_{i_m}$  is a solution. In modified PCP,  $|u_1| = 1$  and  $|v_1| \geq 3$ . Hence,  $|u_2| = 2$  and  $|v_2| \geq 3$ . The resulting set of strings  $u_1, \dots, u_n, v_1, \dots, v_n$  make up our instance of MPCP<sup>dir</sup>.

In the encoding of the halting problem from the cited work,  $|u_i| \leq 3$  for any  $i$  between 1 and  $n$  (this continues to hold even after we add  $\$$  to the first pair as above). A close examination of the proof in the cited work reveals that if the modified PCP instance  $u_1, \dots, u_n, v_1, \dots, v_n \in \Sigma^*$  has a solution  $u_{i_1} \cdots u_{i_m} = v_{i_1} \cdots v_{i_m}$ , then for every  $|u_{i_1}| < j < |u_{i_1} \cdots u_{i_m}|$ , if the  $j^{\text{th}}$  position of  $v_{i_1} \cdots v_{i_m}$  occurs in  $v_{i_k}$  for some  $k$ , then the  $j^{\text{th}}$  position of  $u_{i_1} \cdots u_{i_m}$  occurs in  $u_{i_{k'}}$  for some  $k' > k$  (we call this the directedness property). In short, the reason for this is that for any  $k \in \mathbb{N}$ , if  $v_{i_1} \cdots v_{i_k}$  encodes the first  $\ell + 1$  consecutive configurations of a Turing machine, then  $u_{i_1} \cdots u_{i_k}$  encodes the first  $\ell$  configurations. Hence, for any letter in  $v_{i_{k+1}}$  (which starts encoding  $(\ell + 2)^{\text{th}}$  configuration), the corresponding letter cannot occur in  $u_{i_1} \cdots u_{i_{k+1}}$  (unless the single string  $u_{i_{k+1}}$  encodes the entire  $(\ell + 1)^{\text{st}}$  configuration and starts encoding  $(\ell + 2)^{\text{th}}$  configuration; this however is not possible since there are at most 3 letters in  $u_{i_{k+1}}$  and encoding a configuration requires at least 4 letters). After the last configuration has been encoded, the length of  $u_{i_1} \cdots u_{i_k}$  starts catching up with the length of  $v_{i_1} \cdots v_{i_k}$  with the help of pairs  $(u, v)$  where  $|u| = 2$  and  $|v| = 1$ . However, as long as the lengths do not catch up, the directedness property continues to hold. As soon as the lengths do catch up, it is a solution to the PCP. Only the positions of  $u_{i_1}$  and the last position of the solution violate the directedness property. ■

Given an instance  $\text{pcp}$  of MPCP<sup>dir</sup>, we construct a  $\text{LRV}_{\text{vec}}(X, U)$  formula  $\phi_{\text{pcp}}$  such that  $\text{pcp}$  has a solution iff  $\phi_{\text{pcp}}$  is satisfiable. To do so, we adapt a proof technique from [1], [17] but we need to provide substantial changes in order to fit our logic. Moreover, none of the results in [1], [17] allow to derive our main undecidability result since we use neither past-time temporal operators nor past obligations of the form  $\langle \mathbf{x}, \mathbf{x}' \rangle \approx \langle \varphi? \rangle^{-1} \langle \mathbf{y}, \mathbf{y}' \rangle$

Let  $\bar{\Sigma} = \{\bar{a} \mid a \in \Sigma\}$  be a disjoint copy of  $\Sigma$ . For convenience, we assume that each position of a  $\text{LRV}$  model is labelled by a letter from  $\Sigma \cup \bar{\Sigma}$  (these labels can be easily encoded as equivalence classes

of some extra variables). For such a model  $\sigma$ , let  $\sigma_\Sigma$  (resp.  $\sigma_{\bar{\Sigma}}$ ) be the model obtained by restricting  $\sigma$  to positions labelled by  $\Sigma$  (resp.  $\bar{\Sigma}$ ). If  $u_{i_1} \cdots u_{i_m} = v_{i_1} \cdots v_{i_m}$  is a solution to *pcp*, the idea is to construct a LRV model whose projection to  $\Sigma \cup \bar{\Sigma}$  is  $u_{i_1} \bar{v}_{i_1} \cdots u_{i_m} \bar{v}_{i_m}$ . To check that such a model  $\sigma$  actually represents a solution, we will write  $\text{LRV}_{vec}(X, U)$  formulas to say that “for all  $j$ , if the  $j^{\text{th}}$  position of  $\sigma_\Sigma$  is labelled by  $a$ , then the  $j^{\text{th}}$  position of  $\sigma_{\bar{\Sigma}}$  is labelled by  $\bar{a}$ ”.

The main difficulty is to get a handle on the  $j^{\text{th}}$  position of  $\sigma_\Sigma$ . The difficulty arises since the LRV model  $\sigma$  is an interleaving of  $\sigma_\Sigma$  and  $\sigma_{\bar{\Sigma}}$ . This is handled by using two variables  $x$  and  $y$ . Positions 1 and 2 of  $\sigma_\Sigma$  will have the same value of  $x$ , positions 2 and 3 will have the same value of  $y$ , positions 3 and 4 will have the same value of  $x$  and so on. Generalising, odd positions of  $\sigma_\Sigma$  will have the same value of  $x$  as in the next position of  $\sigma_\Sigma$  and the same value of  $y$  as in the previous position of  $\sigma_\Sigma$ . Even positions of  $\sigma_\Sigma$  will have the same value of  $x$  as in the previous position of  $\sigma_\Sigma$  and the same value of  $y$  as in the next position of  $\sigma_\Sigma$ . To easily identify odd and even positions, an additional label is introduced at each position, which can be  $O$  or  $E$ . The sequence  $\sigma_\Sigma$  looks as follows.

$$\begin{bmatrix} x \\ \text{Odd/Even} \\ \text{Letter} \\ y \end{bmatrix} : \begin{pmatrix} d_1 \\ O_{in} \\ a_1 \\ d'_1 \end{pmatrix} \begin{pmatrix} d_1 \\ E \\ a_2 \\ d'_2 \end{pmatrix} \begin{pmatrix} d_3 \\ O \\ a_3 \\ d'_2 \end{pmatrix} \begin{pmatrix} d_3 \\ E \\ a_4 \\ d'_4 \end{pmatrix} \cdots \begin{pmatrix} d_{m-1} \\ O \\ a_{m-1} \\ d'_{m-2} \end{pmatrix} \begin{pmatrix} d_{m-1} \\ E_{fi} \\ a_m \\ d'_m \end{pmatrix} \quad (\star)$$

We assume that the atomic formula  $a$  ( $a \in \Sigma$ ) is true at some position  $j$  of a model  $\sigma$  iff the position  $j$  is labelled by the letter  $a$ . We denote  $\bigvee_{a \in \Sigma} a$  by  $\Sigma$ . For a word  $u \in \Sigma^*$ , we denote by  $\phi_u^i$  the LRV formula that ensures that starting from  $i$  positions to the right of the current position, the next  $|u|$  positions are labelled by the respective letters of  $u$  (e.g.,  $\phi_{abb}^3 \stackrel{\text{def}}{=} X^3 a \wedge X^4 b \wedge X^5 b$ ). We enforce a model of the form  $(\star)$  above through the following formulas.

- (1) The projection of the labeling of  $\sigma$  on to  $\Sigma \cup \bar{\Sigma}$  is  $(u_1 \bar{v}_1 + u_2 \bar{v}_2) \{u_i \bar{v}_i \mid i \in [n]\}^*$ : to facilitate writing this condition in LRV, we introduce two new variables  $z_b^1, z_b^2$  such that at any position, they have the same value only if that position is not the starting point of some pair  $u_i \bar{v}_i$ .

$$\begin{aligned} & z_b^1 \not\approx z_b^2 \wedge \bigvee_{i \in \{1,2\}} (\phi_{u_i}^0 \wedge \phi_{\bar{v}_i}^{|u_i|+1} \wedge (X^{|u_i v_i|+1} \top \Rightarrow X^{|u_i v_i|+1} z_b^1 \not\approx z_b^2)) \\ & \wedge G \left( z_b^1 \approx z_b^2 \vee \bigvee_{i \in [1,n]} (\phi_{u_i}^0 \wedge \phi_{\bar{v}_i}^{|u_i|+1}) \wedge (X^{|u_i v_i|+1} \top \Rightarrow X^{|u_i v_i|+1} z_b^1 \not\approx z_b^2) \right) \end{aligned}$$

- (2) a) For every data value  $d$ , there are at most two positions  $i, j$  in  $\sigma_\Sigma$  with  $\sigma_\Sigma(i)(x) = \sigma_\Sigma(j)(x) = d$  or  $\sigma_\Sigma(i)(y) = \sigma_\Sigma(j)(y) = d$ .

$$\begin{aligned} & G(\Sigma \Rightarrow (\neg x \approx \langle \Sigma? \rangle x) \vee (x \approx \langle \Sigma \wedge (\neg x \approx \langle \Sigma? \rangle x?) \rangle x)) \\ & \wedge G(\Sigma \Rightarrow (\neg y \approx \langle \Sigma? \rangle y) \vee (y \approx \langle \Sigma \wedge (\neg y \approx \langle \Sigma? \rangle y?) \rangle y)) \end{aligned}$$

The same condition for  $\sigma_{\bar{\Sigma}}$ , enforced with a formula similar to the one above.

- b)  $\sigma_\Sigma$  projected onto  $\{O, O_{in}, E, E_{fi}\}$  is in  $O_{in}(EO)^* E_{fi}$ .

$$\begin{aligned} & O_{in} \wedge G((O \vee O_{in}) \Rightarrow \bar{\Sigma} U(\Sigma \wedge (E \vee E_{fi}))) \wedge G(E \Rightarrow \bar{\Sigma} U(\Sigma \wedge O)) \\ & \wedge G(E_{fi} \Rightarrow \neg X F \Sigma) \end{aligned}$$

$\sigma_{\bar{\Sigma}}$  projected onto  $\{\bar{O}, \bar{O}_{in}, \bar{E}, \bar{E}_{fi}\}$  is in  $\bar{O}_{in}(\bar{E}\bar{O})^* \bar{E}_{fi}$ .

$$\begin{aligned} & \Sigma U \bar{O}_{in} \wedge G((\bar{O} \vee \bar{O}_{in}) \Rightarrow \Sigma U(\bar{\Sigma} \wedge (\bar{E} \vee \bar{E}_{fi}))) \wedge G(\bar{E} \Rightarrow \Sigma U(\bar{\Sigma} \wedge \bar{O})) \\ & \wedge G(\bar{E}_{fi} \Rightarrow \neg X \bar{\Sigma}) \end{aligned}$$

- c) For every position  $i$  of  $\sigma_\Sigma$  labelled by  $O$  or  $O_{in}$ , there exists a future position  $j > i$  of  $\sigma_\Sigma$  labelled by  $E$  or  $E_{fi}$  so that  $\sigma_\Sigma(i)(x) = \sigma_\Sigma(j)(x)$ .

$$G((O \vee O_{in}) \Rightarrow x \approx \langle \Sigma \wedge (E \vee E_{fi})? \rangle x)$$

For every position  $i$  of  $\sigma_{\bar{\Sigma}}$  labelled by  $\bar{O}$  or  $\bar{O}_{in}$ , there exists a future position  $j > i$  of  $\sigma_{\bar{\Sigma}}$  labelled by  $\bar{E}$  or  $\bar{E}_{fi}$  so that  $\sigma_{\bar{\Sigma}}(i)(x) = \sigma_{\bar{\Sigma}}(j)(x)$  (enforced with a formula similar to the one above).

- d) For every position  $i$  of  $\sigma_\Sigma$  labelled by  $E$ , there exists a future position  $j > i$  of  $\sigma_\Sigma$  labelled by  $O$  so that  $\sigma_\Sigma(i)(y) = \sigma_\Sigma(j)(y)$ .

$$G(E \Rightarrow y \approx \langle \Sigma \wedge O? \rangle y)$$

For every position  $i$  of  $\sigma_{\bar{\Sigma}}$  labelled by  $\bar{E}$ , there exists a future position  $j > i$  of  $\sigma_{\bar{\Sigma}}$  labelled by  $\bar{O}$  so that  $\sigma_{\bar{\Sigma}}(i)(y) = \sigma_{\bar{\Sigma}}(j)(y)$  (enforced with a formula similar to the one above).

- (3) For any position  $i$  of  $u_{i_1}$ , the corresponding position  $j$  in  $\bar{v}_{i_1} \cdots \bar{v}_{i_m}$  (which always happens to be in  $v_{i_1}$  MPCP<sup>dir</sup>, since  $|u_{i_1}| \leq 2$  and  $|v_{i_1}| \geq 3$ ) should satisfy  $\sigma(i)(x) = \sigma(j)(x)$  and  $\sigma(i)(y) = \sigma(j)(y)$ . In addition, position  $i$  is labelled with  $a \in \Sigma$  iff position  $j$  is labelled with  $\bar{a} \in \bar{\Sigma}$ .

$$\left( \bigvee_{a \in \Sigma} (O_{in} \wedge a) \Rightarrow \langle x, y \rangle \approx \langle \bar{O}_{in} \wedge \bar{a}? \rangle \langle x, y \rangle \right) \\ \wedge X\Sigma \Rightarrow \bigvee_{a \in \Sigma} Xa \wedge X^3\bar{a} \wedge X(x \approx X^2x \wedge y \approx X^2y)$$

- (4) For any position  $i$  of  $\sigma$  with  $2|u_{i_1}| < i < |u_{i_1} \cdots u_{i_m}|$ , if it is labeled with  $\bar{a} \in \bar{\Sigma}$ , there is a future position  $j > i$  labeled with  $a \in \Sigma$  such that  $\sigma(i)(x) = \sigma(j)(x)$  and  $\sigma(i)(y) = \sigma(j)(y)$ . The following formula assumes that  $|u_{i_1}| \leq 2$ , as it is in MPCP<sup>dir</sup>.

$$X\Sigma \Rightarrow \Sigma U \left( X^2 G \bigvee_{\bar{a} \in \bar{\Sigma}} ((\bar{a} \wedge \neg \bar{E}_{fi}) \Rightarrow (x, y) \approx \langle a? \rangle (x, y)) \right) \quad (X\Sigma \text{ is true when } |u_{i_1}| = 2) \\ \wedge X\bar{\Sigma} \Rightarrow \Sigma U \left( X G \bigvee_{\bar{a} \in \bar{\Sigma}} ((\bar{a} \wedge \neg \bar{E}_{fi}) \Rightarrow (x, y) \approx \langle a? \rangle (x, y)) \right) \quad (X\bar{\Sigma} \text{ is true when } |u_{i_1}| = 1)$$

- (5) If  $i$  and  $j$  are the last positions labeled with  $\Sigma$  and  $\bar{\Sigma}$  respectively, then  $\sigma(i)(x) = \sigma(j)(x)$  and  $\sigma(i)(y) = \sigma(j)(y)$ . In addition, position  $i$  is labelled with  $a \in \Sigma$  iff position  $j$  is labelled with  $\bar{a} \in \bar{\Sigma}$ .

$$G \left( \bigvee_{a \in \Sigma} (E_{fi} \wedge a) \Rightarrow \langle x, y \rangle \approx \langle \bar{E}_{fi} \wedge \bar{a}? \rangle \langle x, y \rangle \right)$$

Given an instance  $pcp$  of MPCP<sup>dir</sup>, the required formula  $\phi_{pcp}$  is the conjunction of all the formulas above.

**Lemma 35.** Given an instance  $pcp$  of MPCP<sup>dir</sup>,  $\phi_{pcp}$  is satisfiable iff  $pcp$  has a solution.

*Proof:* Suppose  $u_{i_1} \cdots u_{i_m} = v_{i_1} \cdots v_{i_m}$  is a solution of  $pcp$ . It is routine to check that, with this solution, a model satisfying  $\phi_{pcp}$  can be built.

Now suppose that  $\phi_{pcp}$  has a satisfying model  $\sigma$ . From condition (1), we get a sequence  $u_{i_1} \bar{v}_{i_1} \cdots u_{i_m} \bar{v}_{i_m}$ . It is left to prove that  $u_{i_1} \cdots u_{i_m} = v_{i_1} \cdots v_{i_m}$ .

Let  $\sigma_\Sigma$  (resp.  $\sigma_{\bar{\Sigma}}$ ) be the model obtained from  $\sigma$  by restricting it to positions labeled with  $\Sigma$  (resp.  $\bar{\Sigma}$ ). Construct a directed graph whose vertices are the positions of  $\sigma_\Sigma$  and there is an edge from  $i$  to  $j$  iff  $i < j$  and  $\sigma_\Sigma(i)(x) = \sigma_\Sigma(j)(x)$  or  $\sigma_\Sigma(i)(y) = \sigma_\Sigma(j)(y)$ . We claim that the set of edges of this directed graph represents the successor relation induced on  $\sigma_\Sigma$  by  $\sigma$ . To prove this claim, we first show that all positions have in degree 1 (except the first one, which has in degree 0) and that all positions have out degree 1 (except the last one, which has out degree 0). Indeed, condition (2a) ensures that for any position, both the in degree and out degree are at most 1. From conditions (2b), (2c) and (2d), each position (except the last one) has out degree at least 1. Hence, all positions except the last one have out degree exactly 1. By the definition of the set of edges, the last position has out degree 0. If more than one position has in degree 0, it will force some other position to have in degree more than 1, which is not possible. By the definition of the set of edges, the first position has in degree 0 and hence, all other positions have in degree exactly 1. To finish proving the claim (that the set of edges of our directed graph represents the successor relation induced on  $\sigma_\Sigma$  by  $\sigma$ ), we will now prove that at any position of  $\sigma_\Sigma$  except the last one, the outgoing edge goes to the successor position in  $\sigma_\Sigma$ . If this is not the case, let  $i$  be the last position where this condition is violated. The outgoing edge from  $i$  then goes to some position  $j > i + 1$ . Since the outgoing edges from each position between  $i + 1$  and  $j - 1$  go to the respective successors, position  $j$  will have in degree 2, which is not possible.

Next we will prove that there cannot be two positions of  $\sigma_\Sigma$  with the same value for variables  $x$  and  $y$ . Suppose there were two such positions and the first one is labeled  $O$  (the argument for  $E$  is similar). If the second position is also labeled  $O$ , then by condition (2c), there is at least one position labeled  $E$  with the same value for variable  $x$ , so there are three positions with the same value for variable  $x$ , violating condition (2a). Hence, the second position must be labelled  $E$ . Then by condition (2d), there is a position after the second position with the same value for variable  $y$ . This implies there are three positions with the same value for variable  $y$ , again violating condition (2a). Therefore, there cannot be two positions of  $\sigma_\Sigma$  with the same value for variables  $x$  and  $y$ .

Finally, we prove that for every position  $i$  of  $\sigma_{\bar{\Sigma}}$ , if  $\bar{a}_i$  is its label, then the unique position in  $\sigma_\Sigma$  with the same value of  $x$  and  $y$  is position  $i$  of  $\sigma_\Sigma$  and carries the label  $a_i$ . For  $1 \leq i \leq |u_{i_1}|$ , this follows from condition (3). For  $i = |u_{i_1} \cdots u_{i_m}|$ , this follows from condition (5). The rest of the proof is by induction on  $i$ . The base case is already proved since  $|u_{i_1}| \geq 1$ . For the induction step, assume the result is true for all positions of  $\sigma_{\bar{\Sigma}}$  up to position  $i$ . Suppose position  $i$  of  $\sigma_{\bar{\Sigma}}$  is labeled by  $\bar{O}$  (the case of  $\bar{E}$  is symmetric). Then by induction hypothesis and (2b), position  $i$  of  $\sigma_\Sigma$  is labeled by  $O$  (or  $O_{in}$ , if  $i = 1$ ). By condition (2c) and the definition of edges in the directed graph that we built, position  $i + 1$  of  $\sigma_{\bar{\Sigma}}$  (resp.  $\sigma_\Sigma$ ) has same value of  $x$  as that of position  $i$ . We know from the previous paragraph that there is exactly one position of  $\sigma_\Sigma$  with the same value of  $x$  and  $y$  as that of position  $i + 1$  in  $\sigma_{\bar{\Sigma}}$ . This position in  $\sigma_\Sigma$  cannot be  $i$  or before due to induction hypothesis. If it is not  $i + 1$  either, then there will be three positions of  $\sigma_\Sigma$  with the same value of  $x$ , which violates condition (2a). Hence, the position of  $\sigma_\Sigma$  with the same value of  $x$  and  $y$  as that of position  $i + 1$  in  $\sigma_{\bar{\Sigma}}$  is indeed  $i + 1$  and it carries the label  $a_i$  by condition (4).  $\blacksquare$

As a conclusion, the satisfiability problem for  $\text{LRV}_{vec}(X, U)$  is undecidable.

## M. Implications for Logics on Data Words

### 1) First Order Logic:

*Proof of Proposition 24:* Through a standard translation we can bring any formula of  $\text{forward-EMSO}^2(+1, <, \sim)$  into a formula of the form

$$\varphi = \exists X_1, \dots, X_n \left( \forall x \forall y \chi \wedge \bigwedge_k \forall x \exists y (x \leq y \wedge \psi_k) \right)$$

that preserves satisfiability, where  $\chi$  and all  $\psi_k$ 's are quantifier-free formulas, and there are no tests for labels. Furthermore, this is a polynomial-time translation. This translation is just the Scott normal form of  $\text{EMSO}^2(+1, <, \sim)$  [1] adapted to  $\text{forward-EMSO}^2(+1, <, \sim)$ , and can be done in the same way.

We now give an exponential-time translation  $tr : \text{forward-EMSO}^2(+1, <, \sim) \rightarrow \text{LRV}$ . For any formula  $\varphi$  of  $\text{forward-EMSO}^2(+1, <, \sim)$ ,  $tr(\varphi)$  is an equivalent (in the sense of satisfiability) LRV formula, whose satisfiability can be tested in  $2\text{EXPSPACE}$  (Corollary 10). This yields an upper bound of  $3\text{EXPSPACE}$  for  $\text{forward-EMSO}^2(+1, <, \sim)$ .

The translation makes use of: a distinguished variable  $x$  that encodes the data values of any data word satisfying  $\varphi$ ; variables  $x_0, \dots, x_n$  that are used to encode the monadic relations  $X_1, \dots, X_n$ ; and a variable  $x_{prev}$  whose purpose will be explained later on. We give now the translation. To translate  $\forall x \forall y \chi$ , we first bring the formula to a form

$$\bigwedge_{m \in M} \neg \exists x \exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y), \quad (\star)$$

where every  $\chi_m^x$  (resp.  $\chi_m^y$ ) is a conjunction of (negations of) atoms of monadic relations on  $x$  (resp.  $y$ ); and  $\chi_m = \mu \wedge \nu$  where  $\mu \in \{x=y, +1(x, y), \neg(+1(x, y) \vee x=y)\}$  and  $\nu \in \{x \sim y, \neg(x \sim y)\}$ .

**Claim 36.**  $\forall x \forall y \chi$  can be translated into an equivalent formula of the form  $(\star)$  in exponential time.

*Proof:* As an example, if

$$\chi = \neg(x \sim y) \vee X(x) \vee X(y),$$

then the corresponding formula would be

$$\bigwedge_{\mu, \chi^x, \chi^y} \left( (\neg \exists x \exists y x \leq y \wedge \mu \wedge \neg X(x) \wedge \chi^y) \wedge (\neg \exists x \exists y x \leq y \wedge \mu \wedge \chi^x \wedge \neg X(y)) \right)$$

for all  $\mu \in \{x=y, +1(x, y), \neg(+1(x, y) \vee x=y)\}$ ,  $\chi^x \in \{X(x), \neg X(x)\}$ ,  $\chi^y \in \{X(y), \neg X(y)\}$ . We can bring the formula into this normal form in exponential time. To this end, we can first bring  $\chi$  into CNF,  $\chi = \bigwedge_{i \in I} \bigvee_{j \in J_i} \nu_{ij}$ , where every  $\nu_{ij}$  is an atom or a negation of an atom. Then,

$$\forall x \forall y \chi \equiv \forall x \forall y \bigwedge_{i \in I} \bigvee_{j \in J_i} \nu_{ij} \equiv \bigwedge_{i \in I} \forall x \forall y \bigvee_{j \in J_i} \nu_{ij} \equiv \bigwedge_{i \in I} \neg \exists x \exists y \bigwedge_{j \in J_i} \neg \nu_{ij}$$

Let  $\nu_{ij}^{x \leftrightarrow y}$  be  $\nu_{ij}$  where  $x$  and  $y$  are swapped. Note that  $\exists x \exists y \bigwedge_{j \in J_i} \neg \nu_{ij}$  is equivalent to  $\exists x \exists y \bigwedge_{j \in J_i} \neg \nu_{ij}^{x \leftrightarrow y}$ . Now for every  $i \in I$ , let

$$\mu_{i,1} = x \leq y \wedge \bigwedge_{j \in J_i} \neg \nu_{ij} \quad \mu_{i,2} = x \leq y \wedge \bigwedge_{j \in J_i} \neg \nu_{ij}^{x \leftrightarrow y}.$$

Note that  $\exists x \exists y \mu_{i,1} \vee \exists x \exists y \mu_{i,2}$  is equivalent to  $\exists x \exists y \bigwedge_{j \in J_i} \neg \nu_{ij}$ . Hence,

$$\bigwedge_{i \in I} \neg \bigvee_{j \in \{1,2\}} \exists x \exists y (x \leq y \wedge \mu_{i,j}) \equiv \bigwedge_{i \in I} \bigwedge_{j \in \{1,2\}} \neg \exists x \exists y (x \leq y \wedge \mu_{i,j})$$

is equivalent to  $\forall x \forall y \chi$ . Finally, every  $\mu_{i,j}$  can be easily split into a conjunction of three formulas (one of binary relations, one of unary relations on  $x$ , and one of unary relations on  $y$ ), thus obtaining a formula of the form  $(\star)$ . This procedure takes polynomial time once the CNF normal form is obtained, and it then takes exponential time in the worst case.  $\blacksquare$

We define  $tr(\chi_m^x)$  as the conjunction of all the formulas  $\mathbf{x}_0 \approx \mathbf{x}_i$  so that  $X_i(x)$  is a conjunct of  $\chi_m^x$ , and all the formulas  $\neg(\mathbf{x}_0 \approx \mathbf{x}_i)$  so that  $\neg X_i(x)$  is a conjunct of  $\chi_m^x$ ; we do similarly for  $tr(\chi_m^y)$ . If  $\mu = +1(x, y)$  and  $\nu = x \sim y$  we translate

$$tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)) = \mathbf{x} \approx \mathbf{Xx} \wedge tr(\chi_m^x) \wedge \mathbf{X}tr(\chi_m^y).$$

If  $\mu = (x = y)$  and  $\nu = x \sim y$  we translate

$$tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)) = tr(\chi_m^x) \wedge tr(\chi_m^y).$$

We proceed similarly for  $\mu = +1(x, y)$ ,  $\nu = \neg(x \sim y)$ ; and the translation is of course  $\perp$  (false) if  $\mu = (x=y)$ ,  $\nu = \neg(x \sim y)$ . The difficult cases are the remaining ones. Suppose  $\mu = \neg(+1(x, y) \vee x=y)$ ,  $\nu = x \sim y$ . In other words,  $x$  is at least two positions before  $y$ , and they have the same data value. Observe that the formula  $tr(\chi_m^x) \wedge \mathbf{x} \approx \langle tr(\chi_m^y) \rangle \mathbf{x}$  does not encode precisely this case, as it would correspond to a weaker condition  $x < y \wedge x \sim y$ . In order to properly translate this case we make use of the variable  $\mathbf{x}_{prev}$ , ensuring that it always has the data value of the variable  $\mathbf{x}$  in the previous position

$$prev = \mathbf{G}(\mathbf{X}\top \Rightarrow \mathbf{x} \approx \mathbf{X}\mathbf{x}_{prev}).$$

We then define  $tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y))$  as

$$tr(\chi_m^x) \wedge \mathbf{x} \approx \langle \mathbf{x}_{prev} \approx \langle tr(\chi_m^y) \rangle \mathbf{x} \rangle \mathbf{x}_{prev}.$$

Note that by nesting twice the future obligation we ensure that the target position where  $tr(\chi_m^y)$  must hold is at a distance of at least two positions. For  $\nu = \neg(x \sim y)$  we produce a similar formula, replacing the innermost appearance of  $\approx$  with  $\not\approx$  in the formula above. We then define  $tr(\forall x \forall y \chi)$  as

$$prev \wedge \bigwedge_{m \in M} \neg \mathbf{F} tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)).$$

To translate  $\forall x \exists y (x \leq y \wedge \psi_k)$  we proceed in a similar way. As before, we bring  $x \leq y \wedge \psi_k$  into the form  $\bigvee_{m \in M} x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y$ , in exponential time. We then define  $tr(\forall x \exists y (x \leq y \wedge \psi_k))$  as

$$prev \wedge \mathbf{G} \bigvee_{m \in M} tr(\exists y (x \leq y \wedge \chi_m \wedge \chi_m^x \wedge \chi_m^y)).$$

Thus,

$$tr(\varphi) = tr(\forall x \forall y \chi) \wedge \bigwedge_k tr(\forall x \exists y (x \leq y \wedge \psi_k)).$$

One can show that the translation  $tr$  defined above preserves satisfiability. More precisely, it can be seen that:



- (1) Any data word whose data values are the  $x$ -projection from a model satisfying  $tr(\varphi)$ , satisfies  $\varphi$ ; and, conversely,
- (2) for any data word satisfying  $\varphi$  with a given assignment for  $X_1, \dots, X_n$  to the word positions, and for any model  $\sigma$  such that
  - $\sigma$  has the same length as the data word,
  - for every position  $i$ ,  $\sigma(i)(x)$  is the data value of position  $i$  from the data word, and  $\sigma(i)(x_0) = \sigma(i)(x_j)$  iff  $X_j$  holds at position  $i$  of the data word, and
  - for every position  $i > 0$ ,  $\sigma(i)(x_{prev}) = \sigma(i-1)(x)$ ,
we have that  $\sigma \models tr(\varphi)$ .

By Corollary 10 we can decide the satisfiability of the translation in 2EXPSpace, and since the translation is exponential, this gives us a 3EXPSpace upper bound for the satisfiability of *forward-EMSO*<sup>2</sup>(+1, <, ~). ■

**Remark 37.** The proof above can be also extended to work with a similar fragment of EMSO<sup>2</sup>(+1, ..., +k, <, ~), that is, EMSO<sup>2</sup>(+1, <, ~) extended with all binary relations of the kind  $+i(x, y)$  for every  $i \leq k$ , with the semantics that  $x$  is  $i$  positions after  $y$ . Hence, we also obtain the decidability of the satisfiability problem for this logic in 3EXPSpace. We do not know if the upper bounds we give for *forward-EMSO*<sup>2</sup>(+1, <, ~) and *forward-EMSO*<sup>2</sup>(+1, ..., +k, <, ~) can be improved.

Our result stating that PLRV<sub>1</sub> is equivalent to reachability in VASS (Corollary 22), can also be seen as a hardness result for FO<sup>2</sup>(<, ~, {+k}<sub>k∈ℕ</sub>), that is, first-order logic with two variables on data words, extended with all binary relations  $+k(x, y)$  denoting that two elements are at distance  $k$ . It is easy to see that this logic captures PLRV<sub>1</sub> and hence that it is equivalent to reachability in VASS, even in the absence of an alphabet.

**Corollary 38.** The satisfiability problem for FO<sup>2</sup>(<, ~, {+k}<sub>k∈ℕ</sub>) is as hard as the reachability problem in VASS, even when restricted to having an alphabet  $\Sigma = \emptyset$ .

## 2) Temporal Logics:

Consider a temporal logic with future operators  $F_=$  and  $F_{\neq}$ , so that  $F_= \varphi$  (resp.  $F_{\neq} \varphi$ ) holds at some position  $i$  of the finite data word if there is some future position  $j > i$  where  $\varphi$  is true, and the data values of positions  $i$  and  $j$  are equal (resp. distinct). We also count with “next” operators  $X_{=}^k$  and  $X_{\neq}^k$  for any  $k \in \mathbb{N}$ , where  $X_{=}^k \varphi$  (resp.  $X_{\neq}^k \varphi$ ) holds at position  $i$  if  $\varphi$  holds at position  $i + k$ , and the data values of position  $i + k$  and  $i$  are equal (resp. distinct). Finally, the logic also features a standard until operator  $U$ , tests for the labels of positions, and it is closed under Boolean operators. We call this logic LTL( $U, F_=, F_{\neq}, \{X_{=}^k, X_{\neq}^k\}_{k \in \mathbb{N}}$ ). There is an efficient satisfiability-preserving translation from LTL( $U, F_=, F_{\neq}, \{X_{=}^k, X_{\neq}^k\}_{k \in \mathbb{N}}$ ) into LRV and back and hence we have the following.

**Proposition 39.** The satisfiability problem for LTL( $U, F_=, F_{\neq}, \{X_{=}^k, X_{\neq}^k\}_{k \in \mathbb{N}}$ ) is 2EXPSpace-complete.

*Proof sketch:* There is a straightforward polynomial-time translation into LRV that preserves satisfiability, where  $F_= \varphi$  is translated as  $x \approx \langle \varphi' ? \rangle x$  and  $F_{\neq} \varphi$  is translated as  $x \not\approx \langle \varphi' ? \rangle x$ ;  $X_{=}^k \varphi$  as  $X^k \varphi' \wedge x \approx X^k x$ ; and any test for label  $a_i$  by  $x_0 \approx x_i$ . ■

In fact, LTL( $U, F_=, F_{\neq}, \{X_{=}^k, X_{\neq}^k\}_{k \in \mathbb{N}}$ ) corresponds to a fragment of the linear-time temporal logic LTL extended with one register for storing and comparing data values. We denote it by LTL<sub>1</sub><sup>↓</sup>, and it was studied in [7]. This logic contains one operator to store the current datum, one operator to test the whether the current datum is equal to the one stored. The *freeze* operator  $\downarrow \varphi$  permits to *store* the current datum in the register and continue the evaluation of the formula  $\varphi$ . The operator  $\uparrow$  *tests* whether the current data value is equal to the one stored in the register. When the temporal operators are limited to  $F, U$  and  $X$ , this logic is decidable with non-primitive-recursive complexity [7].

Indeed LTL( $U, F_=, F_{\neq}, \{X_{=}^k, X_{\neq}^k\}_{k \in \mathbb{N}}$ ) is the fragment where we only allow  $\downarrow$  and  $\uparrow$  to appear in the form of  $\downarrow F(\uparrow \wedge \varphi)$  and  $\downarrow X^k(\uparrow \wedge \varphi)$  —or with  $\neg \uparrow$  instead of  $\uparrow$ . Markedly, this restriction allows us to jump from a non-primitive-recursive complexity of the satisfiability problem, to an elementary 2EXPSpace complexity.