



Self-consistent physical modeling of set/reset operations in unipolar resistive-switching memories

Marc Bocquet, Damien Deleruyelle, Christophe Muller, Jean-Michel Portal

► To cite this version:

Marc Bocquet, Damien Deleruyelle, Christophe Muller, Jean-Michel Portal. Self-consistent physical modeling of set/reset operations in unipolar resistive-switching memories. Applied Physics Letters, 2011, 98 (26), 10.1063/1.3605591 . hal-01779321

HAL Id: hal-01779321

<https://hal.science/hal-01779321>

Submitted on 26 Apr 2018

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Self-consistent physical modeling of set/reset operations in unipolar resistive-switching memories

Marc Bocquet,^{1, a)} Damien Deleruyelle,¹ Christophe Muller,¹ and Jean-Michel Portal¹
 IM2NP, UMR CNRS 6242, Aix-Marseille Université, 38 rue Joliot Curie, 13451 Marseille Cedex 20,
 France

(Dated: 4 May 2012)

This letter deals with a self-consistent physical model for *set/reset* operations involved in unipolar resistive switching memories integrating a transition metal oxide. In this model, *set* operation is described in terms of a local electrochemical reduction of the oxide leading to the formation of metallic conductive filaments. Beside, *reset* operation relies on the thermally-assisted destruction of the formed metallic filaments by Joule heating effect. An excellent agreement is demonstrated with numerous published experimental data suggesting that this model can be confidently implemented into circuit simulators for design purpose.

Memory devices based on resistive switching materials are currently pointed out as promising candidates to replace conventional non-volatile memory devices based on charge-storage beyond 2xnm-technological nodes.¹ In particular, devices integrating a transition metal oxide (so-called TMO) such as NiO, TiO₂, ZnO or Cu_xO, are of growing interest due to their simple Metal/Insulator/Metal (MIM) structure, oxides compatible with complementary metal-oxide-semiconductor technology and low process temperature.² So far, in TMO-based memory devices, the unipolar switching between low resistance state (LRS) and high resistance state (HRS) is explained in terms of creation/destruction of conductive filaments within the oxide.^{3,4} Waser *et al.*⁵ explained that *set*, *i.e.* the transition from HRS to LRS, originates from a local reduction reaction leading to the creation of metallic conductive filaments (CF). During *reset*, local dissipation of Joule power enhances the thermally activated diffusion of defects and/or of different atomic species constituting the CF combined with a local oxidation process.^{6,7} Based on this phenomenological description, several models for *reset* were reported in Refs. 8–10 but very few offer a model for *set*.¹¹ Furthermore, it has to be stressed that there is currently no complete model taking into account both *set* and *reset* operations that could be easily implemented in circuit simulators for design purpose.

In this context, this paper proposes a self-consistent physical model accounting for both *set/reset* operations in NiO-based unipolar resistive switching devices. After uncovering the theoretical background and the set of relevant physical parameters, the model is confronted to quasi-static and dynamic experimental data from literature.

FIG. 1 schematically describes the *set/reset* mechanisms controlling the resistive switching phenomenon. *Set* (*resp.* *reset*) is assumed to be due to the creation (*resp.* destruction) of CF between two electrochemically inert electrodes. Starting from LRS, the existing CF may

be heated by Joule effect thanks to the application of an electric field ξ , as reported in Ref. 12. CF top and bottom boundaries are kept at thermal equilibrium with connected electrodes at room temperature (T_{amb}) whereas the sidewalls are expected to exchange heat flow with the surrounding oxide. Thus, in one dimension geometry the heat equation can be simply written as:

$$\sigma_{CF}(x) \cdot \xi(x)^2 = -K_{thCF} \frac{\partial^2 T_{CF}(x)}{\partial x^2} + h \frac{T_{CF}(x) - T_{Ox}}{t_{Ox}} \quad (1)$$

where σ_{CF} is the CF conductivity, $T_{CF}(x)$ the CF temperature, T_{Ox} the oxide temperature, K_{thCF} the CF thermal conductivity, t_{Ox} the oxide thickness and h the heat transfer coefficient between CF and surrounding oxide.

In LRS CF is assumed to be metallic with a high conductivity σ_{CF} . In contrast, when CF is dissolved, the resistance of MIM structure is controlled by the resulting oxide gap separating the two CF fragments (*cf.* FIG. 1). As a consequence, a lower conductivity noted σ_{Ox} accounts for the electronic transport in HRS. Assuming the current conservation through the whole structure, an analytical expression of the electric field ξ can be derived ($\nabla J = 0$):

$$\xi(x) = I_0 \cdot R(x) \quad (2)$$

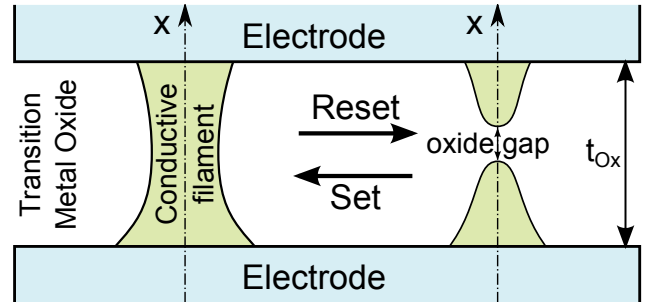


FIG. 1. Schematic representation of a formed and dissolved conductive filament resulting from *set* and *reset* operations respectively in MIM structure. During *reset*, the inner temperature is raised by Joule effect and leads to the destruction of the CF. In contrast, during *set* a electrochemical local reduction process enables restoring CF.

^{a)} Electronic mail: marc.bocquet@im2np.fr

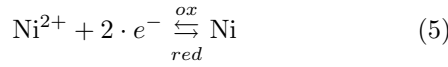
where the resistance per unit length $R(x)$ is

$$R(x) = \frac{1}{r_{CF}^2(x)\pi(\sigma_{CF}(x) - \sigma_{Ox}) + r_{CF_{max}}^2\pi\sigma_{Ox}} \quad (3)$$

$$I_0 = \frac{V_{Cell}}{\int_0^{t_{Ox}} R(x)dx} \quad (4)$$

I_0 is the ohmic current in the MIM structure, V_{Cell} the applied voltage, $r_{CF}(x)$ and $r_{CF_{max}}$ denote respectively the radius and the maximal radius of the CF in cylindrical geometry.

Two distinct mechanisms were considered to describe the creation and the destruction of CF: a local redox process^{5,13} and a thermally-assisted diffusion⁸. One can consider a classical redox equations such as:



in which the reaction velocities for both reduction and oxidation processes may be expressed classically by the Butler-Volmer equation:¹⁴

$$\nu_{red} = k_0 e^{-\frac{\Delta r G_0 + 2(1-\alpha)F(E - E_{eq})}{R \cdot T_{ox}}} (1 - C_{Ni}) \quad (6)$$

$$\nu_{ox} = k_0 e^{-\frac{\Delta r G_0 - 2\alpha F(E - E_{eq})}{R \cdot T_{CF}(x)}} C_{Ni} \quad (7)$$

α is the asymmetry factor, k_0 kinetics constant of electrochemical reaction, R is the ideal gas constant, F is the Faraday constant, $\Delta r G_0$ is the free energy of the reaction at equilibrium and finally E_{eq} is the equilibrium potential. Moreover C_{Ni} denotes the dimensionless concentration of metallic species. To determine the potential of the reduced and oxidized species such as Ni and Ni^{2+} respectively, the mechanism is supposed to be isotropic and the redox potential E is assumed to be equal to $-|V_{Cell}|$.

Finally, in order to properly account for the metallic nature of the CF, its conductivity relies on a temperature coefficient α_T as described by Russo *et al.*¹² where σ_{CF_0} is the CF conductivity at room temperature (T_{amb}):

$$\sigma_{CF}(x) = \frac{\sigma_{CF_0}}{1 + \alpha_T \cdot (T_{CF}(x) - T_{amb})} \quad (8)$$

As already proposed by Russo *et al.*⁸, the thermal rupture of CF in *reset* operation can be described by a local diffusion velocity (ν_{diff}) of the metallic species:

$$\nu_{diff} = k_{diff} \cdot e^{-\frac{E_a}{k_b \cdot T_{CF}(x)}} \cdot C_{Ni} \quad (9)$$

where E_a is the activation energy governing the thermally-assisted exodiffusion of metallic species.

Regarding Eq.5, we assumed that electron density available during *set* could not limit the reduction process. Indeed, during *set* the growth of the CF is initiated

TABLE I. Physical parameters used for simulations

$k_{diff} = 5 \times 10^{10} \text{ s}^{-1}$	$E_a = 1.9 \text{ eV}$
$k_0 = 10^{12} \text{ s}^{-1}$	$r_{CF_{max}} = 60 \text{ nm}$
$\alpha_T = 1.1 \times 10^{-3} \text{ K}^{-1}$	$h = 100 \text{ W}/(\text{K} \cdot \text{m}^2)$
$T_{Ox} = T_{amb}$	$K_{th} = 91 \text{ W}/(\text{K} \cdot \text{m})$ ref.7
$\alpha = 0.5$	$\sigma_{Ox} = 12.5 \text{ k}\Omega^{-1} \text{ cm}^{-1}$ ref.8
$\Delta r G_0 - 2(1-\alpha)F \cdot E_{eq} = 222 \text{ kJ} \cdot \text{mol}^{-1}$	
$\sigma_{CF_0} = 6.67 \text{ m}\Omega^{-1} \text{ cm}^{-1}$ ref.8, 9, and 16	
$t_{Ox} = 160 \text{ nm}$ for ref.11 or $t_{Ox} = 30 \text{ nm}$ for ref.15	

at the cathode side. As the CF grows toward the anode, it is assumed to act as "virtual cathode"⁵ allowing electrons to flow freely from the real cathode toward the reduction site.

Finally, the description of *set* and *reset* mechanisms enables proposing a self-consistent master equation relying on a chemical kinetics:

$$\frac{dC_{Ni}}{dt} = \nu_{red} - \nu_{ox} - \nu_{diff} \quad (10)$$

CF radius ($r_{CF} = r_{CF_{max}} \times C_{Ni}$) is calculated from the electric field (Eq. 2), the CF temperature (Eq. 1), the diffusion and reaction velocities (Eqs. 6, 7&9,) and finally from the concentration of metallic species (Eq. 10). It is has to be mentioned that considering energies involved in Eq. 7&9, the CF destruction is mainly governed by the diffusion process rather than oxidation.

To validate the proposed theoretical approach, the model was confronted to quasi-static and dynamic experimental data from the literature. FIG. 2(a) and 2(b) show quasi-static *set* and *reset* $I(V)$ characteristics mea-

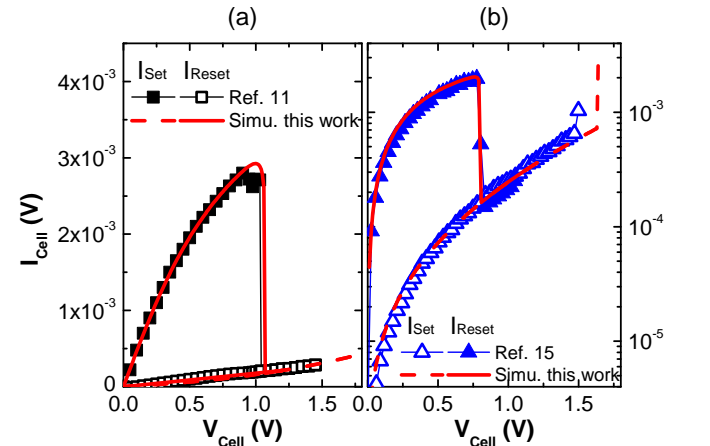


FIG. 2. Experimental $I(V)$ characteristics measured on a NiO-based memory structure presented in (a) ref.11 and (b) ref.15 and the corresponding simulation by using the present model and the physical parameters given in Table I. The CF profile obtained after the *set* operation is used as the initial state to simulate the subsequent *reset* operation.

sured on NiO-based memory elements by Cagli *et al.*¹¹ and Lee *et al.*¹⁵ In both studies, memory elements consisted in Pt/NiO/Pt stack with respective nickel oxide thicknesses of 160 nm¹¹ and 30 nm.¹⁵ Using the set of physical parameters given in Table I together with the actual nickel oxide thickness, the present model shows an excellent agreement with experimental data for both *set* and *reset* operations. Moreover, it must be underlined that the model is used continuously since it relies on a self-consistent kinetics equation accounting for both CF creation and destruction mechanisms. This numerical feature is a key point for a model dedicated to an implementation in computer-aided design tools. From a practical point of view, the CF profile obtained after the *set* operation is used as the initial state to simulate the subsequent *reset* operation.

FIG. 3(a&b) shows that the proposed model satisfactorily matches the experimental increase of *set* and *reset* voltages along with the programming ramp speed reported by Cagli *et al.*¹¹ The increase of *reset* voltage may be interpreted in terms of competition between creation and destruction mechanisms. Indeed, as ramp speed increases, a larger current (*i.e.* a larger applied voltage) is necessary to achieve *reset* in a shorter time. Through Eq. 6, a larger applied voltage implies a strengthening of the reduction process that limits the filament destruction. Finally, the turn back of the structure toward HRS is achieved thanks to an increase of V_{reset} . Beside, the closing of the gap between V_{set} and V_{reset} at highest sweep rates may lead to some *reset/set* instabilities as proposed in Ref. 17.

As reported in numerous works, the resistance in LRS (noted R_{LRS}) and *reset* current strongly depend on the maximum current reached during the preceding *set* operation^{18–21} (referred as I_{SetMax}). This feature can be understood in terms of reduction of CF radius that concomitantly increases the resistance of the

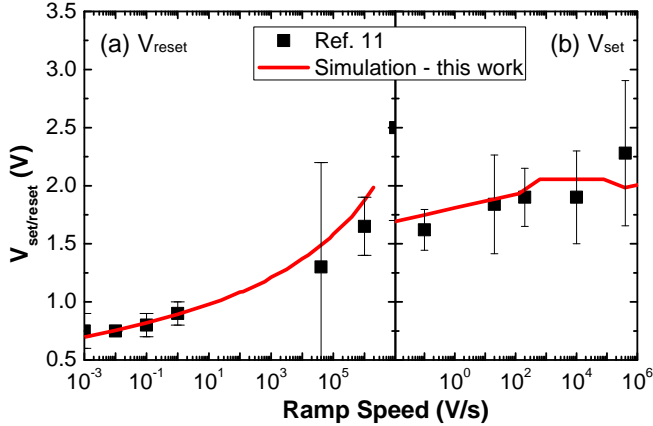


FIG. 3. (a) *Set* and (b) *reset* voltages as a function of the programming ramp speed based on the experimental results reported in Ref. 11. The calculated curves were obtained for the physical parameters given in Table I.

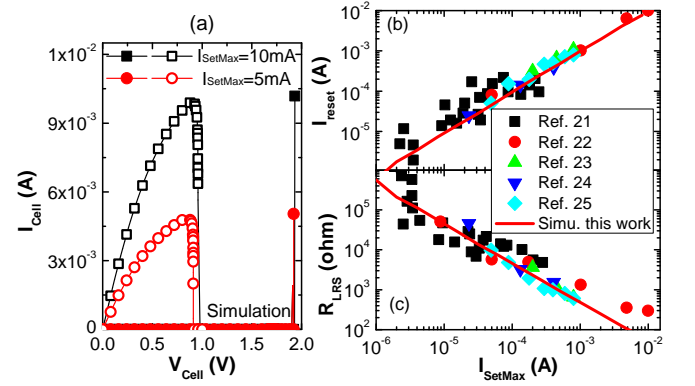


FIG. 4. (a) Simulation of *set* and *reset* $I(V)$ characteristics for $I_{SetMax} = 5$ mA and 10 mA. (b) Maximum current during the *reset* operation (I_{Reset}) and (c) the resistance in LRS as a function of the maximum current during the preceding *set* operation (I_{SetMax}). Experimental data were extracted from Ref. 21–25. The parameters of the Table I were used for simulations.

MIM structure.²¹ FIG. 4(a) reports the simulated *set* and *reset* $I(V)$ characteristics for two different maximum currents I_{SetMax} . As expected, a decrease of the *reset* current is observed together with an increase of R_{LRS} . FIG. 4(b) and 4(c) show the experimental evolutions of I_{reset} and R_{LRS} as a function of the maximum set current I_{SetMax} .²¹ The proposed model matches well the experimental data obtained by various authors and confirms the scalability of the *reset* current I_{reset} in NiO-based resistive memories. The *reset* current may be scaled down by limiting the maximum *set* current through an integrated select device (*e.g.* transistor or load resistor) in series with memory element. Finally, it has to be mentioned that the present model may also be used to simulate retention properties. Nevertheless, a complete set of experimental retention data is required to calibrate the model. This work is under way.

In conclusion, this letter deals with a self-consistent physical model well suited for describing simultaneously *set* and *reset* operations in unipolar resistive switching memories based on transition metal oxide. By gathering local electrochemical reactions and a thermal diffusion mechanism in a single master equation, the model enables accounting for both creation and destruction of conductive filaments. The simulation results satisfactorily match numerous quasi-static and dynamic experimental data published in literature on resistive switching devices. Beside, the model can be easily implemented in circuit simulators and enables predicting relevant trends required for designing innovative architectures such as memory matrix, distributed memories or Field-Programmable Gate Array.

Authors would like to thank Prof. Daniele Ielmini (Politecnico di Milano), Dr. Robert Müller (IMEC) and Dr. Renaud Bouchet (LCP) for fruitful discussions.

- ¹S. Hong, in *Proc. IEEE Int. Electron Devices Meeting (IEDM)* (2010) pp. 292–295.
- ²Y. S. Chen, H. Y. Lee, P. S. Chen, P. Y. Gu, C. W. Chen, W. P. Lin, W. H. Liu, Y. Y. Hsu, S. S. Sheu, P. C. Chiang, W. S. Chen, F. T. Chen, C. H. Lien, and M.-J. Tsai, in *Proc. IEEE Int. Electron Devices Meeting (IEDM)* (2009) pp. 1–4.
- ³G.-S. Park, X.-S. Li, D.-C. Kim, R.-J. Jung, M.-J. Lee, and S. Seo, *Appl. Phys. Lett.* **91**, 222103 (2007).
- ⁴D. Deleruyelle, C. Dumas, M. Carmona, C. Muller, S. Spiga, and M. Fanciulli, *Applied Physics Express* **4**, 051101 (2011).
- ⁵R. Waser, R. Dittmann, G. Staikov, and K. Szot, *Advanced Materials* **21**, 2632 (2009).
- ⁶U. Russo, D. Ielmini, C. Cagli, and A. L. Lacaita, *Transaction on Electron Devices* **56**, 186 (2009).
- ⁷S. H. Chang, S. C. Chae, S. B. Lee, C. Liu, T. W. Noh, J. S. Lee, B. Kahng, J. H. Jang, M. Y. Kim, D.-W. Kim, and C. U. Jung, *Appl. Phys. Lett.* **92**, 183507 (2008).
- ⁸U. Russo, D. Ielmini, C. Cagli, and A. L. Lacaita, *Transaction on Electron Devices* **56**, 193 (2009).
- ⁹Y. Sato, K. Kinoshita, M. Aoki, and Y. Sugiyama, *Applied Physics Letters* **90**, 033503 (2007).
- ¹⁰S. B. Lee, S. C. Chae, S. H. Chang, and T. W. Noh, *Appl. Phys. Lett.* **94**, 173504 (2009).
- ¹¹C. Cagli, D. Ielmini, F. Nardi, and A. L. Lacaita, in *Proc. IEEE Int. Electron Devices Meeting IEDM 2008* (2008) pp. 1–4.
- ¹²U. Russo, D. Ielmini, C. Cagli, A. L. Lacaita, S. Spiga, C. Wiemer, M. Perego, and M. Fanciulli, in *Proc. IEEE Int. Electron Devices Meeting IEDM 2007* (2007) pp. 775–778.
- ¹³F. Pan and V. Subramanian, in *Proc. Int Simulation of Semiconductor Processes and Devices (SISPAD) Conf* (2010) pp. 19–22.
- ¹⁴A. J. Bard and L. R. Faulkner, *Electrochemical Methods: Fundamentals and Applications*, edited by J. Wiley and S. Inc. (New York, January 2001).
- ¹⁵M.-J. Lee, Y. Park, B.-S. Kang, S.-E. Ahn, C. Lee, K. Kim, W. Xianyu, G. Stefanovich, J.-H. Lee, S.-J. Chung, Y.-H. Kim, C.-S. Lee, J.-B. Park, and I.-K. Yoo, in *Proc. IEEE Int. Electron Devices Meeting IEDM 2007* (2007) pp. 771–774.
- ¹⁶T. Ohgai, L. Gravier, X. Hoffer, M. Lindeberg, K. Hjort, R. Spohr, and J.-P. Ansermet, *J. Phys. D Appl. Phys.* **36**, 3109 (2003).
- ¹⁷D. Ielmini, *Electron Device Letter* **31**, 552 (2010).
- ¹⁸D. C. Kim, S. Seo, S. E. Ahn, D.-S. Suh, M. J. Lee, B.-H. Park, I. K. Yoo, I. G. Baek, H.-J. Kim, E. K. Yim, J. E. Lee, S. O. Park, H. S. Kim, U.-I. Chung, J. T. Moon, and B. I. Ryu, *Appl. Phys. Lett.* **88**, 202102 (2006).
- ¹⁹K. Kinoshita, K. Tsunoda, Y. Sato, H. Noshiro, Y. Yamazaki, T. Fukano, S. Yagaki, M. Aoki, and Y. Sugiyama, in *Proc. 22nd IEEE Non-Volatile Semiconductor Memory Workshop* (2007) pp. 66–67.
- ²⁰S. B. Lee, S. C. Chae, S. H. Chang, J. S. Lee, S. Seo, B. Kahng, and T. W. Noh, *Appl. Phys. Lett.* **93**, 212105 (2008).
- ²¹F. Nardi, D. Ielmini, C. Cagli, S. Spiga, M. Fanciulli, L. Goux, and D. Wouters, *Solid-State Electronics* **58**, 42 (2011).
- ²²S. Seo, M. J. Lee, D. H. Seo, E. J. Jeoung, D.-S. Suh, Y. S. Joung, I. K. Yoo, I. R. Hwang, S. H. Kim, I. S. Byun, J.-S. Kim, J. S. Choi, and B. H. Park, *Appl. Phys. Lett.* **85**, 5655 (2004).
- ²³K. Kinoshita, K. Tsunoda, Y. Sato, H. Noshiro, S. Yagaki, M. Aoki, and Y. Sugiyama, *Appl. Phys. Lett.* **93**, 033506 (2008).
- ²⁴T.-N. Fang, S. Kaza, S. Haddad, A. Chen, Y.-C. Wu, Z. Lan, S. Avanzino, D. Liao, C. Gopalan, S. Choi, S. Mahdavi, M. Buynoski, Y. Lin, C. Marrian, C. Bill, M. VanBuskirk, and M. Taguchi, in *Proc. Int. Electron Devices Meeting IEDM '06* (2006) pp. 1–4.
- ²⁵K. Tsunoda, K. Kinoshita, H. Noshiro, Y. Yamazaki, T. Iizuka, Y. Ito, A. Takahashi, A. Okano, Y. Sato, T. Fukano, M. Aoki, and Y. Sugiyama, in *Proc. IEEE Int. Electron Devices Meeting IEDM 2007* (2007) pp. 767–770.