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Oxide based Resistive RAM: ON/OFF Resistance Analysis versus Circuit Variability

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Abstract— A deeper understanding of the impact of variability on Oxide-based Resistive Random Access Memory (so-called OxRRAM) is needed to propose variability tolerant designs to ensure the robustness of the technology. Although research has taken steps to resolve this issue, variability remains an important characteristic for OxRRAMs. In this paper, impact of variability on OxRRAM circuit performances is analysed quantitatively at a circuit level through electrical simulations. Variability is introduced at the memory cell level but also at the peripheral circuitry level. The aim of this study is to determine the contribution of each component of an OxRRAM circuit on the ON/OFF resistance ratio.

Keywords—component; Resistive RAM; Oxide-based RAM (OxRRAM), variability

I. INTRODUCTION

A typical Resistive RAM (RRAM) device consists of two metallic electrodes that sandwich a thin dielectric layer serving as the ion transport and storage medium. The exact mechanism differs significantly among the different materials being used, but the common link among all RRAM devices is an electric field or heat causes ionic movements and local structural changes in the storage medium, which in turn causes a measurable change of the device resistance [1]. In RRAM, the data is stored as two or multiple resistance states of the resistive switching device and relies on a Metal/Insulator/Metal (MIM) stack that can be easily integrated into the Back-End Of Line (BEOL), paving the way for 3D technology.

Resistive switching in transition metal oxides was discovered in thin NiO films decades ago [2]. Since then, a large number of materials showing a resistive switching have been reported in the literature [3-4]. Among them, metal oxides as HfO₂, NiO, TiO₂ or Cu₂O are promising candidates due to their compatibility with CMOS processes. Oxide-based RAM elements (OxRRAMs) present a lot of interesting features like high integration density, high-speed operations (write/erase/read) and satisfactory reliability performances in terms of retention and cycling [5]. In OxRRAM memories, MIM structure are used as a single-bit memory cell, while the semiconductor transistor subsystem enables performing all the peripheral operations (input/output, coding/decoding, line driving, and sense amplification) [6].

Although OxRRAM-based devices have shown encouraging properties, challenges remain, among which the device variability (or reproducibility) is the main. Indeed, the variance from switching cycle to cycle and from device to device can be very large. For example, it has been experimentally demonstrated that such variability is in the range 2×-10× for the ON resistance and 5×-100× for the OFF resistance in Cu₂-O-based devices [7]. Besides, MOSFET mismatch in the transistor subsystem (digital and analog blocks) increases inherent variability of OxRRAM technology [8-9]. On top of that, sensitivity to Single Event Effects (SEEs) caused by a single energetic particle is also investigated [10-11].

In the literature, unipolar, bipolar and non-polar electrical behaviors are reported [1]. In the case of bipolar switching, addressed in this paper, bipolar voltage sweeps are required to switch the memory element (Fig. 1). In working conditions, after an initial electroforming process, the OxRRAM element may be reversibly switched between RESET (high resistance) and SET states (low resistance). Electroforming stage corresponds to a voltage-induced resistance switching from an initial very high resistance state (virgin state) to a conductive state. In Fig. 1, a typical I-V characteristic of a bipolar OxRRAM cell is presented. Resistive switching corresponds to an abrupt change between a High Resistance State (HRS or OFF state) and a Low Resistance State (LRS or ON state). This resistance change is achieved by applying specific voltage to the structure (i.e. V_{SET} and V_{RES}).

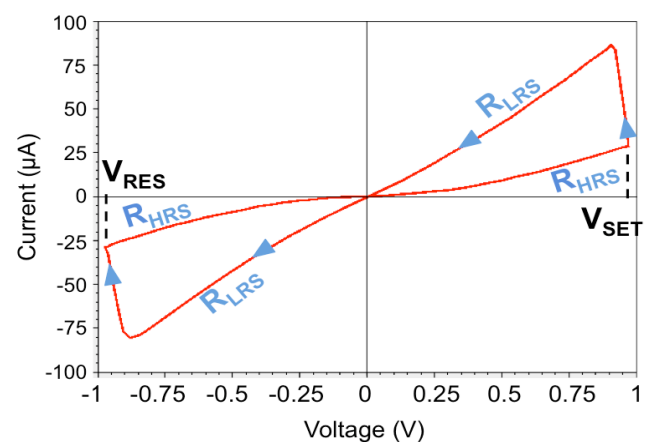


Fig.1 Typical I-V characteristic of a bipolar OxRRAM cell

Based on the memory cell hysteresis presented in Fig. 1, in a first approximation, four OxRRAM critical reliability parameters can be considered: V_{SET} , V_{RES} , R_{HRS} and R_{LRS} . Indeed, any variations of any of these parameters, among the cells in a memory array may lead to a limitation in the overall performance of the memory.

As a result of cell variability impact on OxRRAM cell parameters, circuit performance exhibits much wider variability [12-13]. This inherent drawback of the technology has to be investigated at a circuit level.

In this context, the presented study proposes an evaluation of the impact of variability on the ON/OFF resistance ratio. Section II presents the simulation circuit while section III analyses the contribution of each component of an OxRRAM circuit on the ON/OFF resistance ratio. Section IV concludes the paper.

II. SIMULATION CIRCUIT

A. OxRRAM model

The proposed OxRRAM modeling approach relies on electric field-induced creation/destruction of oxygen vacancies within the switching layer. The model enables continuously accounting for both SET and RESET operations into a single master equation in which the resistance is controlled by the radius of the conduction pathways (r_{CF}) [14]:

$$\frac{dr_{CF}}{dt} = (r_{CFmax} - r_{CF}) \cdot 10^{\beta_{redOx}} \cdot e^{-\frac{E_a - q\alpha_{red}V_{cell}}{k_b T}} - r_{CF} \cdot 10^{\beta_{redOx}} \cdot e^{-\frac{E_a + q\alpha_{ox}V_{cell}}{k_b T}} \quad (1)$$

where β_{redOx} is the nominal oxide reduction rate, E_a is the activation energy, α_{red} and α_{ox} are the transfer coefficients (ranging between 0 and 1), k_b is the Boltzmann constant, T is the temperature and V_{cell} the voltage across the cell.

Moreover, the model makes assumptions of a uniform radius of the conduction pathways, a uniform electric field in the cell and temperature triggered acceleration of the oxide reduction reactions (“redox”). Finally, the total current in the OxRRAM includes two components, i.e. one is related to the conductive species (I_{FC}) and the other to the conduction through the oxide (I_{OX}).

$$I_{CF} = \frac{V_{Cell}}{L_x} \cdot (\pi \cdot r_{CF}^2 \cdot (\sigma_{CF} - \sigma_{OX}) + \pi \cdot r_{CFmax}^2 \cdot \sigma_{OX}) \quad (2)$$

$$I_{OX} = A_{HRS} \cdot S_{Cell} \left(\frac{V_{Cell}}{L_x} \right)^{\beta_{HRS}} \quad (3)$$

where L_x is the oxide thickness, S_{Cell} is the total area of the device, σ_{OX} the oxidation rate and σ_{CF} the reduction rate. To take into account I_{OX} trap assisted current (Poole-Frenkel, Schottky emission, Space Charge Limited Current (SCLC)), a power law between the cell current and the applied bias is considered with two parameters A_{HRS} and β_{HRS} . Finally, the total current flowing through the cell is:

$$I_{Cell} = I_{CF} + I_{OX} \quad (4)$$

I_{CF} is the main contributor to LRS current (I_{LRS}) and I_{OX} is the main contributor to HRS current (I_{HRS}).

The memory cell compact model is calibrated on silicon. The model was confronted to quasi-static and dynamic experimental data before its implementation in electrical circuit simulators. As presented in Fig. 2a, after calibration, the model satisfactorily matches quasi-static and dynamic experimental data measured on actual HfO_2 -based memory elements (TiN/Ti/ HfO_x /TiN stack reported in [15]). To program a bipolar OxRRAM cell, a voltage ramp is needed. In Fig. 2b, the evolution of SET voltages as a function of the ramp speed is presented. The model implementation focused on this dependence which is crucial for the model to be confidently implemented in circuit simulators.

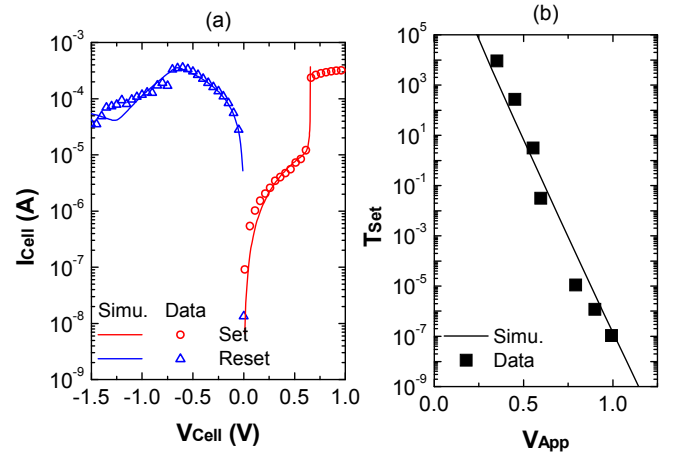


Fig.2 (a) I-V characteristic measured on HfO_2 -based devices [15] and corresponding simulation using a bipolar OxRRAM physical model. (b) Set voltage as a function of the programming ramp

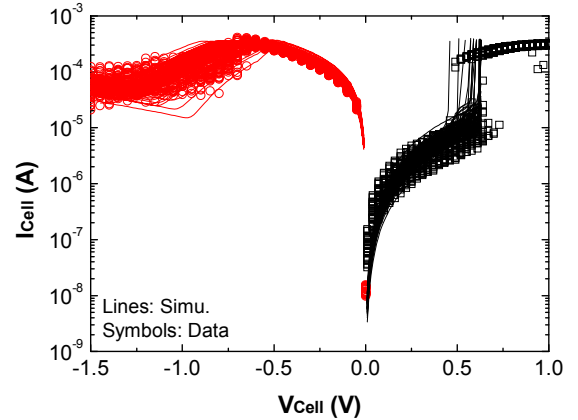


Fig.3 Measured and corresponding simulated I-V characteristic obtained from TiN/Ti/ HfO_2 /TiN devices showing strong variation on R_{LHS} and R_{HRS}

Due to the stochastic nature of the switching process in OxRRAMs, leading to large variability, the OxRRAM model features a variability dependency.

The variation is chosen to fit experimental data as presented in Fig. 3 (I-V characteristic in logarithmic scale). The model

behavior (lines) is consistent with experimental data (symbols). HRS variation is generally larger than LRS variation for this technology. The manufacturing process of a TiN/Hf/HfO₂/TiN stack is presented in [16]. Statistical variations in the switching parameter metrics such as V_{SET} , V_{RESET} , R_{OFF} and R_{ON} have been examined in-depth in the recent past. The origin (“cause”) of all these variations in resistive switching memory are the microscopic transport of oxygen ions/vacancies [17] and the different permutations of filamentary clusters [18] they form during different SET-RESET cycles.

The cell variability modeling is based on OxRRAM card model parameters variation. The variation is chosen to feet experimental data. Variability is introduced through specific model parameters (*i.e.* β_{RedOx} and β_{HRS} parameters presented respectively in Equ. 1 and Equ. 3).

B. OxRRAM elementary circuit

Fig. 4 presents the elementary array used for simulation which is constituted by a 3×3 1T/1R cell matrix, a row decoder, a column decoder and a sense amplifier for the read operation. The 1T/1R memory cell (one MOS transistor in series with one resistor) is seen as an effective alternative to replace embedded DRAM-based memory modules by substituting the capacitor by an OxRRAM resistor. The memory cell is modeled by the OxRRAM cell model presented in section II.A.

The variability analysis is conducted through Monte Carlo simulations. Each Monte Carlo simulation targets specific elements of the circuit: the resistive element, the select transistor, the decoding blocs and the sense amplifier. As a result, R_{ON} and R_{OFF} distribution spreads are extracted. Thus, the contribution of each element in terms of memory performance degradation is demonstrated. The study is conducted using a 28-nm Fully Depleted Silicon-On-Insulator (FDSOI) technology [19].

Memory array cells are first placed in a virgin state. Then, the memory array programming is done in 2 cycles. First, all memory cells are set (logical “1”), then the memory array is reset (logical “0”). R_{ON} value is extracted after the SET operation and R_{OFF} after the RESET operation for each Monte Carlo run.

Variability introduced in the resistive element is chosen to feet experimental data (see Fig. 3) whereas variability of the transistor subsystem is given by the considered technology.

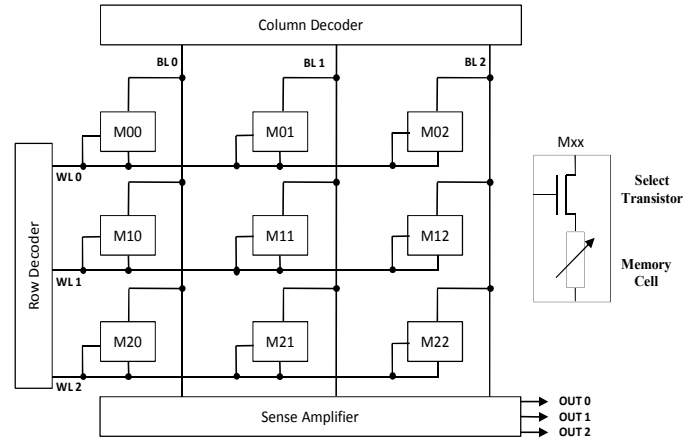


Fig.4 3×3 OxRRAM elementary memory array

III. RESULTS

In this study, 4 different configurations are considered: the “ALL” configuration considers variability in the memory cell (resistive element and select transistor) and the peripheral circuits. In the “CELL” configuration, variability affects only the resistive element. In the “SELECT” configuration, variability affects only the OxRRAM select transistor. In the “NO CELL” configuration, variability affects only the peripheral circuits (transistor subsystem, including the select transistor). In this study, the impact of the select transistor variability is subject to special attention. Indeed, the select transistor compliance allows the control of the maximum available current during the set transition, impacting directly the ON/OFF resistance. Table I summarizes the 4 configuration to simulate.

TABLE I. SIMULATED CONFIGURATIONS

Configuration	RUNS	Elements under variability
« ALL »	400	All circuit elements
« CELL »	400	Resistive element
« SELECT »	400	Select transistor
« NO CELL »	400	All elements excluding the resistive element

Fig 5 presents the impact of variability of the whole circuit elements (resistive element and transistor subsystem: “ALL” configuration) on R_{ON} and R_{OFF} distributions after 400 Monte Carlo simulations. Note that R_{OFF} distribution is much larger than R_{ON} distribution. In order to discriminate the contribution of each element of the circuit on ON/OFF resistances, a set of Monte Carlo simulations are performed with “CELL”, “SELECT” and “NO CELL” configurations.

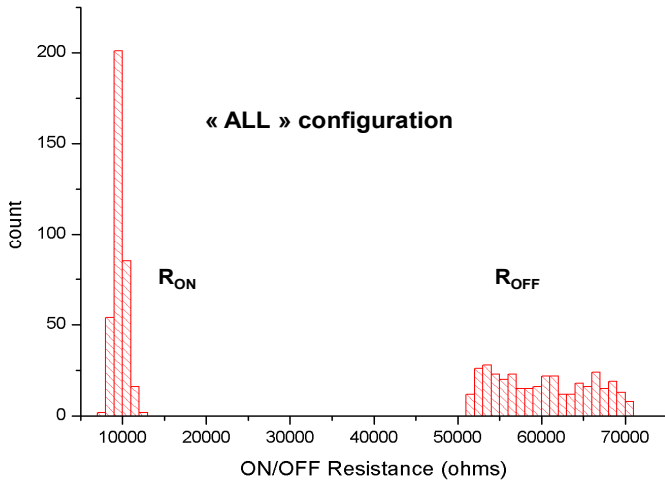


Fig.5 ON/OFF resistance distributions for the "ALL" configuration

Fig.6 presents the impact of variability on R_{OFF} distributions (after RESET) for the 4 configurations. By comparing configuration "ALL" with configuration "CELL", it appears clearly that the impact of cell variability is more important than the impact of the transistor subsystem variability. The third and fourth configurations ("NO CELL" and "SELECT") shows that impact of transistors variability on R_{OFF} is negligible. Besides, the contribution of the select transistor variability is dominant in the peripheral circuit elements. Table II proposes a synthesis of results obtained in Fig. 6 (mean values, standard deviation and standard deviation ratio, reported on maximum standard deviation value).

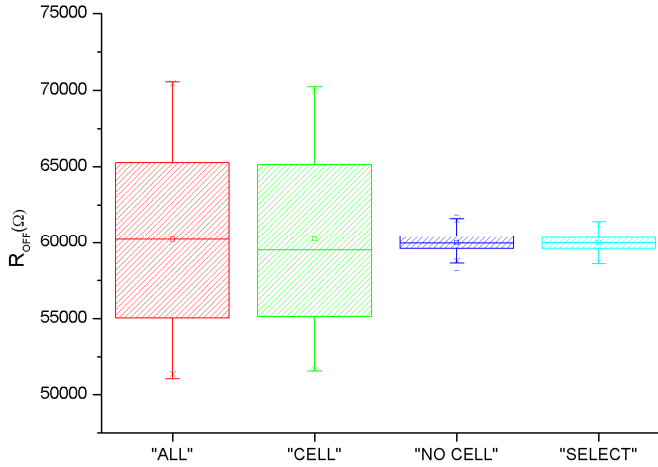


Fig. 6 R_{OFF} distributions

TABLE II. R_{OFF} DISTRIBUTION PARAMETERS

Configuration	Mean (Ω)	σ (Ω)	σ (%)
« ALL »	60200	5598	100
« CELL »	60240	5617	99
« NO CELL »	60004	598	9.6
« SELECT »	59990	536	10.6

Fig. 7 presents the impact of variability on R_{ON} distributions (after SET) for the 4 configurations.

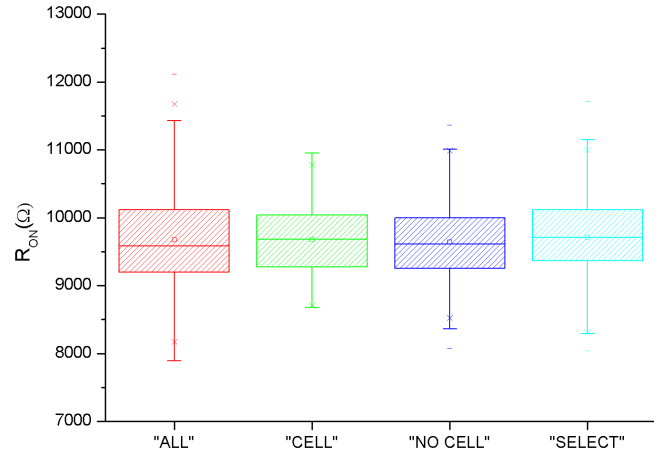


Fig. 7 R_{ON} distributions

TABLE III. R_{ON} DISTRIBUTION PARAMETERS

Configuration	Mean (Ω)	σ (Ω)	σ (%)
« ALL »	9674	724	100
« CELL »	9679	513	70.8
« NO CELL »	9642	550	78.7
« SELECT »	9713	570	75.9

Compared with Fig. 6, it appears clearly that the cell variability impact is less pronounced for low resistive states (LRS), which is a common feature of all OxRRAM technologies [20-23]. Moreover, the contribution of transistors variability is comparable to the contribution of the memory cell variability (see "CELL" and "NO CELL" configurations in Fig. 7). Once again, simulation results show that the impact of the select transistor is not negligible. Table III proposes a synthesis of results obtained in Fig. 7.

Fig.9 shows a comparison between R_{ON} and R_{OFF} distributions. Based on the considered technology, it is shown that resistance variability in RESET state is much more important than variability in the SET state. This variability is mainly due to the memory cell. In addition, the impact of the select transistor is non-negligible. This effect is visible in the SET state where the impact of the memory cell variability is much less important, making the select transistor variability critical.

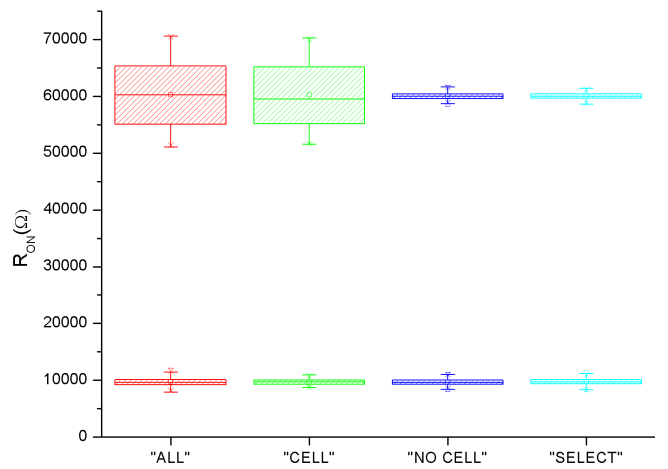


Fig. 8 R_{ON}/R_{OFF} distributions

IV. CONCLUSION

Variability in OxRRAM memories presents a major challenge for fabrication process and design engineers. In the proposed study, OxRRAM variability is evaluated based on an OxRRAM compact model included in an elementary memory array design. Simulation results show a significant impact of cell variability on the memory array ON/OFF resistances. Variability is introduced at the memory cell level but also at the peripheral circuitry level. The aim of this study is to determine the contribution of each component of an OxRRAM circuit on the ON/OFF resistance ratio. It has been demonstrated, based on a specific technology that variability in RESET state is mainly due to the memory cell. In SET state, impact of variability is less important, making the select transistor variability critical. This study underlines the need of variability tolerant designs to ensure the robustness of the technology.

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