



HAL
open science

Circuit-based synthesis of a reflectarray

Alexandre Grossetete, Erwan Fourn, Raphaël Gillard, Maria Garcia-Vigueras

► **To cite this version:**

Alexandre Grossetete, Erwan Fourn, Raphaël Gillard, Maria Garcia-Vigueras. Circuit-based synthesis of a reflectarray. IEEE International Symposium on Antennas and Propagation, Jul 2017, San Diego, United States. hal-01724170

HAL Id: hal-01724170

<https://hal.science/hal-01724170>

Submitted on 6 Mar 2018

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Circuit-based synthesis of a reflectarray

Alexandre Grossetete, Erwan Fourn, Raphael Gillard, Maria Garcia-Vigueras

Institut of Electronics and Telecommunications of Rennes (IETR)

Rennes, France

alexandre.grossetete@insa-rennes.fr

maria.garcia-vigueras@insa-rennes.fr

Abstract—This paper presents a new methodology for synthesizing reflector panel based on a fully analytical model. The present methodology is applied to the simple case of a two levels reflector panel consisting of metallic strips unit-cell. The model generates a maximal error of 3.39° on the reflection phase.

I. INTRODUCTION

The synthesis of a reflectarray consists in controlling the reflected phase on each unit cell to generate the desired phase law. For passive arrays, this is achieved by fixing the geometrical dimensions of each unit-cell [1].

The current methods used to design reflectarrays are mostly based on full-wave analysis. Databases with the electromagnetic parameters of a large number of unit-cell geometres are computed using full-wave simulations and then used to synthesize the given reflectarray. However these methods are time consuming especially for precise databases and complex cells.

The objective of this work is to provide a quick sizing method for all cells in order to reduce calculation time. The proposed solution consists in designing each unit-cell by using its associated equivalent circuit. This method has been recently used in designing reflectarrays antenna [2] based on the equivalent circuit model proposed in [3].

In this paper, we propose a fully analytical synthesis methodology based on a n efficient and physically insightful circuit model [4]. This new approach is applied to the canonic case of a planar reflector composed of metallic strips.

II. EQUIVALENT CIRCUIT BASIS

Fig. 1(a) presents the structure of the reflector unit-cell under consideration of $P_x \times P_y = 6 \times 6 \text{mm}^2$. The pattern considered is a metallic strip of width w . The unit-cell consists of two dielectric slabs of thickness and dielectric permittivity $h_1 = 598 \mu\text{m}$, $\epsilon_{r1} = 3.55$ and $h_2 = 7.9 \text{mm}$, $\epsilon_{r2} = 1$ respectively. The unit cell is illuminated under normal incidence by a TE (E_y field component) polarized impinging wave.

The equivalent circuit of the unit-cell is shown in Fig. 1(b). The strip is modeled by an inductance L_1 . Both dielectric slabs are represented by two transmission lines of lengths $h_{(1,2)}$, characteristic impedances $Z_{c(1,2)}$ and propagation constants $\beta_{(1,2)}$.

Equation (1) expresses the form of the admittance $Y_{L_1}^{Model}$ of the inductance L_1 as a function of the width w and the frequency of operation f_0 . As detailed in [4], A_n^{qs} represents

the amount of energy coupled to each of the infinite high-order harmonics n supported by the periodic structure. $Z_{in,n}^{qs}$ expresses the input impedance seen by each high-order harmonics.

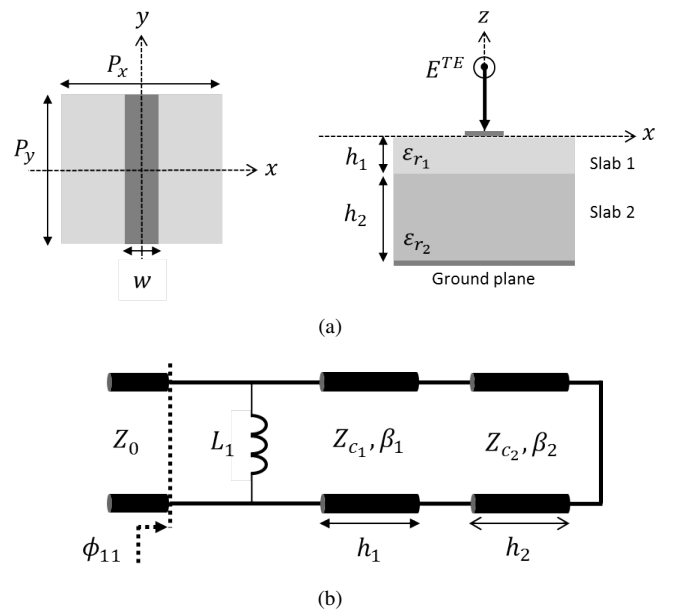


Fig. 1. (a) Unit cell. Top view (left). Cross-section view (right). (b) Associated equivalent circuit.

$$Y_{L_1}^{Model}(f_0, w) = 2 \sum_{n=1}^{\infty} A_n^{qs}(w) Z_{in,n}^{qs}(f_0) \quad (1)$$

III. SYNTHESIS OF THE REFLECTOR

A. Structure of the reflector

Note that, for the sake of simplicity, the structure is supposed to be illuminated by a plane wave under normal incidence. Then, we will call it a reflector instead of reflectarray. We only consider steering in the x_0z -plane, which means the cells along one y -column are all identical and form a continuous strip. Fig. 2 presents the structure of the proposed reflector. This canonical case has the advantage to validate the proposed synthesis methodology. The structure consists of two panels shifted along the z -axis. Indeed, as the pattern is purely inductive and according to the arrangement of the dielectric slabs considered, the phase range available at $f_0 = 12.5 \text{GHz}$

is only of 180° . In order to increase the phase range, a solution consists in putting a second panel shifted of $h_0 = \lambda_0/4$ from the reference plan. A complete phase range of 360° is then obtained.

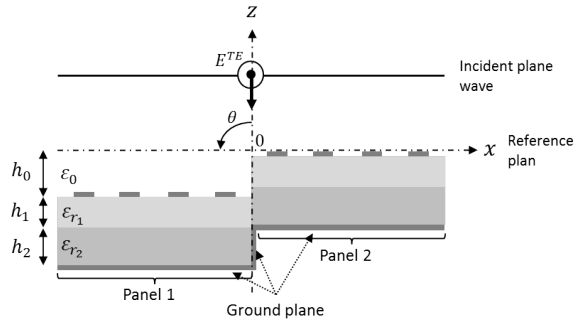


Fig. 2. Design of the reflector.

B. Synthesis of the reflector

The specifications on the radiation pattern are the following. The maximum of the main beam has to appear at an angle of $\theta = -10^\circ$ with a half-power beamwidth (HPBW) of 10° at $f_0 = 12.5\text{GHz}$. In order to do this, a linear phase step of 15.62° has to be applied on the 20 cells of the reflector (10 cells per panel). The first panel has to cover a phase range from -156.27° to -15.62° and the second one from 0° to 140.65° .

These phase specifications are then converted into an admittance specifications on $Y_{L_1}^{Spec}$ (cf. Fig. 3(a)). Each width w is then deduced by equalizing the admittance $Y_{L_1}^{Model}$, using (1), with $Y_{L_1}^{Spec}$ (cf. Fig. 3(b)). The maximal error generated by the model on the reflection phase is 3.39° (cf. Fig.3(c)).

IV. RESULTS

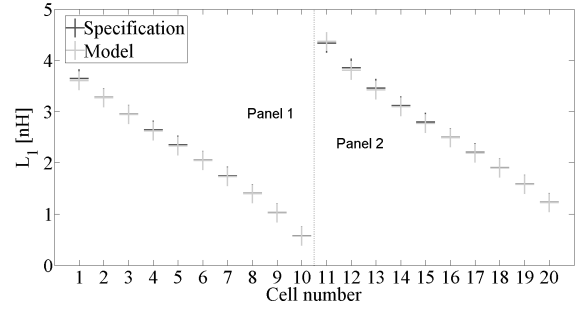
Fig.4 presents the normalized radiation pattern of the reflector simulated under HFSSTM. The full-wave results are in good agreement with the specifications. Indeed, the main lobe presents a maximum at $\theta = -10^\circ$ with a HPBW of 10° .

V. CONCLUSION

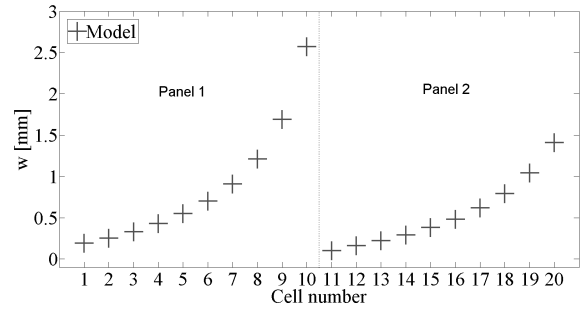
This paper presents a full analytical synthesis methodology of a reflector. Our methodology has been applied to a conical case of a reflector consisted of two shifted panels. The results obtained from the full-wave simulation show a good agreement with the specifications and prove the efficiency (low time consuming) and the accuracy of the analytical model.

REFERENCES

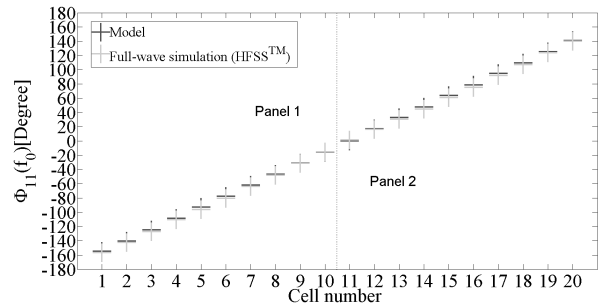
- [1] D. G. Berry, R. G. Malech, and W. A. Kennedy, "The reflectarray antenna," *IEEE Trans. Antennas Propag.*, May 1963.
- [2] S. M. A. M. H. Abadi, K. Ghaemi, and N. Behdad, "Ultra-wideband, true-time-delay reflectarray antennas using ground-plane-backed, miniaturized-element frequency selective surfaces," *IEEE Trans. Antennas Propag.*, vol. 63, no. 2, February 2015.
- [3] O. Luukkonen, C. Simovski, G. Granet, G. Goussetis, D. Lioubtchenko, A. V. Räisänen, and S. A. Tretyakov, "Simple and accurate analytical model of planar grids and high-impedance surfaces comprising metal strips or patches," *IEEE Trans. Antennas Propag.*, vol. 56, no. 6, 2008.



(a) Comparison between the specified inductance L_1^{Spec} (deduced from $Y_{L_1}^{Spec}$) and the model inductance L_1^{Model} (deduced from $Y_{L_1}^{Model}$) for each unit-cell.



(b) Deduction of the values of the width w on each unit-cell.



(c) Accuracy of the analytical model on the reflection phase on each unit-cell.

Fig. 3. Synthesis of the reflector.

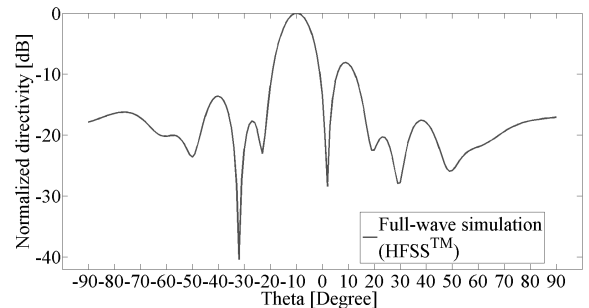


Fig. 4. Simulated radiation pattern in the plan xOz .

- [4] R. Rodriguez-Berral, C. Molero, F. Medina, and F. Mesa, "Analytical wideband model for strip/slit gratings loaded with dielectric slabs," *IEEE Trans. Antennas Propag.*, vol. 60, no. 16, December 2012.