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Study of DC-DC output serial connection for photovoltaic distributed systems

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We present a study of work limits for two dc-dc converters associated in series. This type of structure is usually used to maximize the electrical energy production of a photovoltaic system when it is subjected to shading. We expose our study to research these limitations in the case of expensive shading.

DC-DC converters, series connections, photovoltaic systems, optimal matching in series connections

INTRODUCTION

Research on photovoltaic energy (PV) beyond improvements of cells efficiency have attracting significant attention in the field of power electronics and control algorithms associated in a perspective of maximizing the power generated by the modules. Thus, the development of control laws has recently taken an important place in the innovations made in terms of maximizing efficiency and safing operation. For this, control and electronics have become more complex and multitask. One trend is to approach the electronic interfaces close to the source by discretizing the control power architecture with several different power stages in cascade.

This approach induces research of better managements on differences energies produced. To illustrate our purpose, we have done compared studies on simple cases of connections. A comparison of various possible interconnections between a panel and its charge was proposed in Fig. 1. In the first configuration, PV modules are connected in serial in order to increase the output voltage to one DC-AC converters (Fig. 1.a). This configuration allow decreasing resistive losses, however this solution need the use of a bypass diode [1]. That means that if a production between the PV modules is different, the faulty module will be shorted. This strategy prevents the failed module to work in his receiver mode but a part of the production will be lost.

In the Fig. 1.b and Fig. 1.c, the bypass diode is integrated inside on or more micro-converters. Thus the power generated by each generator can be managed independently and therefore high improved. The main difference of this two distributed structure resides in the interconnection of their output. In the case b, the outputs of the micro-converters are connected in parallel and therefore share the same voltage, the total current is the sum of each output current. This configuration has no structural limitation because there is no dependency between the gain conversion stages. However due to a low output voltage and a high current input, the wiring losses will be greater than in the centralized solution. In case c, the outputs are connected in series and this time sharing the same current [2], [3]. However, the output voltage of the power stages is then probable floating generating instabilities as voltage levels obtained. Robust control loops must constantly monitor that there is no drift [4]. This finding implies that the gains of the converters are connected. However, the advantage is that the currents are not added, minimizing the Joule losses.

Fig. 1. Photovoltaic power architecture

PV models under shadow condition

To well understand the phenomena involved in a moving shadow on the PV modules we use an electrical model of the cells taking into account the Bishop effect that will allow us to represent the inverse characteristic. In sunny conditions optimal voltage V_{OPT} of solar cells have a value between 0.5V and 0.7V. Therefore, the PV modules are most frequently made by connecting the cell in series in order to increase the voltage and facilitate the connection to the grid [5]. Thus cell will add the voltages with the same current flowing through the cells (Fig. 2). In the same principle, even if this configuration is rarely used to scale of the cell it is possible to connect them in parallel to increase the current [6]. These types of associations can be made without association's difficulty but it is recommended to have very similar electrical cells characteristics to avoid disparity and loss of performance. The serial and parallel photovoltaic cells configurations forming the photovoltaic generator (GPV) have its electrical characteristics which vary across the cell involving a mismatch. In the case of the serial connection, the mismatch limits the current across chain of cells. An occultation of a cell under these conditions creates a significant loss of power. When the difference of current between normal cells and the covered cell is large, the voltage of the cell becomes negative and passes in receiver operation. We must then consider the inverse characteristic of the cell and its avalanche area. Fig. 2.b shows the association of three cells of which is shaded. We can see that for a given current, the shaded cell have its voltage becomes negative and approach the breakdown voltage $V_{BR}[6]$, [7]

a) Without bypass diode b) Without bypass diode and with hidden cell

c) With bypass diode and with hidden cell

Fig. 2 : Serial connexion of PV cells under patial shading

The photovoltaic cell in the receiving area will heat up and dissipate the excess temperature. This can lead to the deterioration of the cell. In the case where the current supplied by healthy cells allows polarizing the cell at a higher absolute value of V_{BR} , there will be a breakdown of the junction. The length of the chains of cells in a photovoltaic module is defined by this critical case, that is to say, if one cell is in the dark and all the other cells discharge inside of her. We find chains of cells to the silicon modules of about 20 cells for a voltage V_{BR} around-20V. To increase the voltage and thus the number of cells in series, a protection diode is added in parallel for each string in order to bypass the shaded string if the voltage becomes negative [8], [9].

The effect of the bypass diode will result in a discontinuity of the characteristic I_{PV} (V_{PV}) and did appear in the case of partial shading, two maximum powers (Fig. 2.c). At low voltage (high current), the failed module cannot supply the current and is totally disconnected. At high voltage (low current), the current string is limited by the lowest cell. In the first case, the faulty module does not produce energy, in the second case the faulty module limits the energy of non-shaded modules. This protection solution adopted by manufacturers has several disadvantages, the main ones:

- Creation of several maxima in the characteristic P_{PV} (V_{PV}) generator,
- Power is lost during a shade of one cell of the string,
- Power is limited by the hidden cells.

Another protection diode called non-return diode is used to protect GPV against negative currents that could be come from other modules connected in parallel or the load (eg battery). In this case, the voltage is limited by the lowest string causing a loss of power.

Fig. 3 present the P(V) characteristic of a PV module composed of two groups, which one of the group is progressively shaded. We observe in the curve 1, the discontinuity due to the bypass diode activation. In curve 2, the power extracted in the case of discrete system. Finally, the distributed architecture is therefore useful when the powers differ between the modules.

Fig. 3. Potential PV power gain for discretized PV module

Study of converter series connection

We analyze in our study the case of two DC-DC converters where the output is connect in series and then connect to a DC bus Vout (inverter input voltage or battery charger). Fig. 3 shows the schema where the currents Iout1 and Iout2 outputs are equal.

Fig. 4. Example of series of two DC-DC converters

We can determine from the diagram (Fig.4), the following equations (1) taking α 1 and α 2 as the conversion gains of the converters 1 and 2 starting from zero to infinity.

$$
\begin{Bmatrix}\nV_{out1} = \alpha_1 * V_{in1} \\
V_{out2} = \alpha_2 * V_{in2} \\
V_{out} = V_{out1} + V_{out2} \\
V_{in1} * i_{in1} = V_{out1} * i_{out2} \\
V_{in2} * i_{in2} = V_{out2} * i_{out2}\n\end{Bmatrix}\n\qquad\n\begin{Bmatrix}\ni_{out1} = \frac{i_{in1}}{\alpha_1} \\
i_{out2} = \frac{i_{in2}}{\alpha_2} \\
i_{out} = i_{out1} = i_{out2}\n\end{Bmatrix}
$$
\n(1)

A simple analysis allows us to define the relationship between α 1 and α 2 as follows:

$$
\begin{cases}\n\alpha_1 = \alpha_2 * \frac{i_{\text{in1}}}{i_{\text{in2}}}\n\\ \n\alpha_1 = \frac{V_{\text{out}} - \alpha_2 * V_{\text{in2}}}{V_{\text{in1}}} \n\end{cases}
$$
\n(2)

From (1) and (2), then we find:

$$
\begin{cases}\n\alpha_1 = \frac{V_{\text{out}} * i_{\text{in1}}}{V_{\text{in1}} * i_{\text{in1}} + V_{\text{in2}} * i_{\text{in2}}}\n\\
\alpha_2 = \frac{V_{\text{out}} * i_{\text{in2}}}{V_{\text{in1}} * i_{\text{in1}} + V_{\text{in2}} * i_{\text{in2}}}\n\end{cases}
$$
\n(3)

To define the limits of this interconnection, we perform an analytical study with the following change of variable and using as a reference the converter number 1:

$$
A = \frac{V_{\text{out}}}{V_{\text{in1}}} \qquad \qquad B = \frac{i_{\text{in2}}}{i_{\text{in1}}} \qquad \qquad C = \frac{V_{\text{in1}}}{V_{\text{in2}}}
$$

'A' defines the ratio between the output voltage V_{out} and the input voltage of the first converter. 'B' and 'C' represent reciprocally the ratio between the two inputs current and the ratio between the two inputs voltage. Equation of each gain α will be defined according a fraction of the voltage and of the current of the first converter.

$$
\alpha_{1} = \frac{V_{\text{in1}} * i_{\text{in1}}}{A * (V_{in1} * i_{in1} + C * B * V_{in1} * i_{in1})}
$$
\n
$$
= \frac{1}{A * (1 + C * B)}
$$
\n
$$
\alpha_{2} = \frac{B * V_{\text{in1}} * i_{\text{in1}}}{A * (V_{in1} * i_{in1} + C * B * V_{in1} * i_{in1})}
$$
\n
$$
= \frac{B}{A * (1 + C * B)}
$$
\n(5)

In the case of PV application, the variation of the irradiation impact principally the PV current due to the exponential behavior of PV panel according to the PV voltage. In the first approach we will take account just the current variation and fix $V_{in2}=V_{in1}$ and therefore C=1.

Fig. 5. Conversion gain between the voltage ratio and the current ratio for Vin1 = Vin2.

Fig. 5 shows a 3D view of α1 and α2 according to A and B. We also represented the passage from the buck mode to the boost mode with the area define by $\alpha=1$. We could see that there areas define four working zones where this converter operate in : "Buck-Buck", "Boost-Buck", "Buck-Boost" and "Boost-Boost". Thus, from the solution $(4)=1$ and $(5)=1$, it is possible to know the limit when the converter switching from the step down mode to the step up mode.

Fig. 6. 2D section of the Fig. 4 for $\alpha=1$ **and Vin1=Vin2**

Fig. 6 present the resolution evolution when Vin1=Vin2. We find that for Iin1=Iin2 a gain for the two stage equal to the half of the ratio voltage 'A'. In the other hand, if the ratio between the voltages is different (Vin1 \neq Vin2), this characteristic will be moved (Fig. 7), this phenomena implies that the size of each zones change. To define the limits of the system, we must then consider the minimum and maximum conversion gain of a classical converter.

Fig. 7. 2D section of the Fig. 4 for $a=1$ **and** $V\in V$ **in2**

Fig. 8 shows the curve of the gain of the two converters when the conversion gain is 0.25 for the buck mode and 4.0 for the boost mode. The operation areas are then very small and the possibility to find an operating point then depends on the maximum and minimum gain, the report of the decoupling between the two input currents and finally the output voltage.

Fig. 8. 2D section of the Fig. 4 for $\alpha=1$ **for Vin1=Vin2 with gain limitation [0.25 et 4]**

Experimental results

In this section we present experimental results performed with a cascaded four switch DC-DC Buck-Boost converter (FSBB) [10]. This structure allows functioning as a step down voltage or as a step up voltage. FSBB is principally characterized by a common inductance between the two stages. There are three operating modes described in [10],[11] : the buck mode when the voltage gain is less than 0.95, the boost mode when the gain is greater than 1.05 and an intermediate mode when the gain is \pm 5% of unity. Four switches are controlled via a 30F2020 microcontroller to generate four duty cycle signals D1 for the buck, D2 for the boost and their complementary signals working at a switching frequency of 200 kHz. During intermediate operation, all switches are switched; and duty cycles are given as $D1 + D2 = 1$. We can already say that in this mode the losses will be greater than when operating in boost or buck because all switches are switched during the cutting. Fig. 9 show the electrical schema of the converter, designed to respond to the limit current and limit voltage of a photovoltaic panel TENESOL TE2200 240Wc with $V_{IN} = 37V$, $I_{IN} = 8.3A$, $V_{OUT} = 60V$ and $I_{OUT} = 1A$.

Fig. 9. Electrical schema of the Buck Boost converter

Efficiency of the DC-DC converter according to the total duty cycle define as the addition of the duty cycle D1 of the buck converter and the duty cycle D2 of the boost converter is present Fig. 10.a. We can see that the maximum efficiency is around 93% for a duty cycle around 100%. Another analyze is realized in Fig. 10.b where the voltage gain is compare to the total duty cycle. Under these conditions, we can distinguish the mode buck from the mode boost when the gain is at 1. This curve allows seeing the maximal voltage gain of 4.5 for a boost duty cycle of 85%.

Fig. 10. Experimental resuls of the cascaded buck boost converter (FSBB)

By analyzing the two characteristics for a minimum efficiency of 80%, we can define a minimum gain of 0.25 and a maximum gain of 2.5. From these experimental results we can reproduce the 3D view presented in Fig. 11.a. Results obtained correlate the simulation, we can see the different areas of operation depending on the voltage gain, the current ratio 'B' and the ratio of voltage 'A'. Finally by adding the gain of the two conversion stages, we obtain the total gain of the structure according to the ratio of the currents. Maximum performance is obtained for currents $I_{\text{IN1}} I_{\text{IN2}}$ equivalent. Taking into account the performance we can see the area in which we can get the maximum production of our two modules connected to the inputs of DC-DC converters.

output connected in series.

Fig. 11. Conversion gain between the voltage ratio and the current ratio for Vin1 = Vin2.

Conclusions and perspectives

Residential facilities are the most affected by shading, especially in urban areas, which may be faced with sometimes complex phenomena shading.

These phenomena are difficult to avoid, linked to other nearby homes and urban furniture. This represents the operating conditions where the impact of shading on the energy yield is the most important. Thus, the shade worn due to chimneys, antennas, trees, clouds, or any mobile shadow moving on the modules during the day cannot be avoided. Similarly, we can add to the list of localized shadows that will be fixed in one or more cells: those primarily related soiling, bowel movements of birds, the trees, or any other deposit made by the various weather conditions. These shades will primarily generate electric producible losses generating more or less gap from its theoretical potential, but if these obstacles to sunlight persist, they can also lead to premature deterioration of the PV generator.

In this paper we make a theoretical analysis of the connection limits of two DC-DC converters which their output are connected in series. Experimental results are realized thanks a FSBB converter which allows us to define the limitation due to DC-DC converter behavior and the limitation due to the efficiency.

It can deduce for example that in the ideal case these kind of solution ensure the extraction of maximum power point of each PV modules. Therefore, in the real case, the limitation of the gain does not fully guarantee that this research is successful. Moreover the increasing of the number of stages greatly limits the allowable gap between modules. The MPPT controls operate independently and optimizing the input power of the converter, they can then stabilize at a different operating point of the PPM or even become unstable.

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