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Magneto-transport properties of a random distribution of few-layer graphene patches

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In this study, we address the electronic properties of conducting films constituted of an array of randomly distributed few layer graphene patches and investigate on their most salient galvanometric features in the moderate and extreme disordered limit. We demonstrate that, in annealed devices, the ambipolar behaviour and the onset of Landau level quantization in high magnetic field constitute robust hallmarks of few-layer graphene films. In the strong disorder limit, however, the magneto-transport properties are best described by a variable-range hopping behaviour. A large negative magneto-conductance is observed at the charge neutrality point, in consistency with localized transport regime.

I. INTRODUCTION

The large scale fabrication of graphene is a prerequisite for its industrial integration into complex devices, either as active or as interconnecting parts of electronic circuits.1 For this purpose, the Chemical Vapor Deposition (CVD) of graphene is nowadays the most promising technological solution for producing large areas of polycrystalline graphene films. This technique, however, requires a costly state-of-the-art control of the whole fabrication process in order to achieve the highest quality specimen,2,3 where the electronic mobility can reach 5000 cm2/Vs. When these drastic requirements are not fulfilled, however, one may end up with an inhomogeneous array of interconnected patches of Few Layer Graphene (FLG) with different thicknesses. Furthermore, the presence of structural defects (grain boundaries, point defects, lattice deformation, tears, edge roughness…) as well as the direct chemical environment (charge adsorbates, influence of the substrate, presence of chemical residues…) may strongly modify the performances of the pristine CVD graphene-based devices. The sensitiveness of graphene to disorder and morphology variations was realized since its infancy and many efforts have been undertaken in order to circumvent this issue.4–6 For some dedicated applications, however, lowering the production costs to the detriment of the homogeneity and electronic mobility is advantageous. It is therefore interesting to investigate on such CVD-FLG films and to provide insights on how robust are the electronic properties of such films when subject to strong roughness and disorder. In this manuscript, we focus on two CVD-FLG films deposited onto Si/SiO2 substrates and integrated into FET (Field Effect Transistor) devices using simple and standard fabrication methods. The two devices were fabricated under the same experimental conditions, but one of them (sample S1) was annealed prior to measurement while the other one (sample S2) did not experience any post-fabrication treatment. The resulting transport properties at low temperature and high magnetic field differ substantially pointing towards the role of annealing. Indeed, while sample S1 displays well defined signatures of Landau Level (LL) quantization, sample S2 exhibits poor conduction properties and a negative magneto-conductance whose origin will be discussed.

II. SAMPLE DESCRIPTION

Using a 3-step process at 1000 °C, the FLG films are synthesized by catalytic CVD on copper foil (2 × 2 cm2, 25 μm thick, 99.999% Alfa Aesar) from methane highly diluted into argon and hydrogen. Next, the films are transferred onto Si/SiO2 substrates using a sacrificial PMMA membrane, after the copper foil has been removed using an acid solution (1% HCL + 1% H2O2). The samples are then etched into a standard Hall bar of width W = 50 μm and length L = 300 μm, and are electrically addressed using standard lithography methods. Sample S1 was annealed in Ar/H2 at 300 °C during two hours. Note that the devices are equipped with a back-gate of 300 nm SiO2 layer that allows charge carrier density modulation through the application of a gate voltage Vg. The fabrication techniques may introduce wrinkles, impurities, tearing and other structural defects in the FLG film. Indeed, as shown in Fig. 1(c), the large ratio of Raman peaks 1D/2G confirms the presence of strong disorder7 at the final stage of the fabrication procedure, whereas the D-band is absent in the Raman spectra recorded before the transfer onto SiO2 (not shown) implying a very low initial defect density.8 The optical images of the devices show a
clear slashed structure, where patches of dark and bright spots are clearly visible (Figures 1(a) and 1(b)). Although the macroscopic details cannot be representative of the microscopic disorder, it is evident that the films are not homogeneous, but composed of an array of multi-layer graphene areas electrically connected to each other. It is worth noticing that the small ratio of Raman peaks $I_{2D}/I_G < 1$ adds further credits to the FLG nature of the films. Based on optical microscopy and Raman spectroscopy, the two samples are almost indistinguishable from each other despite the annealing process of sample S1. These techniques are often used to qualify graphene films from CVD processes since they constitute rapid, cheap and non-destructive characterization tools. In the present study, however, we shall show that despite strong similarities, the electronic properties of the two samples turned out to be quite different.

III. WEAK DISORDER LIMIT

First, we focus on sample S1. Its basic electrical characteristics are reproduced in Figure 2(a). Similar to mono-layer graphene, the resistivity of the device is maximum when the carrier density is zero on average and decreases when the sample is doped either with electrons or holes suggesting an ambipolar behavior. We note that the Charge Neutrality Point (CNP) is reached when $V_g \approx 10$ V, accounting for the presence of charged impurities in the close vicinity of the FLG film. As expected, the resistivity peak becomes sharper at low temperature and spans from 1.5 kΩ to 8 kΩ in the investigated range of back-gate voltage. The carrier density $n$ is extracted from low-field Hall effect measurements and displayed in Figure 2(b) for selected values of $V_g$. Assuming a plane capacitor model, the linear relation $n(V_g) = (e/C_0)(V_g - V_{CNP})/(e \times d)$ is favorably compared to the experimental values for the gate dielectric thickness $d = 295$ nm ($e$ is the electron charge, $C_0$ and $C_r = 3.9$ are, respectively, the vacuum and relative dielectric permittivity of SiO2). The slight deviation of the experimental data at $V_g \approx V_{CNP}$ is accounted by the presence of mixed charge polarity in the close vicinity of the CNP. Overall, this result indicates that the gate efficiency ($7.2 \times 10^{10}$ cm$^2$/Vs) is unperturbed by the presence of the multi-layer graphene patches. Indeed, screening effects usually observed in graphite and thick multi-layer graphene flakes are, respectively, the vacuum and relative dielectric permittivity of SiO2. The inhomogeneous distribution of few-layer areas is further evidenced in Figure 2(c) where the ratio between the intensity of the D and G Raman peaks is spatially resolved over a limited zone of the sample (shown in Figure 1(a)). The average size of the FLG flakes roughly reads 5 μm, in agreement with optical microscope observations.

The high field magneto-transport results are shown in Figure 3 for sample S1. A fast data acquisition system is used to record both the longitudinal and Hall resistance during a pulse of magnetic field with maximum value $B = 55$ T and of total duration $\tau \approx 300$ ms. In order to avoid longitudinal and Hall resistance mixing, each curve depicted in Figure 3 corresponds, respectively, to the sum and difference of the data recorded from a positive and negative magnetic field. The arrows show selected back-gate voltages at which magneto-transport measurements have been performed. The arrows show selected back-gate voltages at which magneto-transport measurements have been performed. (a) Evolution of the carrier density as a function of back-gate voltage: the linear behavior is favorably compared to standard plane capacitor model. (b) Magneto-transport measurements have been performed. The arrows show selected back-gate voltages at which magneto-transport measurements have been performed. (c) Raman spectroscopy map of a part of the sample S1 (see dotted-line rectangle in Figure 1(a)), evidencing an inhomogeneous film made of few-layer graphene patches.

\[ R_{xx}(V_g) = \frac{e}{C_0}\left(V_g - V_{CNP}\right) \\
\]
and Hall resistance (Figure 3(b)) shows large oscillations, displaying an expected phase shift as the back-gate voltage is varied. The frequency of magneto-oscillations depends on the carrier density and it is often more convenient to plot the data as a function of the LL filling factor $\nu = n/h/(eB)$ as shown in Figure 3(c) where the Hall conductance $G_{xy}(B)$ is calculated through resistance tensor inversion. Clear conductance quantization at low filling factors is observed. Nevertheless, its complete vanishing is very unlikely since it relies on the emergence of a zeroth LL which, in a first approximation, is a robust property shared by all disordered FLG systems.

**IV. STRONG DISORDER LIMIT**

Let us now focus on sample S2, which was prepared using the same recipe as sample S1 except for post-fabrication annealing. Despite their similar optical microscopy and Raman spectroscopy features, the transport properties of sample S2 are much different than those reported above. First of all, the resistivity is almost two orders of magnitude lower than that of sample S2 (100 kΩ) than in device S1 (3 kΩ) at room temperature. In order to comply with high resistance value, especially at low temperature, the two-probe conductance of device S2 is measured by monitoring the current flowing from source to drain electrodes as a function of bias voltage $V_{bias}$. The I-V characteristics develop a non-linear behaviour when $T < 85 \text{ K}$. This effect is best evidenced in a small device (i.e., in sample S2* of length 9 μm and width 5 μm made from the same FLG film as sample S2) as shown in Figure 4(a). Alternatively, this trend is also visible when considering the differential conductance curves $(dI/dV)$ which were recorded simultaneously with the I-V characteristics by adding a small modulation voltage and using lock-in detection. They provide an alternative representation of the quenched conductance at low bias voltage and low temperature, as illustrated in Figure 4(b). Contrarily to sample S1 and due to severe contamination during the device fabrication
process, we hypothesize that this strongly disordered sample consists of many FLG islands weakly coupled to each other so that it actually behaves as a granular system with large potential barriers between the FLG grains. In the Coulomb blockade regime at low temperature, the conduction takes place through thermally activated hops of the charge carriers between neighboring FLG patches. At high bias voltage, the electrons acquire enough energy and percolate through the FLG array via multiple branching paths. The bias voltage range over which the current suppression is observed varies as a function of the system’s size as well as the back-gate voltage, as shown in Figures 4(c) to 4(f), where the differential conductance as a function of $V_{\text{bias}}$ and $V_g$. The graph is symmetric with respect to $V_g = 10$ V which indicates the CNP. (d) to (f) Color map representation of the differential conductance of sample S2 for $T = 50$ K, 25 K, and 4.2 K. Notice the different $V_{\text{bias}}$ scale as compared to sample S2*.

through a variation in the actual gating potential across the sample, when $V_{\text{bias}}$ is of the same order of magnitude as $V_g$. Interestingly, even if device S2 is in a strongly localized transport regime, its sensitiveness to electrostatic gating remains and the CNP is still experimentally accessible. The color map representation of the device’s characteristic allows an unambiguous determination of the CNP which would have been otherwise difficult to extract by analyzing the conductance against gate voltage alone.

The electronic transport in the non-linear regime at very low temperature is now discussed. For this purpose, the model of Middleton and Wingreen is appropriate and considers an array of capacitively coupled conductors. The I-V characteristics should follow the trend $i(V) = \beta \times (V - V_t)^\alpha$, where $\beta$ is a constant pre-factor, $V_t$ is the threshold bias voltage above which the current is no longer suppressed, and $\alpha$ is a scaling exponent which depends on the system’s characteristics. In Figure 5, we plot the I-V curves recorded at different $V_g$ in a log-log scale with a constant threshold voltage $V_t = 8.5$ V extracted from the curve at $V_g = 0$ V. We obtain a set of linear curves (for sufficiently high bias voltage) with similar slopes $\alpha = 2.9$, but different offsets depending on $V_g$. Such offsets correspond to variations of $V_t$ as compared to its reference
value at \( V_g = 0 \text{ V} \). The extracted exponent \( \alpha \) is consistent with previous reports (and references within), which considers a random array of graphene dots. At zero gate voltage, \( V_g \) is estimated to 8.5, 5, and 2.1 V at 4.2, 7.5, and 12 K, respectively. As \( V_g \) is expected to increase linearly with decreasing temperature, the extrapolation of the threshold voltage \( V_t \) down to \( T = 0 \text{ K} \) provides the global threshold voltage \( V_t = 12 \text{ V} \).

This parameter is directly linked to the total number (\( N \)) of FLG islands in the sample through the relation \( V_t(T = 0 \text{ K}) \approx \frac{1}{C_0} N \), where \( C_0 = 4r_{\text{FLG}} \) represents the FLG self-capacitance (assuming that FLG islands are modeled by 2D disks of radius \( r \)) and \( N \) is a parameter that depends only on the dimensionality and the capacitive coupling \( C \) between neighboring FLG islands. For a weakly coupled 2D square lattice with \( C/C_0 \ll 1 \), numerical simulation provides \( \kappa = 0.33 \ldots \). Alternatively, the number of quantum dots can be approached by the relation \( N = \pi r^2 \approx L \times W \), assuming the whole area of the device is actually covered with FLG disks. Solving both equations simultaneously, we compute \( r \sim 500 \text{ nm} \). Even if the order of magnitude seems realistic, we emphasize that the deduction of \( r \) must be handled very carefully as it relies on several initial assumptions and rough approximations. The system can thus be viewed as a granular metal with mean grain size \( \sim 500 \text{ nm} \) weakly coupled to each other and demonstrating a Coulomb-induced transport gap at low temperature.

Now, we would like to comment the magneto-conductance (MC) shown in Figure 6(a) for sample S2. Overall, a strong negative MC of the order of 60\% is observed at \( B = 55 \text{ T} \). On the other hand, the evolution is not monotonous but first displays a positive MC reaching a maximum for \( B \sim 6 \text{ T} \). The theory of weak localization may provide a natural explanation for such positive MC, however, this hypothesis is rapidly ruled out since (i) it usually applies in moderate disordered (diffusive) systems, (ii) it takes place within a low magnetic field range (\( <1 \text{ T} \)), and (iii) it vanishes with increasing temperature. An alternative scenario can be considered in order to explain the initial positive magneto-conductance: it relies on the Variable Range Hopping (VRH) model in which conduction occurs through charge hoppings between localized states, in the strong disorder limit. Even if this model has not been initially developed for granular systems, many authors have considered it for this purpose, after they noticed an excellent agreement between the predictions of the model and the transport properties of weakly coupled nanoparticle lattices. The localization length \( \xi \) and hopping length \( \ell_{\text{hop}} \) are essential parameters of the VRH model, which are associated to the mean nanoparticle’s size and their spatial separation, respectively. In sample S2, such details are not directly accessible (or deduced with a large uncertainty) so that we will discuss the VRH model within a qualitative framework only. In the strongly localized regime, the hopping probability is computed through the coherent sum over all possible paths connecting two sites and thus depends on the magnetic field. A weak positive MC saturating near \( B^* = \Phi_0 \times \ell_{\text{hop}}^{-3/2} \times \xi^{-1/2} \) is expected. While the localization length is temperature-independent, the hopping length is expected to decrease as the temperature increases. Consequently, \( B^* \) is shifted towards higher value for increasing temperature, in qualitative agreement with our experimental observations (see Figure 6(b)). Furthermore, the initial positive MC amplitude is larger for increasing \( V_{\text{bias}} \), reinforcing the hypothesis that hopping processes are indeed involved in the galvanometric transport properties. This observation is also consistent with Ref. which reported a magnetic-field driven delocalization effect in disordered graphene only in the presence of high electric and magnetic fields. The negative magneto-conductance for \( B > B^* \) can be accounted for by a direct consequence of shrinking of the wavefunctions of the localized states due to magnetic confinement. In the VRH-type conduction regime, the magneto-conductance is expected to behave according to the relation \( \log |\sigma(B)| \propto B^\gamma \), where \( \gamma = 1/2 \) for Mott-VRH and \( \gamma = 1/3 \) for ES-VRH. When plotted in a log-log graph, a linear

![Sample 2, T=4.2K](image)

**FIG. 5.** I-V characteristics of sample 2 plotted in a log-log scale for backgate voltage ranging from \( V_g = -50 \text{ V} \) to \( V_g = +50 \text{ V} \). The common slope of the set of curves is indicated by the dash line. Inset: the extrapolation of the threshold voltage \( V_t \) down to \( T = 0 \text{ K} \) provides the global threshold voltage \( V_t = 12 \text{ V} \).

![Log-B](image)

**FIG. 6.** (a) Magneto-conductance of sample S2 at \( T = 4.2 \text{ K} \) and \( V_g = 0 \text{ V} \) for various bias voltages. The magneto-conductance is positive up to \( \sim 6 \text{ T} \) before turning negative. Inset: Log-Log plot of the high field magneto-conductance of S2, the slope provides \( \gamma \approx 0.57 \) for \( V_{\text{bias}} \sim 40 \text{ V} \) and \( T = 4.2 \text{ K} \). (b) Temperature dependence of the magneto-conductance of sample S2, showing non-monotonous behavior.
behaviour is recovered at high field with $0.46 < \gamma < 0.62$ for all curves, which confirms the functional form of the magneto-conductance and establishes the Mott-VRH scenario for high magnetic field\cite{30} (for clarity, only a representative curve for $V_{\text{bias}} = +46$ V is shown in the insert of Figure 6(a)). It is worth mentioning that, at zero magnetic field and in the temperature range $90$ K $< T < 290$ K, the resistance of device S2 strongly increases when the temperature decreases (not shown) and scales as $R \propto \exp[(T_0/T)^p]$ with $p = 1/3$ (Mott VRH) or $p = 1/2$ (ES-VRH). This behavior provides additional credits to the VRH-like transport regime, together with the large MC amplitude at low temperature. Although the VRH model allows a coherent qualitative interpretation of the experimental results in agreement with the transport regime in sample S2, other phenomena inherent to graphene may superimpose or compete with those described above. In particular, each FLG patch can host LL quantization under intense magnetic field provided the disorder-induced broadening of the LLs remains lower than the energy gap between them. Although this last assumption cannot be directly checked for sample S2, some authors\cite{31} reported on the onset of the $n=0$ LL in hydrogenated graphene with mobility less than $10 \text{cm}^2/\text{Vs}$, arising from the large energy gap between $n=0$ and $n=\pm 1$ LLs ($\Delta E = v_F \times \sqrt{2}heB = 270$ meV at $55$ T). The onset of LLs, peculiar to each FLG patches, is likely to favor negative MC under high magnetic field. This assertion can be further reinforced by performing high field MC studies as the system is progressively driven far from CNP which, regrettably, could not be performed because of sample failure during the course of the experiment.

V. SUMMARY AND REMARKS

To summarize, two CVD-FLG films have been investigated. One of them is composed of FLG islands well interconnected to each other, while they appear separated by strong potential barriers in the second one. Since the FLG films were grown using the same experimental conditions, we suppose that the transfer from the copper catalyst to the SiO$_2$ substrate before device processing as well as post-fabrication annealing are likely to account for such a discrepancy. At low temperature, the devices display much different transport properties but both remain sensitive to electrostatic doping. The galvanometric properties show positive magneto-resistance at high magnetic field and close to the CNP. This effect is likely to originate from the onset of the $n=0$ LL which remains robust even in the presence of very strong disorder. To some extent, this study can be compared with recent reports involving functionalized graphene (graphene, graphene oxide, chlorinated graphene, fluorinated graphene are few examples), which aims at tailoring the electronic properties of graphene at wish.\cite{23,25,32–35} Even if the perspective of a gap opening in the density of states of such systems holds great promises for electronic applications, chemical functionalization\cite{36} inevitably introduces defects in the initial material and it is instructive to compare the resulting conduction properties with low-cost CVD graphene film containing random and uncontrolled disorder instead.

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