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Programmable Current Converter Synthesis for the Evaluation of UV Radiation of Excimer Lamps

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Abstract— This work proposes the development of a power supply for Dielectric Barrier Discharge (DBD) lamps, intended for the evaluation of a water treatment system based on UV radiation. The DBD excimer lamp is an attractive source of UV because of its cost and radiation power. The proposed converter is conceived to control the UV radiation by programming different lamp current intensities, duty cycles and frequencies. For the converter synthesis, a capacitive electric equivalent model of the DBD is used. A simulation of the converter and its control analysis is presented.

Keywords- DBD, current-mode, static converter, excimer

I. INTRODUCTION

The presence of pathogenic microorganisms in drinking water is cause of multiple human diseases. In Colombia, 37% of the superficial waters monitored by the Colombian Institute of Hydrology (IDEAM), presents microorganism levels exceeding the norms established by the national laws [1]. In this context, the need for the development of water treatment systems, for urban and countryside use, is evident.

One of the more advantageous technologies to decontaminate water is based on the use of Ultra Violet spectrum radiation (UV). The UV is able to decontaminate because the UV photons deteriorate the DNA bases of microorganisms [2], [3]. The use of UV radiation presents notorious advantages over other methods based on the use of chlorides or other reagents; for example, the UV does not change the physic-chemical properties of foods or liquids, UV neither let chemical traces in the objects [4].

There are different types of UV sources. Some of the most widely developed are, the mercury discharge lamp, the

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Dielectric Barrier Discharge (DBD) excimer lamp, the laser and newly the LED [5], [6]. The DBD lamp features some advantages over the others sources, e.g. environmentally friendly because is mercury-free, more radiation efficiency and a better cost-radiation ratio when compared with the LED in the 235-310 nm wavelengths [6], [7]. Excimer lamps are also more effective than lasers when radiating large volumes or surfaces.

The DBD operating principle is based on the application of an electric field to a gas, producing an electric discharge that creates excited molecules. The ionized medium produced by the discharge is non-equilibrium plasma.

Nowadays, due to the advances in the DBD construction, the power supply improvements and the plasma physics development, new applications for the DBD have been found, e.g. surface treatment, thin-film deposition, pollution control, laser excitation, flat screens [8], and UV generation by means of excimer decay [9].

In the DBD excimer lamps, different gas species are used to produce UV and VUV in different wavelengths and intensities [9]. Looking for the improvement of the intensity, efficiency and control of the UV emission, several works have been done in the design of power supplies intended to DBDs [10]-[15].

According to this context, of water quality problems and decontamination technologies, the development of a power converter is proposed in this work, in order to evaluate a water treatment system based on DBD lamps. The degrees of freedom offered by this power supply will make possible to find the optimal operating point of the DBD lamp, for the water decontamination process.

II. DBD EXCIMER PLASMA LAMPS

The DBD excimer lamps or simply excilamps are sources of spontaneous narrow-band radiation in the UV and VUV spectrum [2]. In DBD lamps a confined gas is placed between two electrodes, with at least one of the electrodes coated with a dielectric. An electric discharge is generated trough the gas when the electrodes are powered with an electric field. The discharge produces unstable excited molecules known as excimers or exciplexes, these excited molecules generate photons when decaying to their ground state [2].

Excimers and exciplexes are produced by different mechanisms, according to the gas composition, and the construction of the lamp, therefore the power supply that produces the discharge in the gas must be designed according to these specific conditions [9].

In Fig. 1, a DBD excimer lamp with coaxial geometry is presented. This configuration exhibits two electrodes: an outer one made from a metallic mesh around the lamp, the other one is a copper rod in the central axis of the lamp. Between the electrodes there are two dielectric barriers (in silica or quartz glass) and the gas mixture. For this reason, the lamp can be related to a capacitive load. As the electrodes are not in direct contact with the gas, those are not degraded by the reactions during the discharge. Therefore, the DBD lifetime is increased when using this configuration (thousands of hours).

In order to study the DBD behavior, mathematical and computational models have been developed; e.g. using the finite elements method, the calculus of an electrical model based on the gas nature and lamp geometry [17], and particle dynamics simulation [16]. The electric model proposed in [17] offers low computational complexity and is suitable for use in electric simulators, which are used in the design of the DBD power supply.

In Fig. 2, the electric model of a DBD coaxial lamp is shown. In this model, the capacitors $C_{\rm d}$ and $C_{\rm g}$ are related to the dielectric capacitance and the gas capacitance, respectively. In parallel to the gas capacitance, there is a circuit that models the electric phenomena associated to the discharge across the gas and the plasma creation process.

The electric discharge in the gas is produced when the breakdown voltage is reached in the gap. During the discharge the gas voltage remains approximately constant. Consequently, the simplest circuit to represent the discharge is shown in Fig.

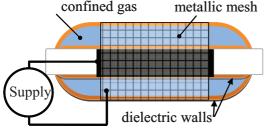


Figure 1. Coaxial DBD excimer lamp

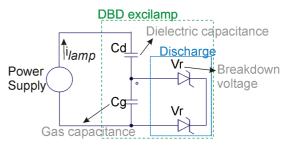


Figure 2. Simplified electric model of the DBD excimer lamp

2, where the zener diodes have a reverse voltage equal to the gas breakdown voltage [11].

III. PROPOSED POWER SUPPLY CONCEPT

A. Why to use a Current Mode Power Supply

According to [11], the UV-radiation intensity from the DBD lamp is proportional to the lamp current amplitude and waveform.

Since we are interested in performing experimental measures to evaluate the DBD lamp UV radiation, driving the lamp current is a good way to control the UV emission and therefore to determine optimal operating conditions. Due to the capacitive nature of the DBD, an alternating current with zero average must be supplied to the lamp, in order to achieve the steady state of the lamp voltage.

B. How to control the lamp power

In Fig. 3, there are three different cases in which the UV radiation intensity, efficiency and waveform would be evaluated and related to the frequency, duty cycle, and amplitude of the current that powers the lamp.

In case a, the lamp current flow is continuous (positive or negative). In b, the current frequency is different with respect to a and there is a relaxation time with zero current in the lamp. In c, the current amplitude is bigger than in a and b, but with a longer relaxation time.

Consequently, we propose a converter featuring variable

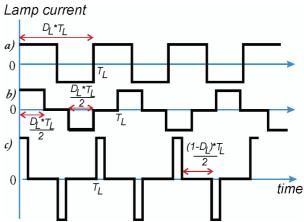


Figure 3. Three possible cases of the lamp current waveform with variable duty cycle, frequency and amplitude.

frequency, amplitude and duty cycle of the lamp current. To note that this converter is not intended to improve the power supply efficiency. The converter will be designed to bring the flexibility to evaluate the UV-radiation behavior and its disinfection effectiveness under several operating conditions.

IV. CONVERTER SYNTHESIS

A constant current source supplies a current inverter, and the inverter is connected to the lamp. The lamp current amplitude depends only on the constant current source at the input, and the inverter is used to change the direction of the lamp current and turn it off when necessary, as explained in III.

The current inverter and the constant current source topologies are explained, as follows:

A. Current Inverter

The current inverter is implemented with a full H-bridge connected in series with a constant current source, as shown in Fig.4. By switching the different legs of the bridge it is possible to control: the duty cycle D_L , frequency f_L (or period T_L), of the lamp current i_L . By driving the bridge with the proper switching sequence, the desired lamp current waveform can be obtained (Fig.5).

In order to prevent an abrupt disconnection of the current source, which will produce an overvoltage, the H-bridge control signals are overlapped during t_{ov} , when commuting the

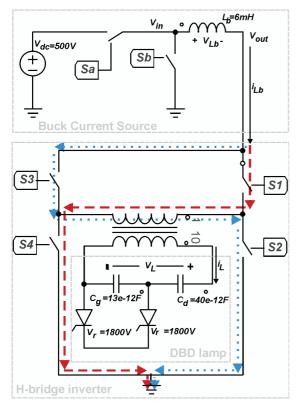


Figure 4. Converter circuit topology. The main sections are highlighted: Buck constant current source, H-bridge current inverter and DBD lamp model.

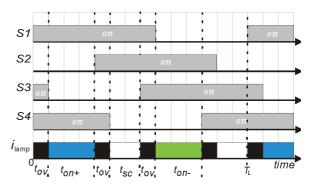


Figure 5. Example sequence in the inverter switching, during t_{on} the i_L is positive, during t_{on} the i_L is negative, t_{ov} is the overlapping time and during t_{sc} the current source output is connected to ground.

current from one leg to another.

When supplying the DBD lamp with the current waveform shown in Fig.6a, the lamp voltage would have the waveform displayed in Fig.8b accordingly to the electric model in Fig.2. The breakdown time t_r is the time that takes the gas capacitance voltage v_{Cg} to reach the gas breakdown voltage v_r starting from given initial conditions. During t_r the lamp is modeled with two equivalent series capacitors (Fig.2), and the lamp voltage v_L is governed by (1).

After t_r , the gas voltage v_{Cg} remains constant and it is modeled as a constant voltage source of value v_r (Fig. 2). Thenceforth, the lamp voltage continues to increase linearly (2), since C_d is still being charged with constant i_m .

At the time $T_L/2$ the lamp current becomes negative, therefore the two capacitors start to be discharged with constant current. As the v_{Cg} starts to decrease, the gas is modeled again as Cg instead of v_r , and similar expressions to (1) and (2) can be obtained for v_L .

$$v_L = I_{in} * \frac{c_g * c_d}{c_g + c_d} * t + v_{cg}|_{t=0} + v_{cd}|_{t=0}.$$
 (1)

$$v_L = I_{in} * C_d * t + v_r + v_{Cd}|_{t=tr}.$$
 (2)

Measured at the current source output, the voltage v_{out} (Fig.4) has the waveform in Fig.6c; where $v_{out} = v_L$, during the $0 \rightarrow T_L/2$ interval, and $v_{out} = -v_L$, in the $T_L/2 \rightarrow T_L$ interval. This is explained because the H-bridge inverts the polarity of the lamp voltage from $T_L/2$ to T_L , by connecting the negative terminal and positive terminals of the lamp to the current source output and to ground, respectively.

B. Current Source

The inverter switching signals have been studied with the assumption that in steady state, the inverter is supplied with constant current, always available to supply the lamp. Consequently, a constant current source operating in continuous mode is necessary. The Buck converter (Fig. 4) is a converter topology featuring this characteristic.

The Buck converter in current mode is capable to supply a constant average current with no discontinuities. In this converter, the value of the constant current is controlled with a feed-back closed loop which will be explained in section V.

The buck topology implies that the source output current i_{out} is equal to the inductance current i_{Lb} , which, from the circuit theory, is function of the inductance voltage v_{Lb} (4). Hence, the fact that i_{Lb} is constant, implies that the DC component of v_{Lb} (5) is zero, and consequently (6) must be respected.

$$v_{Lb} = v_{in} - v_{out}. (3)$$

$$i_{Lb}(t_o + T) = \frac{1}{L_b} \int_{t_o}^{t_o + T} v_{Lb} dt + i_{Lb}(t_o).$$
 (4)

$$\bar{v} = \frac{1}{T} \int_{t}^{t+T} v \, dt. \tag{5}$$

$$\overline{v_{Lb}} = \frac{1}{T} \int_{t}^{t+T} (v_{in} - v_{out}) dt = \overline{v_{in}} - \overline{v_{out}} = 0$$
, implies:

$$\overline{v_{ln}} = \overline{v_{out}}$$
 , the steady state condition. (6)

In Fig.7, are shown the dielectric and gas voltage waveforms as reflected at the inverter input terminal. The sum of these two voltages is v_{out} . The average of v_{out} is calculated (5) by evaluating the average of v_{cd_in} and v_{cg_in} , as shown in (7). Using (7) and the voltage waveforms of v_{cd_in} and v_{cg_in} , (8) is deduced for the steady state.

$$\overline{v_{out}} = \overline{v_{cd_{in}}} + \overline{v_{cg_{in}}}. \tag{7}$$

$$\overline{v_{out}} = \frac{v_r(T_L - 2t_r)}{T_L}.$$
 (8)

V. CONVERTER CONTROL

A. Control of the Inverter

The inverter is controlled in open loop by four signals, one for each switch. The frequency, duty cycle and delay of the four inverter switching signals (Fig.4) is easily computed to match the suited f_L , D_L and overlapping time, as can be inferred from Fig.5. Those calculations for the generation of the switching signals can be implemented in a digital programmable system, as an FPGA device or a DSP with PWM capabilities.

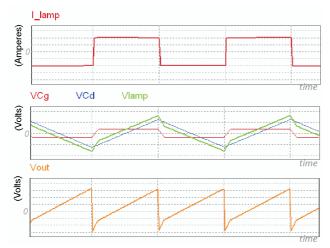


Figure 6. Waveforms of: a.) Lamp current i_L b) Dielectric capacitance voltage v_{Cd} , gas capacitance voltage v_{Cg} and lamp voltage v_L . c) Inverter input voltage v_{out}

B. Control of the Current Source

A hysteresis control of the inductance average current is proposed. The basic idea behind the control is to close the switch *Sa* or the switch *Sb* in Fig.4, with complementary control signals, in order to increase or decrease the inductance current respectively, around the DC current value.

Upper and lower current boundaries of the inductance current are used to control the switches around the desired average current; i_{max} , i_{min} respectively. If the inductance current i_{Lb} is lower than i_{min} the switch S_a is turned ON, if i_{Lb} surpass the upper limit i_{max} then the switch S_b is turned ON (Fig.8d). Using this control strategy the switching frequency is not constant.

Besides the control strategy we have to consider two main problems which affect the controller design and the components selection: the v_{out} instant values and resonance.

The voltage v_{out} is not constant and during some intervals v_{out} could be greater than v_{dc} or negative. This is explained because the peak voltage v_{out} does not depend on v_{dc} , as can be seen in Fig.7.

During the intervals which $v_{out} > v_{dc}$, the inductor current cannot be increased because the inductance voltage $v_{Lb}=v_{dc}-v_{out}$ is negative, therefore i_L will continue to decrease (Fig.8d) out of control until $v_{out} > v_{dc}$. Similarly, when $v_{out} < 0$, i_{Lb} will continue to rise, out of control, even if Sb is turned ON.

The inductance current is proportional to the voltage integral (4), then is valid to say that the shadowed area above v_{dc} in Fig.7, where $v_{out} > v_{dc}$, represents the maximum decrease in the inductance current after the instant when Sa has been turned ON.

The estimation of this area depends on the breakdown time t_r .of the gas voltage. This time is calculated with (9), which is deduced from the waveform of the gas capacitor voltage in Fig.6b. In order to simplify the expression of the mentioned area, it is assumed an inductance current for which is valid to neglect t_r when compared to T_t to obtain the waveform for v_{out}

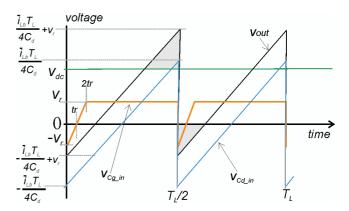


Figure 7. v_{out} is composed by the sum of v_{cg} and v_{cd} , inverted by the H-bridge during the interval (T/2,T). If t_r is neglected, the waveform of v_{out} can be simplified as shown.

shown in Fig.7. This is valid when (10) is accomplished.

From the geometry of the v_{out} waveform in Fig.7 and based on (4), this negative current ripple is calculated in (11) neglecting t_r . In the same manner, the positive current ripple is calculated from the shadowed area corresponding to $v_{out} < 0$ (Fig.7).

$$t_r = \frac{2v_r c_g}{\overline{\iota_{l,b}}}. (9)$$

$$t_r \ll \frac{T_L}{2}$$
, implies: $\overline{t_{Lb}} \gg \frac{4v_r C_g}{T_I}$. (10)

$$\Delta i_{Lb+} = \frac{1}{2L_b} \left(\frac{T_L}{4} - \frac{v_r * C_d}{\overline{i_{Lb}}} \right) \left(v_r - \frac{\overline{i_{Lb}} * T_L}{4C_d} \right). \tag{11}$$

$$\Delta i_{Lb-} = \frac{1}{2L_b} \left(\frac{T_L}{4} + \frac{(v_r - v_{cd}) * C_d}{\overline{\iota_{Lb}}} \right) \left(v_r - v_{cd} + \frac{\overline{\iota_{Lb}} * T_L}{4C_d} \right). \quad (12)$$

With (9)-(12) it can be found the approximated value of the maximum i_{Lb} ripple produced as a consequence of the characteristic waveform of v_{out} .

On the other hand, note that the buck inductance and the DBD capacitances are connected as an L_bC_d series resonant circuit, originating sinusoidal oscillations between the switching instants. If the control is not fast enough to control this oscillation, the stability of the converter is affected.

The resonant frequency can be estimated by neglecting the breakdown time (10) and in consequence neglecting the effect of the gas capacitance Cg. It is desired to avoid uncontrollable oscillations of the current between the buck switching instants

A criteria to reduce the effects of the sinusoidal oscillation, is to have a resonant frequency f_{osc} such that i_{Lb} does not start to decrease due to the oscillation before the next switching instant, this is done with an oscillation period T_{osc} greater or equal to $2T_L$. This condition is achieved when (13) and (14) are respected.

$$T_{osc} = 2\pi \sqrt{L_b C_d} \ge 2T_L. \tag{13}$$

$$\rightarrow$$
 in the worst case: $L_b = \left(\frac{T_L}{\pi}\right)^2 \frac{1}{C_d}$ (14)

VI. SIMULATION

Using the circuit simulation software PSIM®, a simulation of the converter has been done in order to validate the converter topology and the explained equations.

Based on a DBD excimer lamp with the equivalent electric model in Fig.4, and the experiments by [13], it has been determined the lamp power to be supplied and the lamp current frequency. Because the lamp voltage achieves approximately 6kV under the experimental conditions [13], a step-up transformer has been implemented to connect the inverter to the lamp. This will reduce the voltage in the converter side, which is necessary to protect the converter components against

overvoltage. It has been found that it is possible to use commercial electronic switches working up to 1000V, in consequence a transformer ratio 1/10 is used.

From (6)-(8), v_{dc} =500V has been selected. With this voltage, the buck switches Sa-Sb can perform an average duty cycle, such that v_{in} accomplishes (6), with a breakdown voltage of 180V, seen in the primary side of the transformer.

The buck inductance L_b is calculated from (9)-(12), with a suited i_L maximum ripple of 10%. In the simulated circuit, shown in Fig.4, ideal bidirectional switches have been used, because the aim of this design is to validate the converter concept. Based on simulations, proper switch implementation can be done.

The waveforms in Fig.8 correspond to the simulation of the converter controlling the H-bridge for D_L =100%, with t_{ov} =0s in the H-bridge switches. Other parameters of the converter simulation are described in Fig.8. As can be observed in Fig.8d waveform I(Lb), which represents i_{Lb} , the ripple of the inductance current does not overpass i_{max} neither i_{min} beyond the maximum 10% calculated from (11)-(12).

The waveforms in Fig.9, correspond to the simulation of the converter controlling the H-bridge for D_L =60%, with t_{ov} =100ns in the H-bridge switches. As can be observed in Fig.9d, waveform I(Lb), which represents i_{Lb} , does not overpass i_{max} because v_{out} is never negative. This is explained because during t_{ov} , the lamp is connected in parallel to a short circuit, and this connection is reflected through the ideal transformer. It is predicted that, if in the circuit are included the parasitic elements and the response times corresponding to commercial components, this connection during t_{ov} is not a perfect short-circuit because the inductance of the transformer (not included in the simulation) and the switching delays will not allow an instant change in v_L .

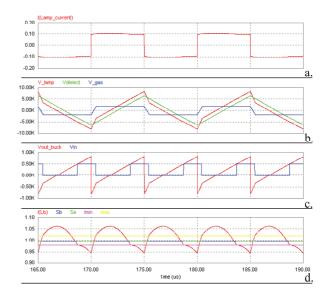


Figure 8. i_{max} =1.02 A; i_{min} =0.98 A; f_L =100 kHz; t_{ov} = D_L =100%

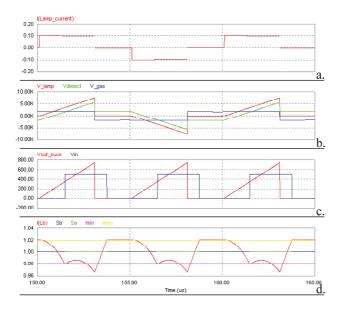


Figure 9. i_{max} =1.02 A; i_{min} =0.98 A; f_L =100 kHz; t_{ov} = D_L =60%

VII. CONCLUSION AND PERSPECTIVES

The proposed converter topology allows the successful control of the DBD lamp current. This structure can vary frequency, duty cycle and amplitude of current, making it suitable to be implemented for the evaluation of UV radiation in excimer lamps.

The operating principle of the converter and the control strategy are developed with a theoretical analysis, and validated using simulations, which include the electrical model of the DBD lamp. This study shows that a high value inductance is expected in the buck converter, in order to obtain an acceptable output current ripple.

Within the forthcoming challenges, it is important to mention the design of the magnetic circuits: the buck inductance and the high-voltage transformer; additionally, the study of alternative control strategies, as PWM, as an option to reduce the necessary inductance value.

This converter will be a convenient tool to identify the interactions between the electric variables and the UV water decontamination process efficiency.

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