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Identification of GaN Buffer Traps in Microwave Power AlGaN/GaN HEMTs Through Low Frequency S-Parameters Measurements and TCAD-Based Physical Device Simulations

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ABSTRACT In this paper, the type, activation energy (E_a) and cross section (σ_n) of the GaN buffer traps existing in the AlGaN/GaN high-electron mobility transistors are investigated through low frequency (LF) S-parameters measurements. Furthermore, we present the 2-D physics based TCAD numerical simulation analysis of this device. The dc simulation results are calibrated to match with the experimentally measured I - V characteristics and this allows to qualitatively estimate the concentration of traps (N_T) present in the GaN buffer. Knowing the measured trap energy level and the estimated trap concentration N_T , TCAD physical simulations are performed at various temperatures in order to extract the LF- Y_{22} admittance parameter. Interestingly, the LF- Y_{22} simulation results are found to be in good agreement with the measurements and this result strongly suggests that LF admittance dispersion is an effective tool in identifying the traps present in the GaN buffer. Moreover, this paper reveals that acceptor-like traps with an apparent concentration of $5.0 \times 10^{16} \text{ cm}^{-3}$ and with the apparent trap energy level of 0.4 eV below the conduction band are located in the GaN buffer.

INDEX TERMS Gallium-nitride (GaN), high electron mobility transistor (HEMT), buffer traps, TCAD simulation, low frequency S-parameters.

I. INTRODUCTION

In recent years, GaN material receives significant attention due to its superior material properties [1], [2] such as wide bandgap, high electron mobility, high saturation velocity and high breakdown electric field. Moreover, there has been tremendous development focused on the growth of GaN high electron mobility transistor (HEMT) device technology including improving the material quality of epitaxial [3] and passivation layers [2], selecting the appropriate substrate materials [3], [4] implementing the field plates [2], [5] and ultra-short gate lengths [6], [7] and optimizing the AlGaN barrier thickness [6]. All these technological improvements enabled GaN HEMT technology as a disruptive technology for high-power microwave and mm-wave applications [8]. Furthermore, GaN HEMT device is capable of delivering high breakdown voltage, high switching speed and

low on-resistance (R_{ON}), which makes it ideal for power switching applications [9].

Although GaN HEMT devices have demonstrated their superior performance [3], [6], the presence of deep levels in the structure degrades the dynamic device performance [10]. Indeed, the density of two-dimensional electron gas (2DEG) at the heterostructure interface is altered due to the capture of electrons by the deep levels either present at the surface, or barrier or buffer layer [10]. Moreover, these traps are slow in nature and thus undermines the device reliability [11], [12]. Several types of trapping effects have been reported in [9]–[11]. However, the origin and properties of these traps remain unclear. Trapping effects causes current collapse – a recoverable reduction in drain current after the application of high biasing voltage, transconductance-frequency dispersion, gate lag, drain lag, and restricted microwave output

power [9]. Several studies have also demonstrated that the presence of Fe-doped GaN buffer causes enhanced current collapse mechanism [13] and thereby deviates from expected microwave performance. Therefore, the analysis of traps and identifying the ways to minimize the trapping effects is an active research area to explore. There are various measurement techniques [9]–[16], such as deep level transient spectroscopy (DLTS), gate and drain lag transients, high-voltage transient CV, frequency dependent transconductance dispersion and low frequency noise (LFN) measurements are available to investigate the trapping behavior of the GaN HEMT devices.

In this work, traps in the buffer region of a GaN/AlGaIn/GaN HEMT transistor grown on SiC substrate have been characterized using low frequency S-parameters measurements [17]. These measurements allow us to determine the activation energy E_a and the cross section σ_n of the traps existing in the GaN buffer. Furthermore, we also present the TCAD physics based DC and LF- Y_{22} simulation results to validate our measurement technique. Our simulation results strongly suggests that LF output admittance measurement technique could be an efficient tool for identifying the traps in the buffer region of GaN HEMT devices. Furthermore, our simulation results insist that traps in the AlGaIn barrier cannot be identified using the aforementioned technique. The paper is organized as follows. Section II briefly describes the HEMT device under test and it also discusses the extraction of trap parameters from LF measurements. In Section III, we present results obtained using LF S-parameters measurements. Furthermore, we also describe the physics-based TCAD numerical device simulation approach of GaN/AlGaIn/GaN HEMT device and discusses the obtained DC and LF admittance simulation results. Finally, Section IV concludes the paper.

II. TRAP CHARACTERIZATION METHODOLOGY

The GaN/AlGaIn/GaN HEMTs were grown by metal-organic chemical vapor deposition (MOCVD) on a SiC substrate. The fabricated transistor has a gate length (L_G) of 0.25 μm and a width, W_G of 600 μm (eight fingers, $n = 8$), whereas the nominal gate-source (L_{SG}) and gate-drain (L_{GD}) spacings are 0.8 and 2.7 μm , respectively. A field-plate is placed from middle of the gate electrode and extends a distance L_{FP} (field-plate length) of 1 μm towards the drain electrode.

On-wafer pulsed-IV measurements have been carried out by using an AMCAD BILT pulsed IV system. The gate and drain terminals are pulsed at $V_{GSQ} = V_{DSQ} = 0$ V. The measured pulsed I-V characteristics are used to calibrate the TCAD physical simulation model. The corresponding measurement results are presented along with the simulation results in the next section. The low frequency S-parameters measurements in the frequency range of 10 Hz to 10 MHz have been carried out using an Agilent E5061B network analyzer for various thermal chuck (T_{chuck}) between 25°C

and 100°C. More details on the measurement configuration used for this work is given in [18]. A standard SOLT method is used to calibrate the network analyzer.

The measured LF S-parameters are converted into their equivalent Y-parameters. The traps in the GaN buffer cause the frequency dispersion in the measured device output admittance Y_{22} parameter. The corresponding emission time (τ_n) of the traps can be calculated from the peak value of imaginary part of the Y_{22} parameter by using (1) [17]:

$$f_{l,peak} = f_{\text{Im}ag[Y_{22}]} = \frac{1}{2\pi\tau_n} \quad (1)$$

As the temperature increases, the electron emission rate increases, hence the emission time constant decreases which causes the peak value of Y_{22} parameter shift towards higher frequencies. Therefore, the activation energy (E_a) and cross section (σ_n) of the traps can be determined using the Arrhenius equation (2) [17]:

$$\frac{e_n}{T^2} = \frac{\sigma_n \cdot A_n}{g} \exp\left(-\frac{E_a}{k \cdot T}\right) \quad (2)$$

with

$$A_n = \frac{N_C \cdot V_{th}}{T^2} \text{ and } e_n = \frac{1}{\tau_n}$$

where, e_n is the trap emission rate, T is the temperature, σ_n is the capture cross section of traps, N_C is the effective density of states of electrons in the conduction band, V_{th} is the thermal velocity of electrons, g is the degeneracy factor ($g = 1$), E_a is the trap activation energy and k is the Boltzmann constant.

The emission time constant is calculated at each measurement temperature using (1). Then, by using the Arrhenius equation, the plot of $\ln(\tau_n T^2)$ versus $1/kT$ yields the straight line, whose slope and intercept determine the apparent activation energy and cross section of the traps existing in the GaN buffer.

III. RESULTS AND DISCUSSIONS

A. EXPERIMENTAL

Fig. 1(a) shows the imaginary part of the Y_{22} parameter extracted using LF S-parameter measurements for the frequency range of 100 Hz to 10 MHz and for the bias conditions of $V_{DS} = 30$ V and $I_D = 57$ mA (deep class AB operation). This operation mode is selected since it is being widely preferred nowadays for designing RFPA. The emission time constant is calculated as a function of temperature from the peak value of imaginary part of Y_{22} using (1). Fig. 1(b) shows the Arrhenius plot of traps extracted using LF measurements. The apparent activation energy and cross section of the traps determined are 0.4 eV and $1.89 \times 10^{-16} \text{cm}^2$, respectively. Therefore, these results confirm that traps exist in GaN buffer with an apparent activation energy of 0.4 eV below the conduction band. The physical origin of this traps could be related to iron doping present in the GaN buffer.

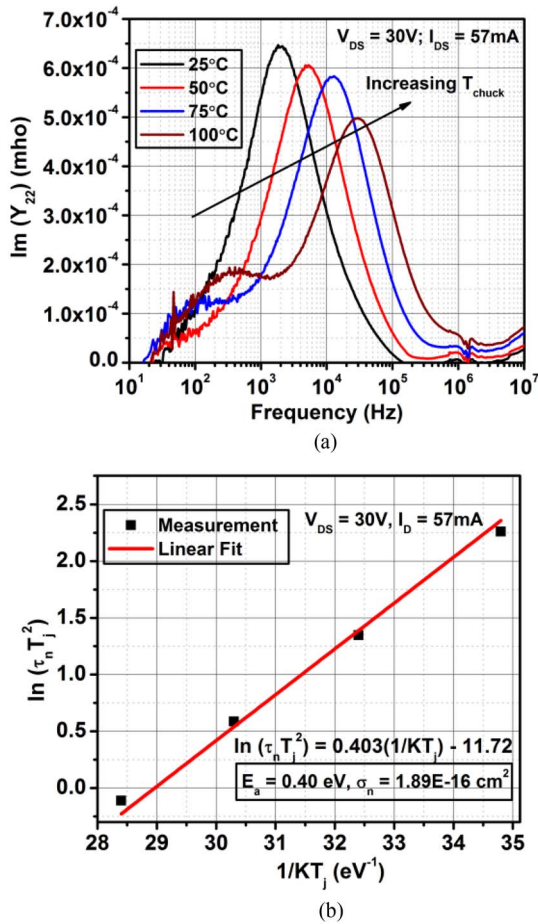


FIGURE 1. (a) Imaginary part of the measured Y_{22} -parameter vs. frequency for the T_{chuck} ranges between 25°C and 100°C. Bias conditions: $V_{DS} = 30\text{ V}$ and $I_D = 57\text{ mA}$. (b) Extracted Arrhenius plot using Y_{22} -parameter.

TABLE 1. Summary of the material parameters used in simulations.

Material property	Units	GaN	AlGaIn
Electron Mobility	$\text{cm}^2/\text{V} \cdot \text{s}$	1200	300
Bandgap	eV	3.50	4.39
Relative permittivity	-	8.9	8.8
Electron affinity	eV	4.0	3.41
Electron saturation velocity	cm/s	2.5×10^7	1.1×10^7
Effective conduction band density of states	cm^{-3}	2.23×10^{18}	2.71×10^{18}
Effective valence band density of states	cm^{-3}	2.51×10^{19}	2.06×10^{19}

B. TCAD PHYSICAL SIMULATION

Two-dimensional physical simulations have been carried out using the commercially available TCAD Sentaurus software from Synopsys Inc. Poisson's equation, and continuity equations for both electrons and holes, and the drift-diffusion (DD) model transport equations are solved self-consistently. The cross section of the structure used for the simulation is shown in Fig. 2 (a). More experimental details on the thickness of each layers of the device cannot

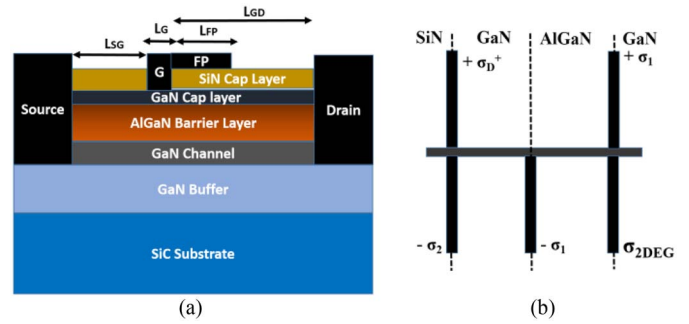


FIGURE 2. (a) Cross-sectional structure of the simulated GaN/AlGaIn/GaN HEMT on SiC substrate. (b) Polarization charges defined in the physical simulation.

be given due to confidential reasons. The presence of spontaneous and piezoelectric polarization charges induces the electric field in the device structure and this contributes to the formation of 2DEG density at the AlGaIn/GaN heterostructure interface, even without the requirements for any intentional AlGaIn barrier doping [19]. In absence of any external electric fields, the polarization charges formed are of course, equal in magnitude and opposite in sign to maintain the overall charge neutrality of the device [20]. The polarization charges used in the simulation have been calculated based on the theoretical method given in [19]. Fig. 2(b) shows the polarization charges used at each material interface in the physical simulator. The values of σ_1 and σ_2 are $\pm 1.23 \times 10^{13} \text{ cm}^{-2}$ and $-2.0 \times 10^{12} \text{ cm}^{-2}$, respectively. Moreover, the surface donors are introduced at the GaN/SiN interface in order to form the 2DEG in the channel. Donor-like traps (σ_D^+) with a density of $1.0 \times 10^{13} \text{ cm}^{-2}$ and a specified energy level of 0.2 eV below the conduction band are defined. Iron doping (Fe-doped) are introduced in the GaN buffer with a constant concentration of $3.0 \times 10^{16} \text{ cm}^{-3}$. In addition, we consider acceptor-like traps in the GaN buffer whose trap energy is placed at 0.4 eV below the conduction band and the assumed trap concentration is $5.0 \times 10^{16} \text{ cm}^{-3}$. Electron (σ_n) and hole (σ_p) capture cross sections of GaN buffer traps are assumed to be 3.2×10^{-18} and $1.0 \times 10^{-20} \text{ cm}^2$, respectively. For the simulation, constant mobility and field dependent mobility models are included for electrons and holes and Shockley-Read-Hall recombination model is used for carrier generation and recombination. The summary of the material parameters used in our simulations are listed in Table 1.

C. COMPARISON: IDENTIFICATION OF GAN BUFFER TRAPS

The simulation model is calibrated to match with the experimentally measured I-V characteristics. Further details about calibrating the TCAD physical models can be found in [18]. Fig. 3(a) and 3(b) show the simulated and measured transfer and output characteristics of the device at $T_{chuck} = 25^\circ\text{C}$. Fig. 3(c) shows the comparison of the simulated and measured I_D - V_{DS} characteristics at $V_{GS} = 0\text{ V}$

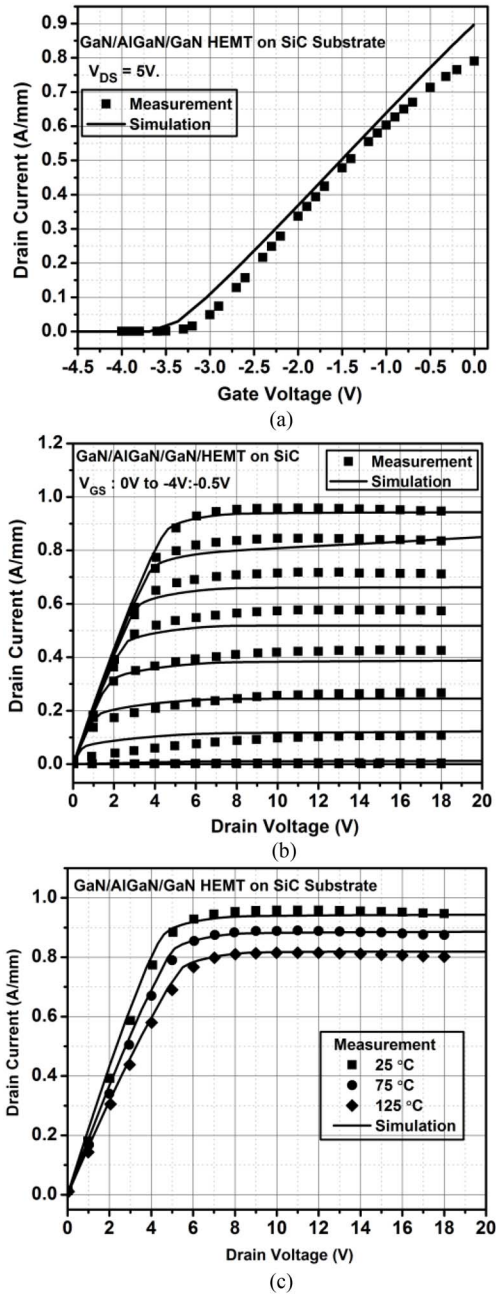


FIGURE 3. Comparison of the simulated (solid lines) and measured (symbols) I-V characteristics. (a) Transfer characteristics at $V_{DS} = 5$ V and $T_{chuck} = 25^\circ\text{C}$. (b) Output characteristics for varying V_{GS} between 0 V and -4 V in steps of 0.5 V and at $T_{chuck} = 25^\circ\text{C}$. (c) Output characteristics for T_{chuck} varying between 25°C and 125°C and at $V_{GS} = 0$ V.

and for varying T_{chuck} between 25°C and 125°C . The good agreement between the simulation results and the measured data confirms the validity of the TCAD simulation model calibration.

Using the calibrated TCAD simulation model, LF admittance parameters simulation has been carried out at bias conditions of $V_{DS} = 30$ V and $I_D = 57$ mA. The self-heating effect of the device has been taken into account

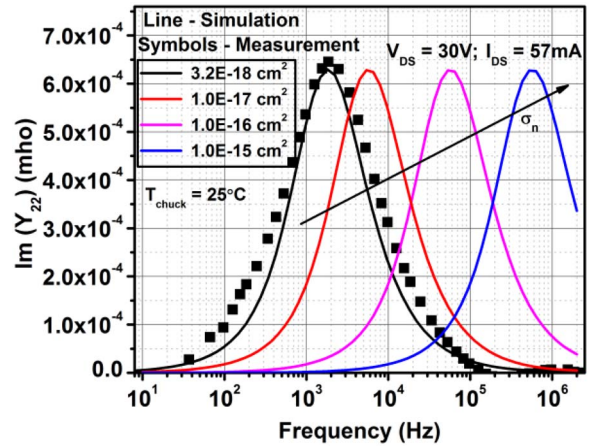


FIGURE 4. Imaginary part of the simulated (lines) and measured (symbols) Y_{22} -parameter vs. frequency for $T_{chuck} = 25^\circ\text{C}$ and for different electron cross section parameters (σ_n). Bias conditions: $V_{DS} = 30$ V and $I_D = 57$ mA.

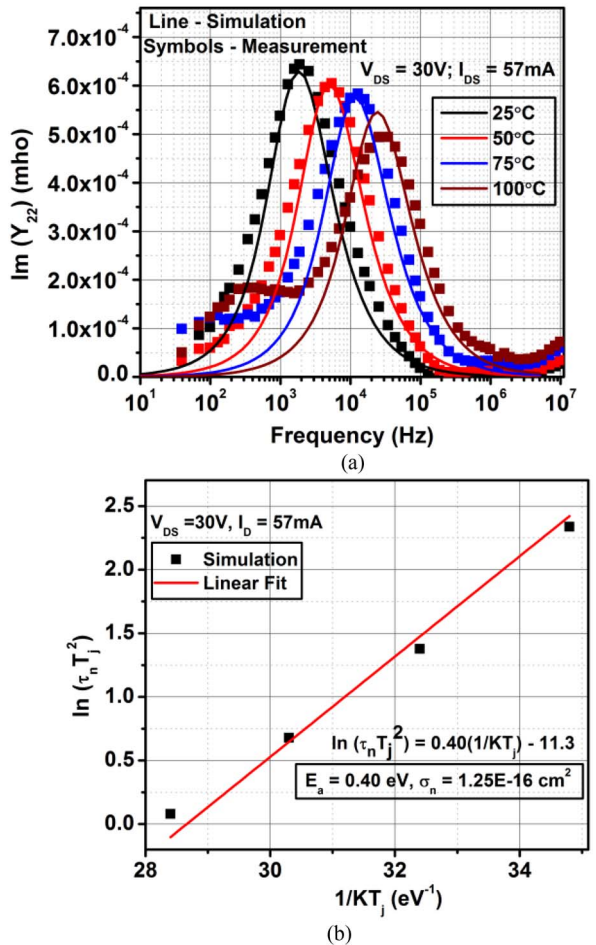


FIGURE 5. (a) Imaginary part of the simulated (lines) and measured (symbols) Y_{22} -parameter vs. frequency for the T_{chuck} ranges between 25°C and 100°C . Bias conditions: $V_{DS} = 30$ V and $I_D = 57$ mA. (b) Extracted Arrhenius plot using the simulated Y_{22} -parameter.

by extracting the thermal resistance (R_{TH}) of the device using the method described in [21]. The extracted R_{TH} of the device is 20.42°C/W . Thus, by knowing the R_{TH} value,

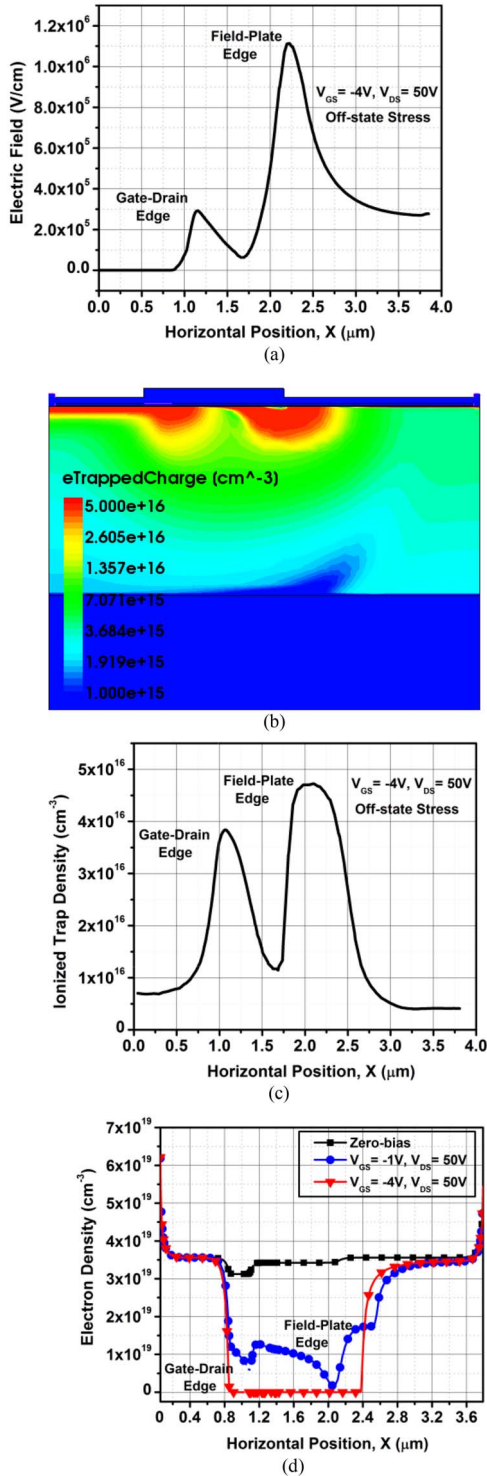


FIGURE 6. TCAD simulation results of AlGaIn/GaN HEMT under off-state stress conditions ($V_{DS} = 50$ V and $V_{GS} = -4$ V). (a) Electric field profile (b) Ionized acceptor traps density profile (c) Ionized trap density distribution through the GaN buffer layer (d) Electron distribution through the GaN buffer layer for zero bias ($V_{DS} = V_{GS} = 0$ V) and different gate bias conditions.

the T_{chuck} and the power dissipation (P_{diss}), the junction temperature (T_j) has been estimated for different T_{chuck} and are used in simulations. Fig. 4 shows the simulated Y_{22}

parameter for different electron cross section parameters (σ_n) and for $T_{chuck} = 25^\circ\text{C}$. The σ_n value of $3.2 \times 10^{-18} \text{ cm}^2$ yields an excellent match between the measured and simulated Y_{22} parameters over the temperature range of 25°C to 125°C . Therefore, this particular value of σ_n is used in all our TCAD physical simulations. Fig. 5 (a) shows the comparison of the simulated and measured imaginary part of the Y_{22} parameter for the frequency range of 10 Hz to 10 MHz and for various T_{chuck} . A good agreement obtained between the TCAD simulations and measurements confirms the presence of acceptor-like traps in the GaN buffer of the device. To the best of our knowledge, this is the first time that such a comparison between the simulated and measured Y_{22} parameter has been reported for the GaN HEMT device technology. Fig. 5(b) shows the Arrhenius plot extracted using the simulated Y_{22} parameter. The extracted trap energy level is 0.4 eV which corresponds to the acceptor trap energy level value introduced in the physical simulation.

The device is biased into off-state stress conditions ($V_{DS} = 50$ V and $V_{GS} = -4$ V, below pinch-off) in order to analyze the electric field profile in the device. The electric field obtained from the TCAD simulation is shown in Fig. 6(a). Two peaks are observed, i.e., one at the drain side edge of gate terminal and the other at the field-plate edge in gate-drain spacing region. Moreover, the electric field below the field-plate edge is higher than under the drain side gate edge [15]. The corresponding ionized trap charge density profile is shown in Fig. 6 (b). It can be observed that maximum trapping (Fig. 6 (b) and (c)) occurs below the field-plate edge where the electric field is maximum. Furthermore, these ionized acceptor traps depletes the 2DEG in the channel. The corresponding simulated electron density in the channel region for different device biases is shown in Fig. 6 (d). It could be concluded that trapping occurs both under the drain side of gate edge and field-plate edge.

D. IDENTIFICATION OF AlGaIn BARRIER TRAPS

Fig. 7 (a) shows the imaginary part of the simulated Y_{22} parameter when both the acceptor-like and donor-like traps are introduced in the AlGaIn barrier. Acceptor-like traps are placed at 0.6 eV below the conduction band whereas donor-like traps are placed at 0.3 eV below the conduction band. The assumed trap concentration (N_T) is $1 \times 10^{16} \text{ cm}^{-3}$ and the electron and hole cross sections are assumed to be $\sigma_n = 1 \times 10^{-18} \text{ cm}^2$ and $\sigma_p = 1 \times 10^{-20} \text{ cm}^2$, respectively. The traps introduced in the AlGaIn barrier does not cause any dispersion in the simulated Y_{22} parameter. Moreover, when the traps are introduced in both the AlGaIn barrier (both acceptor and donor-like traps) and GaN buffer ($N_T = 5 \times 10^{16} \text{ cm}^{-3}$, $E_{TA} = 0.4$ eV), dispersion occurs in the simulated admittance parameter (Fig. 7 (b)) and the activation energy value extracted from the Arrhenius plot (Fig. 7 (c)) corresponds to the trap energy level value used in the GaN buffer. This result demonstrates that AlGaIn barrier traps cannot be identified using this method. However, traps in

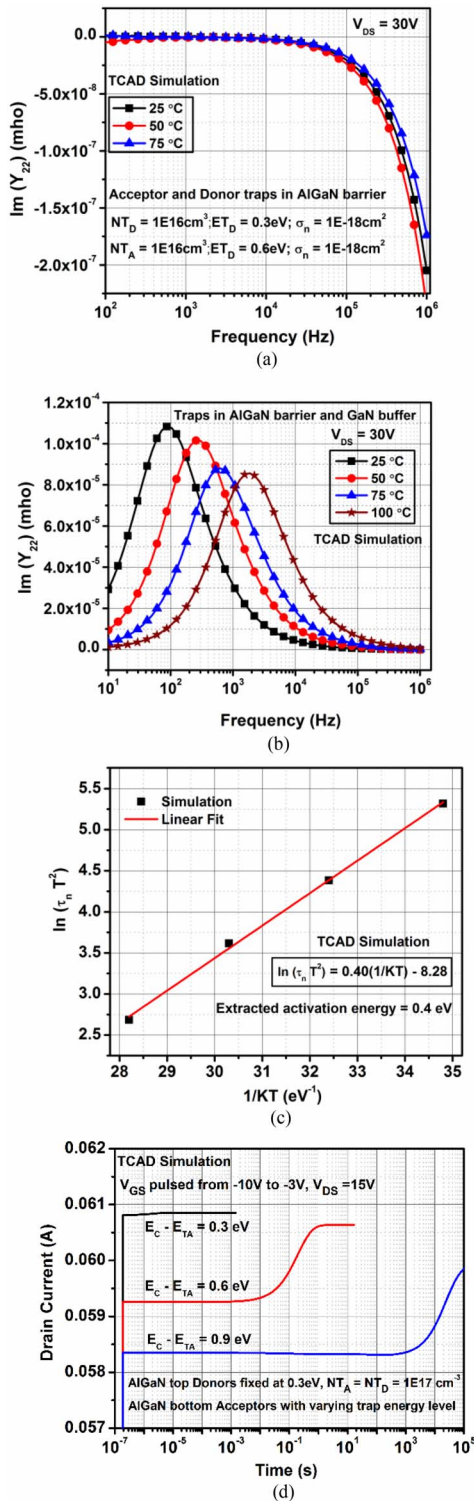


FIGURE 7. Imaginary part of the simulated Y_{22} -parameter vs. frequency for different T_{chuck} . (a) Traps in the AlGaIn barrier. (b) Traps in both AlGaIn barrier and GaN buffer. (c) Extracted Arrhenius plot using simulated Y_{22} -parameter. (d) Simulated gate-lag transient characteristics with acceptor and donor-like traps in the AlGaIn barrier.

the AlGaIn barrier can be identified via different measurement techniques such as gate/drain lag transients. Fig. 7 (d) shows the simulated transient characteristics of the device

when the gate is pulsed from -10 V to -3 V and the drain voltage is fixed at 15 V. Donor-like traps are introduced in the AlGaIn barrier at the specified energy level of 0.3 eV below the conduction band. Similarly, acceptor-like traps are placed at the bottom region of AlGaIn barrier which is close to the GaN channel. Traps energy level (E_{TA}) is considered as a varying parameter to study the influence of energy level of traps on transient characteristics. It can be clear from the Fig. 7 (d) that the traps introduced in the AlGaIn barrier capture electrons from the channel and therefore, the drain current remains at a lower value for some period until the traps start to re-emit the captured electrons. Moreover, deeper the energy level of acceptor-like traps, more the possibility of ionization of acceptors, larger the time required to reach the steady state drain current. Furthermore, the donor-like traps has less influence (verified using several simulations) on the simulated transient characteristics as they are located away from the channel. This result suggests that traps in the AlGaIn barrier can be identified using gate-lag measurements.

IV. CONCLUSION

In this work, we have extracted the activation energy and cross section of traps present in the GaN buffer of the GaN/AlGaIn/GaN HEMT device using LF S-parameter measurements. Moreover, we have also presented the TCAD physical simulation results for this device. The TCAD physical simulation model has been calibrated and then, LF admittance simulations have been performed. A good agreement has been achieved between the simulated and measured Y_{22} parameter. This strongly suggests that LF admittance measurement is an effective tool for characterizing GaN buffer traps. Furthermore, our simulation results reveal that traps in the AlGaIn barrier can be identified using gate/drain lag transient measurements. Therefore, the simulation results presented here establish a correlation with measurements in order to identify the location of traps and this could provide an efficient feedback for further improving the GaN HEMT technology and its respective RF performance.

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