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An Analog-to-Digital Converter Immunity Modeling based on a Stochastic Approach

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Abstract—This paper deals with the immunity study of a 12-bits SAR (Successive Approximation Register) Analog-to-Digital converter from Analog Devices facing to an EM (ElectroMagnetic) disturbance. The RF (Radio Frequency) disturbances are injected through the V_{dd} pin of the studied component and its behavior is modeled. Due to the dispersion of conversion results, the approach proposed here is based on a stochastic modeling. The identification of the statistical distributions, describing the behavior of the disturbed component, is performed using the Akaike information criterion. Modeling results are compared to DPI (Direct Power Injection) measurements.

Index Terms—A/D converter, EMC/EMI, CDF (Cumulative Density Function), IB (Immunity Behavioral), DPI (Direct Power Injection), PDF (Probability Density Function), Stochastic process.

I. INTRODUCTION

THE automotive, avionic and military industries face a significant dilemma arise by the continuous and fast evolution of integrated circuit manufacturing processes.

As a consequence of that progress, once an IC is no longer manufactured, the embedded system that uses it becomes obsolete. Today, the electronic parts that compose a product have a life cycle that is significantly shorter than the life cycle of the product. To face that problem, many obsolescence mitigation approaches are proposed. The two most common approaches, followed by the industrials, consist in buying and storing or multi-sourcing.

The first method, although costly (inventory management), is not necessarily efficient. The issue here is related to the storage conditions which can impair the performance of the integrated circuits. Indeed, the storage conditions can modify the components features during the time. The second method consists in use of components with identical shape fit and function using newer technologies.

But, in all cases from an EMC (ElectroMagnetic Compatibility) point of view, these methods are not sufficient to insure the functionality and EMC compliance of the final system. This is why industrials are seeking for new methods to anticipate EMC/EMI (ElectroMagnetic Compatibility/Electromagnetic Interferences) related to COTS (Commercial Off-The-Shelf Products) issues, and so avoid

additional production costs. One of the solution is to develop predictive models which will be used in simulation tools to ensure long term EMC compliance of electronic equipment (emissivity, immunity) [1-2]. These models can be electrical in order to be used in electrical simulator as Spice, ADS, Multisim or can be behavioral in order to be used in simulator like Matlab or Simpler which can use VHDL-AMS descriptions for example [3-10].

The aim of this paper is to present the methodology to determine a behavioral model of an Analog-to-Digital converter from Analog Devices face to an EM disturbance. In this study, we consider a black box modeling approach, where the electromagnetic disturbances are described by pure mathematical models with strong abstraction level.

Here, the immunity of this IC against the EM disturbances is calculated thanks to a stochastic modeling approach. The key motivation for this work is the non-deterministic nature of the conversion results given by the component under test and the dispersion range due to accuracy [9-10]. Moreover, the model is developed and validated thanks to DPI (Direct Power Injection) measurements. The simulated and measured immunity levels are then compared and analyzed.

After a brief description of the considered DUT and PCB in section II, section III introduces the immunity study and the measurement setup used to extract the influent parameters. Then section IV presents the study of the component behavior on nominal conditions. Section V presents in details the approach followed to develop and to validate the immunity model. The conclusions and perspectives are finally exposed in section VI.

II. DESCRIPTION OF THE DEVICE UNDER TEST AND THE DEMONSTRATOR

This part of the paper gives an overview of the considered device under test and the electronic board used as demonstrator.

A. Analog-to-digital converter characteristics

The tested component is the AD7476 from Analog Devices. It is a high speed, low power and successive approximation Analog-to-Digital Converter (ADC). The conversion process and data acquisition are controlled using a SPI (Serial Peripheral Interface) communication interface. Table 1 summarizes some of its characteristics.

Table 1: AD7476 configuration and function descriptions

Architecture	SAR (Successive Approximation Register)
Resolution	12 bits (serial data stream)
Analog Input	
Power supply Input (V_{dd})	From 2.35 V to 5.25 V
Analog Input (V_{in})	From 0 V to V_{dd}
Frequency input (f_{in})	≤ 100 kHz
Logic Input/Output	
selk (serial clock)	Clock source for AD7476 conversion process ($f_{selk} = 20$ MHz)
\overline{CS} (Chip Select)	Initiating conversions on the AD7476 and framing the serial data transfer
Sdata	16 bits frame

B. Demonstrator characteristics

The component is mounted on the PCB presented in Fig. 1. The tested board has four layers with an overall thickness equal to 1.6 mm. The material used is the standard FR4 epoxy.

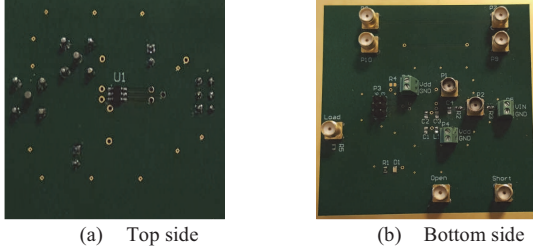


Fig. 1. Demonstrator.

III. IMMUNITY STUDY AND MEASUREMENT SETUP

A. Immunity modelling

The study and behavioral modelling of a component's immunity conventionally comprises two parts [5-7], [9-10]. The first corresponds to the determination of the coupling paths by which the perturbation is transmitted to the sensitive part of the component and the second consists in evaluating the behavior of the sensitive core. Finally, the model output is compared to an application-dependent user-definable threshold.

The present paper is focused on the modeling of the active part of the device in order to characterize the device dysfunction due to electromagnetic disturbance. The model output should describe the IC behavioral response to a disturbing signal.

B. DPI measurement setup

The DPI (Direct Power Injection) measurements were carried out by injecting an interference signal through a bias tee connected to one of IC pins as presented in Fig. 2. Here, we will focus on injecting interferences only through the V_{dd} pin (power supply pin of the ADC). This injection of interferences through V_{in} (voltage to be convert by the ADC) has been performed but will not be presented in this paper.

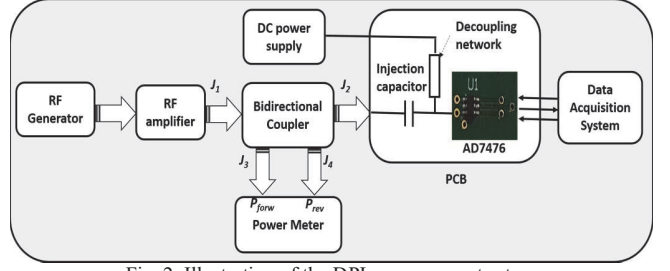


Fig. 2. Illustration of the DPI measurement setup.

As in classical DPI tests, we have to quantify the power that induces the device under test dysfunction. The power P_{trans} absorbed by the tested component is considered as a relevant parameter to characterize the signal drift at the observable output. It is deduced from forward and reverse powers (P_{forw} and P_{rev} , respectively) as shown by Eq. (1).

$$P_{trans} = P_{forw} - P_{rev} \quad (1)$$

Note that P_{forw} and P_{rev} are measured from the bidirectional coupler with a power meter (see Fig. 2).

IV. STUDY OF THE AD7476 IN STATIC MODE

Before injecting the RF disturbances through different pins of the ADC, we study the influence of V_{dd} and/or V_{in} on the conversion results under nominal conditions.

The test performed here consists in making N conversions for different pairs of values (V_{dd} , V_{in}). The decimal value of the conversion result can be calculated from Eq. (2).

$$D = E \left\{ \frac{V_{in}}{V_{dd}} 2^{12} \right\} \quad (2)$$

Where E means the integer part. From this equation, it is possible to recalculate (V_{out}) using Eq. (3).

$$V_{out} = \frac{V_{dd}}{2^{12}} D \quad (3)$$

The effect of the interferences will be observed by comparing V_{out} to V_{in} . According to the datasheet V_{out} should at most be equal to $V_{in} \pm 1$ LSB. This 1 LSB "error" is due to the intrinsic operation of the component. Note that the LSB (Least Significant bit) size for the AD7476 is $V_{dd}/4096$ and it is considered as the smallest possible variation of the output voltage. According to the datasheet of the AD7476, the V_{dd} range is from 2.7 V to 5.25 V and the analog input range (V_{in}) is from 0 V to V_{dd} . To test a large number of combinations of V_{dd} and V_{in} , we choose a V_{dd} variation from 2.7 V to 5.25 V with a step of about 0.25 V. Then we made a ten-random draw between 0 and 1 and the obtained value multiplied by V_{dd} gives V_{in} . For each couple of values (V_{dd} , V_{in}), the number of conversions is fixed to 100. Table 2 summarizes the ADC tested configurations.

Table 2: AD7476 tested configurations

Parameters	values
V_{dd}	[2.7 V; 5.25 V]; step ≈ 0.25 V
V_{in}	$V_{dd} * rand^1(10)$
Nbr of conversions	100

¹Rand is function to generate values distributed randomly

To quantify the error between V_{in} and V_{out} for 100 conversions without interferences, we use an indicator called MAPE (Mean Absolute Percent Error) which is expressed in percentage. It is calculated as the average of the unsigned percentage error, as shown in Eq. (4).

$$MAPE = \frac{1}{N} * \sum_{k=1}^N \left| \frac{V_{out} - V_{in}}{V_{in}} \right| * 100, V_{in} \neq 0. \quad (4)$$

For the values of V_{dd} between 2.7 V and 4 V and whatever V_{in} values, MAPE is quasi-null remaining less than 2%. However, for the values of V_{dd} between 4.25 V and 5.25 V the conversion errors are much more important and are increasing from 4% to 32%. Actually, the static parameters given in the datasheet are obtained for values of V_{dd} between 2.7 V and 3.6 V. This is why our study will be performed for V_{dd} values inferior or equal to 4 V.

Looking at most of the conversions results for each given pair of values (V_{dd} , V_{in}), we observe a dispersion in the results of 1 LSB as shown on the trace of the histograms plotted in Fig.3(a).

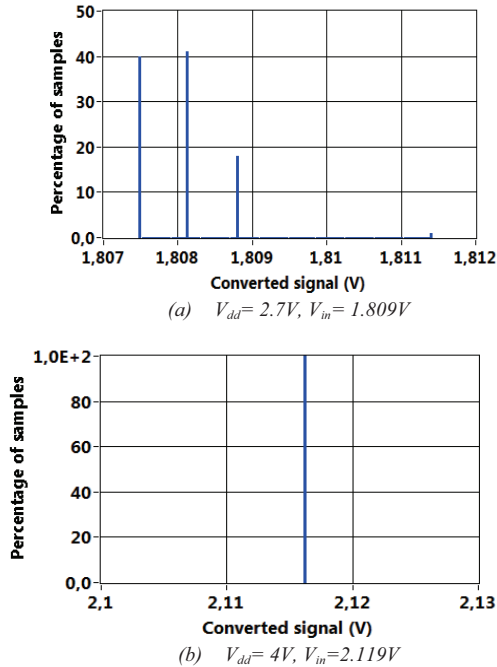


Fig. 3. Distribution of 100 conversions as a function of V_{out} performed by the ADC for different configurations (V_{dd} , V_{in})

As the obtained distributions are neither “predictable” nor “reproducible”, we have isolated some pairs of values (V_{dd} , V_{in}), for which the result of one hundred conversions is only represented by one bar as for example in the case of Fig. 3(b).

This means that the average is equal to V_{in} and that the standard deviation is equal to zero. The best pair of values identified here is: $V_{dd} = 4$ V and $V_{in} = 2.119$ V.

This pair of values is used to build the conducted immunity behavior model of the AD7476 in static mode against interference signals in the frequency bandwidth from 10 MHz to 1 GHz. At this stage of the study, thanks to [9-10] it is expected that DPI measurements will introduce dispersion into the ADC conversion results. According to previous study, that dispersion in the conversion results, corresponding to a stochastic behavior of the ADC, is due to the random phase of the interference and the stage of the ADC conversion process, driven by the system’s clock.

V. IMMUNITY BEHAVIORAL MODELLING BY STOCHASTIC APPROACH

The aim of this part of the study is to determine a mathematical function describing the immunity behavior of the component when an interference signal is injected through the V_{dd} pin. In order to define this model, several DPI measurements are performed according to Table 3 and Fig. 2.

Table 3: DPI configuration test

Parameters	values
V_{dd}	4 V
V_{in}	2.119 V
P_{forw}	[-20 dBm; 35 dBm]; step =1
f_{req}	[10 MHz; 1GHz]
Nbr of conversions	100

First of all, the interference signal is injected through V_{dd} and then 100 conversions and acquisitions are performed. This operation is repeated 1624 times (28 frequency values * 58 power values). For each RF disturbance injection, we obtain 100 values. As expected, the DPI measurement introduces dispersions in the conversion results as shown in Fig.4.

This is why we choose a stochastic approach to model the behavior of the A/D converter. Hence, for each pair of (P_{forw} , f_{req}) we have to identify the statistical distribution that reproduces as much as possible the samples obtained in measurement.

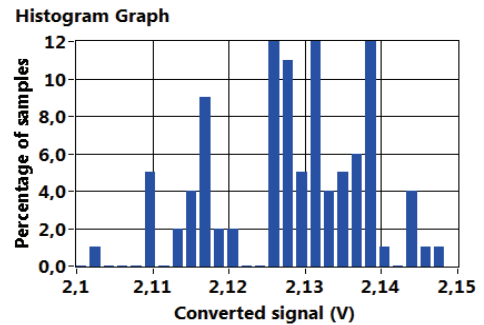


Fig. 4. Distribution of 100 conversions performed by the ADC during DPI test: $P_{forw} = 26.14$ dBm and $f_{req} = 1$ GHz

The most common law identification approach in the literature consists in using the cumulative distribution function (CDF) in addition with the calculation of the KS (Kolmogorov-Smirnov) distance [11]. In the case of our study, the selection

of models (or statistical distribution) is performed thanks to the MAICE (Minimum Akaike Information Criterion Estimate). This last one is widely used in literature and it is considered as an efficient tool for selecting parametric models [11]. The Akaike information criterion (AIC) is defined by the very practical and successful method of MLE (Maximum Likelihood Estimate) and by the number of the model parameters (k). The AIC is calculated by the formula given by Eq. (5).

$$AIC = (-2) * \log(MLE) + 2 * k \quad (5)$$

Thus, the best candidate statistical distribution will be the one that minimizes AIC and has the smallest number of parameters. Note that the number of parameters is useful to make a decision when the MLE is identical for two models. In order to validate the selected model, we use the Kolmogorov-Smirnov test. This statistical hypothesis test is often used to determine whether a random variable follows a given statistical distribution known for its continuous distribution function. We seek to approximate a sequence of N independent realization of a random variable by a continuous statistical function whose probability density function is fed by empirical characteristic parameters. The list of tested distributions and numbers of their parameters is given in Table 4.

Table 4: Tested statistical distributions and their parameters

Continuous statistical distribution	Number of parameters	Color code
Weibull	2	Red
Laplace	2	Blue
Uniform	2	Dark Red
Exponential	1	Green
Rician	2	Light Blue
Gamma	2	Light Green
Lognormal	2	Yellow
Rayleigh	1	Light Blue
Normal	2	Dark Blue
Extreme value (EV)	2	Pink
Nakagami	2	Black
Generalized extreme value (GEV)	3	Grey

The information criterion described above was used to select the most appropriate distribution (among the twelve statistical distributions listed in Table 4) for each injected disturbance. The statistical distribution selected by MAICE is then validated by the KS test. For this study, the confidence level of KS test is fixed to 10 %. This approach allows us to identify the best distribution that describes the 100 conversions performed by the A/D converter.

Figure 5 is an image composed of 1624 pixels. Each pixel corresponds to one injection disturbance condition (P_{forw}, f_{req}) and its color depends on the selected best law. Recall that each statistical distribution is characterized by a finite number of parameters (see Table 4) that are stored in a lookup table and depend on the frequency and the power of the injected signal. The models identified by the information criterion were validated by the Kolmogorov-Smirnov test. To show the adequacy of the selected model and the measurement data we make comparison between the empirical CDF (samples

resulting from measurements) and the theoretical CDF (samples generated by the identified law). As an example, figure 6 proposes the comparison between empirical and 3 different theoretical CDFs for one pixel. Thus, we can validate the selected model for each 1624 studied cases.

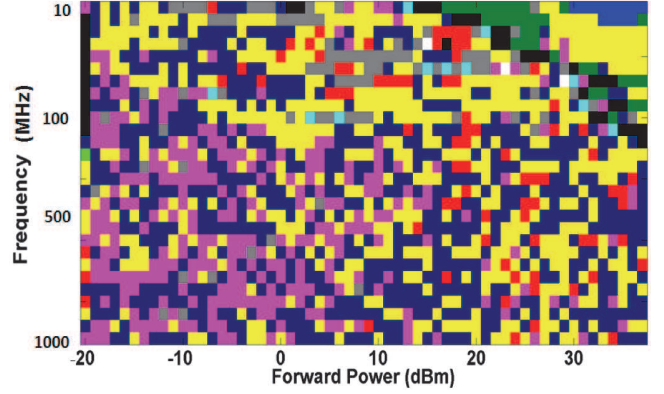


Fig. 5. The selected models when RF disturbances are injected through the V_{dd} pin

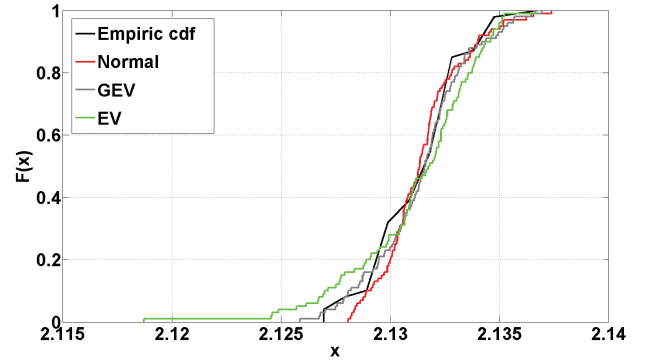


Fig. 6. Comparison between the empirical and theoretical CDFs ($f_{req} = 60$ MHz and $P_{forw} = -8$ dBm)

To simplify the mapping of Fig. 5, we decide to retain only one distribution law for all the 1624 studied cases. So, we plot the number of occurrence of each law on the mapping, the result is presented in Fig.7.

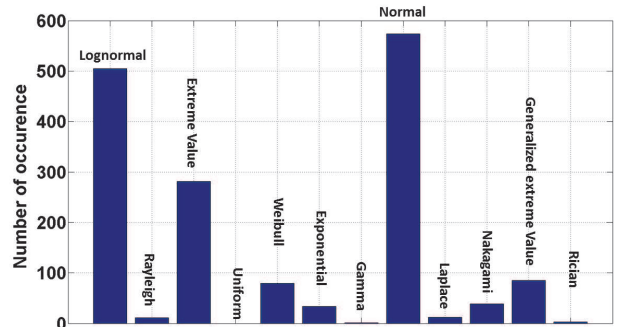


Fig. 7. Number of occurrence of each law

The results show a strong dominance of the normal, the lognormal, and the extreme values distributions. Finally, the selected model is the normal distribution. Its PDF (probability density function) is given by Eq. (6).

$$f(x; \mu, \sigma) = \frac{1}{\sigma(freq, P_{inj})\sqrt{2\pi}} \exp\left(-\frac{\left(x - \left(\frac{\mu(freq, P_{inj})}{2.119} * V_{in}\right)\right)^2}{2\sigma(freq, P_{inj})^2}\right) \quad (6)$$

Here, μ is the mean and σ is the standard deviation. These two parameters depend on the power and the frequency of the injected disturbance signal and are stored into a lookup table. By using the factor 2.119, corresponding to the particular V_{in} previously chosen in section IV, Eq. (6) can be used to model the behavior of the component whatever the V_{in} value is. Then, we compare results obtained from DPI measurement techniques to those generated by the model. The immunity criterion is computed using the MAPE (see Eq. (4)) quantifying the errors of conversion in percent. It is fixed to 0.7% (corresponding to an error of 15 LSB). Here, the conversion results (V_{out}) are generated by the PDF of the selected model and it is compared to the signal to be converted (V_{in}). Figure 8 gives the immunity curves between 10 MHz and 1 GHz plotted by using different laws.

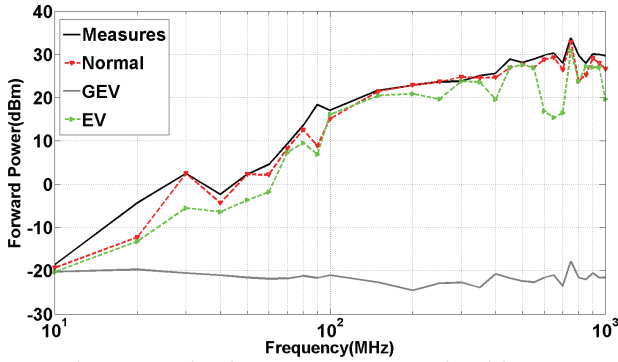


Fig. 8. Comparison between measurement and models

The results confirm that the normal law allows modelling the immunity behavior with a good agreement.

Finally, the purpose is to demonstrate that the model works for different values of V_{in} . Note that the choice of the polarized voltage V_{dd} has no impact on the conversion results when it takes value between 2.7 V and 4 V. As shown in Table 5, the DPI curves are computed and measured in three configurations of the ADC.

Table 5: Tested configurations

Test number	Vdd (V)	Vin (V)
test 1	4	2.12
test 2	2.7	0.88
test 3	3.5	1.9

Figures 9(a), 9(b) and 9(c) (test1, test2 and test3, respectively) give the immunity curves between 10 MHz and 1 GHz plotted using the normal law.

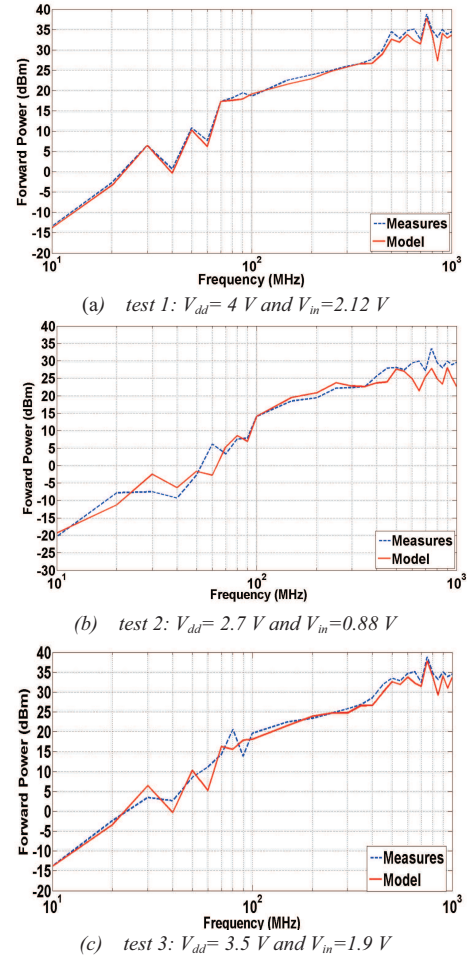


Fig.9. Comparison between measurements and model

We observe for all considered cases that the immunity curves increase with the frequency. The immunity curves derived from measurements and the model give good results.

VI. CONCLUSION

In this paper, we presented a new modeling approach for the construction of an immunity behavioral model for the AD7476 from Analog Devices. Thanks to the information criterion, the behavior of the A/D converter against the RF disturbances was modeled by statistical distributions whose parameters depend on the power and the frequency of the injected signal. The immunity criterion used here made it possible to identify the level of power required to induce an average percentage of the errors of conversion. The simulated immunity curves are in a good agreement with those obtained through DPI measurements.

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