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A Switched Reluctance Motor Drive using Photovoltaic Transistors: Principle, Prototype, Experimental and Numerical Results

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Abstract—A solar-powered switch reluctance motor drive using photovoltaic transistors is presented. The expression "photovoltaic transistor (PVT)" is used to designate a conventional photovoltaic cell used as a light-controlled power transistor. To obtain a motor drive, a set of PVTs controls the current fed from an external DC power source to the motor phases. The control is achieved by modulating the sunlight hitting the PVTs using a shutter driven by the motor rotor. If the external DC source is a solar panel, the resulting system is able to convert light energy into mechanical energy, without the need of any brushes or other power electronics components. This system could be more affordable and reliable than conventional ones, and therefore is well suited for off-grid applications like water pumping. This article first discusses the operation of a photovoltaic transistor through the proposition and the validation of a model. Then, the operating principle of a PVT inverter is clarified. Finally, experimental and numerical results on the first PVT inverter-fed switched reluctance motor are reported. A prototype was built using a switched reluctance motor 6/4 and 12 PVTs. It was here connected to an external 12 V DC power source as a step before using a solar photovoltaic source. Results showed that the PVT inverter-fed switched reluctance motor was operating as expected and provided useful power.

Keywords—photovoltaics, solar cell, inverter, switched reluctance motor, water pumping.

I. INTRODUCTION

In today's market, there exist already electric motors driven by light through photovoltaic cells. But, such motors require brushes [1] or DC/AC converters [2]. This limits the reliability of these motors and subjects them to maintenance, making this technology unsuitable for some applications, like water pumping in isolated areas. In this context, earlier we built a "photovoltaic motor" (Fig. 1c) [3, 4]. This is an electric motor using photovoltaic (PV) cells optically controlled by a shutter driven by the motor rotor to convert light energy into mechanical energy, without the need of any brushes or other power electronics. The photovoltaic motor potentially requires little maintenance and could have a low cost on overall life cycle. But its controllability is limited, and so its efficiency is low.

We investigate here a new architecture that is shown in Fig. 1d: the photovoltaic transistor inverter-fed switched reluctance motor (SRM) [5]. As photovoltaic motors, it uses PV cells optically controlled by a shutter connected to the

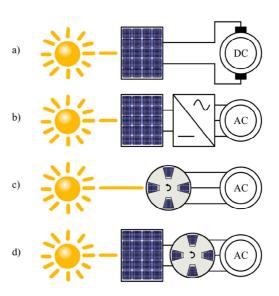


Figure 1. (a) PV cells + DC motor, (b) PV cells + power electronics + AC (brushless) motor, (c) Photovoltaic motor [3,4], (d) Photovoltaic transistor inverter-fed motor

rotor. However, here the PV cells are not used as current generators, but as light-controlled power transistors (PVT) that control the current fed to the motor from an external DC source (also photovoltaic). Such architecture has the same advantages and limitations as the photovoltaic motor. But for a given shutter diameter (imposing a mechanical constraint), the power output of a PVT inverter-fed SRM is higher than a photovoltaic motor. Note that there is no energy storage system: the motor is stopped when there is no sunshine (as any other solar system without energy storage). The intended application should therefore comply with such operation, like water pumping with storage in a water tank [6-8].

On the one hand, various patents exist about the optical control of the current fed from an external source to a motor by means of photosensitive elements and a shutter driven by the rotor. [9, 10, 11] proposed to drive a permanent magnet synchronous motor (PMSM) with unipolar currents switched by photodiodes, phototransistors or photoresistors. [12, 13] presented various configurations where the PMSM currents were bipolar. [14] patented a light-activated thyristors converter-fed PMSM. [15] used solar cells to drive a PMSM

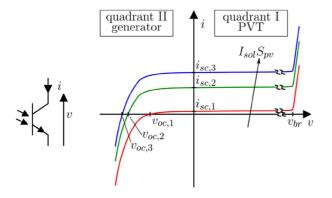


Figure 2. Photovoltaic transistor convention and idealized current-voltage curves for various I_{sol} S_{pv} . v_{oc} is the open-circuit voltage (~0.6 V). v_{br} is the breakdown voltage (10-30 V). i_{sc} is the short-circuit current.

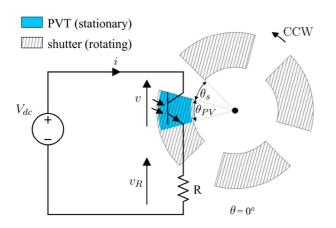


Figure 3. Photovoltaic transistor characterization experimental setup $(\theta_{PV} = \theta_s = 30 \text{ deg})$.

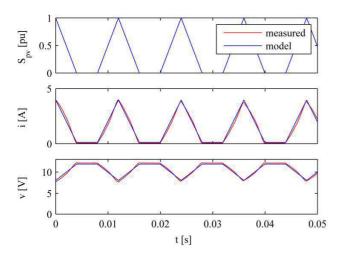


Figure 4. Photovoltaic transistor characterization experimental and simulation results ($R = 1 \Omega$, $V_{dc} = 12 \text{ V}$, $I_{sol} \approx 600 \text{ W/m}^2$, 1252 r/min, $\sim 43^{\circ}\text{C}$).

with bipolar currents. [16] patented a switched reluctance motor controlled with solar cells. On the other hand, the

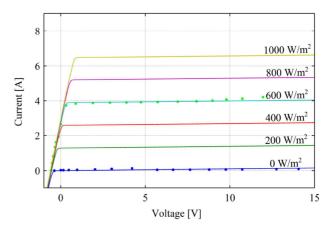


Figure 5. Photovoltaic transistor current-voltage curves for various I_{sol} , measured at ~43°C (dots) and simulated (lines), below breakdown voltage.

scientific literature on the topic is very scarce. The only work we could find on the topic is [17]. In that work, the author studied, experimentally only, a photovoltaic transistor inverter-fed PMSM with unipolar currents. He notably showed that the no-load rotation speed depends on the relative angle of the rotor and of the shutter. But no useful power could be obtained with the prototype, and no model was proposed to help us understand and optimize the system.

We tackle here these two points in a bottom-up approach. First, we study the operation of photovoltaic cells used as photovoltaic transistors. A model is developed and validated. Then, we explain in details the working principle of a photovoltaic transistor inverter-fed switched reluctance motor. Finally, we verify the concept by building a prototype and comparing experimental data with simulations.

II. PHOTOVOLTAIC TRANSISTOR

A. PVT operation

In this article, we adopt the convention shown in Fig.2 with the understanding that the symbol represents a photovoltaic transistor (and not a phototransistor). The classical operation quadrant of a photovoltaic (PV) cell is quadrant II (generator). If we operate the PV cell in quadrant I, it can be used as a light-controlled transistor (PVT). Indeed from Fig. 2, for a given polarization $0 < v < v_{br}$, the current i is approximately equal to the short circuit current i_{sc} . And i_{sc} is approximately proportional to the product of the normal solar irradiance I_{sol} by the normalized illuminated surface S_{pv} . Therefore, one can control the PVT current by varying I_{sol} or S_{pv} .

To clarify the PVT operation when S_{pv} varies and I_{sol} is constant, consider the experimental setup shown in Fig. 3: a PVT and a resistance are connected in series with a DC voltage source. The illumination on the PVT is modified by a shutter rotating in the counterclockwise direction (CCW). The PVT is a PV cell MPO Solo (monocrystalline silicon), efficiency 18.8 %, $i_{sc} = 8.897$ A, and $v_{oc} = 0.640$ V at standard testing conditions [18]. It is cut merely so that the PVT angle

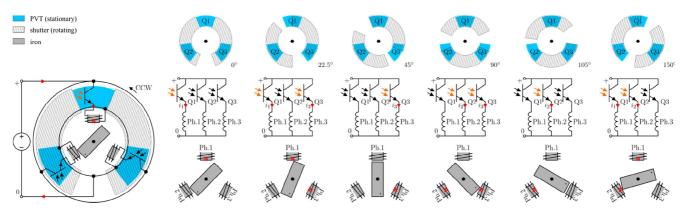


Figure 6. Three-phase PVT inverter-fed SRM, overview and operating principle (simplified magnetic circuit).

 θ_{PV} and the shutter aperture angle θ_s are both 30 degrees. The measured PVT voltage and current are shown in Fig. 3. When the shutter blocks the light on the PVT ($S_{pv}=0$ pu), the PVT current is almost zero ie. the PVT is OFF. As a result, the PVT voltage is V_{dc} . But when the shutter lets the light hit the PVT ($S_{pv}>0$ pu), the PVT current is positive ie. the PVT is ON. Besides, the PVT current is to a good approximation proportional to the illuminated surface S_{pv} . The PVT voltage is then approximately equal to V_{dc} -R i_{sc} . We carried out measurements for shutter rotation speeds ranging from $100 \, \text{r/min}$ up to $1250 \, \text{r/min}$, and V_{dc} between 8 and $12 \, \text{V}$. In that range, a similar behavior was observed and no capacitive effect was noticeable. This is the ground for using a static model in the following.

B. {PVT+shutter} modeling

We call photovoltaic transistor a conventional photovoltaic cell used as a light-controlled power transistor. It can therefore be simulated using the well-known 1-diode model (below breakdown voltage) [19]. From above observations, the shutter optical control is modeled by multiplying the input irradiance I_{sol} by the normalized illuminated surface S_{pv} . With the convention of Fig. 2, the PVT current i and voltage v are linked by

$$i = I_{sol}S_{pv}\frac{I_{ph0}}{I_{sol0}} - I_s\left(\frac{e^{(-v + R_s i)}}{N V_t} - 1\right) - \frac{-v + R_s i}{R_p}$$
 (1)

where I_{ph0} is the measured solar-generated current for a given irradiance I_{sol0} , I_s is the diode saturation current, R_s is the series resistor, N is diode quality factor, V_t is the thermal voltage, and R_p is the parallel resistor. The variation of the PVT parameters caused by the temperature is here neglected [19]. This model is equivalent to the model using lookup tables used in [4], but proves to be numerically faster and more stable. The implementation is done with Matlab Simulink and the Simscape toolbox [20].

C. PVT model calibration

To reduce the uncertainties, the parameters of the PVT model must be obtained from measurement of the same PVT, under the same light source and for the same average temperature [4]. Therefore, the PVT current-voltage curves

shown in Fig. 5 were recorded right after the measurements reported in Fig. 4. The parameters of the 1-diode model are then obtained by trial and error, so that the model fits the measured current-voltage curves. In this process, it is useful to point out that R_s affects the slop of the curve at the open-circuit voltage, while R_p determines the slop of the curves in quadrant I. Note that the test bench irradiance was estimated to $\sim 600 \text{ W/m}^2$ by using a PV cell calibrated under sunlight.

D. PVT model validation

We performed the simulation corresponding to the setup shown in Fig. 3. Results are well in line with the measurements as shown in Fig. 4. Similarly, close alignments were obtained for shutter rotation speeds ranging from 100 r/min up to 1250 r/min, and V_{dc} between 8 and 12 V. This validates the proposed PVT model.

III. PVT INVERTER-FED SRM

A. Circuit and operation

For simplicity's sake, we consider here a simplified 3-phase SRM [21] having 3 stator teeth and 2 rotor teeth (SRM 3/2 without back yoke). The aim is not to introduce an optimal system, but to demonstrate the operating principle. The adopted inverter topology has one switch per phase, where each switch is a PVT (Fig. 6). The optical control of the PVT is obtained by using a shutter that is mechanically connected to the rotor, and that rotates synchronously with it. The DC voltage source could be replaced by a solar PV source for autonomous operation.

A step-by-step operation of the PVT inverter-fed SRM is shown in Fig. 6. For an SRM 3/2, the PVT angle θ_{PV} and the shutter aperture angle θ_s are selected to be 45 degrees so that there is no generator (breaking) torque during operation. Assuming the rotor initial angle is between -15 and 15 degrees, the shutter lets the light hit PVT Q1. PVT Q1 is conducting, a current i_I magnetizes the armature coil Ph.1. This causes the rotor to rotate in the CCW direction to reduce reluctance. This rotation lets progressively the light hit PVT Q3. As a result, a current i_3 magnetizes the armature coil Ph.3. After a 45 degrees rotation, the shutter blocks the light on PVT Q1. The current i_I goes to zero, as well as the armature coil Ph.1 magnetization. But PVT Q3 is still conducting: the

magnetization of the armature coil Ph.3 causes the rotor to go on rotating in the CCW direction. Similarly PVT Q2 becomes conducting, and allows the magnetization of Ph.2 by the current i_2 . After a 105 degrees rotation, the shutter blocks the light on PVT Q3. The current i_3 goes to zero, as well as the armature coil Ph.3 magnetization. PVT Q2 maintains the rotation till the light hit again PVT Q1. Thus, a positive torque is generated continuously. The inherent torque ripple is acceptable for the type of application envisaged.

Note that with this simple architecture, only one sense of rotation is possible and the rotor speed depends on the irradiance. Besides, there is no current freewheeling: the demagnetization is dissipative (see details in section III.E®).

B. PV cell utilization factor

The main drawback of "photovoltaic motors" is that only a small percentage of the total photovoltaic cell area is exposed to light at any given time. This is because the shutter blocks the light on all the cells, periodically. As a result, the maximum PV cell utilization factor is 1/3 [3]. In the PVT inverter-fed SRM, the shutter blocks the light only above the PVTs, whereas the cells of the external solar PV source are always exposed to the light. This allows us to obtain an overall PV cell utilization factor close to 1. A direct consequence is that, for a given shutter diameter, the power output of a PVT inverter-fed SRM is higher than a photovoltaic motor.

C. Prototype and measurements

To verify the concept, we built the prototype shown in Fig. 7 on its test bench with halogen lamps and a brake. The motor is a SRM 6/4 made of UI transformer cores [4]. Two stator coils are winded around each stator core, forming one half-phase (Fig. 8). Because there is no mutual coupling between the half-phases, one can define an equivalent halfphase coil B₀ (Fig. 9). The parameters of the SRM are given in Table I. The resistance of B_0 is 0.21 Ω , and its flux linkagecurrent characteristic is shown in Fig. 10. Each coil is then connected in series with one PVT, similar to the one described in section II. The PVT assembly is shown in Fig. 11. Transil diodes (voltage surge protector, $v_{br,t} = 27 \text{ V}$) are connected in parallel to the PVTs in order to protect the PVTs from breakdown voltage (Fig. 9). The PVT optical commutation control is parameterized by the synchronization angle θ_0 defined in Fig. 12.

In this article, as a first step and to facilitate testing, the external solar PV source is replaced by a 12 V DC power source. For the following measurements, one half-phase is disconnected ie. only 5 half-phases are connected. Fig. 13 shows the measured half-phase current i_0 , voltage v_0 , and PVT voltage v for various applied braking torque T_M , under a mean illumination of ~600 W/m². The measured torque-speed curve and output power-speed curve of our prototype are plotted in Figs. 14 and 15. The current and voltages were measured with a ZES Zimmer LMG500. The brake is a Magtrol AHB 1.5. This shows that the PVT inverter-fed switched reluctance motor is operating as expected, and provides a useful output mechanical power. Finally, we underline that the motor is able



Figure 7. PVT inverter-fed SRM prototype and its test bench.

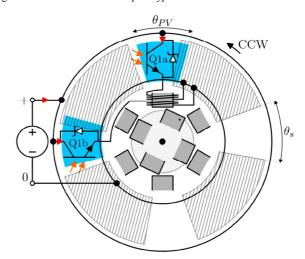


Figure 8. PVT inverter-fed SRM prototype overview (only one half-phase is shown, simplified magnetic circuit).

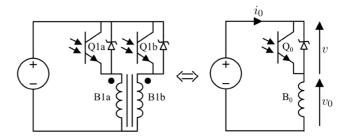


Figure 9. PVT inverter-fed SRM prototype electrical circuit of one halfphase and its equivalent circuit.

to self-start from any initial position. The operation with one half-phase disconnected is a further proof of the reliability of the system.

D. Modeling and validation

In order to understand the complex interaction between the PVTs and the SRM, we develop in the following a numerical model of the system. Beyond this article, the model aims at

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Table 1 - Sixivi difficultions and fattings			
Parameter	Value		
Stator/rotor poles	6/4		
Rotor outer diameter	100 mm		
Stator outer diameter	232 mm		
Airgap	0.1 mm		
Effective length	75 mm		
Half-phase resistance	0.21 Ω		
Half-phase aligned inductance	24.2 mH ^(a)		
Half-phase unaligned inductance	1.4 mH ^(a)		
Rated half-phase current	18 A ^(b)		
Rated power	150 W ^(b)		
Rated speed	700 r/min (b)		

(a) cf. Fig. 10.

(b) for $I_{sol} = 1000 \text{ W/m}^2$ and $V_{dc} = 12 \text{ V}$.

helping us to evaluate the range of application of PVT inverter-fed SRMs, then to optimize the output power with minimal global cost. The modeling difficulty lies in correctly taking into consideration the SRM nonlinearity, the high level of integration, and the uncommon dynamic operation of the PV cells.

We assume that the DC power source is a perfect voltage source. Because each half-phase is magnetically independent, it is enough to simulate one half-phase using the equivalent circuit shown in Fig. 9. The total torque is then estimated by multiplying the average torque by the number of half-phases. We model the equivalent PVT Q_0 with the {PVT+shutter} model introduced in section II, with multiplying coefficient to account for the two PVTs in parallel [19, eq. (24)]. We model the equivalent phase B_0 with a nonlinear abc-model using one 2D lookup table for the current-flux linkage and one 2D lookup table for the electromagnetic torque [22, 23].

We performed the simulation corresponding to our prototype. Results have been added to Figs. 13, 14 and 15. Initially, the parameters of the PVT 1-diode model were obtained by trial and error similarly to what was done in section II.C. But, the estimated OFF-state PVT current was too high; therefore we increased R_p until simulation and measurements matched. Despites the uncertainties linked to the various parts of the system, our model predicts accurately the half-phase current and voltage. This validates our modeling approach. Nevertheless, the output power is overestimated by up to 10%. This discrepancy can be explained by various factors. (i) We assume that all the halfphases are identical. In particular, we assume no dispersion of the PVTs parameters. (ii) We assume an average PVT operating temperature when calibrating the PVT model parameters (section II.C). But the PVT temperature varies with the phase current, and with the shutter rotation that increases convection cooling. (iii) We use the friction torque measured at room temperature, but the viscosity coefficient of air increases with the temperature. (iv) We use a simplified iron loss model. (v) The irradiance on the test bench is not perfectly uniform and might depend on the halogen lamp temperature.

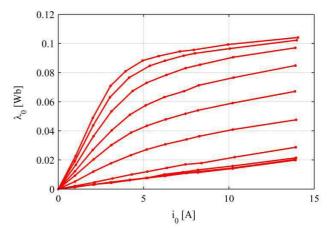


Figure 10. Flux linkage-current characteristic $\lambda_0(\theta, i_0)$ of the equivalent coil B₀ (defined in Fig. 9) obtained from measurements. $\theta = 0, 5, 10, 15, 20, 25, 30, 35, 40, 45$ deg.

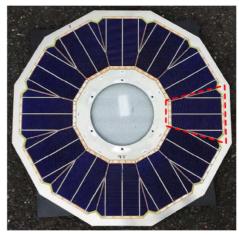


Figure 11. PVT assembly. The dashed line delimitates one cut PVT. The diameter of the assembly is 33 cm.

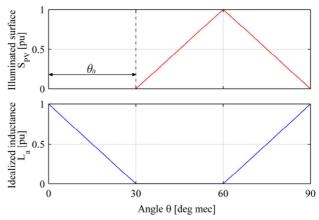


Figure 12. Top: Normalized illuminated PVT surface. Bottom: Idealized phase inductance L_a . $\theta = 0$ deg corresponds to the SRM aligned position. The shutter synchronization is defined by θ_θ .

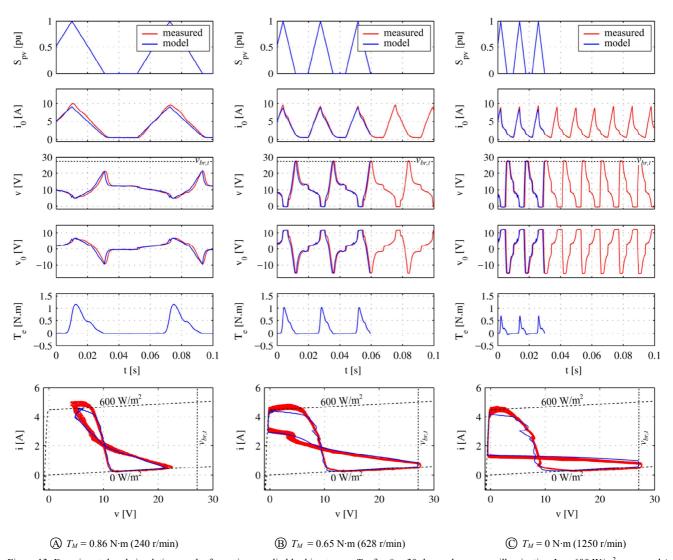


Figure 13. Experimental and simulation results for various applied braking torque T_M , for $\theta_0 = 30$ deg under a mean illumination $I_{sol} \approx 600 \text{ W/m}^2$. v, v_0 , and i_0 are defined in Fig. 9. i is the PVT current ($i=i_0/2$). S_{PV} is the simulated PVT illuminated surface. T_e is the simulated instantaneous electromagnetic torque per half-phase. $v_{br,t}$ is the transil diode breakdown voltage.

E. Discussion

Previous measurements and simulations reveal some characteristics of the system, which can be useful in the future for optimizing the system. Consider Fig. 13:

- At point (a), corresponding to low speed and high torque operation, the half-phase current has the same triangular shape as the PVT illuminated surface. This indicates that the current is well controlled by the PVT.
- At point ®, near the maximum power point, the half-phase current is quasi-triangular. It is then still controlled by the PVT but with voltage limitations. During the magnetization, the PVT operates shortly in generator mode (quadrant II, see Fig. 2). During the demagnetization, as there is no current freewheeling, the current does not go back to the power supply but
- continues flowing through the PVT. As a result the PVT voltage increases till it reaches the transil diode breakdown voltage ($v_{br,t} = 27$ V). Then the demagnetization current goes through the transil diode. The transil diode enables us to apply the maximal negative voltage to the half-phase. Thus the demagnetization is dissipative.
- At point \mathbb{O} , corresponding to high speed and low torque, the half-phase voltage tends to the voltage that would be obtained with a full wave voltage supply. During most of the magnetization, the half-phase positive voltage is maximal thanks to the PVT operating in quadrant II. And during most of the demagnetization, the half-phase negative voltage is maximal while the PVT voltage is limited to the transil diode breakdown voltage $v_{br,t}$.

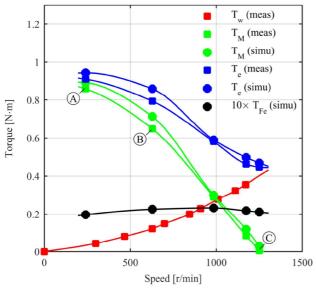


Figure 14. Experimental and simulated torque-speed curves for θ_0 = 30 deg under a mean illumination $I_{sol} \approx 600$ W/m². T_w is the friction torque (bearings, windage and shutter), T_M is the output mechanical torque, T_e is the electromagnetic torque. T_{Fe} is the torque associated with iron loss $(T_e - T_{Fe} - T_w - T_M = 0)$ in steady-state operation).

Finally, the model allows us to predict the performance of our PVT inverter-fed SRM for other operating conditions. For example, under a mean illumination of 1000 W/m² with all the 6 half-phases connected to a 12 V DC power source, the maximum output mechanical power is estimated to ~130 W at 650 r/min. This is sufficient for applications such as standalone water pumping, and could be even increased by optimizing the drive.

IV. CONCLUSION

We introduced a new solar electric motor architecture that uses photovoltaic cells as light-controlled transistors with an optical shutter. This system can be considered as an improvement of the "photovoltaic switched reluctance motor" described in [3, 4]. The use of solar cells as light-controlled power transistors is original, and could be extended to other applications.

As a proof of concept, we assembled and tested successfully the first PVT inverter-fed SRM. Because of its unique operating principle and the high nonlinearity of its components (PVT and SRM), we needed a good model of this system in order to evaluate its range of applications, then to optimize the output power with minimal global cost. Here we modeled the system in a bottom-up approach, starting by the PVTs. Our model has been validated on the full speed range by comparison with measurements made on our prototype supplied by a DC voltage source.

Later the model will be extended to simulate a PVT inverter-fed SRM supplied by an external solar PV source. This will add complexity because the external solar PV source operating point will vary continuously. In parallel, more work

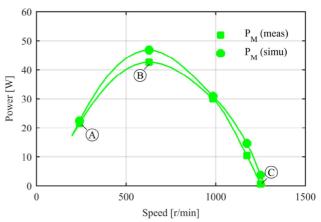


Figure 15. Experimental and simulated output mechanical power-speed curves for $\theta_0 = 30$ deg under a mean illumination $I_{sol} \approx 600 \text{ W/m}^2$.

is needed to optimize the drive (PVT and electromagnetic structure), enhance the performances of the PVTs and test the system's reliability.

Such architecture is expected to be well suited for fully autonomous applications in isolated areas where reliability is a major concern, like water pumping with storage in a water tank. The efficiency of the PVT inverter-fed SRM is low, but (a) the energy input is free, (b) the price of solar cells has been sharply decreasing, (c) the price of the drive may be quite acceptable, and (d) the balance of consumption of nonrenewable primary energy over the overall life cycle should be very good, but it remains to be demonstrated. Work is underway to optimize the system taking reliability into account, and to estimate its cost on overall life cycle, which is an appropriate metric to evaluate if the low efficiency can be compensated by a higher reliability.

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