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High-efficiency single etch step surface grating couplers engineered by subwavelength structured metamaterials

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Abstract-We report our recent developments of high-efficiency fiber-chip surface grating couplers engineered by subwavelength grating structuration. The intriguing technological concept of subwavelength grating index engineering provides extraordinary degree of freedom in designing and fabricating high-performance optical coupling interfaces for sub-micrometric silicon-on-insulator technology. We present an overview of various demonstrations of high-efficiency surface grating couplers, particularly develop for silicon photonics applications in the near-infrared spectral range (near 1.3 μm and 1.55 μm wavelengths) such as datacom and telecom optical interconnects.

Efficient optical coupling interfaces are one of the key building blocks in photonics integrated circuitry. Coupling of light to and from microphotonic waveguides has been recognized as a major practical challenge since the early years of integrated optics. The coupling is particularly difficult for silicon-on-insulator technology, since the cross-sectional area of silicon waveguides is more than two orders of magnitude smaller than that of a standard single-mode fiber. The surface grating couplers are an effective approach to circumvent this issue [1-5].

We have demonstrated by theory and experiments a series of subwavelength refractive index engineered surface grating couplers that require only a one fabrication step of lithography patterning and reactive ion etching, while providing the state-of-the-art coupling performance [2-5]. Specifically, we have reported various apodized grating couplers with an optimized directionality, with a coupling efficiency of up to -2.2 dB and -2.5 dB for wavelengths near 1.55 and 1.3 μm , respectively [2-4]. The grating couplers were implemented in a standard 220-nm-thick silicon-on-insulator substrates, as typically offered by silicon photonics foundries. In addition, a sub-decibel fiber-chip coupling efficiency of -0.7 dB has been experimentally realized for the first time in 220-nm silicon-on-insulator waveguides by utilizing the complementary metal-oxide-semiconductor-compatible manufacturing via backside processing [5].

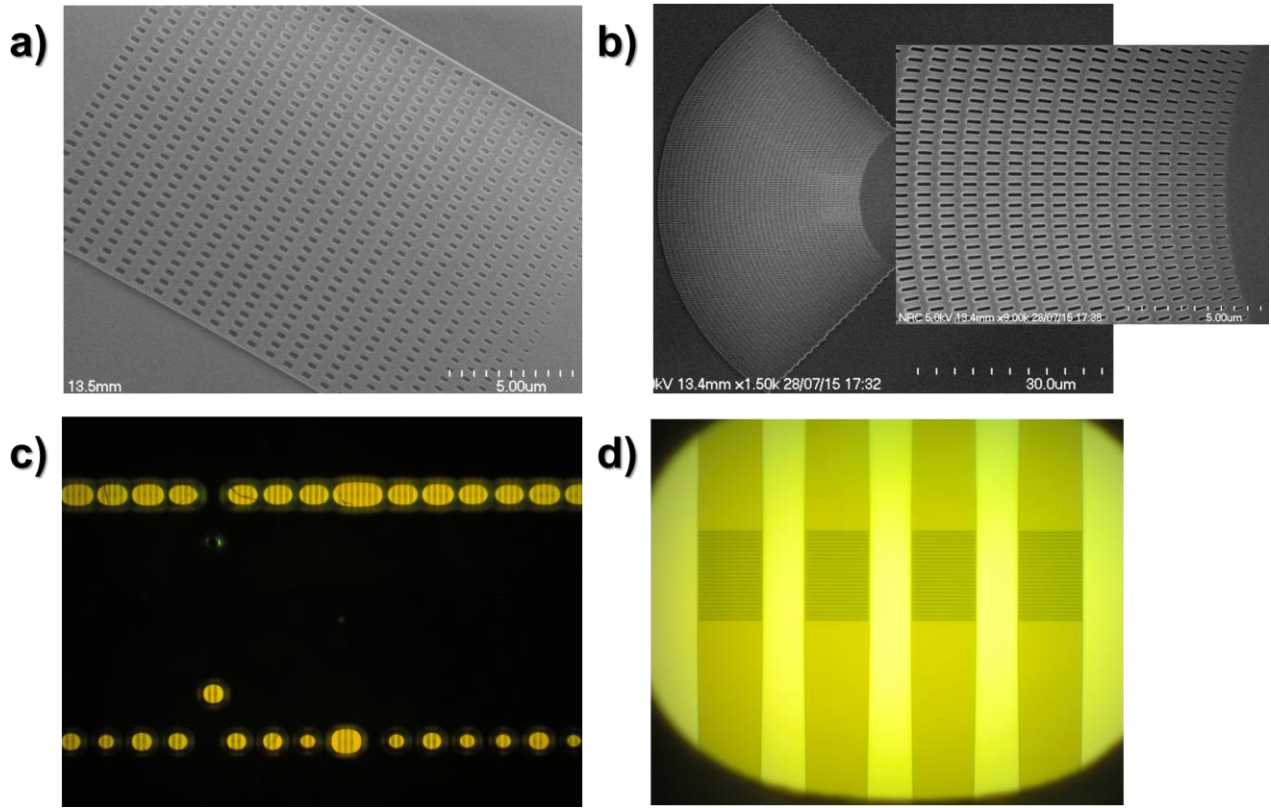


Figure: Single-etch apodized surface grating couplers in (a) linearly-tapered and (b) curved layout configurations, and subwavelength grating nanostructure inside the trenches. (c) Optical micrograph image of the surface grating couplers layout with metal reflector underneath. (d) Detail view of the surface grating coupler with metal reflector.

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