

Synchronverter-Based Emulation and Control of HVDC Transmission

Raouia Aouini, Bogdan Marinescu, Khadija Ben Kilani, Mohamed Elleuch

▶ To cite this version:

Raouia Aouini, Bogdan Marinescu, Khadija Ben Kilani, Mohamed Elleuch. Synchronverter-Based Emulation and Control of HVDC Transmission. IEEE Transactions on Power Systems, 2016, vol 13 (1286), pp.278-286. 10.1109/TPWRS.2015.2389822. hal-01421701

HAL Id: hal-01421701 https://hal.science/hal-01421701v1

Submitted on 22 Dec 2016

HAL is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers. L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

Synchronverter-based Emulation and Control of HVDC transmission

R. Aouini, B. Marinescu, *Member, IEEE*, K. Ben Kilani, *Member, IEEE*, and M. Elleuch, *Member, IEEE*

Abstract-- This paper presents a new control strategy for High Voltage Direct Current (HVDC) transmission based on the the sending-end rectifier controls synchronverter concept: emulate a synchronous motor (SM), and the receiving end inverter emulates a synchronous generator (SG). The two converters connected with a DC line provide what is called a Synchronverter HVDC (SHVDC). The structure of the SHVDC is firstly analyzed. It is shown that the droop and voltage regulations included in the SHVDC structure are necessary and sufficient to well define the behavior of SHVDC. The standard parameters of the SG cannot be directly used for this structure. A specific tuning method of these parameters is proposed in order to satisfy the usual HVDC control requirements. The new tuning method is compared with the standard vector control in terms of local performances and fault critical clearing time (CCT) in the neighboring zone of the link. The test network is a 4 machine power system with parallel HVDC/AC transmission. The results indicate the contribution of the proposed controller to enhance the stability margin of the neighbour AC zone of the link.

Index Terms-- HVDC, synchronverter, synchronous generator/motor, regulator parameters tuning, residues, optimization.

I. INTRODUCTION

HIGH Voltage Direct Current (HVDC) systems uses direct current for bulk electric transmission power based on high power electronics which provide the opportunity to enhance controllability, stability and power transmission capability of AC transmission systems [1]. Practical conversion of power between AC and DC became possible with the development of power electronics devices such as thyristors and Voltage Source Converters (VSC). The VSC technology offers even more flexibility, new capabilities for voltage support, independent controls active/reactive power and easier integration of wind farms [2]. These HVDC transmission systems are specifically used to connect asynchronous grids, as for example the England-France interconnection [3]. Nonetheless, other equally important HVDC applications concern complex AC interconnected systems in order to enhance the power transmission capacity and meet the growing demand.

The planned Spain-France interconnection [4], which will use the VSC technology scaled up to 2000 MW, is such an

example. These emerging applications gave rise to the coexistence of parallel HVDC/HVAC, and consequently, an increased level of AC/DC/AC converted power injected into AC networks.

Many studies have shown that the methods of controlling HVDC converters have an impact on stability of the system in which the link is inserted. The main trend in control techniques for VSC HVDC links are based on the well established vector control scheme. For example, in [5] the standard vector control has been modified to improve the dynamic performance of a parallel AC/DC interconnection. In [6] the VSC-HVDC operating characteristics are determined by a decoupled PI controller to provide decoupled and independent control of the active and reactive powers for each converter. Generally, and due to their simple structure and robustness, PI controllers have been adjusted to meet HVDC specifications. For example, the authors in [7] proposed a robust control scheme for a parallel AC/DC system. In [8], an adaptive optimal control was developed for an HVDC system.

Recently, in [9]-[14], the authors have proposed a different control method for which an inverter can be operated to mimic the behavior of a synchronous generator (SG) and the resulting closed-loop has been called a *synchronverter* [9]. Since the operation of AC systems via SGs voltage/frequency regulation is rather well known [15], the synchronverter concept led to new applications. For example, in [16], a STATCOM controller was synthesized from the mathematical model of synchronous generators operated in a compensator mode.

In this work, the synchronverter concept is adapted to converters of an HVDC transmission. The idea is a conceptual control strategy of the DC line, where the sending-end rectifier controls emulate a synchronous motor (SM), and the receiving end inverter emulates a SG, both along with their controls. This resulting Synchronverter based HVDC will be called SHVDC in the paper. It is shown that the parameters usually used for the regulations of the SG of the same capacity cannot be directly used for the SHVDC. This led us to propose a tuning method of the HVDC converters parameters in order to satisfy both the local HVDC control requirements and to improve the transient stability of the neighbor AC zone of the HVDC link. The rest of the paper is organized as follows: in Section II, the conventional modeling and control of an HVDC-VSC transmission system is briefly overviewed. The synchronverter concept is extended in Section III to the case of an HVDC link. In Section IV, the SHVDC structure

proposed in Section III for the HVDC link is analyzed from a structural point of view. An analytic method to tune the parameters of the controllers of the SHVDC in order to meet the desired performances is given in Section V, while validation tests are presented in Section VI.

II. CONVENTIONAL MODELING AND CONTROL OF AN HVDC-VSC TRANSMISSION SYSTEM

An HVDC system consists of three parts: a rectifier station, an inverter station and a high-voltage DC transmission line as shown in Fig. 1. Each converter is connected to the AC network via a grid impedance Z_g representing the short-circuit power at the connection point.

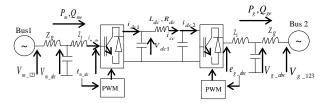


Fig. 1. Two terminal HVDC-VSC link

For its advantage on control capability, the VSC technology is assumed. Each converter holds two degrees of freedom for the control. Usually, these degrees are used for the control of the reactive power at each side, of the active power and for the DC voltage [6]. Each station controls its reactive power independently of the other station. The flow of active power in the DC transmission system must be balanced, which means that the active power entering the HVDC system must mutch the active power leaving it, plus the losses in the DC transmission system. To achieve this power balance, one of the stations has to control the active power, while the other station should be designed for the DC voltage control. Usually, the two converters of an HVDC are controlled by two independent loops and each of these controls is based on the vector control approach in the d-q frame, using cascaded PI controllers [2]: the outer control loop generates the respective d-q current references to the inner current control loop.

III. SYNCHRONVERTER-BASED HVDC

In this section, a control strategy based on synchronverter technology is adapted for HVDC-VSC converters where the HVDC converters are run as synchronous machines. The synchronverter proposed in [9] is an inverter that mimics the structure and the regulations of a synchronous generator (SG). The usual control strategies used for conventional SG can thus be used for the inverter. For a complete DC system, a rectifier and an inverter are bound by a DC link. Therefore, to provide an HVDC structure, another synchronverter working as a synchronous motor (SM) based on the same mathematical derivation is necessary. As a result, the DC power is sent from the SM to the SG. Firstly, an overview of the synchronverter concept is presented, where the inverter is modeled according the mathematical model of a SG.

Next, the synchronverter concept is extended to a rectifier that mimics a SM.

A. Overview of the synchronverter concept

A synchronverter is an inverter which regulations are chosen such that the resulting closed-loop mimics the behavior of a conventional SG. For the purpose of the development, we recall the structure of a three phase round rotor synchronous machine [15] as shown in Fig. 2.

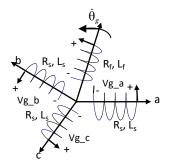


Fig. 2. Structure of an idealized three-phase round-rotor SG

The stator windings can be seen as concentrated coils having self-inductance L and mutual inductance (-M). The field winding can be seen as a concentrated coil having a self-inductance L_f . The phase terminal voltage vector $V_{g_abc} = [V_{ga} \ V_{gb} \ V_{gc}]^T$ may be expressed by

$$V_{g_abc} = -R_s i_{g_abc} - L_s \frac{di_{g_abc}}{dt} + e_{g_abc} , \qquad (1)$$

where $i_{g_abc} = [i_{ga} \ i_{gb} \ i_{gc}]^T$ is the stator phase currents vector, $L_s = L + M$, R_s are, respectively, the inductance and the resistance of the stator windings. The back emf vector $e_{g_abc} = [e_{ga} \ e_{gb} \ e_{gc}]^T$ is the back emf given by

$$e_{g_abc} = M_g s \, \theta_g s \tilde{i} n \, \theta_g \,, \tag{2}$$

where M_{g} is the flux field, θ_{g} is the electric rotor angle and

$$\tilde{\sin\theta_g} = \left[\sin\theta_g \sin(\theta_g - \frac{2\pi}{3}) \sin(\theta_g + \frac{2\pi}{3}) \right]^T,$$
 (3)

The mechanical equation of the machine is given by

$$J_{\sigma}\ddot{\theta}_{\sigma} = T_{\sigma m} - T_{\sigma e} - D_{\sigma n} s \theta_{\sigma} , \qquad (4)$$

where J_g is the combined moment of inertia of generator and turbine, T_{gm} is the mechanical torque, T_{ge} is the electromagnetic torque and D_{gp} is a damping factor. The torque T_{ge} is given by

$$T_{ge} = M_g < i_{g_abc}, \tilde{\sin \theta_g} > , \tag{5}$$

where the operator $\langle .,. \rangle$ denotes the conventional inner product in \mathbb{R}^3 .

The real and reactive powers generated by the SG are, respectively

$$P_{g} = M_{g} s \theta_{g} < i_{g_abc}, \tilde{\sin \theta_{g}} > , \tag{6}$$

$$Q_{ge} = -M_g s \theta_g < i_{g-abc}, \tilde{\cos} \theta_g > . \tag{7}$$

Based on the SG model above (Eq. 1-7), the concept of synchronverter is developed. The latter consists of an inverter on which specific controls are built and structured, as shown in Fig. 3. On the figure, we depict (i) the power

part consisting of the inverter plus the LC filter and (ii) controls assured by the electronic part.

Such control structure is shown to be equivalent to a SG with capacitor bank connected in parallel with the stator terminal [9]. More specifically, with the terminal voltages V_{g_abc} and the voltages e_{g_abc} defined on the Fig. 3, the voltage equation (1) holds. The voltage $e_{g_abc} = [e_{ga} \ e_{gb} \ e_{gc}]^T$ corresponds to the back emf of the virtual rotor. The inverter switches are operated so that the average values of e_{g_a} , e_{g_b} and e_{g_c} over a switching period are to be equal to e_{g_abc} given in (2). This can be achieved by the usual PWM technique. It's worth noting that the VSC technology used in this work is not exclusive. However, it is suited for generating pulse control signals. The electronic part controls the switches in the power part. These two parts interact via the signals e_{g_abc} and i_{g_abc} .

The synchronverter given in Fig. 3 is connected to the grid via the impedance (L_g , R_g), thus, its output current and voltage may be given by

$$V_{g_abc} = \frac{1}{C_r s} (i_{g_abc} - i_{g_123}) , \qquad (8)$$

$$i_{g_{-123}} = \frac{1}{(R_{\circ} + L_{\circ}s)} (V_{g_{-}abc} - V_{g_{-123}}). \tag{9}$$

We may write a swing equation for the synchronverter, similar to equation (3):

$$\ddot{\theta}_{g} = \frac{1}{J_{g}} (T_{gm} - T_{ge} - D_{gp} s \theta_{g}),$$

where the mechanical torque $T_{\rm gm}$ is a control input, $\theta_{\rm g}$ is the angle position of the synchronverter. The electromagnetic torque $T_{\rm ge}$ depends on $i_{\rm g_abc}$ and $\theta_{\rm g}$.

To mimic the droop of the SG, the following frequency droop control loop is proposed

$$T_{gm} = T_{gm - ref} + D_{gp} \left(\omega_n - s \theta_g \right). \tag{10}$$

The synchronverter thus shares load with the other generators of the AC grid to which it is connected in proportion with the static droop coefficient $D_{\rm gp}$. In (10), $T_{\rm gm\ -ref}$ is the mechanical torque applied to the rotor and it is generated by a PI controller as shown in Fig. 3 to regulate the real power output $P_{\rm g}$

$$T_{gm_ref} = (K_{p_P_g} + \frac{K_{i_P_g}}{s})(P_g - P_{g_ref}). \tag{11}$$

The reactive power Q_g is controlled by a voltage droop control loop using a voltage droop coefficient D_{gq} , in order to regulate the field excitation M_g , which is proportional to the voltage generated following (12) and (13)

$$M_{g} = \frac{1}{k_{g}s} (Q_{gm} - Q_{ge}), \qquad (12)$$

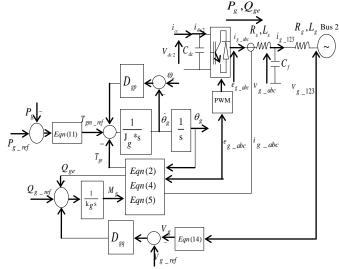


Fig. 3. Model of the synchronverter: power and electronic parts [9]

$$Q_{gm} = Q_{g_{ref}} + D_{gq} (V_{g_{ref}} - V_{g}), \qquad (13)$$

where $V_{_{\varrho}}$ is the output voltage amplitude computed by

$$V_{g} = \frac{2}{\sqrt{3(V_{ga}V_{gb} + V_{ga}V_{gc} + V_{gb}V_{gc}}} \ . \tag{14}$$

B. Extension of the synchronverter concept to the HVDC emulation

To obtain an HVDC transmission system, in addition to a SG emulated by a synchronverter as in the section above, a rectifier that mimics a SM and a DC line are needed. The resulting system, a SM/SG and a DC line are called Synchronverter High-Voltage Direct Current (SHVDC).

B.1. The synchronverter model of a SM

The synchronverter model of a SM consists of an LC filter and mainly controls shown in the same Fig. 3. It's nearly the same model discussed above, apart from the fact that the sign convention for the stator current $i_{m_abc} = [i_{ma} \ i_{mb} \ i_{mc}]^T$ is changed. Equations (1), (4), (8) and (9) are re-written in motor convention

$$V_{m_abc} = R_s i_{m_abc} + L_s \frac{di_{m_abc}}{dt} + e_{m_abc} , \qquad (15)$$

$$\ddot{\theta}_{m} = \frac{1}{I_{m}} (T_{me} - T_{mm} - D_{mp} \dot{\theta}_{m}) , \qquad (16)$$

$$V_{m_abc} = \frac{1}{C_f s} (i_{m_123} - i_{m_abc}), \qquad (17)$$

$$i_{m_{-123}} = \frac{1}{(R_g + L_g s)} (V_{m_{-123}} - V_{m_{-abc}}).$$
 (18)

The bloc equations (2), (5) and (7) remain the same

$$e_{m \ abc} = M_m s \theta_m s \tilde{\sin} \theta_m , \qquad (19)$$

$$T_{me} = M_m < i_{m_abc}, \tilde{\sin \theta_m} > , \qquad (20)$$

$$Q_{me} = M_m s \theta_m < i_{m_abc}, c\tilde{os} \theta_m > , \qquad (21)$$

$$P_{m} = M_{m} s \theta_{m} < i_{m-abc}, \tilde{sin} \theta_{m} > . \tag{22}$$

The frequency and the voltage droop controls are also similar to the SG emulation case; except for the power regulation (11) which is replaced by the DC voltage control loop as defined by (24)

$$T_{mm} = T_{mm - nef} + D_{mp}(\omega_n - s\theta_m) , \qquad (23)$$

$$T_{mm-ref} = (K_{p_vdc} + \frac{K_{i_vdc}}{s})(V_{dc_ref} - V_{dcl}), \qquad (24)$$

$$Q_{mm} = Q_{m_{ref}} + D_{mq} (V_{m_{ref}} - V_{m}), \qquad (25)$$

$$M_{m} = \frac{-1}{k_{m}s}(Q_{mm} - Q_{me}) , \qquad (26)$$

$$V_{m} = \frac{2}{\sqrt{3(V_{md}V_{mb} + V_{md}V_{mc} + V_{mb}V_{mc}}}.$$
 (27)

B.2. Coupling equations

The circuit equations of the DC line (Fig. 1 & 3) are

$$V_{dc1} = \frac{1}{C_{dc}s} (i_{dc1} - i_{cc}), \qquad (28)$$

$$V_{dc2} = \frac{1}{C_{cs}} (i_{cc} - i_{dc2}), \qquad (29)$$

$$i_{cc} = \frac{1}{L_{do}S} (V_{dc1} - V_{dc2} - R_{dc}i_{cc}) \cdot$$
 (30)

The AC and DC circuits are coupled by the active power relation

$$P_{dc1} = V_{dc1} i_{dc1}, (31)$$

$$P_{dc2} = V_{dc2} i_{dc2}, (32)$$

$$P_{del} = P_{m} \quad , \tag{33}$$

$$P_{dc2} = P_g . ag{34}$$

IV. STRUCTURAL ANALYSIS OF SYNCHRONVERTERS BASED $$\operatorname{\mathsf{HVDC}}$$

In this section, the SHVDC is analyzed from a structural point of view. More precisely, the droop and voltage regulations introduced in [9] for the synchronverter in Fig. 3 are appropriate in the sense that they cancel all the degrees of freedom of the system, so that, with given initial conditions, the trajectories of the closed-loop system in Fig. 3 are uniquely defined. Roughly speaking, the number and the nature of the regulations are necessary and sufficient for this system. When coupling two synchronverters as in Section II to form the SHVDC, this fact is not obvious. Moreover, controls for the power exchange and the DC voltage have been added by equations (11) and respectively (24). The scope of this section is to prove that the proposed structure is well-defined from the point of view mentioned above. As the model of the SHVDC is given by equations (1)-(34) in a general Differential Algebraic Equations (DAE) form, the approach introduced in [17] and related references can be directly used. In this latter, a linear system Σ is a module M defined by a matrix equation

$$S(s)w = 0, (35)$$

where S(s) is a matrix which elements are polynomials in the derivation operator s = d/dt. For the SHVDC, w is given by (36), and S(s) results from the linear approximation of equations (1)-(34). Let k = length(w) and r = rank(S(s)).

$$\begin{split} & w = [i_{g_abc}, V_{g_abc}, e_{g_abc}, i_{g_123}, i_{m_abc}, V_{m_abc}, e_{m_abc}, i_{m_123}, P_g, \\ & T_{ge}, T_{gm}, T_{gm_ref}, \theta_g, P_m, T_{me}, T_{mm}, T_{mm_ref}, \theta_m, Q_{ge}, Q_{gm}, M_g, V_g, \\ & V_{dcl}, i_{dcl}, P_{dcl}, Q_{me}, Q_{mm}, M_m, V_m, V_{dc2}, i_{dc2}, i_{rc}, P_{dc2}, s\tilde{i}n \theta_g \,]^T \end{split}$$

From [17], the integer k = m - r is the rank of the module M, which coincides with the rank of the linear system Σ .

The number of the independent inputs $u = \{u_1...u_m\}$ of the system M given by (35) is the rank of the module which defines the system m=rank (M). For the SHVDC case, the rank of the module M defined by the SHVDC is zero, which means that the regulations included in (1)-(34) are necessary and sufficient to well define the behavior of this structure. The choice of the regulations parameters in order to ensure stability and desired performances is next discussed.

V. TUNING OF A SYNCHRONVERTER BASED HVDC

The SHVDC structure is investigated in this section on the simple system of Fig. 1. A four machines system will be used in Section VI. The simulations tool in the Matlab/Simulink toolbox.

A. Tuning with standard parameters of classic SG

The main advantage of the synchronverter is to put into evidence the structure of classic generators for which the controls are well known. For this reason, we firstly adopted for the new SHVDC structure standard parameters of a SG which are given in the first column of Table III. They mainly correspond to 5% governor droop [15] (1/ $D_{\rm gp}$ and 1/ $D_{\rm mp}$) and a 3% AVR gain (1/D $_{\mbox{\tiny gq}}$ and 1/D $_{\mbox{\tiny mq}}$) [18]. The responses of the SHVDC to step changes in active/reactive power references are given in Fig. 4 in solid lines in comparison with the ones in dotted lines obtained with a classic vector control (see, e.g., [19]). Although the voltage and reactive power dynamics are quite similar, a poorly damped response is obtained for the active power P with the SHVDC. As a consequence, the usual parameters of a synchronous generator of the same capacity cannot be directly used for the SHVDC and a specific tuning of the parameters of the latter was done as presented in the next section.

B. Specific framework for the tuning of the SHVDC parameters

B.1. HVDC control specifications: we first recall the usual full set of control specifications for a VSC based HVDC, as presented in Section II. Set-points for the transmitted active power, the reactive power and the voltage at the points of coupling have to be tracked with the following transient performances [6, 20]:

- the response time of the active/reactive power is normally in the range of 50~ms to 150~ms.
- the response time for voltage is about 100 ms to 500 ms. The dynamic of the HVDC should also improve the stability of the neighboring (see, e.g., [5]).

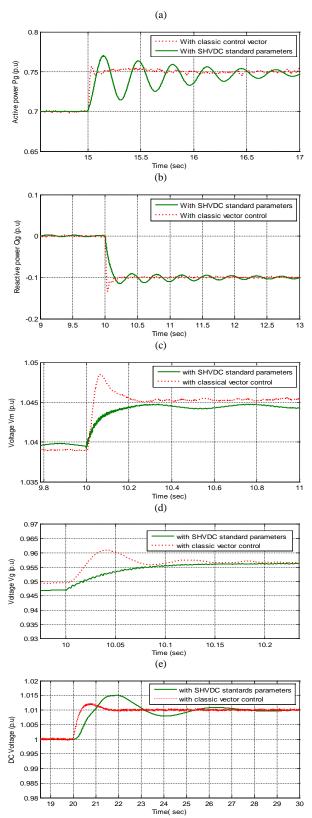


Fig. 4. Responses with standard SG parameters. (a) Response of P_g to a +0.05 step in P_{g_ref} (p.u); (b) Response of Q_g to a -0.1 step in Q_{g_ref} (p.u); (c) Response of V_g to a -0.1 step change in Q_{g_ref} (p.u); (d) Response of V_m to a -0.1 step change in Q_{m_ref} (p.u); (e) Response of V_{dc} to a +0.01 step in V_{dc_ref} (p.u).

<u>B.2. Control structure</u>: in order to satisfy these specifications, the SHVDC parameters should be tuned simultaneously and in a coordinated way. For this tuning, the SHVDC shown in Fig. 1 is put into the feedback system structure presented in Fig. 5 where $\tilde{\Sigma}$ is modeled by equations (1) to (34) with the exception of equations (10), (11), (13), (24), (25) and (26). All control parameters are grouped in the following diagonal matrix

 $K(s,q) = diag(D_{gp}, D_{mp}, K_{p_Vdc}, K_{i-Vdc}, D_{gq}, D_{mq}, K_{p_p_g}, K_{i-p_g})$. (37)

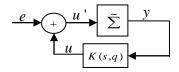


Fig. 5. Feedback system

where D_{gp} , D_{mp} are, respectively, the static frequency droop coefficients of the SG and the SM; D_{gq} , D_{mq} are, respectively, the voltage droop coefficient of the SG and of the SM; $K_{p_{Y_{dc}}}$, $K_{i_{Y_{gc}}}$ are the DC voltage PI control parameters; and $K_{p_{P_g}}$, $K_{i_{P_g}}$ are the active power PI control parameters.

Note that all elements of the matrix K(s,q) are tuned via the pole placement presented in Section V.B.3 to meet HVDC performance specifications given in Section V.B.1.

The inputs u and the outputs y are

$$\begin{split} u = & [T_{mm}, Q_{mm}, T_{mm-ref_kp}, T_{mm-ref_ki}, T_{gm}, Q_{gm}, T_{gm-ref_kp}, \\ & T_{gm-ref_ki} \end{bmatrix}^T \end{split}$$

$$\begin{aligned} & y = & [\omega_{n} \text{-s}\theta_{m}, \, V_{m_ref} - V_{m}, \, V_{dc_ref} - V_{dc}, \, \frac{V_{dc_ref} - V_{dc}}{s}, \, \omega_{n} \text{-s}\theta_{g}, \\ & V_{g_ref} - V_{g}, \, P_{g_ref} - P_{g}, \, \frac{P_{g_ref} - P_{g}}{s}]^{T}. \end{aligned}$$

<u>B.3. Regulator parameters and residues</u>: let H(s) be the transfer matrix of a linear approximation of $\tilde{\Sigma}$ and consider each closed-loop of the feedback system in Fig. 5 which corresponds only to input u_i and output y_i

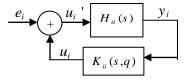


Fig. 6. Single input/Single output feedback system

More specifically, $H_{ii}(s)$ and $K_{ii}(s)$ are the (i,i) transfer functions of H(s), K(s,q), respectively.

Proposition (e.g., [21]): The sensitivity of a pole λ of the closed-loop in Fig. 6 with respect to a parameter q of the regulator K_{ii} is

$$\frac{\partial \lambda}{\partial q} = r_{\lambda} \frac{\partial K_{ii}(s, q)}{\partial q} \tag{38}$$

where r_{λ} is the residue of $H_{ii}(s)$ at pole λ .

Note that, for our case. (37), $\frac{\partial K_{ii}(s,q)}{\partial q}\Big|_{s=\lambda} = 1$.

Also, the poles of $H_{ii}(s)$ are among the poles of H(s). As a consequence, the residues of the linear approximation of $\tilde{\Sigma}$ can be used for the pole placement as shown in Section V.D. Before, some basic notions of modal analysis are recalled.

C. Modal analysis of the system

This analysis is based on a state representation of the transfer matrix H(s) of the form

$$\Sigma$$
: \dot{x} =A x+B u , y=C x . (39)

The poles λ_i of H(s) correspond to the eigenvalues (modes) of A. The participation factors measure the relative participation of the k^{th} state variable in the i^{th} mode:

$$p_{ki} = \psi_{ik} * \varphi_{ki} , \qquad (40)$$

where φ_i and ψ_i are respectively the right and the left eigenvectors of A associated with the mode λ_i . They are given in Table II for system (39).

Each element $H_{ij}(s)$ of H(s) can be expanded in partial fractions

$$H_{ij}(s) = \sum_{k=1}^{k=n} \frac{r_k}{s - \lambda_k},$$
 (41)

where r_k is the residue of $H_{ii}(s)$ at pole λ_i expressed as

$$r_k = C_i \varphi_k \psi_k B_j , \qquad (42)$$

where C_i is the ith line of C and B_i is the jth column of B [21].

D. Coordinated tuning of SHVDC parameters

First, desired locations λ_i^* can be computed for each pole λ_i starting from the control specifications given in Section B.1. To ensure a second order type response without overshoot, the damping of the corresponding complex mode λ_i is imposed to be $\zeta_i = 0.7$. The frequency of the mode is deduced from the desired response time t_r given in Section B.1: $\omega_{i\,0} = 3/t_r$ [21]. Thus, the desired location of λ_i is $\lambda_i^* = -\zeta_i \omega_{i\,0} \pm j \omega_{i\,0} \sqrt{1-\zeta_i^2}$ and the results for the dynamics of interest for the SHVDC are given in Table I (column 3).

It should be noted that, the dynamic of reactive power is the same as the dynamic of the voltage. The connections in Table I between the dynamics of interest and the modes are established based on the participations factors given in Table II. Only the highest participation factor is considered, therefore each dynamic is associated to only one pole for the computation of desired poles λ_i^* . However, from the same column of Table II; several poles have also significant participations to the same dynamic which led us to compute the gains K in a coordinated way. More specifically, if Λ denotes the set indices j from 1 to 8 for which $H_{ij}(s)$ has λ_i as pole, the contribution of each control gain in the shift of the pole is

$$\lambda_i = \lambda_i^0 + \sum_{i \in \Lambda} r_{ij} K_j , \qquad (43)$$

where λ_i^0 is the initial (open-loop) location of the pole λ_i and r_{ij} is the residue of $H_{ij}(s)$ in λ_i . Finally, the pole placement is the solution of the following optimization problem

$$\{K_{j}^{*}, j=1...8\} = \underset{K_{j}}{\arg\min} \sum_{i} \|\lambda_{i}^{*} - \lambda_{i}\|^{2},$$
 (44)

where λ_i is given by (43).

TABLE I
DESIRED MODES MEETING THE HVDC SPECIFICATIONS

| Dynamics of interest | λ_i^{0} | λ_i^* | $r_{_{\lambda_{_{l}}^{0}}}$ |
|------------------------------------|-------------------|---------------|-----------------------------|
| Voltage $V_{\scriptscriptstyle m}$ | -3.8713 | -10 | -0.543 |
| Voltage V_g | 3.537 | -10 | -0.65 |
| Active Power P_m | $1.8{\pm}\ 5.89i$ | -21±21.42i | -0.06 ±0.01i |
| Active Power P_g | -3.3±10.3i | -21±21.42i | $-0.06 \pm 0.1i$ |
| Reactive Power Q_m | -3.8713 | -10 | -0.543 |
| Reactive Power Q_g | 3.537 | -10 | -0.65 |

TABLE II
PARTICIPATION FACTORS

| % | $P_{a_V_m/Q_m}$ | $P_{a_V_g/Q_g}$ | $P_{a_{-}P_{m}}$ | $P_{a_{-}P_{g}}$ |
|-----------------------------|------------------|------------------|------------------|------------------|
| λ_{V_m/Q_m}^0 | 57 | 2.1 | 21.8 | 28 |
| $\lambda_{P_m}^{0}$ | 0.2 | 71.9 | 0.3 | 15.8 |
| $\lambda_{{V_S}/{Q_S}}^{0}$ | 21.7 | 7.6 | 40 | 2.2 |
| $\lambda_{P_g}^0$ | 1.8 | 14 | 1.7 | 42.8 |

TABLE III SHVDC PARAMETERS

| | SHVDC standard | SHVDC optimal | |
|-----------------------------------|----------------|---------------|--|
| K | | | |
| | parameters | parameters | |
| D_{mp} | 20 | 94.82 | |
| D_{mq} | 33.33 | 50.24 | |
| $K_{p_V_{dc}}$ | 5 | 15.149 | |
| $K_{i_V_{dc}}$ | 2 | 16.501 | |
| D_{gp} | 20 | 92.843 | |
| D_{gq} | 33.33 | 57.20 | |
| | 5 | 27.61 | |
| $K_{p_{-}P_{g}}$ $K_{i_{-}P_{g}}$ | 3 | 12.62 | |

The optimal parameters in the second column of Table III were obtained with (44) solved for the desired locations in Table I. The response of SHVDC for the link in Fig. 1 with these tuned parameters is given in Fig. 7 in solid lines in comparison with the ones in dotted lines obtained with a classic vector control. The active power, the voltage and the reactive power dynamics are similar which prove that the performances given in Section B.1 are correctly ensured by the proposed control methodology. Notice, however, that less overshoots with the new tuned parameters are obtained. The robustness of this approach is studied in the next section on a four machines case.

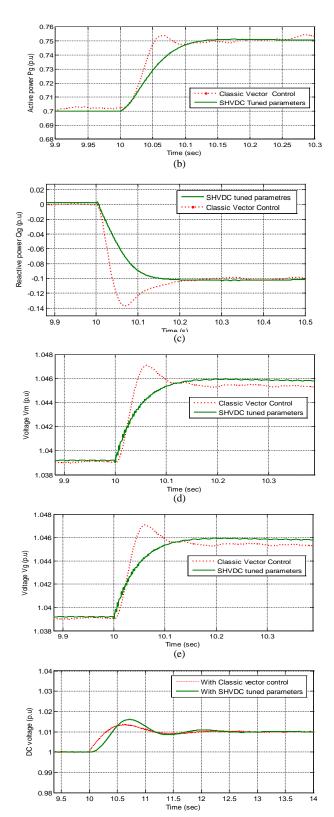


Fig. 7. Responses with optimal parameters. (a) Response of P_g to a +0.05 step in P_{g_ref} (p.u); (b) Response of Q_g to a -0.1 step in Q_{g_ref} (p.u); (c) Response of V_g to a -0.1 step change in Q_{g_ref} (p.u); (d) Response of V_m to -0.1 step change in Q_{m_ref} (p.u); (e) Response of V_{dc} to a +0.01 step in V_{dc_ref} (p.u).

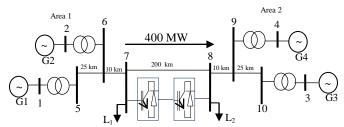


Fig. 8. Two area test system with parallel AC and DC lines

VI. FOUR MACHINES TEST POWER SYSTEM

In this section, the specific tuning SHVDC parameters presented in Section V is tested and compared with the classic vector control on the IEEE 4 machines benchmark [15] shown in Fig. 8. The two areas are connected with an AC line in parallel with one HVDC link. The 150 km HVDC cable link has a rated power of 200 MW and a DC voltage rating of ± 100 kV. Each area consists of two identical generating units of 400 MVA/20 kV rating. Each of the units is connected through transformers to the 100 kV transmission line. There is a power transfer of 400 MW from Area 1 to Area 2. The detailed system data is given in the appendix. The loads L_1 and L_2 are modeled as constant impedances.

The control SHVDC approach is assessed for local performances and for transient stability. The desired HVDC performances given in Section V are fulfilled as shown by the step responses in Fig. 9. As for the case of the two-machine system, responses with less overshoot are obtained compared to the vector control approach.

Fig. 10 shows the response of the angular speed of generator G1 to a three phase short circuit fault of 100 ms duration, occurring in the mid-point of the AC transmission line. We can depict a better dynamic performance with the proposed controller: the transient oscillations with the SHVDC control are more damped. For the same fault, transient stability is assessed by the Critical Clearing Time (CCT), which is the maximum time duration that a short-circuit may act without losing the system capacity to recover to a steady-state (stable) operation.

The obtained CCTs for a three-phase short-circuit fault in the mid-point of the AC transmission line are presented in Table IV for the classic vector control, and the proposed SHVDC control. We can see that the SHVDC control with tuned parameters improves the transient dynamics of the system and thus augments the transient stability margins of the neighbor network. This is due to the fact that the dynamics of the neighbour zone are taken into account at the synthesis stage via the oscillatory modes in Table I. The gains of the controllers are computed to damp these modes and thus to diminish the general swing of the zone and not only for the local HVDC dynamics. Moreover, as the structure of the SHVDC mimics the closed-loop of a standard synchronous generator, an inertia with a positive impact of the transient stability of the neighbour zone is emulated.

TABLE IV
CRITICAL CLEARING TIMES WITH BOTH CONTROL STRATEGIES

| Control strategy | CCT (ms) |
|-------------------------|----------|
| Tuning SHVDC parameters | 250 |
| Classic vector control | 170 |

(a)

Classic vector contol SHVDC tuned parameteres Active power Pg [p.u] 0.98 0.96 0.94 0.92 0.9 0.88 9.8 10 10.2 10.4 10.6 10.8 Time (sec) (b)

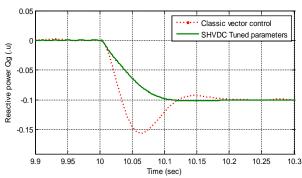


Fig. 9. Responses of the IEEE 4 machines benchmark. (a) Response of P_g to a -0.1 step in $P_{g,ref}$ (p.u); (b) Response of Q_g to a -0.1 step in $Q_{g,ref}$ (p.u).

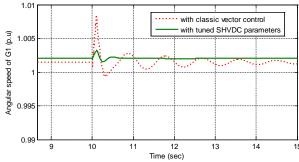


Fig. 10. Speed response of G1 to a 100 ms short-circuit

Remark: Real-time implementation

The SHVDC system is based on the Sinusoidal Pulse Width Modulation (SPWM) switching using a single-phase triangular carrier wave with a frequency of 27 times fundamental frequency (1350 Hz). This is compliant with the real time implementation which relies on a time response of 1-3 μs [22]. Indeed, in the real time simulation, the converter stations and the connecting DC cable are simulated in three separate small time step networks [23]. The three subnetworks are connected on the DC side through travelling wave transmission line models with a travel time in the range of the small time step size (i.e. $\sim 3 \mu s$). Both converter stations are interfaced to the AC systems with the help of interfacing transformers connecting the small and the normal time step areas.

VII. CONCLUSION

This paper proposed a new control strategy of HVDC transmission based on the synchronverter concept. The proposed synchronverter structure could not be controlled using directly the standard parameters of the SG. Thus, a specific tuning method was proposed based on; firstly, the sensitivity analysis of the system poles to the control parameters then, their placement linear approximation via residues. Better results than with the classic VSC control were obtained for the following points:

- Better dynamic performances (less overshoot and more damping) because this approach allows one to analytically take into account dynamic specifications at the tuning stage.
- Better stability margin of the neighbor zone for two reasons. First, swing information is directly taken into account at the synthesis stage in terms of the less damped modes of the neighbor zone and not only for the local HVDC dynamics as it is the case for the standard VSC control. Next, the SHVDC emulates inertia of two rotating machines and this has a direct damping effect for the neighbor zone.
- Help power system operators in system operation since the SG and their controls are better known.

It is noted that this kind of tuning is surely under optimal in the sense that the constraints to keep the structure of the classical generators and their regulations imposed by the synchronverter principle limits the performances of the resulting closed-loop. Moreover, the advantage of inertia emulation mentioned above could be lost in the context of a grid with many power electronics and few physical SG since the grid frequency is no longer (well) defined in this case.

VIII. REFERENCES

- D. Murali, M. Rajaram, and N. Reka, "Comparison of FACTS Devices for Power System Stability Enhancement," *International Journal of Computer Applications* (0975

 – 8887), 2010, Volume 8

 – No.4.
- [2] F. Wang, L. Bertling, and T. Le, "An Overview Introduction of VSC-HVDC: State-of-art and Potential Applications in Electric Power Systems," Cigré 2011.
- [3] F. Goodrich, and B. Andersen, "The 2000 MW HVDC link between England and France," *Power Eng. J.*, 1987, 1, (2), pp. 69–74.
- [4] Y. Decoeur, "France-Spain interconnections, First step for smart grids", inelfe, Madrid, March 26th, 2012.
- [5] A.E. Hammad, J. Gagnon and D. McCallum, "Improving the dynamic performance of a complex AC/DC system by HVDC control modifications," *IEEE Trans. Power Delivery*, 1990, 5, (4), pp. 1934– 1943
- [6] S. Li, T.A. Haskew, and L. Xu, "Control of HVDC light system using conventional and direct current vector control approaches," *IEEE Trans. Power Electr.*, 2010, 25, (12), pp. 3106–3118.
- [7] H. Latorre, and M. Ghandhari, "Improvement of power system stability by using a VSC-HVDC, "Int. J. Electr. Power Energy Syst, 2011, 33, (2), pp. 332–339.
- [8] N. Rostamkolai, A.G. Phadke, W.F. Long, and J.S. Thorp, "An adaptative optimal control strategy for dynamic stability enhacement of AC/DC power systems," *IEEE Trans. Power Syst.*, 1988, 3, (3), pp. 1139–1145.
- [9] Q.-C. Zhong, and G.Weiss, "Synchronverters: Inverters that mimic synchronous generators," *IEEE Trans. Ind. Electron.*, Apr. 2011, vol. 58, no. 4, pp. 1259–1267.
- [10] L. Zhang, L. Harnefors, and H.-P. Nee, "Power synchronization control of grid-connected voltage-source converters," *IEEE Trans. Power Syst*, May 2010, vol. 25, no. 2, pp. 809–820.
- [11] H.-P. Beck, and R. Hesse, "Virtual synchronous machine," in Proc. of the 9th International Conference on Electrical Power Quality and Utilisation (EPQU), 2007, pp. 1–6.

- [12] J. Driesen, and K. Visscher, "Virtual synchronous generators," in Proc. of IEEE Power and Energy Society General Meeting, 2008, pp. 1–3.
- [13] Y. Chen, R. Hesse, D. Turschner, and H.-P. Beck, "Improving the grid power quality using virtual synchronous machines," in 2011 International Conference on Power Engineering, Energy and Electrical Drives (POWERENG), 2011, pp. 1–6.
- [14] M. Torres, and L. A. C. Lopes, "Frequency control improvement in an autonomous power system: An application of virtual synchronous machines," in 2011 IEEE 8th International Conference on Power Electronics and ECCE Asia (ICPE & ECCE), 2011, pp. 2188–2195.
- [15] P. Kundur, "Power system stability and control", Mc Graw-HillInc, 1994.
- [16] P-L. Nguyen, Q-C. Zhong, F. Blaabjerg, and J-M. Guerrero, " Synchronverter-based Operation of STATCOM to Mimic Synchronous Condensers", 2012 7th IEEE Conference on Industrial Electronics and Applications (ICIEA).
- [17] H. Bourlès, and B. Marinescu, "Linear-Time Varying Systems, Algebraic-Analytic Approach", Springer-Verlag, LNCIES 410, 2011.
- [18] IEEE Recommended Practice for Excitation System Models for Power System Stability Studies, *IEEE Power Engineering Society*, 21 April 2006.
- [19] S. Li, T.A. Haskew, L. Xu, "Control of HVDC light system using conventional and direct current vector control approaches", *IEEE Trans. Power Electr.*, 2010, 25, (12), pp. 3106–3118.
- [20] M-K-S. Sangathan, J- Nehru "Performance of high-voltage direct Current (HVDC) systems with Line-commutated converters", bureau of find Indian standard Manak Bhavan, 9 Bahadur Shah Zafar Marg New Delhi 110002, April 2013.
- [21] G. Rogers, "Power System Oscillations", Kluwer Academic, 2000.
- [22] P.A. Forsyth, T.L. Maguire, D. Shearer, D. Rydmell, "Testing Firing Pulse Controls for a VSC Based HVDC Scheme with a Real Time Timestep < 3 µs", the International Conference on Power Systems Transients (IPST2009) in Kyoto, Japan June 3-6, 2009.
- [23] Pinaki Mitra, Vinothkumar K, Lidong Zhang, "Dynamic Performance Study of a HVDC Grid Using Real-Time Digital Simulator", IEEE Workshop on Complexity in Engineering 11 June, Aachen, Germany 2012.

IX. APPENDIX

Network data (Fig. 1)

Bus system 1 & 2: line voltage=100 kV, frequency=50 Hz, Short circuit power=600 MVA, Rs=0.75 Ω , Ls=0.2 H

27th AC filter in AC system 1 & 2: reactive power=18 MVAR, tuning frequency=1620 Hz, quality factor=15.

54th AC filter in AC system 1 & 2: reactive power=22 MVAR, tuning frequency=3240 Hz, quality factor=15.

DC system: voltage= ± 100 kV, rated DC power=200 MW, Pi line R=0.0139 Ω /km, L=159 μ H/km, C=0.331 μ F/km, Pi line length= 150 km, switching frequency=1620 Hz, DC capacitor=70 μ F, smoothing reactor: R=0.0251 Ω , L= 8mH.

Parameters of the classic vector control: current loop: kp=0.6, ki=6, reactive power control: ki=20, active power control: ki=20, DC voltage control: kp=5, ki=2.

Network data (Fig. 8)

Generators: Rated 400 MVA, 20 kV

Xl (p.u): leakage Reactance = 0.18

Xd (p.u.): d-axis synchronous reactance = 1.305, T'd0 (s): d-axis open circuit sub-transient time constant =0.296, T'd0 (s): d-axis open circuit transient time constant = 1.01

Xq (p.u): q-axis synchronous reactance = 0.053, Xq (p.u): q-axis synchronous reactance = 0.474, X''q (p.u): q-axis sub-transient reactance =0.243, T''q0 (s): q-axis open circuit sub transient time constant=0.1

M =2H (s): Mechanical starting time = 6.4

Governor control system

R (%): permanent droop =5, servo-motor: ka = 10/3, ta (s) = 0.07, regulation PID: kp = 1.163, ki= 0.105, kd= 0

Excitation control system

Amplifier gain: ka = 200, amplifier time constant: Ta (s) = 0.001, damping filter gain kf = 0.001, time constant te (s) = 0.1

27th AC filter in AC system 1 & 2: reactive power=18 MVAR, tuning frequency=1620 Hz, quality factor=15.

54th AC filter in AC system 1 & 2: reactive power=22 MVAR, tuning frequency=3240 Hz, quality factor=15.

DC system: voltage= ± 100 kV, rated DC power=200 MW, Pi line R=0.0139 Ω /km, L=159 μ H/km, C=0.331 μ F/km, Pi line length= 150 km, switching frequency=1620 Hz, DC capacitor=70 μ F, smoothing reactor: R=0.0251 Ω , L= 8mH.

Generator transformers Rated 400 MVA, 20/100 kV

Coupling Delta/ Yg

Primary resistance (p.u) =0.002, Primary inductance (p.u) =0.12 Secondary resistance (p.u) =0.002, Secondary inductance (p.u) =0.12

Loads: PL1=200 MW, PL2= 1GW

AC transmission lines

Resistance per phase $(\Omega/\text{km}) = 0.03$

Inductance per phase (mH/km) =0.32

Capacitance per phase (nF/km) = 11.5

Parameters of the classic vector control: current loop: kp=0.6, ki=8, reactive power control: ki=40, active power control: ki=40, DC voltage control: kp=1, ki=10.